AQ-200 Modbus map

Table of contents

1 AQ-21x frame Modbus map 2
1.1 FVSPE Modbus map 2
1.2 T Modbus map 380
1.3 M Modbus map 758
1.4 R Modbus map 1136
1.5 G Modbus map 1516
2 AQ-25x frame Modbus map 1895
3 Contact and reference information 2308

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1 AQ-21x frame Modbus map

1.1 FVSPE Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware FVSPE_v2.1.1.3-233. The firmware is used by the following device types:

- AQ-F201
- AQ-F205
- AQ-F210
- AQ-F215
- AQ-V211
- AQ-S214
- AQ-S215
- AQ-P215
- AQ-E215

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.1 - 1. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index

Holding Register	Parameter	Access	Length	
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR103				HR102 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR104	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR134	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR164	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR194	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR224	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR254	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR284	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR314	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR344	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR374	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR404	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR434	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR459				HR458 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR487				HR486 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR488	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR518	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR548	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR578	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR608	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR638	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR668	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR698	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR728	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR758	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR788	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR817				HR816 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR818	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR847				HR846 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR873				HR872 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR874	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR898	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR923				HR922 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR924	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR953				HR952 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR954	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR984	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1014	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1044	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1074	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1104	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1134	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1164	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	POW1:Reset 3ph Energies	RW	1	0=-;1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:Dis 1-32	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7;7b=DI8; (Hi-16bits)
HR1250				HR1249 (Lo-16bits)
HR1251	DIGI:Dis 33-64	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38;6b=DI39
HR1252				HR1251 (Lo-16bits)
HR1253	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b=O8;8b=
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8
HR1256	SGS:Force SG	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1257	SGS:Force SG change	RW	1	0=Disabled;1=Enabled
HR1258	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=SG1...6;6=
HR1259	SGS:Remote SG change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1260	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1261	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1262	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR1263	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1264	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1265	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1266	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1267	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1268	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1269	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1271	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1273	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1275	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1277	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1278	DEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1279	DEF1:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1280	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1281	DEF2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1282	DEF2:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1284	DEF3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1285	DEF3:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1287	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1288	DEF4:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1289	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1290	IEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR1292	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1296	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	HOC2:Ih>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1317	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1338	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)
HR1342	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1352	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1359	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4=CLPU b
HR1380	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloaded;4=C

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Holding Register	Parameter	Access	Length	
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Blocked;4b=2
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1385	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1386	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1387	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1389	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1390	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1391	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1394	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1395	TOLF1:Temp C or F deg	RW	1	0=C;1=F
HR1396	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1397	TOLF1:RTD Sensor	R	1	0=Invalid;1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set;1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1401	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=Running
HR1402	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1403	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1404	TOLM1:Time constants	RW	1	0=Single;1=Multiple
HR1405	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1408	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Blocked

Holding Register	Parameter	Access	Length	
HR1411	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1415	OV2:U>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1416	OV3:U>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1417	OV4:U>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1418	UV1:U< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1419	UV2:U<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1420	UV3:U<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1421	UV4:U<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1422	NOV1:U0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1423	NOV1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1425	NOV2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1427	NOV3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1429	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1443	FRQV1:f>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1444	FRQV1:f>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1445	FRQV1:f>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1446	FRQV1:f< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1447	FRQV1:f<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1448	FRQV1:f<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1449	FRQV1:f<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On high overloa
HR1459	OPW1:P> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1460	UPW1:P< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1461	RPW1:Prev> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1463	SEF1:U03rd< Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1466	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked

Holding Register	Parameter	Access	Length	
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=VHz(1) Blocked;2=VHz(1) Start;3=VHz(1) Alarm;4=
HR1469	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=Alarm
HR1470	ARC1:I/I0 Arc> condition	R	1	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4b=Z3 Trip
HR1471	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1472	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1473	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTBusfail;3=VTLinefail;4=Blocked
HR1474	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live VTS Ok SE
HR1475	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR1476	THD1:THD condition	R	1	0=Normal;1=Start;2=Alarm;3=Blocked
HR1477	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On
HR1478	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo cmd;5=BK
HR1479	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR1480	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=SYN1 Vco
HR1481				HR1480 (Lo-16bits)
HR1482	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=SYN2 Vco
HR1483				HR1482 (Lo-16bits)
HR1484	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=SYN3 Vco
HR1485				HR1484 (Lo-16bits)
HR1486	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1487	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1488	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1489	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1490	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1491	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1492	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1493	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad

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TN00011

Holding Register	Parameter	Access	Length	
HR1494	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1495	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1496	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR is runni Requested;14b=AR4 Requested;15b=AR5 Requested;16b=Exec
HR1497				HR1496 (Lo-16bits)
HR1498	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Delay;4=AR
HR1499	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set higher than U
HR1500				HR1499 (Lo-16bits)
HR1501	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Operation bl
HR1502				HR1501 (Lo-16bits)
HR1503	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low set Decre
HR1504	VRG1:Voltage meas condition	R	1	0=Not configured;1=System U12;2=System U23;3=System U31;
HR1505	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLT detected
HR1506	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1507	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLT detected
HR1508	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1509	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLT FW;2=FLT REV
HR1510	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=No voltage
HR1511	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1512	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLT detected
HR1513	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1514				HR1513 (Lo-16bits)
HR1515	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1518				HR1517 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1519	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=A8;8b=A
HR1520				HR1519 (Lo-16bits)
HR1521	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=A39;7b= (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1524				HR1523 (Lo-16bits)
HR1525	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1530				HR1529 (Lo-16bits)
HR1531	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1532				HR1531 (Lo-16bits)
HR1533	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1536				HR1535 (Lo-16bits)
HR1537	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1538				HR1537 (Lo-16bits)
HR1539	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1542				HR1541 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1543	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1544				HR1543 (Lo-16bits)
HR1545	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1548				HR1547 (Lo-16bits)
HR1549	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1550				HR1549 (Lo-16bits)
HR1551	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1554				HR1553 (Lo-16bits)
HR1555	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1556				HR1555 (Lo-16bits)
HR1557	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1560				HR1559 (Lo-16bits)
HR1561	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1562				HR1561 (Lo-16bits)
HR1563	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1566				HR1565 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1567	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1568				HR1567 (Lo-16bits)
HR1569	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1572				HR1571 (Lo-16bits)
HR1573	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1574				HR1573 (Lo-16bits)
HR1575	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1578				HR1577 (Lo-16bits)
HR1579	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1584				HR1583 (Lo-16bits)
HR1585	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1590				HR1589 (Lo-16bits)
HR1591	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1592				HR1591 (Lo-16bits)
HR1593	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1594				HR1593 (Lo-16bits)
HR1595	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1596				HR1595 (Lo-16bits)
HR1597	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1598				HR1597 (Lo-16bits)
HR1599	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1600				HR1599 (Lo-16bits)
HR1601	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1602				HR1601 (Lo-16bits)
HR1603	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1606				HR1605 (Lo-16bits)
HR1607	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1608				HR1607 (Lo-16bits)
HR1609	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1610				HR1609 (Lo-16bits)
HR1611	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1612				HR1611 (Lo-16bits)
HR1613	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1614				HR1613 (Lo-16bits)
HR1615	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1616				HR1615 (Lo-16bits)
HR1617	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1618				HR1617 (Lo-16bits)
HR1619	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1620				HR1619 (Lo-16bits)
HR1621	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1622				HR1621 (Lo-16bits)
HR1623	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1624				HR1623 (Lo-16bits)
HR1625	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1626				HR1625 (Lo-16bits)
HR1627	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1630				HR1629 (Lo-16bits)
HR1631	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1632				HR1631 (Lo-16bits)
HR1633	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1634				HR1633 (Lo-16bits)
HR1635	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=VHz(2) Start;2=VHz(2) Alarm;3=VHz(2) Trip;4=VHz(
HR1636	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1639	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1652				HR1651 (Lo-16bits)
HR1653	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1688				HR1687 (Lo-16bits)
HR1689	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1690				HR1689 (Lo-16bits)
HR1691	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1692				HR1691 (Lo-16bits)
HR1693	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1694				HR1693 (Lo-16bits)
HR1695	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1702				HR1701 (Lo-16bits)
HR1703	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1712	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1713	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1714	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1715	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1716	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR1717				HR1716 (Lo-16bits)
HR1718	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR1719				HR1718 (Lo-16bits)
HR1720	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1721				HR1720 (Lo-16bits)
HR1722	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1723				HR1722 (Lo-16bits)
HR1724	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR1725	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher

Holding Register	Parameter	Access	Length	
HR1726	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked:2=Trip;3=Alarm
HR1727	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	PGS1:PS1 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1730	VJP1:Voltage meas selected	R	1	0=Selection Ok;1=Selection not available
HR1731	PGS1:PS2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1732	PGS1:PS3 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1733	PGS1:PS4 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1734	PGS1:PS5 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1735	PGS1:PS6 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1736	PGS1:PS7 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1737	PGS1:PS8 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1738	PGS1:PS9 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1739	PGS1:PS10 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1740	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1741				HR1740 (Lo-16bits)
HR1742	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1743				HR1742 (Lo-16bits)
HR1744	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1745				HR1744 (Lo-16bits)
HR1746	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1747				HR1746 (Lo-16bits)
HR1748	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1749				HR1748 (Lo-16bits)
HR1750	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1751				HR1750 (Lo-16bits)
HR1752	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1753				HR1752 (Lo-16bits)
HR1754	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1755				HR1754 (Lo-16bits)
HR1756	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1757				HR1756 (Lo-16bits)
HR1758	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1759				HR1758 (Lo-16bits)
HR1760	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1761				HR1760 (Lo-16bits)
HR1762	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1763				HR1762 (Lo-16bits)
HR1764	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled;1=Activated
HR1767	CTRL:Local/Remote status	R	1	0=Remote;1=Local
HR1768	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1769				HR1768 (Lo-16bits)
HR1770	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1773				HR1772 (Lo-16bits)
HR1774	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1775				HR1774 (Lo-16bits)
HR1776	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1777				HR1776 (Lo-16bits)
HR1778	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1779				HR1778 (Lo-16bits)
HR1780	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1781				HR1780 (Lo-16bits)
HR1782	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1783				HR1782 (Lo-16bits)
HR1784	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1795				HR1794 (Lo-16bits)
HR1796	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	GSYN:Synchronizer mode	RW	1	0=Disabled;1=Activated
HR1801	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)
HR1803	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1811	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1837	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1838				HR1837 (Lo-16bits)
HR1839	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1840				HR1839 (Lo-16bits)
HR1841	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	VREC:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1852	VREC:Scale current values to primary	RW	1	0=No;1=Yes
HR1853	VREC:Record fault types (Overcurrrent & Voltage Over/Under) faults	RW	1	0=No;1=Yes
HR1854	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1862	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	RTB:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1873	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR1874	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)
HR1878	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1879				HR1878 (Lo-16bits)
HR1880	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)
HR1886	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1888	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR1891				HR1890 (Lo-16bits)
HR1892	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1895				HR1894 (Lo-16bits)
HR1896	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1897				HR1896 (Lo-16bits)
HR1898	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1899				HR1898 (Lo-16bits)
HR1900	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1901				HR1900 (Lo-16bits)
HR1902	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1903				HR1902 (Lo-16bits)
HR1904	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1907				HR1906 (Lo-16bits)
HR1908	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1909				HR1908 (Lo-16bits)
HR1910	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1913				HR1912 (Lo-16bits)
HR1914	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1919				HR1918 (Lo-16bits)
HR1920	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1921				HR1920 (Lo-16bits)
HR1922	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1923				HR1922 (Lo-16bits)
HR1924	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1925				HR1924 (Lo-16bits)
HR1926	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1927				HR1926 (Lo-16bits)
HR1928	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1929				HR1928 (Lo-16bits)
HR1930	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1931				HR1930 (Lo-16bits)
HR1932	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1933				HR1932 (Lo-16bits)
HR1934	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1935				HR1934 (Lo-16bits)
HR1936	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1937				HR1936 (Lo-16bits)
HR1938	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1939				HR1938 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1940	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1941				HR1940 (Lo-16bits)
HR1942	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1943				HR1942 (Lo-16bits)
HR1944	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1945				HR1944 (Lo-16bits)
HR1946	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1947				HR1946 (Lo-16bits)
HR1948	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1949				HR1948 (Lo-16bits)
HR1950	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1951				HR1950 (Lo-16bits)
HR1952	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1953				HR1952 (Lo-16bits)
HR1954	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1955				HR1954 (Lo-16bits)
HR1956	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1957				HR1956 (Lo-16bits)
HR1958	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR1959				HR1958 (Lo-16bits)
HR1960	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1961				HR1960 (Lo-16bits)
HR1962	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR1965				HR1964 (Lo-16bits)
HR1966	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1967				HR1966 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1968	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1969				HR1968 (Lo-16bits)
HR1970	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1971	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1972	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1973	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1974	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1975	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1976	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1977	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1978	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1979	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1980	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1981	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1982	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1983	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1984	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1985	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1986	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR1987	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1988				HR1987 (Lo-16bits)
HR1989	NOC1:I> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1990	NOC1:I> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1991	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1992				HR1991 (Lo-16bits)
HR1993	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1994				HR1993 (Lo-16bits)
HR1995	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1996				HR1995 (Lo-16bits)
HR1997	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1998				HR1997 (Lo-16bits)
HR1999	NOC1:Delay type	RW	1	0=DT;1=IDMT
HR2000	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2001				HR2000 (Lo-16bits)
HR2002	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2003	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2004	NOC1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2005	NOC1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2006	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2007				HR2006 (Lo-16bits)
HR2008	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2009				HR2008 (Lo-16bits)
HR2010	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2011				HR2010 (Lo-16bits)
HR2012	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2013				HR2012 (Lo-16bits)
HR2014	NOC1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2015	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2016	NOC2:I>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2017	NOC2:I>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2018	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2019				HR2018 (Lo-16bits)
HR2020	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2021				HR2020 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2022	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2023				HR2022 (Lo-16bits)
HR2024	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2025				HR2024 (Lo-16bits)
HR2026	NOC2:Delay type	RW	1	0=DT;1=IDMT
HR2027	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2030	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2031	NOC2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2032	NOC2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2033	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2042	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2043	NOC3:I>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2044	NOC3:I>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2045	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2046				HR2045 (Lo-16bits)
HR2047	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2048				HR2047 (Lo-16bits)
HR2049	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2050				HR2049 (Lo-16bits)
HR2051	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2052				HR2051 (Lo-16bits)
HR2053	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR2054	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2055				HR2054 (Lo-16bits)
HR2056	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2057	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2058	NOC3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2059	NOC3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2060	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2061				HR2060 (Lo-16bits)
HR2062	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2063				HR2062 (Lo-16bits)
HR2064	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2065				HR2064 (Lo-16bits)
HR2066	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2067				HR2066 (Lo-16bits)
HR2068	NOC3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2069	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2070	NOC4:I>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2071	NOC4:I>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2072	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2073				HR2072 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2074	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2075				HR2074 (Lo-16bits)
HR2076	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2077				HR2076 (Lo-16bits)
HR2078	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2079				HR2078 (Lo-16bits)
HR2080	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR2081	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2084	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2085	NOC4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2086	NOC4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2087	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)
HR2091	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2092				HR2091 (Lo-16bits)
HR2093	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2094				HR2093 (Lo-16bits)
HR2095	NOC4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2096	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2097	DOC1:Idir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2098	DOC1:Idir> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2099	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2100				HR2099 (Lo-16bits)
HR2101	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	DOC2:Idir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2108	DOC2:Idir>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2109	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2116				HR2115 (Lo-16bits)
HR2117	DOC3:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2118	DOC3:Idir>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2119	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2120				HR2119 (Lo-16bits)
HR2121	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2122				HR2121 (Lo-16bits)
HR2123	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2124				HR2123 (Lo-16bits)
HR2125	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2126				HR2125 (Lo-16bits)
HR2127	DOC4:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2128	DOC4:Idir>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2129	DOC4:Idir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2130				HR2129 (Lo-16bits)
HR2131	DOC4:Idir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	DOC4:Idir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2134				HR2133 (Lo-16bits)
HR2135	DOC4:Idir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2136				HR2135 (Lo-16bits)
HR2137	NEF1:I0> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2138	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2139				HR2138 (Lo-16bits)
HR2140	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR2143	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2144				HR2143 (Lo-16bits)
HR2145	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2146	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2147	NEF1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR2148	NEF1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2149	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2150				HR2149 (Lo-16bits)
HR2151	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2152				HR2151 (Lo-16bits)
HR2153	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2154				HR2153 (Lo-16bits)
HR2155	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	NEF1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2158	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2159	NEF2:I0>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2160	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2161				HR2160 (Lo-16bits)
HR2162	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR2165	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2168	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2169	NEF2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2170	NEF2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2171	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2174				HR2173 (Lo-16bits)
HR2175	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2178				HR2177 (Lo-16bits)
HR2179	NEF2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2180	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2181	NEF3:I0>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2182	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2183				HR2182 (Lo-16bits)
HR2184	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR2187	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2190	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2191	NEF3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2192	NEF3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2193	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)
HR2195	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2201	NEF3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2202	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2203	NEF4:I0>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2204	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)
HR2206	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR2209	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2210				HR2209 (Lo-16bits)
HR2211	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2212	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2213	NEF4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2214	NEF4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2215	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2218				HR2217 (Lo-16bits)
HR2219	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2220				HR2219 (Lo-16bits)
HR2221	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2222				HR2221 (Lo-16bits)
HR2223	NEF4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2224	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2225	DEF1:I0dir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2226	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2227				HR2226 (Lo-16bits)
HR2228	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2229				HR2228 (Lo-16bits)
HR2230	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2231				HR2230 (Lo-16bits)
HR2232	DEF2:I0dir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2233	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2240	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2241				HR2240 (Lo-16bits)
HR2242	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2243				HR2242 (Lo-16bits)
HR2244	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2245				HR2244 (Lo-16bits)
HR2246	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2247	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2251	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)
HR2253	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=Start On (Alarm);3=Start On (Alarm & Tr
HR2254	NOC1:I> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2255	NOC2:I>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2256	NOC3:I>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2257	NOC4:I>>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2258	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR2259				HR2258 (Lo-16bits)
HR2260	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2261				HR2260 (Lo-16bits)
HR2262	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2263				HR2262 (Lo-16bits)
HR2264	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2265				HR2264 (Lo-16bits)
HR2266	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2267				HR2266 (Lo-16bits)
HR2268	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2269				HR2268 (Lo-16bits)
HR2270	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2271				HR2270 (Lo-16bits)
HR2272	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2273				HR2272 (Lo-16bits)
HR2274	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2275				HR2274 (Lo-16bits)
HR2276	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2277				HR2276 (Lo-16bits)
HR2278	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2279				HR2278 (Lo-16bits)
HR2280	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2281				HR2280 (Lo-16bits)
HR2282	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2283				HR2282 (Lo-16bits)
HR2284	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2285				HR2284 (Lo-16bits)
HR2286	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2287				HR2286 (Lo-16bits)
HR2288	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2289				HR2288 (Lo-16bits)
HR2290	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2291				HR2290 (Lo-16bits)
HR2292	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2293				HR2292 (Lo-16bits)
HR2294	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR2295				HR2294 (Lo-16bits)
HR2296	ROC1:Idrw> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2297	ROC1:Idrw> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2298	ROC2:Idrw>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR2299	ROC2:Idrw>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2300	ROC3:Idrw>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2301	ROC3:Idrw>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2302	ROC4:Idrw>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2303	ROC4:Idrw>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2304	ROC5:Idrw>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2305	ROC5:Idrw>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2306	ROC6:Idrw>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2307	ROC6:Idrw>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2308	ROC7:Idrw>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2309	ROC7:Idrw>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2310	ROC8:Idrw>>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2311	ROC8:Idrw>>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2312	ROV1:Urw>/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2313	ROV2:Urw>>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2314	ROV3:Urw>>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2315	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2316	TOLM1:TM> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2317	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2318	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2319	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2320	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2321	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2322	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked

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Holding Register	Parameter	Access	Length	
HR2323	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2324	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)
HR2328	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2331				HR2330 (Lo-16bits)
HR2332	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2337				HR2336 (Lo-16bits)
HR2338	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2339				HR2338 (Lo-16bits)
HR2340	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2343				HR2342 (Lo-16bits)
HR2344	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2345				HR2344 (Lo-16bits)
HR2346	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2347				HR2346 (Lo-16bits)
HR2348	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2363				HR2362 (Lo-16bits)
HR2364	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)
HR2370	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2371				HR2370 (Lo-16bits)
HR2372	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2379				HR2378 (Lo-16bits)
HR2380	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2381				HR2380 (Lo-16bits)
HR2382	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2383				HR2382 (Lo-16bits)
HR2384	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2385				HR2384 (Lo-16bits)
HR2386	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2387				HR2386 (Lo-16bits)
HR2388	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2389				HR2388 (Lo-16bits)
HR2390	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2391				HR2390 (Lo-16bits)
HR2392	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2393				HR2392 (Lo-16bits)
HR2394	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2395				HR2394 (Lo-16bits)
HR2396	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2397				HR2396 (Lo-16bits)
HR2398	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2401				HR2400 (Lo-16bits)
HR2402	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2403				HR2402 (Lo-16bits)
HR2404	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2417				HR2416 (Lo-16bits)
HR2418	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2419				HR2418 (Lo-16bits)
HR2420	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2427				HR2426 (Lo-16bits)
HR2428	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	TOLT1:TT> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2437	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2438	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2439	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2440	TOLF1:TF> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2441	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2442	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2443	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2444	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)
HR2446	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2455				HR2454 (Lo-16bits)
HR2456	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2480	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)
HR2496	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)
HR2498	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2499				HR2498 (Lo-16bits)
HR2500	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2506	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)
HR2508	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2515				HR2514 (Lo-16bits)
HR2516	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR2517	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR2518	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2519				HR2518 (Lo-16bits)
HR2520	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2521				HR2520 (Lo-16bits)
HR2522	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2523				HR2522 (Lo-16bits)
HR2524	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2525				HR2524 (Lo-16bits)
HR2526	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2527				HR2526 (Lo-16bits)
HR2528	ARC1:I/I0 Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2529				HR2528 (Lo-16bits)
HR2530	ARC1:I/I0 Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2531				HR2530 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2532	ARC1:I/I0 Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2533				HR2532 (Lo-16bits)
HR2534	ARC1:I/I0 Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2535				HR2534 (Lo-16bits)
HR2536	ARC1:I/I0 Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2537				HR2536 (Lo-16bits)
HR2538	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2539				HR2538 (Lo-16bits)
HR2540	DIGI:DI1 Counter Clear	RW	1	0=-;1=Clear
HR2541	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	DIGI:DI2 Counter Clear	RW	1	0=-;1=Clear
HR2544	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2545				HR2544 (Lo-16bits)
HR2546	DIGI:DI3 Counter Clear	RW	1	0=-;1=Clear
HR2547	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	DIGI:DI4 Counter Clear	RW	1	0=-;1=Clear
HR2550	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2551				HR2550 (Lo-16bits)
HR2552	DIGI:DI5 Counter Clear	RW	1	0=-;1=Clear
HR2553	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	DIGI:DI6 Counter Clear	RW	1	0=-;1=Clear
HR2556	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2557				HR2556 (Lo-16bits)
HR2558	DIGI:DI7 Counter Clear	RW	1	0=-;1=Clear
HR2559	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2561	DIGI:DI8 Counter Clear	RW	1	0=-;1=Clear
HR2562	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2563				HR2562 (Lo-16bits)
HR2564	DIGI:DI9 Counter Clear	RW	1	0=-;1=Clear
HR2565	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	DIGI:DI10 Counter Clear	RW	1	0=-;1=Clear
HR2568	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2569				HR2568 (Lo-16bits)
HR2570	DIGI:DI11 Counter Clear	RW	1	0=-;1=Clear
HR2571	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	DIGI:DI12 Counter Clear	RW	1	0=-;1=Clear
HR2574	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2575				HR2574 (Lo-16bits)
HR2576	DIGI:DI13 Counter Clear	RW	1	0=-;1=Clear
HR2577	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	DIGI:DI14 Counter Clear	RW	1	0=-;1=Clear
HR2580	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2581				HR2580 (Lo-16bits)
HR2582	DIGI:DI15 Counter Clear	RW	1	0=-;1=Clear
HR2583	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	DIGI:DI16 Counter Clear	RW	1	0=-;1=Clear
HR2586	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2587				HR2586 (Lo-16bits)
HR2588	DIGI:DI17 Counter Clear	RW	1	0=-;1=Clear
HR2589	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2591	DIGI:DI18 Counter Clear	RW	1	0=-;1=Clear
HR2592	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2593				HR2592 (Lo-16bits)
HR2594	DIGI:DI19 Counter Clear	RW	1	0=-;1=Clear
HR2595	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	DIGI:DI20 Counter Clear	RW	1	0=-;1=Clear
HR2598	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2599				HR2598 (Lo-16bits)
HR2600	DIGI:DI21 Counter Clear	RW	1	0=-;1=Clear
HR2601	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	DIGI:DI22 Counter Clear	RW	1	0=-;1=Clear
HR2604	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	DIGI:DI23 Counter Clear	RW	1	0=-;1=Clear
HR2607	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2608				HR2607 (Lo-16bits)
HR2609	DIGI:DI24 Counter Clear	RW	1	0=-;1=Clear
HR2610	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2611				HR2610 (Lo-16bits)
HR2612	DIGI:DI25 Counter Clear	RW	1	0=-;1=Clear
HR2613	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	DIGI:DI26 Counter Clear	RW	1	0=-;1=Clear
HR2616	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2617				HR2616 (Lo-16bits)
HR2618	DIGI:DI27 Counter Clear	RW	1	0=-;1=Clear
HR2619	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2621	DIGI:DI28 Counter Clear	RW	1	0=-;1=Clear
HR2622	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2623				HR2622 (Lo-16bits)
HR2624	DIGI:DI29 Counter Clear	RW	1	0=-;1=Clear
HR2625	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	DIGI:DI30 Counter Clear	RW	1	0=-;1=Clear
HR2628	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2629				HR2628 (Lo-16bits)
HR2630	DIGI:DI31 Counter Clear	RW	1	0=-;1=Clear
HR2631	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2632				HR2631 (Lo-16bits)
HR2633	DIGI:DI32 Counter Clear	RW	1	0=-;1=Clear
HR2634	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2635				HR2634 (Lo-16bits)
HR2636	DIGI:DI33 Counter Clear	RW	1	0=-;1=Clear
HR2637	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DIGI:DI34 Counter Clear	RW	1	0=-;1=Clear
HR2640	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2641				HR2640 (Lo-16bits)
HR2642	DIGI:DI35 Counter Clear	RW	1	0=-;1=Clear
HR2643	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DIGI:DI36 Counter Clear	RW	1	0=-;1=Clear
HR2646	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2647				HR2646 (Lo-16bits)
HR2648	DIGI:DI37 Counter Clear	RW	1	0=-;1=Clear
HR2649	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2650				HR2649 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2651	DIGI:DI38 Counter Clear	RW	1	0=-;1=Clear
HR2652	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2653				HR2652 (Lo-16bits)
HR2654	DIGI:DI39 Counter Clear	RW	1	0=-;1=Clear
HR2655	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DIGI:DI40 Counter Clear	RW	1	0=-;1=Clear
HR2658	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2659				HR2658 (Lo-16bits)
HR2660	DIGI:DI41 Counter Clear	RW	1	0=-;1=Clear
HR2661	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DIGI:DI42 Counter Clear	RW	1	0=-;1=Clear
HR2664	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2665				HR2664 (Lo-16bits)
HR2666	DIGI:DI43 Counter Clear	RW	1	0=-;1=Clear
HR2667	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DIGI:DI44 Counter Clear	RW	1	0=-;1=Clear
HR2670	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2671				HR2670 (Lo-16bits)
HR2672	DIGI:DI45 Counter Clear	RW	1	0=-;1=Clear
HR2673	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DIGI:DI46 Counter Clear	RW	1	0=-;1=Clear
HR2676	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2677				HR2676 (Lo-16bits)
HR2678	DIGI:DI47 Counter Clear	RW	1	0=-;1=Clear
HR2679	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2681	DIGI:DI48 Counter Clear	RW	1	0=-;1=Clear
HR2682	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2683				HR2682 (Lo-16bits)
HR2684	DIGI:DI49 Counter Clear	RW	1	0=-;1=Clear
HR2685	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DIGI:DI50 Counter Clear	RW	1	0=-;1=Clear
HR2688	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2689				HR2688 (Lo-16bits)
HR2690	DIGI:DI51 Counter Clear	RW	1	0=-;1=Clear
HR2691	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)
HR2703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2710	NOC1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR2711	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2712	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2713	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2714	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2715	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2716	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2717	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)
HR2727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2728				HR2727 (Lo-16bits)
HR2729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2730				HR2729 (Lo-16bits)
HR2731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)
HR2733	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2734	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2735	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2736	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR2737	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2738	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2739	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2740	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2741	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2742	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2743	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2744	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2745	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2746	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2747	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2748	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2749	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2750	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2751	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2752	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2753	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2754	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2755	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2756	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2757	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR2758	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2759	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2760	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2761	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2762	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2763	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2764	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2765	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2785	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2815	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2830	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2831	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2832	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2833	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2834	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2835	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2836	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2837	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2840				HR2839 (Lo-16bits)
HR2841	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2854	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2855	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2856	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2857	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2858	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2859	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2860	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2861	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2863	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2878	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2879	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2880	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2881	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2882	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2883	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2884	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2885	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2886	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2887	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2888	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2889	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2890	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2891	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2892	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2893	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2910	NOC2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR2911	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2912	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2913	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2914	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2915	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2916	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2917	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)
HR2927	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2928				HR2927 (Lo-16bits)
HR2929	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2930				HR2929 (Lo-16bits)
HR2931	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2932				HR2931 (Lo-16bits)
HR2933	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2934	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2935	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2936	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR2937	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2938	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2939	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2940	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2941	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2942	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2943	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2944	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2945	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2946	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2947	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2948	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2949	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2950	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2951	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2952	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2953	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2954	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2955	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2956	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2957	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR2958	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2959	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2960	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2961	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2962	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2963	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2964	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2965	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2974				HR2973 (Lo-16bits)
HR2975	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2976				HR2975 (Lo-16bits)
HR2977	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2978				HR2977 (Lo-16bits)
HR2979	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2980				HR2979 (Lo-16bits)
HR2981	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2982				HR2981 (Lo-16bits)
HR2983	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2984				HR2983 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2985	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2986				HR2985 (Lo-16bits)
HR2987	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2988				HR2987 (Lo-16bits)
HR2989	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3004				HR3003 (Lo-16bits)
HR3005	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3006				HR3005 (Lo-16bits)
HR3007	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3008				HR3007 (Lo-16bits)
HR3009	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3010				HR3009 (Lo-16bits)
HR3011	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3012				HR3011 (Lo-16bits)
HR3013	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3015	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3018				HR3017 (Lo-16bits)
HR3019	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3030	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3031	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3032	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3033	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3034	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3035	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3036	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3037	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3038				HR3037 (Lo-16bits)
HR3039	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3040				HR3039 (Lo-16bits)
HR3041	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3042				HR3041 (Lo-16bits)
HR3043	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3044				HR3043 (Lo-16bits)
HR3045	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3046				HR3045 (Lo-16bits)
HR3047	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3048				HR3047 (Lo-16bits)
HR3049	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3050				HR3049 (Lo-16bits)
HR3051	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3052				HR3051 (Lo-16bits)
HR3053	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3054	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3055	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3056	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3057	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3058	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3059	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3060	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3061	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3063	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3078	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3079	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3080	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3081	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3082	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3083	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3084	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3085	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3086	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3087	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3088	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3089	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3090	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3091	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3092	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3093	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)
HR3103	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3110	NOC3:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3111	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3112	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3113	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3114	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3115	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3116	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3117	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3126				HR3125 (Lo-16bits)
HR3127	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3128				HR3127 (Lo-16bits)
HR3129	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3130				HR3129 (Lo-16bits)
HR3131	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3132				HR3131 (Lo-16bits)
HR3133	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3134	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3135	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3136	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3137	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3138	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3139	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3140	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3141	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3142	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3143	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3144	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3145	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3146	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3147	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3148	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3149	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3150	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3151	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3152	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3153	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3154	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3155	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3156	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3157	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3158	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3159	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3160	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3161	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3162	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3163	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3164	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3165	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3185	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3206				HR3205 (Lo-16bits)
HR3207	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3208				HR3207 (Lo-16bits)
HR3209	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3210				HR3209 (Lo-16bits)
HR3211	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3215	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3230	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3231	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3232	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3233	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3234	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3235	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3236	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3237	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3238				HR3237 (Lo-16bits)
HR3239	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3240				HR3239 (Lo-16bits)
HR3241	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3248				HR3247 (Lo-16bits)
HR3249	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3254	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3255	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3256	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3257	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3258	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3259	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3260	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3261	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3263	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3278	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3279	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3280	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3281	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3282	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3283	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3284	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3285	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3286	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3287	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3288	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3289	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3290	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3291	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3292	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3293	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3310	NOC4:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3311	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3312	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3313	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3314	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3315	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3316	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3317	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)
HR3327	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3330				HR3329 (Lo-16bits)
HR3331	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3334	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3335	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3336	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3337	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3338	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3339	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3340	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3341	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3342	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3343	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3344	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3345	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3346	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3347	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3348	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3349	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3350	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3351	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3352	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3353	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3354	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3355	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3356	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3357	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3358	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3359	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3360	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3361	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3362	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3363	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3364	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3365	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3374				HR3373 (Lo-16bits)
HR3375	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)
HR3377	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3382				HR3381 (Lo-16bits)
HR3383	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3384				HR3383 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3385	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3386				HR3385 (Lo-16bits)
HR3387	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3388				HR3387 (Lo-16bits)
HR3389	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3390				HR3389 (Lo-16bits)
HR3391	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3392				HR3391 (Lo-16bits)
HR3393	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3394				HR3393 (Lo-16bits)
HR3395	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3396				HR3395 (Lo-16bits)
HR3397	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)
HR3405	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)
HR3407	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3410				HR3409 (Lo-16bits)
HR3411	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3415	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3430	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3431	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3432	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3433	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3434	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3435	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3436	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3437	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3440				HR3439 (Lo-16bits)
HR3441	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3446				HR3445 (Lo-16bits)
HR3447	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3448				HR3447 (Lo-16bits)
HR3449	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3450				HR3449 (Lo-16bits)
HR3451	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3452				HR3451 (Lo-16bits)
HR3453	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3454	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3455	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3456	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3457	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3458	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3459	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3460	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3461	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3463	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3470				HR3469 (Lo-16bits)
HR3471	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3472				HR3471 (Lo-16bits)
HR3473	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3474				HR3473 (Lo-16bits)
HR3475	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3476				HR3475 (Lo-16bits)
HR3477	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3478	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3479	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3480	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3481	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3482	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3483	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3484	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3485	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3486	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3487	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3488	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3489	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3490	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3491	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3492	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3493	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3494	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3495	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3496	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3497	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3498	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3499	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3500	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3501	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)
HR3519	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)
HR3523	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)
HR3547	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)
HR3549	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3550	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3551	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3552	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3553	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3554	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3555	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3556	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3557	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3559	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3566				HR3565 (Lo-16bits)
HR3567	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3568				HR3567 (Lo-16bits)
HR3569	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3570				HR3569 (Lo-16bits)
HR3571	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3572				HR3571 (Lo-16bits)
HR3573	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3574	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3575	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3576	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3577	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3578	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3579	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3580	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3581	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3582	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3583	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3584	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3585	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3586	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3587	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3588	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3589	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3590	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3591	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3592	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3593	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3594	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3595	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3596	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3597	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3598	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3599	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3600	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3601	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3602	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3603	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3604	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3605	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3614				HR3613 (Lo-16bits)
HR3615	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3616				HR3615 (Lo-16bits)
HR3617	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3618				HR3617 (Lo-16bits)
HR3619	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3620				HR3619 (Lo-16bits)
HR3621	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3622				HR3621 (Lo-16bits)
HR3623	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3624				HR3623 (Lo-16bits)
HR3625	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3626				HR3625 (Lo-16bits)
HR3627	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3628				HR3627 (Lo-16bits)
HR3629	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3630				HR3629 (Lo-16bits)
HR3631	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3632				HR3631 (Lo-16bits)
HR3633	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3634				HR3633 (Lo-16bits)
HR3635	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3636				HR3635 (Lo-16bits)
HR3637	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3652				HR3651 (Lo-16bits)
HR3653	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)
HR3655	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)
HR3659	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3664				HR3663 (Lo-16bits)
HR3665	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3670	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3671	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3672	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3673	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3674	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3675	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3676	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3677	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3680				HR3679 (Lo-16bits)
HR3681	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3686				HR3685 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3687	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3688				HR3687 (Lo-16bits)
HR3689	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3690				HR3689 (Lo-16bits)
HR3691	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3692				HR3691 (Lo-16bits)
HR3693	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3694	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3695	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3696	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3697	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3698	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3699	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3700	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3701	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)
HR3703	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3710				HR3709 (Lo-16bits)
HR3711	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3712				HR3711 (Lo-16bits)
HR3713	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3714				HR3713 (Lo-16bits)
HR3715	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3716				HR3715 (Lo-16bits)
HR3717	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3718	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3719	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3720	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3721	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3722	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3723	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3724	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3725	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3726	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3727	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3728	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3729	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3730	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3731	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3732	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3733	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3734	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3735	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3736	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3737	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3738	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3739	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3740	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3741	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3777	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3790	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3791	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3792	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3793	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3794	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3795	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3796	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3797	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)
HR3799	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3805	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3806				HR3805 (Lo-16bits)
HR3807	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3808				HR3807 (Lo-16bits)
HR3809	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3810				HR3809 (Lo-16bits)
HR3811	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3812				HR3811 (Lo-16bits)
HR3813	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3814	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3815	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3816	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3817	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3818	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3819	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3820	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3821	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3822	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3823	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3824	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3825	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3826	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3827	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3828	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3829	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3830	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3831	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3832	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3833	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3834	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3835	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3836	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3837	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3838	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3839	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3840	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3841	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3842	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3843	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3844	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3845	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3851	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3862				HR3861 (Lo-16bits)
HR3863	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3864				HR3863 (Lo-16bits)
HR3865	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3866				HR3865 (Lo-16bits)
HR3867	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3881	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3910	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3911	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3912	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3913	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3914	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3915	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3916	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3917	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)
HR3927	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3933	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3934	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3935	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3936	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3937	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3938	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3939	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3940	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3941	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3958	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3959	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3960	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3961	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3962	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3963	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3964	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3965	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3966	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3967	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3968	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3969	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3970	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3971	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3972	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3973	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3974	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR3975	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3976	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3977	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3978	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3979	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3980	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3981	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4025	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4030	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4031	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4032	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4033	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4034	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4035	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4036	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4037	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4051	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4054	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4055	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4056	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4057	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4058	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4059	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4060	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4061	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4062	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4063	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4064	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4065	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4066	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4067	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4068	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4069	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4070	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4071	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4072	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4073	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4074	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4075	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4076	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4077	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4078	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4079	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4080	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4081	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4082	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4083	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4084	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4085	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)
HR4089	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4090				HR4089 (Lo-16bits)
HR4091	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4094				HR4093 (Lo-16bits)
HR4095	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4099	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)
HR4101	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4108				HR4107 (Lo-16bits)
HR4109	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4112				HR4111 (Lo-16bits)
HR4113	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4114				HR4113 (Lo-16bits)
HR4115	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4116				HR4115 (Lo-16bits)
HR4117	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4120				HR4119 (Lo-16bits)
HR4121	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4129	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)
HR4141	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4150	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4151	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4152	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4153	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4154	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4155	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4156	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4157	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4166				HR4165 (Lo-16bits)
HR4167	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4168				HR4167 (Lo-16bits)
HR4169	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4170				HR4169 (Lo-16bits)
HR4171	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4172				HR4171 (Lo-16bits)
HR4173	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4174	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4175	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4176	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4177	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4178	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4179	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4180	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4181	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4190				HR4189 (Lo-16bits)
HR4191	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4192				HR4191 (Lo-16bits)
HR4193	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4194				HR4193 (Lo-16bits)
HR4195	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4196				HR4195 (Lo-16bits)
HR4197	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4198	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4199	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4200	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4201	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4202	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4203	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4204	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4205	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4206	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4207	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4208	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4209	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4210	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4211	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4212	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4213	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4214	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4215	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4216	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4217	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4218	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4219	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4220	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4221	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4245	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)
HR4265	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4270	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4271	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4272	DOC4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4273	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4274	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4275	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4276	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4277	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4286				HR4285 (Lo-16bits)
HR4287	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4288				HR4287 (Lo-16bits)
HR4289	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4290				HR4289 (Lo-16bits)
HR4291	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4292				HR4291 (Lo-16bits)
HR4293	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4294	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4295	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4296	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4297	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4298	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4299	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4300	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4301	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4302	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4303	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4304	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4305	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4306	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4307	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4308	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4309	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4310	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4311	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4312	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4313	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4314	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4315	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4316	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4317	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4318	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4319	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4320	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4321	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4322	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4323	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4324	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4325	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4336				HR4335 (Lo-16bits)
HR4337	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4338				HR4337 (Lo-16bits)
HR4339	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4340				HR4339 (Lo-16bits)
HR4341	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4342				HR4341 (Lo-16bits)
HR4343	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4348				HR4347 (Lo-16bits)
HR4349	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4366				HR4365 (Lo-16bits)
HR4367	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4368				HR4367 (Lo-16bits)
HR4369	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4370				HR4369 (Lo-16bits)
HR4371	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4372				HR4371 (Lo-16bits)
HR4373	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4378				HR4377 (Lo-16bits)
HR4379	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4390	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4391	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4392	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4393	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4394	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4395	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4396	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4397	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4403	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4414	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4415	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4416	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4417	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4418	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4419	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4420	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4421	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4427	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4438	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4439	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4440	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4441	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4442	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4443	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4444	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4445	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4446	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4447	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4448	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4449	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4450	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4451	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4452	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4453	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4470	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4471	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4472	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4473	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4474	NEF1:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4475	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4476	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4477	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)
HR4493	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4494	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4495	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4496	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4497	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4498	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4499	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4500	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4501	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4502	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4503	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4504	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4505	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4506	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4507	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4508	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4509	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4510	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4511	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4512	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4513	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4514	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4515	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4516	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4517	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4518	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4519	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4520	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4521	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4522	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4523	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4524	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4525	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4534				HR4533 (Lo-16bits)
HR4535	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4538				HR4537 (Lo-16bits)
HR4539	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4540				HR4539 (Lo-16bits)
HR4541	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4542				HR4541 (Lo-16bits)
HR4543	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4544				HR4543 (Lo-16bits)
HR4545	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4549	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4566				HR4565 (Lo-16bits)
HR4567	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4568				HR4567 (Lo-16bits)
HR4569	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4570				HR4569 (Lo-16bits)
HR4571	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4572				HR4571 (Lo-16bits)
HR4573	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4579	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4590	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4591	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4592	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4593	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4594	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4595	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4596	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4597	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4598				HR4597 (Lo-16bits)
HR4599	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4600				HR4599 (Lo-16bits)
HR4601	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4602				HR4601 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4603	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4604				HR4603 (Lo-16bits)
HR4605	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4606				HR4605 (Lo-16bits)
HR4607	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4608				HR4607 (Lo-16bits)
HR4609	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4610				HR4609 (Lo-16bits)
HR4611	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4612				HR4611 (Lo-16bits)
HR4613	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4614	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4615	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4616	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4617	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4618	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4619	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4620	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4621	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4627	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4638	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4639	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4640	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4641	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4642	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4643	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4644	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4645	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4646	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4647	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4648	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4649	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4650	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4651	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4652	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4653	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4670	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4671	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4672	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4673	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4674	NEF2:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4675	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4676	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4677	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4686				HR4685 (Lo-16bits)
HR4687	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4688				HR4687 (Lo-16bits)
HR4689	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4690				HR4689 (Lo-16bits)
HR4691	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4692				HR4691 (Lo-16bits)
HR4693	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4694	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4695	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4696	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4697	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4698	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4699	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4700	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4701	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4702	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4703	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4704	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4705	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4706	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4707	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4708	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4709	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4710	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4711	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4712	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4713	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4714	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4715	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4716	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4717	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4718	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4719	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4720	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4721	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4722	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4723	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4724	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4725	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)
HR4731	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4734				HR4733 (Lo-16bits)
HR4735	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4736				HR4735 (Lo-16bits)
HR4737	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4738				HR4737 (Lo-16bits)
HR4739	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4740				HR4739 (Lo-16bits)
HR4741	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4742				HR4741 (Lo-16bits)
HR4743	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4744				HR4743 (Lo-16bits)
HR4745	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4746				HR4745 (Lo-16bits)
HR4747	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4748				HR4747 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4749	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)
HR4753	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4766				HR4765 (Lo-16bits)
HR4767	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4768				HR4767 (Lo-16bits)
HR4769	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4770				HR4769 (Lo-16bits)
HR4771	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4772				HR4771 (Lo-16bits)
HR4773	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4778				HR4777 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4779	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)
HR4781	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4790	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4791	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4792	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4793	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4794	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4795	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4796	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4797	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4798				HR4797 (Lo-16bits)
HR4799	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4800				HR4799 (Lo-16bits)
HR4801	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4802				HR4801 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4803	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4804				HR4803 (Lo-16bits)
HR4805	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4806				HR4805 (Lo-16bits)
HR4807	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4808				HR4807 (Lo-16bits)
HR4809	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4810				HR4809 (Lo-16bits)
HR4811	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4812				HR4811 (Lo-16bits)
HR4813	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4814	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4815	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4816	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4817	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4818	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4819	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4820	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4821	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4827	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)
HR4833	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4838	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4839	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4840	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4841	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4842	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4843	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4844	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4845	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4846	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4847	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4848	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4849	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4850	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4851	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4852	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4853	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4870	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4871	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4872	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4873	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4874	NEF3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4875	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4876	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4877	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4886				HR4885 (Lo-16bits)
HR4887	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4888				HR4887 (Lo-16bits)
HR4889	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4890				HR4889 (Lo-16bits)
HR4891	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4892				HR4891 (Lo-16bits)
HR4893	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4894	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4895	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4896	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4897	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4898	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4899	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4900	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4901	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4902	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4903	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4904	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4905	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4906	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4907	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4908	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4909	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4910	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4911	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4912	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4913	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4914	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4915	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4916	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4917	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4918	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4919	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4920	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4921	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4922	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4923	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4924	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4925	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4934				HR4933 (Lo-16bits)
HR4935	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4936				HR4935 (Lo-16bits)
HR4937	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4938				HR4937 (Lo-16bits)
HR4939	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4940				HR4939 (Lo-16bits)
HR4941	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4942				HR4941 (Lo-16bits)
HR4943	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4944				HR4943 (Lo-16bits)
HR4945	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4946				HR4945 (Lo-16bits)
HR4947	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4948				HR4947 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4949	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)
HR4961	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4966				HR4965 (Lo-16bits)
HR4967	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4968				HR4967 (Lo-16bits)
HR4969	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4970				HR4969 (Lo-16bits)
HR4971	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4972				HR4971 (Lo-16bits)
HR4973	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4979	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4990	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4991	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4992	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4993	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4994	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4995	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4996	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4997	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4998				HR4997 (Lo-16bits)
HR4999	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5000				HR4999 (Lo-16bits)
HR5001	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5002				HR5001 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5003	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5004				HR5003 (Lo-16bits)
HR5005	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5006				HR5005 (Lo-16bits)
HR5007	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5008				HR5007 (Lo-16bits)
HR5009	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5010				HR5009 (Lo-16bits)
HR5011	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5012				HR5011 (Lo-16bits)
HR5013	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5014	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5015	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5016	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5017	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5018	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5019	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5020	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5021	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5027	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5038	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5039	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5040	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5041	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5042	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5043	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5044	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5045	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5046	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5047	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5048	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5049	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5050	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5051	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5052	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5053	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5070	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5071	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5072	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5073	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5074	NEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR5075	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5076	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5077	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5086				HR5085 (Lo-16bits)
HR5087	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5088				HR5087 (Lo-16bits)
HR5089	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5090				HR5089 (Lo-16bits)
HR5091	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5092				HR5091 (Lo-16bits)
HR5093	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5094	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5095	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5096	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5097	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5098	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5099	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5100	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR5101	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5102	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5103	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5104	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5105	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5106	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5107	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5108	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5109	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5110	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5111	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5112	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5113	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5114	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5115	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5116	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5117	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5118	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5119	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5120	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR5121	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5122	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5123	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5124	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5125	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5134				HR5133 (Lo-16bits)
HR5135	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5136				HR5135 (Lo-16bits)
HR5137	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5138				HR5137 (Lo-16bits)
HR5139	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5140				HR5139 (Lo-16bits)
HR5141	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5142				HR5141 (Lo-16bits)
HR5143	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5144				HR5143 (Lo-16bits)
HR5145	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5146				HR5145 (Lo-16bits)
HR5147	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5148				HR5147 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5149	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5150				HR5149 (Lo-16bits)
HR5151	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5152				HR5151 (Lo-16bits)
HR5153	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5154				HR5153 (Lo-16bits)
HR5155	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5156				HR5155 (Lo-16bits)
HR5157	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)
HR5163	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5170				HR5169 (Lo-16bits)
HR5171	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5179	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5190	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5191	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5192	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5193	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5194	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5195	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5196	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5197	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5203	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5214	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5215	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5216	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5217	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5218	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5219	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5220	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5221	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5227	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5238	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5239	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5240	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5241	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5242	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5243	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5244	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5245	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5246	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5247	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5248	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5249	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5250	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5251	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5252	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5253	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5254	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5255	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5256	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5257	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5258	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5259	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5260	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5261	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)
HR5265	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)
HR5289	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)
HR5313	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)
HR5319	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)
HR5335	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5341	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5342				HR5341 (Lo-16bits)
HR5343	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5344				HR5343 (Lo-16bits)
HR5345	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5346				HR5345 (Lo-16bits)
HR5347	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5348				HR5347 (Lo-16bits)
HR5349	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5350				HR5349 (Lo-16bits)
HR5351	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5352				HR5351 (Lo-16bits)
HR5353	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5354				HR5353 (Lo-16bits)
HR5355	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5356				HR5355 (Lo-16bits)
HR5357	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5358	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5359	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5360	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5361	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5362	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5363	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5364	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5365	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5391	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5406				HR5405 (Lo-16bits)
HR5407	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5408				HR5407 (Lo-16bits)
HR5409	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5410				HR5409 (Lo-16bits)
HR5411	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5412				HR5411 (Lo-16bits)
HR5413	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5414				HR5413 (Lo-16bits)
HR5415	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5416				HR5415 (Lo-16bits)
HR5417	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5418				HR5417 (Lo-16bits)
HR5419	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5420				HR5419 (Lo-16bits)
HR5421	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5422				HR5421 (Lo-16bits)
HR5423	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5424				HR5423 (Lo-16bits)
HR5425	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5426				HR5425 (Lo-16bits)
HR5427	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5428				HR5427 (Lo-16bits)
HR5429	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5430				HR5429 (Lo-16bits)
HR5431	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5432				HR5431 (Lo-16bits)
HR5433	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5434				HR5433 (Lo-16bits)
HR5435	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5436				HR5435 (Lo-16bits)
HR5437	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

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Holding Register	Parameter	Access	Length	
HR5446	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5447	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5448	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5449	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5450	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5451	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5452	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5453	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5454	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5455	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5456	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5457	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5458	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5459	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5460	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5461	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)
HR5463	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)
HR5487	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5502				HR5501 (Lo-16bits)
HR5503	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5504				HR5503 (Lo-16bits)
HR5505	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5506				HR5505 (Lo-16bits)
HR5507	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5508				HR5507 (Lo-16bits)
HR5509	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5510	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5511	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5512	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5513	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5514	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5515	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5516	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5517	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5518	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR5519	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5520	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5521	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5522	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5523	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5524	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5525	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5526	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5527	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5528	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5529	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5530	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5531	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5532	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5533	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5534	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5535	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5536	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5537	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5538	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR5539	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5540	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5541	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5550				HR5549 (Lo-16bits)
HR5551	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5552				HR5551 (Lo-16bits)
HR5553	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5554				HR5553 (Lo-16bits)
HR5555	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5556				HR5555 (Lo-16bits)
HR5557	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5558				HR5557 (Lo-16bits)
HR5559	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5560				HR5559 (Lo-16bits)
HR5561	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5562				HR5561 (Lo-16bits)
HR5563	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5564				HR5563 (Lo-16bits)
HR5565	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5566				HR5565 (Lo-16bits)
HR5567	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5568				HR5567 (Lo-16bits)
HR5569	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5570				HR5569 (Lo-16bits)
HR5571	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5572				HR5571 (Lo-16bits)
HR5573	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)
HR5591	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)
HR5597	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5598				HR5597 (Lo-16bits)
HR5599	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5606	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5607	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5608	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5609	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5610	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5611	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5612	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5613	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5616				HR5615 (Lo-16bits)
HR5617	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5622				HR5621 (Lo-16bits)
HR5623	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5630	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5631	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5632	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5633	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5634	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5635	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5636	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5637	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)
HR5639	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5647	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5654	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5655	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5656	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5657	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5658	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5659	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5660	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5661	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5662	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5663	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5664	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5665	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5666	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5667	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5668	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5669	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5670	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5671	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5672	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5673	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5674	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5675	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5676	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5677	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)
HR5685	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5691	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)
HR5695	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)
HR5709	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5715	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)
HR5719	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)
HR5733	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5739	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)
HR5757	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5758				HR5757 (Lo-16bits)
HR5759	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5760				HR5759 (Lo-16bits)
HR5761	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5762				HR5761 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5763	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5764				HR5763 (Lo-16bits)
HR5765	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5766				HR5765 (Lo-16bits)
HR5767	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5768				HR5767 (Lo-16bits)
HR5769	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5770				HR5769 (Lo-16bits)
HR5771	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5772				HR5771 (Lo-16bits)
HR5773	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5774	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5775	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5776	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5777	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5778	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5779	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5780	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5781	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5787	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)
HR5807	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5813	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5822				HR5821 (Lo-16bits)
HR5823	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5824				HR5823 (Lo-16bits)
HR5825	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5826				HR5825 (Lo-16bits)
HR5827	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5828				HR5827 (Lo-16bits)
HR5829	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5830				HR5829 (Lo-16bits)
HR5831	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5832				HR5831 (Lo-16bits)
HR5833	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5834				HR5833 (Lo-16bits)
HR5835	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5836				HR5835 (Lo-16bits)
HR5837	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5838				HR5837 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5839	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5840				HR5839 (Lo-16bits)
HR5841	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5842				HR5841 (Lo-16bits)
HR5843	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5844				HR5843 (Lo-16bits)
HR5845	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5846				HR5845 (Lo-16bits)
HR5847	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5848				HR5847 (Lo-16bits)
HR5849	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5850				HR5849 (Lo-16bits)
HR5851	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5852				HR5851 (Lo-16bits)
HR5853	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5862	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5863	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5864	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5865	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	
HR5866	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5867	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5868	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5869	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5870	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5871	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5872	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5873	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5874	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5875	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5876	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5877	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5891	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5915	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5918				HR5917 (Lo-16bits)
HR5919	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5920				HR5919 (Lo-16bits)
HR5921	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5922				HR5921 (Lo-16bits)
HR5923	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5924				HR5923 (Lo-16bits)
HR5925	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5926	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5927	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5928	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5929	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5930	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5931	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5932	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5933	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5934	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5935	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5936	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5937	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5938	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR5939	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5940	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5941	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5942	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5943	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5944	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5945	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5946	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5947	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5948	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5949	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5950	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5951	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5952	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5953	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5954	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5955	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5956	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5957	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5966				HR5965 (Lo-16bits)
HR5967	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5968				HR5967 (Lo-16bits)
HR5969	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5970				HR5969 (Lo-16bits)
HR5971	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5972				HR5971 (Lo-16bits)
HR5973	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5974				HR5973 (Lo-16bits)
HR5975	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5976				HR5975 (Lo-16bits)
HR5977	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5978				HR5977 (Lo-16bits)
HR5979	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5980				HR5979 (Lo-16bits)
HR5981	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5982				HR5981 (Lo-16bits)
HR5983	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5984				HR5983 (Lo-16bits)
HR5985	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5986				HR5985 (Lo-16bits)
HR5987	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5988				HR5987 (Lo-16bits)
HR5989	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5990				HR5989 (Lo-16bits)
HR5991	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)
HR5995	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6014				HR6013 (Lo-16bits)
HR6015	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)
HR6017	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6020				HR6019 (Lo-16bits)
HR6021	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6022	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6023	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6024	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6025	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6026	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6027	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6028	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6029	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6038				HR6037 (Lo-16bits)
HR6039	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6043	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6046	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6047	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6048	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6049	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6050	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6051	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6052	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6053	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)
HR6063	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6069	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6070	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6071	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6072	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6073	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6074	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6075	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6076	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6077	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6078	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6079	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6080	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6081	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6082	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6083	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6084	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6085	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6086	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

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Holding Register	Parameter	Access	Length	
HR6087	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6088	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6089	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6090	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6091	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6092	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6093	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)
HR6107	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)
HR6131	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)
HR6143	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)
HR6151	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)
HR6155	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6174				HR6173 (Lo-16bits)
HR6175	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6176				HR6175 (Lo-16bits)
HR6177	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6178				HR6177 (Lo-16bits)
HR6179	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6180				HR6179 (Lo-16bits)
HR6181	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6182				HR6181 (Lo-16bits)
HR6183	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6184				HR6183 (Lo-16bits)
HR6185	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6186				HR6185 (Lo-16bits)
HR6187	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6188				HR6187 (Lo-16bits)
HR6189	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6190	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6191	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6192	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6193	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6194	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6195	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6196	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6197	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)
HR6203	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6209	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)
HR6229	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6238				HR6237 (Lo-16bits)
HR6239	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6240				HR6239 (Lo-16bits)
HR6241	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6242				HR6241 (Lo-16bits)
HR6243	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6244				HR6243 (Lo-16bits)
HR6245	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6246				HR6245 (Lo-16bits)
HR6247	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6248				HR6247 (Lo-16bits)
HR6249	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6250				HR6249 (Lo-16bits)
HR6251	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6252				HR6251 (Lo-16bits)
HR6253	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6254				HR6253 (Lo-16bits)
HR6255	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6256				HR6255 (Lo-16bits)
HR6257	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6258				HR6257 (Lo-16bits)
HR6259	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6260				HR6259 (Lo-16bits)
HR6261	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6262				HR6261 (Lo-16bits)
HR6263	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6264				HR6263 (Lo-16bits)
HR6265	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6266				HR6265 (Lo-16bits)
HR6267	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6268				HR6267 (Lo-16bits)
HR6269	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6278	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6279	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6280	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6281	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6282	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6283	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6284	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6285	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6286	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6287	DEF3:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR6288	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6289	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6290	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6291	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6292	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6293	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)
HR6307	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6313	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)
HR6331	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6334				HR6333 (Lo-16bits)
HR6335	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6336				HR6335 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6337	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6338				HR6337 (Lo-16bits)
HR6339	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6340				HR6339 (Lo-16bits)
HR6341	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6342	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6343	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6344	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6345	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6346	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6347	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6348	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6349	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6350	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6351	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6352	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6353	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6354	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6355	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6356	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6357	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6358	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6359	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR6360	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6361	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6362	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6363	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6364	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6365	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6366	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6367	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6368	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6369	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6370	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6371	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6372	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6373	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6382				HR6381 (Lo-16bits)
HR6383	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6384				HR6383 (Lo-16bits)
HR6385	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6386				HR6385 (Lo-16bits)
HR6387	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6388				HR6387 (Lo-16bits)
HR6389	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6390				HR6389 (Lo-16bits)
HR6391	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6392				HR6391 (Lo-16bits)
HR6393	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6394				HR6393 (Lo-16bits)
HR6395	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6396				HR6395 (Lo-16bits)
HR6397	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6398				HR6397 (Lo-16bits)
HR6399	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6400				HR6399 (Lo-16bits)
HR6401	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6402				HR6401 (Lo-16bits)
HR6403	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6404				HR6403 (Lo-16bits)
HR6405	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6406				HR6405 (Lo-16bits)
HR6407	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6414				HR6413 (Lo-16bits)
HR6415	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)
HR6423	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6436				HR6435 (Lo-16bits)
HR6437	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6438	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6439	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6440	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6441	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6442	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6443	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6444	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6445	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)
HR6459	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6462	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6463	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6464	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6465	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6466	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6467	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6468	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6469	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)
HR6485	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6486	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6487	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6488	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6489	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR6490	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6491	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6492	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6493	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6494	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6495	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6496	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6497	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6498	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6499	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6500	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6501	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6502	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6503	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6504	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6505	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6506	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6507	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6508	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6509	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)
HR6527	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6534				HR6533 (Lo-16bits)
HR6535	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)
HR6551	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6590				HR6589 (Lo-16bits)
HR6591	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6592				HR6591 (Lo-16bits)
HR6593	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6594				HR6593 (Lo-16bits)
HR6595	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6596				HR6595 (Lo-16bits)
HR6597	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6598				HR6597 (Lo-16bits)
HR6599	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6600				HR6599 (Lo-16bits)
HR6601	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6602				HR6601 (Lo-16bits)
HR6603	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6604				HR6603 (Lo-16bits)
HR6605	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR6606	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6607	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6608	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6609	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6610	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6611	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6612	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6613	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)
HR6625	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6654				HR6653 (Lo-16bits)
HR6655	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6656				HR6655 (Lo-16bits)
HR6657	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6658				HR6657 (Lo-16bits)
HR6659	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6660				HR6659 (Lo-16bits)
HR6661	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6662				HR6661 (Lo-16bits)
HR6663	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6664				HR6663 (Lo-16bits)
HR6665	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6666				HR6665 (Lo-16bits)
HR6667	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6668				HR6667 (Lo-16bits)
HR6669	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6670				HR6669 (Lo-16bits)
HR6671	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6672				HR6671 (Lo-16bits)
HR6673	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6674				HR6673 (Lo-16bits)
HR6675	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6676				HR6675 (Lo-16bits)
HR6677	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6678				HR6677 (Lo-16bits)
HR6679	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6680				HR6679 (Lo-16bits)
HR6681	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6682				HR6681 (Lo-16bits)
HR6683	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6684				HR6683 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6685	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6694	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6695	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6696	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6697	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6698	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6699	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6700	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6701	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6702	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6703	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6704	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6705	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6706	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6707	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6708	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6709	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6711	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)
HR6729	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6735	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6750				HR6749 (Lo-16bits)
HR6751	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6752				HR6751 (Lo-16bits)
HR6753	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6754				HR6753 (Lo-16bits)
HR6755	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6756				HR6755 (Lo-16bits)
HR6757	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6758	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6759	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR6760	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6761	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6762	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6763	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6764	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6765	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6766	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6767	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6768	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6769	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6770	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6771	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6772	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6773	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6774	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6775	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6776	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6777	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6778	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6779	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6780	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6781	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR6782	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6783	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6784	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6785	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6786	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6787	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6788	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6789	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6798				HR6797 (Lo-16bits)
HR6799	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6800				HR6799 (Lo-16bits)
HR6801	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6802				HR6801 (Lo-16bits)
HR6803	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6804				HR6803 (Lo-16bits)
HR6805	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6806				HR6805 (Lo-16bits)
HR6807	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6808				HR6807 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6809	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6810				HR6809 (Lo-16bits)
HR6811	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6812				HR6811 (Lo-16bits)
HR6813	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6822				HR6821 (Lo-16bits)
HR6823	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6830				HR6829 (Lo-16bits)
HR6831	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6839	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6854	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6855	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6856	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6857	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6858	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6859	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6860	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6861	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6862				HR6861 (Lo-16bits)
HR6863	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6864				HR6863 (Lo-16bits)
HR6865	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6866				HR6865 (Lo-16bits)
HR6867	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6868				HR6867 (Lo-16bits)
HR6869	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6878	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6879	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6880	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6881	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6882	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6883	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6884	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6885	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6886				HR6885 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6887	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6888				HR6887 (Lo-16bits)
HR6889	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6890				HR6889 (Lo-16bits)
HR6891	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6892				HR6891 (Lo-16bits)
HR6893	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6894				HR6893 (Lo-16bits)
HR6895	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6896				HR6895 (Lo-16bits)
HR6897	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6898				HR6897 (Lo-16bits)
HR6899	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6900				HR6899 (Lo-16bits)
HR6901	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6902	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6903	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6904	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6905	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6906	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6907	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6908	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6909	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6910	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6911	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6912	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6913	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6914	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6915	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6916	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6917	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)
HR6925	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6931	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6958				HR6957 (Lo-16bits)
HR6959	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6966	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6967	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6968	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6969	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6970	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6971	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6972	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6973	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6982				HR6981 (Lo-16bits)
HR6983	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6984				HR6983 (Lo-16bits)
HR6985	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6986				HR6985 (Lo-16bits)
HR6987	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6988				HR6987 (Lo-16bits)
HR6989	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6990	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6991	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6992	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6993	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6994	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6995	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6996	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6997	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6998	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6999	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7000	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7001	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7002	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7003	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7004	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7005	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7006	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7007	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR7008	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7009	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7010	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7011	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7012	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7013	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7014	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7015	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7016	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7017	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7018	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7019	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7020	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7021	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7024				HR7023 (Lo-16bits)
HR7025	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)
HR7029	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7032				HR7031 (Lo-16bits)
HR7033	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)
HR7041	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7054				HR7053 (Lo-16bits)
HR7055	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)
HR7059	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7062				HR7061 (Lo-16bits)
HR7063	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7070				HR7069 (Lo-16bits)
HR7071	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7072				HR7071 (Lo-16bits)
HR7073	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7074				HR7073 (Lo-16bits)
HR7075	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7076				HR7075 (Lo-16bits)
HR7077	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7084				HR7083 (Lo-16bits)
HR7085	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7086	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7087	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7088	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7089	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7090	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7091	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7092	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7093	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7106				HR7105 (Lo-16bits)
HR7107	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7108				HR7107 (Lo-16bits)
HR7109	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7110	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7111	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7112	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7113	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7114	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7115	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7116	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7117	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7118	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7119	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7120	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7121	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7122	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7123	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7124	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7125	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)
HR7155	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7161	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7174	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7175	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7176	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7177	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7178	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7179	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7180	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7181	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7187	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7194				HR7193 (Lo-16bits)
HR7195	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7198	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7199	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7200	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7201	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7202	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7203	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7204	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7205	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7206	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7207	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7208	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7209	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7210	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7211	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7212	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7213	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7214	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7215	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7216	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7217	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7218	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7219	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7220	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7221	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7222	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7223	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7224	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7225	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7226	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7227	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7228	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7229	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7232				HR7231 (Lo-16bits)
HR7233	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)
HR7255	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7262				HR7261 (Lo-16bits)
HR7263	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)
HR7285	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7292				HR7291 (Lo-16bits)
HR7293	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7294	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7295	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7296	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7297	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7298	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7299	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7300	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7301	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7318	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7319	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7320	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7321	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7322	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7323	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7324	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7325	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7326	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7327	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7328	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7329	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7330	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7331	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7332	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7333	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)
HR7355	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7363	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)
HR7381	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7382	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7383	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7384	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7385	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7386	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7387	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7388	CUB3:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7389	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7406	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7407	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7408	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7409	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7410	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7411	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7412	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7413	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7414	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7415	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7416	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7417	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7418	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7419	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7420	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7421	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7422	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7423	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7424	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7425	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7426	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7427	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7428	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7429	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7430	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7431	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7432	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7433	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7434	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7435	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7436	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7437	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)
HR7455	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7463	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7493	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7502	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7503	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7504	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7505	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7506	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7507	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7508	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7509	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)
HR7511	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7519	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7526	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7527	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7528	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7529	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7530	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7531	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7532	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7533	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7534	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7535	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7536	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7537	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7538	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR7539	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7540	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7541	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7563	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7589	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7590	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7591	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7592	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7593	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7594	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7595	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7596	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7597	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7614	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7615	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7616	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7617	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7618	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7619	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7620	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7621	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7622	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7623	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7624	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7625	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7626	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7627	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7628	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7629	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7630	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7631	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7632	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7633	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7634	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7635	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7636	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7637	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7638	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7639	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7640	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7641	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7642	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7643	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7644	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7645	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7663	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7693	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7710	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7711	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7712	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7713	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7714	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7715	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7716	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7717	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7719	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7734	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7735	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7736	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7737	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7738	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7739	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7740	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7741	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7742	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7743	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7744	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7745	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7746	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7747	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7748	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7749	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7750	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7751	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7752	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7753	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7754	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7755	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7756	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7757	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7758				HR7757 (Lo-16bits)
HR7759	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7760				HR7759 (Lo-16bits)
HR7761	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7762				HR7761 (Lo-16bits)
HR7763	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7767	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7774	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7775	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7776	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7777	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7778	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7779	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7780	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7781	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7794				HR7793 (Lo-16bits)
HR7795	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7824				HR7823 (Lo-16bits)
HR7825	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7846	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7847	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7848	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7849	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7850	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7851	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7852	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7853	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7854	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7855	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7856	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7857	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7858	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7859	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7860	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7861	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7862	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7863	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7864	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7865	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7866	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7867	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7868	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7869	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7870	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7871	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7872	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7873	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7874	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7875	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7876	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7877	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)
HR7891	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7894	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7895	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7896	OV2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7897	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7898	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7899	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7900	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7901	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)
HR7917	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7923	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7953	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7966	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7967	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7968	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7969	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7970	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7971	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7972	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7973	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7974	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7975	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7976	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7977	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7978	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7979	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7980	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7981	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7982	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7983	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7984	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7985	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7986	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7987	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7988	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7989	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7990	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7991	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7992	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7993	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7994	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7995	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7996	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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TN00011

Holding Register	Parameter	Access	Length	
HR7997	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8014	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8015	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8016	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8017	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8018	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8019	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8020	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8021	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8026				HR8025 (Lo-16bits)
HR8027	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8032				HR8031 (Lo-16bits)
HR8033	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8034				HR8033 (Lo-16bits)
HR8035	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8036				HR8035 (Lo-16bits)
HR8037	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8038				HR8037 (Lo-16bits)
HR8039	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8054				HR8053 (Lo-16bits)
HR8055	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8084				HR8083 (Lo-16bits)
HR8085	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8086	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8087	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8088	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8089	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8090	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8091	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8092	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8093	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8094	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8095	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8096	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8097	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8098	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8099	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8100	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8101	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8102	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8103	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8104	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8105	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8106	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8107	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8108	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8109	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8110	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8111	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8112	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8113	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8114	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8115	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8116	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8117	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8122				HR8121 (Lo-16bits)
HR8123	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8127	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)
HR8131	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8134	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8135	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8136	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8137	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8138	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8139	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8140	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8141	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8154				HR8153 (Lo-16bits)
HR8155	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8170				HR8169 (Lo-16bits)
HR8171	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8172				HR8171 (Lo-16bits)
HR8173	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8184				HR8183 (Lo-16bits)
HR8185	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8206	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8207	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8208	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8209	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8210	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8211	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8212	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8213	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8214	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8215	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8216	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8217	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8218	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8219	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8220	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8221	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8222	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8223	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8224	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8225	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8226	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8227	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8228	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8229	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8230	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8231	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8232	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8233	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8234	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8235	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8236	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8237	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)
HR8253	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8257	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8264				HR8263 (Lo-16bits)
HR8265	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8266				HR8265 (Lo-16bits)
HR8267	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8268				HR8267 (Lo-16bits)
HR8269	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8270	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8271	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8272	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8273	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8274	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8275	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8276	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8277	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8282				HR8281 (Lo-16bits)
HR8283	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8285	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8313	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8342	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8343	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8344	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8345	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8346	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8347	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8348	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8349	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8350	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8351	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8352	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8353	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8354	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8355	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8356	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8357	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8358	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8359	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8360	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8361	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8362	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8363	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8364	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8365	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8366	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8367	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8368	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8369	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8370	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8371	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8372	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8373	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8387	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8398				HR8397 (Lo-16bits)
HR8399	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8400				HR8399 (Lo-16bits)
HR8401	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8402				HR8401 (Lo-16bits)
HR8403	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8404				HR8403 (Lo-16bits)
HR8405	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8406	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8407	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8408	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8409	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8410	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8411	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8412	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8413	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8416				HR8415 (Lo-16bits)
HR8417	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8443	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8473	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8478	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8479	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8480	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8481	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8482	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8483	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8484	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8485	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8486	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8487	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8488	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8489	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8490	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8491	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8492	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8493	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8494	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8495	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8496	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8497	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8498	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8499	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8500	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8501	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8502	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8503	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8504	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8505	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8506	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8507	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8508	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8509	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8515	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)
HR8527	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)
HR8533	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8536				HR8535 (Lo-16bits)
HR8537	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8538				HR8537 (Lo-16bits)
HR8539	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8540				HR8539 (Lo-16bits)
HR8541	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8542	UV3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR8543	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8544	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8545	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8546	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8547	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8548	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8549	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8569	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8596				HR8595 (Lo-16bits)
HR8597	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8599	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8614	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8615	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8616	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8617	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8618	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8619	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8620	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8621	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8622	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8623	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8624	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8625	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8626	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8627	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8628	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8629	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8630	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8631	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8632	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8633	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8634	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8635	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8636	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8637	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8638	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8639	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8640	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8641	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8642	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8643	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8644	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8645	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8666				HR8665 (Lo-16bits)
HR8667	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8668				HR8667 (Lo-16bits)
HR8669	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8672				HR8671 (Lo-16bits)
HR8673	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8674				HR8673 (Lo-16bits)
HR8675	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8676				HR8675 (Lo-16bits)
HR8677	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8678	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8679	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8680	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8681	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8682	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8683	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8684	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8685	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)
HR8689	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8692				HR8691 (Lo-16bits)
HR8693	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8694				HR8693 (Lo-16bits)
HR8695	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8723	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)
HR8737	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8750	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8751	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8752	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8753	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8754	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8755	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8756	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8757	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8758	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8759	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8760	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8761	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8762	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8763	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8764	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8765	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8766	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8767	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8768	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8769	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8770	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8771	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8772	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8773	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8774	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8775	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8776	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8777	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8778	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8779	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8780	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8781	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8797	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8808				HR8807 (Lo-16bits)
HR8809	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8810				HR8809 (Lo-16bits)
HR8811	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8812				HR8811 (Lo-16bits)
HR8813	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8814	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8815	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8816	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8817	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8818	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8819	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8820	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8821	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8823	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)
HR8847	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8850				HR8849 (Lo-16bits)
HR8851	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)
HR8853	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)
HR8877	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)
HR8879	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8880				HR8879 (Lo-16bits)
HR8881	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8886	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8887	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8888	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8889	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8890	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8891	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8892	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8893	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8894	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8895	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8896	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8897	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8898	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8899	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8900	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8901	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8902	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8903	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8904	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8905	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8906	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8907	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8908	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8909	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8910	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8911	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8912	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8913	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8914	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8915	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8916	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8917	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8918				HR8917 (Lo-16bits)
HR8919	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8920				HR8919 (Lo-16bits)
HR8921	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8922				HR8921 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8923	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8924				HR8923 (Lo-16bits)
HR8925	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8926				HR8925 (Lo-16bits)
HR8927	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8928				HR8927 (Lo-16bits)
HR8929	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8930				HR8929 (Lo-16bits)
HR8931	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8932				HR8931 (Lo-16bits)
HR8933	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8934				HR8933 (Lo-16bits)
HR8935	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8936				HR8935 (Lo-16bits)
HR8937	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8938				HR8937 (Lo-16bits)
HR8939	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8940				HR8939 (Lo-16bits)
HR8941	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8942				HR8941 (Lo-16bits)
HR8943	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8944				HR8943 (Lo-16bits)
HR8945	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8946				HR8945 (Lo-16bits)
HR8947	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8948				HR8947 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8949	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8950	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8951	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8952	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8953	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8954	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8955	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8956	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8957	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)
HR8961	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)
HR8973	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8974				HR8973 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8975	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8982				HR8981 (Lo-16bits)
HR8983	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8984				HR8983 (Lo-16bits)
HR8985	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8986				HR8985 (Lo-16bits)
HR8987	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8988				HR8987 (Lo-16bits)
HR8989	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9004				HR9003 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9005	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9022	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9023	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9024	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9025	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9026	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9027	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9028	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9029	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9030	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9031	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9032	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9033	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9034	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9035	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9036	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9037	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9038	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9039	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9040	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9041	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9042	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9043	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9044	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9045	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9046	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9047	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9048	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR9049	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9050	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9051	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9052	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9053	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9054				HR9053 (Lo-16bits)
HR9055	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9056				HR9055 (Lo-16bits)
HR9057	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9058				HR9057 (Lo-16bits)
HR9059	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9060				HR9059 (Lo-16bits)
HR9061	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9062				HR9061 (Lo-16bits)
HR9063	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9064				HR9063 (Lo-16bits)
HR9065	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9066				HR9065 (Lo-16bits)
HR9067	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9068				HR9067 (Lo-16bits)
HR9069	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9070				HR9069 (Lo-16bits)
HR9071	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9072				HR9071 (Lo-16bits)
HR9073	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9074				HR9073 (Lo-16bits)
HR9075	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9076				HR9075 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9077	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9078				HR9077 (Lo-16bits)
HR9079	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9080				HR9079 (Lo-16bits)
HR9081	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9082				HR9081 (Lo-16bits)
HR9083	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9084				HR9083 (Lo-16bits)
HR9085	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9086	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9087	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9088	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9089	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9090	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9091	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9092	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9093	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9103	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9118				HR9117 (Lo-16bits)
HR9119	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9120				HR9119 (Lo-16bits)
HR9121	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9122				HR9121 (Lo-16bits)
HR9123	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9124				HR9123 (Lo-16bits)
HR9125	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9132				HR9131 (Lo-16bits)
HR9133	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)
HR9145	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9158	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9159	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9160	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9161	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9162	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9163	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9164	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9165	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9166	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9167	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9168	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9169	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9170	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9171	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9172	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9173	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9174	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9175	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9176	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9177	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9178	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9179	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9180	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9181	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9182	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9183	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9184	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9185	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9186	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9187	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9188	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9189	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9194				HR9193 (Lo-16bits)
HR9195	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9196				HR9195 (Lo-16bits)
HR9197	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9198				HR9197 (Lo-16bits)
HR9199	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9200				HR9199 (Lo-16bits)
HR9201	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9202				HR9201 (Lo-16bits)
HR9203	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9204				HR9203 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9205	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9206				HR9205 (Lo-16bits)
HR9207	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9208				HR9207 (Lo-16bits)
HR9209	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9210				HR9209 (Lo-16bits)
HR9211	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9216				HR9215 (Lo-16bits)
HR9217	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9218				HR9217 (Lo-16bits)
HR9219	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9220				HR9219 (Lo-16bits)
HR9221	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9222	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9223	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9224	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9225	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9226	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9227	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9228	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9229	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9231	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)
HR9245	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9258				HR9257 (Lo-16bits)
HR9259	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9262				HR9261 (Lo-16bits)
HR9263	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9264				HR9263 (Lo-16bits)
HR9265	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9266				HR9265 (Lo-16bits)
HR9267	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9268				HR9267 (Lo-16bits)
HR9269	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR9288				HR9287 (Lo-16bits)
HR9289	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9294	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9295	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9296	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9297	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9298	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9299	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9300	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9301	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9302	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9303	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9304	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9305	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9306	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9307	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9308	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9309	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9310	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9311	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9312	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9313	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9314	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9315	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9316	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9317	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9318	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9319	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9320	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9321	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9322	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9323	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9324	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9325	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9326	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9327	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9328	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9329	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9330	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9331	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9332	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9333	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9361	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9366	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9367	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9368	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9369	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9370	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9371	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9372	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9373	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9391	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9406	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9407	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9408	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9409	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9410	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9411	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9412	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9413	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9421	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9446	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9447	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9448	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9449	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9450	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9451	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9452	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9453	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9464				HR9463 (Lo-16bits)
HR9465	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9466				HR9465 (Lo-16bits)
HR9467	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9468				HR9467 (Lo-16bits)
HR9469	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9481	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9486	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9487	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9488	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9489	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9490	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9491	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9492	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9493	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9511	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9526	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9527	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9528	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9529	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9530	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9531	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9532	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9533	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9541	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9566	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9567	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9568	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9569	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9570	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9571	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9572	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9573	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9580				HR9579 (Lo-16bits)
HR9581	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9582				HR9581 (Lo-16bits)
HR9583	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9584				HR9583 (Lo-16bits)
HR9585	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9586				HR9585 (Lo-16bits)
HR9587	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9588				HR9587 (Lo-16bits)
HR9589	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9590				HR9589 (Lo-16bits)
HR9591	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9592				HR9591 (Lo-16bits)
HR9593	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9594				HR9593 (Lo-16bits)
HR9595	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9596				HR9595 (Lo-16bits)
HR9597	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9598				HR9597 (Lo-16bits)
HR9599	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9601	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9602				HR9601 (Lo-16bits)
HR9603	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9604				HR9603 (Lo-16bits)
HR9605	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9606	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9607	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9608	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9609	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9610	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9611	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9612	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9613	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9614				HR9613 (Lo-16bits)
HR9615	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9616				HR9615 (Lo-16bits)
HR9617	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9618				HR9617 (Lo-16bits)
HR9619	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9620				HR9619 (Lo-16bits)
HR9621	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9622				HR9621 (Lo-16bits)
HR9623	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9624				HR9623 (Lo-16bits)
HR9625	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9626				HR9625 (Lo-16bits)
HR9627	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9628				HR9627 (Lo-16bits)
HR9629	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9630				HR9629 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9631	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9632				HR9631 (Lo-16bits)
HR9633	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9634				HR9633 (Lo-16bits)
HR9635	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9636				HR9635 (Lo-16bits)
HR9637	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9638				HR9637 (Lo-16bits)
HR9639	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9640				HR9639 (Lo-16bits)
HR9641	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9642				HR9641 (Lo-16bits)
HR9643	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9644				HR9643 (Lo-16bits)
HR9645	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9660				HR9659 (Lo-16bits)
HR9661	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9689	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9691	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9692	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9693	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9694	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9695	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9696	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9697	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9698	CUB1:I2> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9699	CUB2:I2>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9700	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9701	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9702	OV1:U> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9703	OV2:U>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9704	OV3:U>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9705	OV4:U>>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9706	UV1:U< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9707	UV2:U<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9708	UV3:U<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9709	UV4:U<<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9710	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9711	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9712	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9713	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9714	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled;1=Activated
HR9715	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9743	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9750				HR9749 (Lo-16bits)
HR9751	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9752				HR9751 (Lo-16bits)
HR9753	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9754				HR9753 (Lo-16bits)
HR9755	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9769	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	DIGI:DIs (cpu)	R	2	0b=DI1;1b=DI2;2b=DI3; (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	DIGI:DIs (input option 1)	R	2	0b=DI4;1b=DI5;2b=DI6;3b=DI7;4b=DI8;5b=DI9;6b=DI10;7b=DI1
HR9774				HR9773 (Lo-16bits)
HR9775	DIGI:DIs (input option 2)	R	2	0b=DI12;1b=DI13;2b=DI14;3b=DI15;4b=DI16;5b=DI17;6b=DI18
HR9776				HR9775 (Lo-16bits)
HR9777	DIGI:DIs (input option 3)	R	2	0b=DI20;1b=DI21;2b=DI22;3b=DI23;4b=DI24;5b=DI25;6b=DI26
HR9778				HR9777 (Lo-16bits)
HR9779	DIGI:DIs (input option 4)	R	2	0b=D28;1b=DI29;2b=DI30;3b=DI31;4b=DI32;5b=DI33;6b=DI34;
HR9780				HR9779 (Lo-16bits)
HR9781	DIGI:DIs (input option 5)	R	2	0b=DI36;1b=DI37;2b=DI38;3b=DI39;4b=DI40;5b=DI41;6b=DI42
HR9782				HR9781 (Lo-16bits)
HR9783	DIGI:DIs (input option 6)	R	2	0b=DI44;1b=DI45;2b=DI46;3b=DI47;4b=DI48;5b=DI49;6b=DI50
HR9784				HR9783 (Lo-16bits)
HR9785	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9797	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9802	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9803	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled;1=Activated
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

1.2 T Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware T_v2.1.1.3-233. The firmware is used by the following device types:

- AQ-T216
- AQ-T215

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.2 - 2. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

Holding Register	Parameter	Access	Length	
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR452	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR508	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR538	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR568	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR598	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR628	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR658	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR688	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR718	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR748	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR778	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR807				HR806 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR808	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR838	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR865				HR864 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR889				HR888 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR943				HR942 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR944	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR973				HR972 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR974	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1004	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1034	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1064	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1094	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1124	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1154	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	POW1:Reset 3ph Energies	RW	1	0=-;1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:Dis 1-32	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7;7b=DI8; (Hi-16bits)
HR1250				HR1249 (Lo-16bits)
HR1251	DIGI:Dis 33-64	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38;6b=DI39
HR1252				HR1251 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1253	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b=O8;8b=
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8
HR1256	SGS:Force SG	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1257	SGS:Force SG change	RW	1	0=Disabled;1=Enabled
HR1258	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=SG1...6;6=
HR1259	SGS:Remote SG change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1260	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1261	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1262	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1263	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1264	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1265	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1266	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1267	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1268	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1269	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1271	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1273	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1275	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1277	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1278	DEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1279	DEF1:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1280	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1281	DEF2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1282	DEF2:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;

Holding Register	Parameter	Access	Length	
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1284	DEF3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1285	DEF3:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1287	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1288	DEF4:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1289	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1290	IEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1292	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1296	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1312	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	HOC2:Ih>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1317	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1338	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1342	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1359	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4=CLPU b
HR1380	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloaded;4=C
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Blocked;4b=2
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1385	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1386	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1387	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1389	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1390	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1391	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1394	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1395	TOLF1:Temp C or F deg	RW	1	0=C;1=F
HR1396	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1397	TOLF1:RTD Sensor	R	1	0=Invalid;1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set;1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1401	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=Running

Holding Register	Parameter	Access	Length	
HR1402	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1403	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1404	TOLM1:Time constants	RW	1	0=Single;1=Multiple
HR1405	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1408	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Blocked
HR1411	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1415	OV2:U>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1416	OV3:U>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1417	OV4:U>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1418	UV1:U< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1419	UV2:U<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1420	UV3:U<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1421	UV4:U<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1422	NOV1:U0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1423	NOV1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1425	NOV2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1427	NOV3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1429	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input

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Holding Register	Parameter	Access	Length	
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1443	FRQV1:f>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1444	FRQV1:f>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1445	FRQV1:f>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1446	FRQV1:f< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1447	FRQV1:f<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1448	FRQV1:f<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1449	FRQV1:f<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On high overloa
HR1459	OPW1:P> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1460	UPW1:P< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1461	RPW1:Prev> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1463	SEF1:U03rd< Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1466	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=VHz(1) Blocked;2=VHz(1) Start;3=VHz(1) Alarm;4=
HR1469	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=Alarm
HR1470	ARC1:I/I0 Arc> condition	R	1	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4b=Z3 Trip
HR1471	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1472	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1473	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTBusfail;3=VTLinefail;4=Blocked
HR1474	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live VTS Ok SE
HR1475	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR1476	THD1:THD condition	R	1	0=Normal;1=Start;2=Alarm;3=Blocked
HR1477	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On
HR1478	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo cmd;5=Bk
HR1479	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR1480	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=SYN1 Vco
HR1481				HR1480 (Lo-16bits)
HR1482	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=SYN2 Vco
HR1483				HR1482 (Lo-16bits)
HR1484	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=SYN3 Vco
HR1485				HR1484 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1486	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1487	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1488	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1489	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1490	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1491	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1492	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1493	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1494	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1495	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1496	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR is runni Requested;14b=AR4 Requested;15b=AR5 Requested;16b=Exec
HR1497				HR1496 (Lo-16bits)
HR1498	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Delay;4=AR
HR1499	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set higher than U
HR1500				HR1499 (Lo-16bits)
HR1501	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Operation bl
HR1502				HR1501 (Lo-16bits)
HR1503	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low set Decre
HR1504	VRG1:Voltage meas condition	R	1	0=Not configured;1=System U12;2=System U23;3=System U31;
HR1505	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLT detected
HR1506	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1507	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLT detected
HR1508	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1509	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLT FW;2=FLT REV

Holding Register	Parameter	Access	Length	
HR1510	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=No voltage
HR1511	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1512	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLT detected
HR1513	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1514				HR1513 (Lo-16bits)
HR1515	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1518				HR1517 (Lo-16bits)
HR1519	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=A8;8b=A
HR1520				HR1519 (Lo-16bits)
HR1521	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=A39;7b= (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1524				HR1523 (Lo-16bits)
HR1525	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1530				HR1529 (Lo-16bits)
HR1531	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1532				HR1531 (Lo-16bits)
HR1533	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1535	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1536				HR1535 (Lo-16bits)
HR1537	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1538				HR1537 (Lo-16bits)
HR1539	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1542				HR1541 (Lo-16bits)
HR1543	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1544				HR1543 (Lo-16bits)
HR1545	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1548				HR1547 (Lo-16bits)
HR1549	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1550				HR1549 (Lo-16bits)
HR1551	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1554				HR1553 (Lo-16bits)
HR1555	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1556				HR1555 (Lo-16bits)
HR1557	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1559	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1560				HR1559 (Lo-16bits)
HR1561	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1562				HR1561 (Lo-16bits)
HR1563	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1566				HR1565 (Lo-16bits)
HR1567	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1568				HR1567 (Lo-16bits)
HR1569	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1572				HR1571 (Lo-16bits)
HR1573	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1574				HR1573 (Lo-16bits)
HR1575	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1578				HR1577 (Lo-16bits)
HR1579	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1583	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1584				HR1583 (Lo-16bits)
HR1585	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1590				HR1589 (Lo-16bits)
HR1591	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1592				HR1591 (Lo-16bits)
HR1593	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1594				HR1593 (Lo-16bits)
HR1595	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1596				HR1595 (Lo-16bits)
HR1597	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1598				HR1597 (Lo-16bits)
HR1599	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1600				HR1599 (Lo-16bits)
HR1601	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1602				HR1601 (Lo-16bits)
HR1603	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1606				HR1605 (Lo-16bits)
HR1607	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1608				HR1607 (Lo-16bits)
HR1609	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1610				HR1609 (Lo-16bits)
HR1611	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1612				HR1611 (Lo-16bits)
HR1613	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1614				HR1613 (Lo-16bits)
HR1615	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1616				HR1615 (Lo-16bits)
HR1617	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1618				HR1617 (Lo-16bits)
HR1619	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1620				HR1619 (Lo-16bits)
HR1621	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1622				HR1621 (Lo-16bits)
HR1623	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1624				HR1623 (Lo-16bits)
HR1625	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1626				HR1625 (Lo-16bits)
HR1627	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1630				HR1629 (Lo-16bits)
HR1631	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1632				HR1631 (Lo-16bits)
HR1633	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1634				HR1633 (Lo-16bits)
HR1635	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=VHz(2) Start;2=VHz(2) Alarm;3=VHz(2) Trip;4=VHz(
HR1636	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1639	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1642				HR1641 (Lo-16bits)
HR1643	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1688				HR1687 (Lo-16bits)
HR1689	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1690				HR1689 (Lo-16bits)
HR1691	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1692				HR1691 (Lo-16bits)
HR1693	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1694				HR1693 (Lo-16bits)
HR1695	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1712	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1713	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1714	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1715	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1716	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR1717				HR1716 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1718	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR1719				HR1718 (Lo-16bits)
HR1720	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1721				HR1720 (Lo-16bits)
HR1722	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1723				HR1722 (Lo-16bits)
HR1724	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR1725	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher
HR1726	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked:2=Trip;3=Alarm
HR1727	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	PGS1:PS1 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1730	VJP1:Voltage meas selected	R	1	0=Selection Ok;1=Selection not available
HR1731	PGS1:PS2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1732	PGS1:PS3 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1733	PGS1:PS4 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1734	PGS1:PS5 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1735	PGS1:PS6 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1736	PGS1:PS7 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1737	PGS1:PS8 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1738	PGS1:PS9 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1739	PGS1:PS10 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1740	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1741				HR1740 (Lo-16bits)
HR1742	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1743				HR1742 (Lo-16bits)
HR1744	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1745				HR1744 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1746	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1747				HR1746 (Lo-16bits)
HR1748	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1749				HR1748 (Lo-16bits)
HR1750	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1751				HR1750 (Lo-16bits)
HR1752	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1753				HR1752 (Lo-16bits)
HR1754	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1755				HR1754 (Lo-16bits)
HR1756	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1757				HR1756 (Lo-16bits)
HR1758	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1759				HR1758 (Lo-16bits)
HR1760	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1761				HR1760 (Lo-16bits)
HR1762	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1763				HR1762 (Lo-16bits)
HR1764	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled;1=Activated
HR1767	CTRL:Local/Remote status	R	1	0=Remote;1=Local
HR1768	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1769				HR1768 (Lo-16bits)
HR1770	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1773				HR1772 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1774	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1775				HR1774 (Lo-16bits)
HR1776	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1777				HR1776 (Lo-16bits)
HR1778	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1779				HR1778 (Lo-16bits)
HR1780	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1781				HR1780 (Lo-16bits)
HR1782	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1783				HR1782 (Lo-16bits)
HR1784	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1795				HR1794 (Lo-16bits)
HR1796	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	GSYN:Synchronizer mode	RW	1	0=Disabled;1=Activated
HR1801	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1803	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1829	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1838				HR1837 (Lo-16bits)
HR1839	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1840				HR1839 (Lo-16bits)
HR1841	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	VREC:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1852	VREC:Scale current values to primary	RW	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR1853	VREC:Record fault types (Overcurrrent & Voltage Over/Under) faults	RW	1	0=No;1=Yes
HR1854	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)
HR1862	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	RTB:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1873	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR1874	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)
HR1878	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1879				HR1878 (Lo-16bits)
HR1880	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)
HR1886	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)
HR1888	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR1891				HR1890 (Lo-16bits)
HR1892	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1895				HR1894 (Lo-16bits)
HR1896	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1897				HR1896 (Lo-16bits)
HR1898	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1899				HR1898 (Lo-16bits)
HR1900	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1901				HR1900 (Lo-16bits)
HR1902	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1903				HR1902 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1904	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1907				HR1906 (Lo-16bits)
HR1908	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1909				HR1908 (Lo-16bits)
HR1910	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1919				HR1918 (Lo-16bits)
HR1920	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1921				HR1920 (Lo-16bits)
HR1922	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1923				HR1922 (Lo-16bits)
HR1924	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1925				HR1924 (Lo-16bits)
HR1926	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1927				HR1926 (Lo-16bits)
HR1928	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1929				HR1928 (Lo-16bits)
HR1930	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1931				HR1930 (Lo-16bits)
HR1932	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1933				HR1932 (Lo-16bits)
HR1934	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1935				HR1934 (Lo-16bits)
HR1936	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1937				HR1936 (Lo-16bits)
HR1938	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1939				HR1938 (Lo-16bits)
HR1940	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1941				HR1940 (Lo-16bits)
HR1942	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1943				HR1942 (Lo-16bits)
HR1944	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1945				HR1944 (Lo-16bits)
HR1946	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1947				HR1946 (Lo-16bits)
HR1948	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1949				HR1948 (Lo-16bits)
HR1950	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1951				HR1950 (Lo-16bits)
HR1952	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1953				HR1952 (Lo-16bits)
HR1954	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1955				HR1954 (Lo-16bits)
HR1956	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1957				HR1956 (Lo-16bits)
HR1958	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1959				HR1958 (Lo-16bits)
HR1960	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1961				HR1960 (Lo-16bits)
HR1962	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR1965				HR1964 (Lo-16bits)
HR1966	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1967				HR1966 (Lo-16bits)
HR1968	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1969				HR1968 (Lo-16bits)
HR1970	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1971				HR1970 (Lo-16bits)
HR1972	NOC1:I> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1973	NOC1:I> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1974	NOC1:I> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1975	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1976				HR1975 (Lo-16bits)
HR1977	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1978				HR1977 (Lo-16bits)
HR1979	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1982				HR1981 (Lo-16bits)
HR1983	NOC1:Delay type	RW	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR1984	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1985				HR1984 (Lo-16bits)
HR1986	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1987	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1988	NOC1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1989	NOC1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1990	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1991				HR1990 (Lo-16bits)
HR1992	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1993				HR1992 (Lo-16bits)
HR1994	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1995				HR1994 (Lo-16bits)
HR1996	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1997				HR1996 (Lo-16bits)
HR1998	NOC1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1999	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2000	NOC2:I>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2001	NOC2:I>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2002	NOC2:I>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2003	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2009	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:Delay type	RW	1	0=DT;1=IDMT
HR2012	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2013				HR2012 (Lo-16bits)
HR2014	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2015	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2016	NOC2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2017	NOC2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2018	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2019				HR2018 (Lo-16bits)
HR2020	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2021				HR2020 (Lo-16bits)
HR2022	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2023				HR2022 (Lo-16bits)
HR2024	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2025				HR2024 (Lo-16bits)
HR2026	NOC2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2027	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2028	NOC3:I>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR2029	NOC3:I>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2030	NOC3:I>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2031	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2035	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR2040	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2041				HR2040 (Lo-16bits)
HR2042	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2043	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2044	NOC3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2045	NOC3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2046	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2047				HR2046 (Lo-16bits)
HR2048	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2049				HR2048 (Lo-16bits)
HR2050	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2051				HR2050 (Lo-16bits)
HR2052	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2053				HR2052 (Lo-16bits)
HR2054	NOC3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2055	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2056	NOC4:I>>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR2057	NOC4:I>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2058	NOC4:I>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2059	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2061	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR2068	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2069				HR2068 (Lo-16bits)
HR2070	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2071	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2072	NOC4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2073	NOC4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2074	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2075				HR2074 (Lo-16bits)
HR2076	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2077				HR2076 (Lo-16bits)
HR2078	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2079				HR2078 (Lo-16bits)
HR2080	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2081				HR2080 (Lo-16bits)
HR2082	NOC4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2083	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2084	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2085				HR2084 (Lo-16bits)
HR2086	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2087				HR2086 (Lo-16bits)
HR2088	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2089				HR2088 (Lo-16bits)
HR2090	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2091				HR2090 (Lo-16bits)
HR2092	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2093				HR2092 (Lo-16bits)
HR2094	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2095				HR2094 (Lo-16bits)
HR2096	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2097				HR2096 (Lo-16bits)
HR2098	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2099				HR2098 (Lo-16bits)
HR2100	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2101				HR2100 (Lo-16bits)
HR2102	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2103				HR2102 (Lo-16bits)
HR2104	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2105				HR2104 (Lo-16bits)
HR2106	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2107				HR2106 (Lo-16bits)
HR2108	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2109				HR2108 (Lo-16bits)
HR2110	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2111				HR2110 (Lo-16bits)
HR2112	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2113				HR2112 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2114	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2115				HR2114 (Lo-16bits)
HR2116	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2117				HR2116 (Lo-16bits)
HR2118	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2119				HR2118 (Lo-16bits)
HR2120	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2121				HR2120 (Lo-16bits)
HR2122	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2123				HR2122 (Lo-16bits)
HR2124	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2125				HR2124 (Lo-16bits)
HR2126	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2127				HR2126 (Lo-16bits)
HR2128	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2129				HR2128 (Lo-16bits)
HR2130	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2131				HR2130 (Lo-16bits)
HR2132	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2133				HR2132 (Lo-16bits)
HR2134	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2135				HR2134 (Lo-16bits)
HR2136	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2137				HR2136 (Lo-16bits)
HR2138	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2139				HR2138 (Lo-16bits)
HR2140	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2142	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2149				HR2148 (Lo-16bits)
HR2150	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2151				HR2150 (Lo-16bits)
HR2152	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2153				HR2152 (Lo-16bits)
HR2154	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2155				HR2154 (Lo-16bits)
HR2156	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2157				HR2156 (Lo-16bits)
HR2158	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2159				HR2158 (Lo-16bits)
HR2160	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2161				HR2160 (Lo-16bits)
HR2162	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2165	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2166	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2167	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2168	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2169	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2170	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit

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TN00011

Holding Register	Parameter	Access	Length	
HR2171	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2172	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2173	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2174	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2175	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2176	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2177	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2178	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2179	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2180	DOC1:Idir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2181	DOC1:Idir> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2182	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2183				HR2182 (Lo-16bits)
HR2184	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2187				HR2186 (Lo-16bits)
HR2188	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2189				HR2188 (Lo-16bits)
HR2190	DOC2:Idir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2191	DOC2:Idir>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2192	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2193				HR2192 (Lo-16bits)
HR2194	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2195				HR2194 (Lo-16bits)
HR2196	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2197				HR2196 (Lo-16bits)
HR2198	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2199				HR2198 (Lo-16bits)
HR2200	DOC3:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2201	DOC3:Idir>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2202	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2203				HR2202 (Lo-16bits)
HR2204	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)
HR2206	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2209				HR2208 (Lo-16bits)
HR2210	DOC4:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2211	DOC4:Idir>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2212	DOC4:Idir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2213				HR2212 (Lo-16bits)
HR2214	DOC4:Idir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2215				HR2214 (Lo-16bits)
HR2216	DOC4:Idir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2217				HR2216 (Lo-16bits)
HR2218	DOC4:Idir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2219				HR2218 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2220	NEF1:I0> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2221	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2222				HR2221 (Lo-16bits)
HR2223	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2224				HR2223 (Lo-16bits)
HR2225	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR2226	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2227				HR2226 (Lo-16bits)
HR2228	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2229	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2230	NEF1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2231	NEF1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2232	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2233				HR2232 (Lo-16bits)
HR2234	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2235				HR2234 (Lo-16bits)
HR2236	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2237				HR2236 (Lo-16bits)
HR2238	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2239				HR2238 (Lo-16bits)
HR2240	NEF1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2241	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2242	NEF2:I0>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2243	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2246				HR2245 (Lo-16bits)
HR2247	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR2248	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2249				HR2248 (Lo-16bits)
HR2250	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2251	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2252	NEF2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2253	NEF2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2254	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2255				HR2254 (Lo-16bits)
HR2256	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2257				HR2256 (Lo-16bits)
HR2258	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2259				HR2258 (Lo-16bits)
HR2260	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2261				HR2260 (Lo-16bits)
HR2262	NEF2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2263	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2264	NEF3:I0>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2265	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)
HR2267	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR2270	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2271				HR2270 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2272	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2273	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2274	NEF3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2275	NEF3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2276	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2277				HR2276 (Lo-16bits)
HR2278	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2279				HR2278 (Lo-16bits)
HR2280	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2281				HR2280 (Lo-16bits)
HR2282	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2283				HR2282 (Lo-16bits)
HR2284	NEF3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2285	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2286	NEF4:I0>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2287	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)
HR2291	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR2292	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2293				HR2292 (Lo-16bits)
HR2294	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2295	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2296	NEF4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR2297	NEF4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2298	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2299				HR2298 (Lo-16bits)
HR2300	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2301				HR2300 (Lo-16bits)
HR2302	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2303				HR2302 (Lo-16bits)
HR2304	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2305				HR2304 (Lo-16bits)
HR2306	NEF4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2307	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2308	DEF1:I0dir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2309	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)
HR2311	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2314				HR2313 (Lo-16bits)
HR2315	DEF2:I0dir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2316	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2317				HR2316 (Lo-16bits)
HR2318	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2319				HR2318 (Lo-16bits)
HR2320	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2321				HR2320 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2322	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2323	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2324				HR2323 (Lo-16bits)
HR2325	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2326				HR2325 (Lo-16bits)
HR2327	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2328				HR2327 (Lo-16bits)
HR2329	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2330	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2331				HR2330 (Lo-16bits)
HR2332	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR2337				HR2336 (Lo-16bits)
HR2338	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2339				HR2338 (Lo-16bits)
HR2340	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2343				HR2342 (Lo-16bits)
HR2344	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2345				HR2344 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2346	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2363				HR2362 (Lo-16bits)
HR2364	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2370	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	ROC1:Idrw> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2373	ROC1:Idrw> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2374	ROC2:Idrw>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2375	ROC2:Idrw>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2376	ROC3:Idrw>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2377	ROC3:Idrw>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2378	ROC4:Idrw>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2379	ROC4:Idrw>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2380	ROC5:Idrw>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2381	ROC5:Idrw>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2382	ROC6:Idrw>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2383	ROC6:Idrw>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2384	ROC7:Idrw>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2385	ROC7:Idrw>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2386	ROC8:Idrw>>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2387	ROC8:Idrw>>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2388	ROV1:Urw>/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2389	ROV2:Urw>>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2390	ROV3:Urw>>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2391	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2392	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=Start On (Alarm);3=Start On (Alarm & Tr
HR2393	TOLM1:TM> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0

Holding Register	Parameter	Access	Length	
HR2394	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2395	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2396	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2397	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR2398	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2401	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2402	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2403	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2404	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2417				HR2416 (Lo-16bits)
HR2418	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2419				HR2418 (Lo-16bits)
HR2420	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	TOLT1:TT> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2437	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2438	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2439	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2440	TOLF1:TF> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2441	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2442	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2443	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2444	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2445				HR2444 (Lo-16bits)
HR2446	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)
HR2456	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2468	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2496	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)
HR2498	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2499				HR2498 (Lo-16bits)
HR2500	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)
HR2508	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2515				HR2514 (Lo-16bits)
HR2516	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR2517	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR2518	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2519				HR2518 (Lo-16bits)
HR2520	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2521				HR2520 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2522	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2523				HR2522 (Lo-16bits)
HR2524	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2525				HR2524 (Lo-16bits)
HR2526	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2527				HR2526 (Lo-16bits)
HR2528	ARC1:I/I0 Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2529				HR2528 (Lo-16bits)
HR2530	ARC1:I/I0 Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2531				HR2530 (Lo-16bits)
HR2532	ARC1:I/I0 Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2533				HR2532 (Lo-16bits)
HR2534	ARC1:I/I0 Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2535				HR2534 (Lo-16bits)
HR2536	ARC1:I/I0 Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2537				HR2536 (Lo-16bits)
HR2538	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2539				HR2538 (Lo-16bits)
HR2540	DIGI:DI1 Counter Clear	RW	1	0=-;1=Clear
HR2541	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	DIGI:DI2 Counter Clear	RW	1	0=-;1=Clear
HR2544	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2545				HR2544 (Lo-16bits)
HR2546	DIGI:DI3 Counter Clear	RW	1	0=-;1=Clear
HR2547	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2549	DIGI:DI4 Counter Clear	RW	1	0=-;1=Clear
HR2550	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2551				HR2550 (Lo-16bits)
HR2552	DIGI:DI5 Counter Clear	RW	1	0=-;1=Clear
HR2553	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	DIGI:DI6 Counter Clear	RW	1	0=-;1=Clear
HR2556	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2557				HR2556 (Lo-16bits)
HR2558	DIGI:DI7 Counter Clear	RW	1	0=-;1=Clear
HR2559	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	DIGI:DI8 Counter Clear	RW	1	0=-;1=Clear
HR2562	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2563				HR2562 (Lo-16bits)
HR2564	DIGI:DI9 Counter Clear	RW	1	0=-;1=Clear
HR2565	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	DIGI:DI10 Counter Clear	RW	1	0=-;1=Clear
HR2568	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2569				HR2568 (Lo-16bits)
HR2570	DIGI:DI11 Counter Clear	RW	1	0=-;1=Clear
HR2571	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	DIGI:DI12 Counter Clear	RW	1	0=-;1=Clear
HR2574	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2575				HR2574 (Lo-16bits)
HR2576	DIGI:DI13 Counter Clear	RW	1	0=-;1=Clear
HR2577	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2579	DIGI:DI14 Counter Clear	RW	1	0=-;1=Clear
HR2580	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2581				HR2580 (Lo-16bits)
HR2582	DIGI:DI15 Counter Clear	RW	1	0=-;1=Clear
HR2583	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	DIGI:DI16 Counter Clear	RW	1	0=-;1=Clear
HR2586	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2587				HR2586 (Lo-16bits)
HR2588	DIGI:DI17 Counter Clear	RW	1	0=-;1=Clear
HR2589	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)
HR2591	DIGI:DI18 Counter Clear	RW	1	0=-;1=Clear
HR2592	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2593				HR2592 (Lo-16bits)
HR2594	DIGI:DI19 Counter Clear	RW	1	0=-;1=Clear
HR2595	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	DIGI:DI20 Counter Clear	RW	1	0=-;1=Clear
HR2598	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2599				HR2598 (Lo-16bits)
HR2600	DIGI:DI21 Counter Clear	RW	1	0=-;1=Clear
HR2601	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	DIGI:DI22 Counter Clear	RW	1	0=-;1=Clear
HR2604	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	DIGI:DI23 Counter Clear	RW	1	0=-;1=Clear
HR2607	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2608				HR2607 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2609	DIGI:DI24 Counter Clear	RW	1	0=-;1=Clear
HR2610	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2611				HR2610 (Lo-16bits)
HR2612	DIGI:DI25 Counter Clear	RW	1	0=-;1=Clear
HR2613	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	DIGI:DI26 Counter Clear	RW	1	0=-;1=Clear
HR2616	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2617				HR2616 (Lo-16bits)
HR2618	DIGI:DI27 Counter Clear	RW	1	0=-;1=Clear
HR2619	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)
HR2621	DIGI:DI28 Counter Clear	RW	1	0=-;1=Clear
HR2622	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2623				HR2622 (Lo-16bits)
HR2624	DIGI:DI29 Counter Clear	RW	1	0=-;1=Clear
HR2625	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	DIGI:DI30 Counter Clear	RW	1	0=-;1=Clear
HR2628	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2629				HR2628 (Lo-16bits)
HR2630	DIGI:DI31 Counter Clear	RW	1	0=-;1=Clear
HR2631	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2632				HR2631 (Lo-16bits)
HR2633	DIGI:DI32 Counter Clear	RW	1	0=-;1=Clear
HR2634	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2635				HR2634 (Lo-16bits)
HR2636	DIGI:DI33 Counter Clear	RW	1	0=-;1=Clear
HR2637	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2639	DIGI:DI34 Counter Clear	RW	1	0=-;1=Clear
HR2640	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2641				HR2640 (Lo-16bits)
HR2642	DIGI:DI35 Counter Clear	RW	1	0=-;1=Clear
HR2643	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DIGI:DI36 Counter Clear	RW	1	0=-;1=Clear
HR2646	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2647				HR2646 (Lo-16bits)
HR2648	DIGI:DI37 Counter Clear	RW	1	0=-;1=Clear
HR2649	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2650				HR2649 (Lo-16bits)
HR2651	DIGI:DI38 Counter Clear	RW	1	0=-;1=Clear
HR2652	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2653				HR2652 (Lo-16bits)
HR2654	DIGI:DI39 Counter Clear	RW	1	0=-;1=Clear
HR2655	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DIGI:DI40 Counter Clear	RW	1	0=-;1=Clear
HR2658	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2659				HR2658 (Lo-16bits)
HR2660	DIGI:DI41 Counter Clear	RW	1	0=-;1=Clear
HR2661	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DIGI:DI42 Counter Clear	RW	1	0=-;1=Clear
HR2664	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2665				HR2664 (Lo-16bits)
HR2666	DIGI:DI43 Counter Clear	RW	1	0=-;1=Clear
HR2667	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2669	DIGI:DI44 Counter Clear	RW	1	0=-;1=Clear
HR2670	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2671				HR2670 (Lo-16bits)
HR2672	DIGI:DI45 Counter Clear	RW	1	0=-;1=Clear
HR2673	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DIGI:DI46 Counter Clear	RW	1	0=-;1=Clear
HR2676	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2677				HR2676 (Lo-16bits)
HR2678	DIGI:DI47 Counter Clear	RW	1	0=-;1=Clear
HR2679	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DIGI:DI48 Counter Clear	RW	1	0=-;1=Clear
HR2682	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2683				HR2682 (Lo-16bits)
HR2684	DIGI:DI49 Counter Clear	RW	1	0=-;1=Clear
HR2685	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DIGI:DI50 Counter Clear	RW	1	0=-;1=Clear
HR2688	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2689				HR2688 (Lo-16bits)
HR2690	DIGI:DI51 Counter Clear	RW	1	0=-;1=Clear
HR2691	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2699	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)
HR2703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2710	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2711	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2712	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2713	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2714	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2715	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2716	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2717	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2728				HR2727 (Lo-16bits)
HR2729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2730				HR2729 (Lo-16bits)
HR2731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)
HR2733	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2734	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2735	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2736	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2737	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2738	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2739	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2740	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2741	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2742	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2743	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2744	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2745	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2746	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2747	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2748	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2749	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2750	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2751	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2752	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2753	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2754	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2755	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2756	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2757	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2758	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2759	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2760	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2761	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2762	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2763	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2764	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2765	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2774				HR2773 (Lo-16bits)
HR2775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2804				HR2803 (Lo-16bits)
HR2805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2830	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2831	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2832	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2833	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2834	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2835	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2836	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2837	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2854	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2855	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2856	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2857	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2858	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2859	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2860	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2861	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2878	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2879	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2880	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2881	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2882	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2883	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2884	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2885	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2886	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2887	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2888	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2889	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2890	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2891	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2892	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2893	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2910	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2911	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2912	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2913	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2914	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2915	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2916	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2917	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2927	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2928				HR2927 (Lo-16bits)
HR2929	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2930				HR2929 (Lo-16bits)
HR2931	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2932				HR2931 (Lo-16bits)
HR2933	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2934	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2935	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2936	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2937	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2938	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2939	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2940	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2941	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2942	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2943	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2944	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2945	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2946	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2947	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2948	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2949	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2950	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2951	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2952	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2953	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2954	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2955	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2956	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2957	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2958	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2959	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2960	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2961	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2962	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2963	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2964	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2965	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2974				HR2973 (Lo-16bits)
HR2975	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2976				HR2975 (Lo-16bits)
HR2977	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2978				HR2977 (Lo-16bits)
HR2979	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2980				HR2979 (Lo-16bits)
HR2981	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2982				HR2981 (Lo-16bits)
HR2983	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2984				HR2983 (Lo-16bits)
HR2985	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2986				HR2985 (Lo-16bits)
HR2987	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2988				HR2987 (Lo-16bits)
HR2989	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3004				HR3003 (Lo-16bits)
HR3005	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3006				HR3005 (Lo-16bits)
HR3007	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3008				HR3007 (Lo-16bits)
HR3009	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3010				HR3009 (Lo-16bits)
HR3011	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3012				HR3011 (Lo-16bits)
HR3013	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)
HR3015	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3018				HR3017 (Lo-16bits)
HR3019	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3030	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3031	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3032	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3033	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3034	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3035	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3036	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3037	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3038				HR3037 (Lo-16bits)
HR3039	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3040				HR3039 (Lo-16bits)
HR3041	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3042				HR3041 (Lo-16bits)
HR3043	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3044				HR3043 (Lo-16bits)
HR3045	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3046				HR3045 (Lo-16bits)
HR3047	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3048				HR3047 (Lo-16bits)
HR3049	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3050				HR3049 (Lo-16bits)
HR3051	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3052				HR3051 (Lo-16bits)
HR3053	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3054	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3055	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3056	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3057	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3058	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3059	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3060	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3061	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3078	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3079	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3080	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3081	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3082	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3083	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3084	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3085	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3086	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3087	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3088	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3089	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3090	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3091	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3092	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3093	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)
HR3103	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3110	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3111	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3112	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3113	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3114	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3115	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3116	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3117	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3126				HR3125 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3127	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3128				HR3127 (Lo-16bits)
HR3129	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3130				HR3129 (Lo-16bits)
HR3131	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3132				HR3131 (Lo-16bits)
HR3133	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3134	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3135	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3136	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3137	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3138	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3139	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3140	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3141	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3142	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3143	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3144	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3145	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3146	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3147	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3148	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3149	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3150	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3151	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3152	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3153	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3154	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3155	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3156	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3157	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3158	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3159	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3160	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3161	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3162	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3163	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3164	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3165	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3174				HR3173 (Lo-16bits)
HR3175	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3204				HR3203 (Lo-16bits)
HR3205	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3206				HR3205 (Lo-16bits)
HR3207	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3208				HR3207 (Lo-16bits)
HR3209	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3210				HR3209 (Lo-16bits)
HR3211	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3230	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3231	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3232	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3233	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3234	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3235	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3236	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3237	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3238				HR3237 (Lo-16bits)
HR3239	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3240				HR3239 (Lo-16bits)
HR3241	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3248				HR3247 (Lo-16bits)
HR3249	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3254	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3255	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3256	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3257	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3258	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3259	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3260	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3261	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3278	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3279	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3280	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3281	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3282	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3283	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3284	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3285	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3286	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3287	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3288	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3289	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3290	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3291	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3292	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3293	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3310	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3311	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3312	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3313	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3314	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3315	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3316	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3317	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3327	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3330				HR3329 (Lo-16bits)
HR3331	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3334	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3335	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3336	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3337	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3338	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3339	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3340	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3341	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3342	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3343	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3344	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3345	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3346	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3347	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3348	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3349	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3350	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3351	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3352	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3353	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3354	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3355	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3356	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3357	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3358	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3359	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3360	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3361	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3362	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3363	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3364	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3365	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3374				HR3373 (Lo-16bits)
HR3375	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)
HR3377	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3382				HR3381 (Lo-16bits)
HR3383	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3384				HR3383 (Lo-16bits)
HR3385	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3386				HR3385 (Lo-16bits)
HR3387	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3388				HR3387 (Lo-16bits)
HR3389	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3390				HR3389 (Lo-16bits)
HR3391	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3392				HR3391 (Lo-16bits)
HR3393	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3394				HR3393 (Lo-16bits)
HR3395	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3396				HR3395 (Lo-16bits)
HR3397	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3404				HR3403 (Lo-16bits)
HR3405	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)
HR3407	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3410				HR3409 (Lo-16bits)
HR3411	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)
HR3415	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3430	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3431	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3432	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3433	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3434	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3435	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3436	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3437	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3440				HR3439 (Lo-16bits)
HR3441	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3446				HR3445 (Lo-16bits)
HR3447	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3448				HR3447 (Lo-16bits)
HR3449	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3450				HR3449 (Lo-16bits)
HR3451	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3452				HR3451 (Lo-16bits)
HR3453	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3454	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3455	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3456	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3457	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3458	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3459	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3460	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3461	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3470				HR3469 (Lo-16bits)
HR3471	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3472				HR3471 (Lo-16bits)
HR3473	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3474				HR3473 (Lo-16bits)
HR3475	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3476				HR3475 (Lo-16bits)
HR3477	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3478	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3479	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3480	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3481	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3482	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3483	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3484	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3485	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3486	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3487	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3488	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3489	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3490	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3491	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3492	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3493	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3494	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3495	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3496	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir

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Holding Register	Parameter	Access	Length	
HR3497	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3498	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3499	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3500	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3501	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)
HR3519	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)
HR3523	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3547	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)
HR3549	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3550	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3551	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3552	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3553	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3554	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3555	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3556	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3557	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)
HR3559	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3566				HR3565 (Lo-16bits)
HR3567	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3568				HR3567 (Lo-16bits)
HR3569	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3570				HR3569 (Lo-16bits)
HR3571	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3572				HR3571 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3573	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3574	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3575	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3576	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3577	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3578	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3579	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3580	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3581	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3582	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3583	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3584	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3585	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3586	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3587	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3588	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3589	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3590	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3591	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3592	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3593	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3594	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3595	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3596	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3597	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3598	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3599	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3600	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3601	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3602	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3603	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3604	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3605	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3614				HR3613 (Lo-16bits)
HR3615	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3616				HR3615 (Lo-16bits)
HR3617	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3618				HR3617 (Lo-16bits)
HR3619	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3620				HR3619 (Lo-16bits)
HR3621	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3622				HR3621 (Lo-16bits)
HR3623	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3624				HR3623 (Lo-16bits)
HR3625	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3626				HR3625 (Lo-16bits)
HR3627	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3628				HR3627 (Lo-16bits)
HR3629	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3630				HR3629 (Lo-16bits)
HR3631	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3632				HR3631 (Lo-16bits)
HR3633	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3634				HR3633 (Lo-16bits)
HR3635	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3636				HR3635 (Lo-16bits)
HR3637	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3652				HR3651 (Lo-16bits)
HR3653	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)
HR3655	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)
HR3659	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3670	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3671	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3672	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3673	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3674	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3675	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3676	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3677	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3678				HR3677 (Lo-16bits)
HR3679	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3680				HR3679 (Lo-16bits)
HR3681	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3686				HR3685 (Lo-16bits)
HR3687	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3688				HR3687 (Lo-16bits)
HR3689	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3690				HR3689 (Lo-16bits)
HR3691	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3692				HR3691 (Lo-16bits)
HR3693	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3694	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3695	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3696	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3697	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3698	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3699	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3700	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3701	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)
HR3703	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3710				HR3709 (Lo-16bits)
HR3711	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3712				HR3711 (Lo-16bits)
HR3713	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3714				HR3713 (Lo-16bits)
HR3715	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3716				HR3715 (Lo-16bits)
HR3717	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3718	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3719	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3720	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3721	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3722	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3723	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3724	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3725	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3726	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3727	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3728	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3729	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3730	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3731	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3732	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3733	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3734	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3735	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3736	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3737	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3738	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3739	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3740	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3741	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)
HR3777	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3790	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3791	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3792	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3793	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3794	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3795	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3796	DOC2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3797	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)
HR3799	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3806				HR3805 (Lo-16bits)
HR3807	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3808				HR3807 (Lo-16bits)
HR3809	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3810				HR3809 (Lo-16bits)
HR3811	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3812				HR3811 (Lo-16bits)
HR3813	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3814	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3815	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3816	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3817	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3818	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3819	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3820	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3821	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3822	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3823	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3824	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3825	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3826	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3827	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3828	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3829	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3830	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3831	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3832	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3833	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3834	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3835	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3836	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3837	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3838	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3839	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3840	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3841	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR3842	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3843	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3844	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3845	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)
HR3851	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3862				HR3861 (Lo-16bits)
HR3863	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3864				HR3863 (Lo-16bits)
HR3865	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3866				HR3865 (Lo-16bits)
HR3867	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3871	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3901	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3910	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3911	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3912	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3913	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3914	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3915	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3916	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3917	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3925	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)
HR3927	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)
HR3933	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3934	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3935	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3936	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3937	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3938	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3939	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3940	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3941	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3949	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3958	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3959	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3960	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3961	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3962	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3963	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3964	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3965	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3966	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3967	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3968	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3969	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3970	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3971	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3972	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3973	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3974	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3975	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3976	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3977	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3978	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3979	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3980	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3981	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3991	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4016				HR4015 (Lo-16bits)
HR4017	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4030	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4031	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4032	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4033	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4034	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4035	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4036	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4037	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4044				HR4043 (Lo-16bits)
HR4045	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4054	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4055	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4056	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4057	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4058	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4059	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4060	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4061	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4062	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4063	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4064	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4065	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4066	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4067	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR4068	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4069	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4070	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4071	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4072	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4073	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4074	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4075	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4076	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4077	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4078	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4079	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4080	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4081	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4082	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4083	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4084	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4085	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)
HR4089	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4090				HR4089 (Lo-16bits)
HR4091	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4094				HR4093 (Lo-16bits)
HR4095	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)
HR4099	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)
HR4101	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4108				HR4107 (Lo-16bits)
HR4109	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4112				HR4111 (Lo-16bits)
HR4113	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4114				HR4113 (Lo-16bits)
HR4115	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4116				HR4115 (Lo-16bits)
HR4117	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4120				HR4119 (Lo-16bits)
HR4121	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)
HR4141	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4150	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4151	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4152	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4153	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4154	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4155	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4156	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4157	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4166				HR4165 (Lo-16bits)
HR4167	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4168				HR4167 (Lo-16bits)
HR4169	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4170				HR4169 (Lo-16bits)
HR4171	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4172				HR4171 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4173	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4174	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4175	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4176	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4177	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4178	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4179	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4180	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4181	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4190				HR4189 (Lo-16bits)
HR4191	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4192				HR4191 (Lo-16bits)
HR4193	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4194				HR4193 (Lo-16bits)
HR4195	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4196				HR4195 (Lo-16bits)
HR4197	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4198	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4199	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4200	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4201	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4202	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4203	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4204	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4205	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4206	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4207	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4208	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4209	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4210	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4211	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4212	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4213	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4214	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4215	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4216	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4217	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4218	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4219	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4220	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4221	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4265	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4270	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4271	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4272	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4273	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4274	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4275	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4276	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4277	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4286				HR4285 (Lo-16bits)
HR4287	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4288				HR4287 (Lo-16bits)
HR4289	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4290				HR4289 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4291	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4292				HR4291 (Lo-16bits)
HR4293	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4294	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4295	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4296	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4297	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4298	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4299	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4300	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4301	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4302	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4303	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4304	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4305	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4306	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4307	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4308	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4309	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4310	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4311	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4312	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4313	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4314	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4315	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4316	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4317	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4318	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4319	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4320	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4321	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4322	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4323	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4324	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4325	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4336				HR4335 (Lo-16bits)
HR4337	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4338				HR4337 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4339	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4340				HR4339 (Lo-16bits)
HR4341	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4342				HR4341 (Lo-16bits)
HR4343	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4366				HR4365 (Lo-16bits)
HR4367	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4368				HR4367 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4369	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4370				HR4369 (Lo-16bits)
HR4371	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4372				HR4371 (Lo-16bits)
HR4373	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4390	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4391	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4392	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4393	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4394	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4395	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4396	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4397	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4414	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4415	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4416	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4417	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4418	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4419	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4420	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4421	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4438	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4439	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4440	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4441	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4442	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4443	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4444	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4445	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4446	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4447	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4448	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4449	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4450	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4451	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4452	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4453	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4470	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4471	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4472	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4473	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4474	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4475	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4476	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4477	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4492				HR4491 (Lo-16bits)
HR4493	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4494	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4495	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4496	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4497	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4498	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4499	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4500	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4501	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4502	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4503	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4504	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4505	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4506	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4507	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4508	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4509	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4510	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4511	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4512	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4513	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4514	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4515	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4516	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4517	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4518	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4519	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4520	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4521	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4522	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4523	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4524	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4525	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4534				HR4533 (Lo-16bits)
HR4535	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4538				HR4537 (Lo-16bits)
HR4539	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4540				HR4539 (Lo-16bits)
HR4541	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4542				HR4541 (Lo-16bits)
HR4543	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4544				HR4543 (Lo-16bits)
HR4545	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)
HR4549	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4566				HR4565 (Lo-16bits)
HR4567	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4568				HR4567 (Lo-16bits)
HR4569	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4570				HR4569 (Lo-16bits)
HR4571	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4572				HR4571 (Lo-16bits)
HR4573	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4590	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4591	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4592	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4593	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4594	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4595	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4596	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4597	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4598				HR4597 (Lo-16bits)
HR4599	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4600				HR4599 (Lo-16bits)
HR4601	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4602				HR4601 (Lo-16bits)
HR4603	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4604				HR4603 (Lo-16bits)
HR4605	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4606				HR4605 (Lo-16bits)
HR4607	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4608				HR4607 (Lo-16bits)
HR4609	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4610				HR4609 (Lo-16bits)
HR4611	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4612				HR4611 (Lo-16bits)
HR4613	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4614	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4615	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4616	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4617	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4618	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4619	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4620	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4621	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4638	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4639	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4640	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4641	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4642	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4643	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4644	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4645	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4646	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4647	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4648	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4649	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4650	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4651	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4652	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4653	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4670	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4671	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4672	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4673	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4674	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4675	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4676	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4677	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4686				HR4685 (Lo-16bits)
HR4687	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4688				HR4687 (Lo-16bits)
HR4689	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4690				HR4689 (Lo-16bits)
HR4691	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4692				HR4691 (Lo-16bits)
HR4693	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4694	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4695	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4696	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4697	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4698	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4699	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4700	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4701	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4702	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4703	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4704	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4705	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4706	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4707	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4708	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4709	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4710	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4711	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4712	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4713	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4714	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4715	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4716	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4717	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4718	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4719	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4720	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4721	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4722	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4723	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4724	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4725	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)
HR4731	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4734				HR4733 (Lo-16bits)
HR4735	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4736				HR4735 (Lo-16bits)
HR4737	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4738				HR4737 (Lo-16bits)
HR4739	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4740				HR4739 (Lo-16bits)
HR4741	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4742				HR4741 (Lo-16bits)
HR4743	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4744				HR4743 (Lo-16bits)
HR4745	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4746				HR4745 (Lo-16bits)
HR4747	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4748				HR4747 (Lo-16bits)
HR4749	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)
HR4753	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4766				HR4765 (Lo-16bits)
HR4767	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4768				HR4767 (Lo-16bits)
HR4769	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4770				HR4769 (Lo-16bits)
HR4771	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4772				HR4771 (Lo-16bits)
HR4773	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4778				HR4777 (Lo-16bits)
HR4779	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)
HR4781	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4790	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4791	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4792	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4793	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4794	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4795	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4796	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4797	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4798				HR4797 (Lo-16bits)
HR4799	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4800				HR4799 (Lo-16bits)
HR4801	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4802				HR4801 (Lo-16bits)
HR4803	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4804				HR4803 (Lo-16bits)
HR4805	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4806				HR4805 (Lo-16bits)
HR4807	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4808				HR4807 (Lo-16bits)
HR4809	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4810				HR4809 (Lo-16bits)
HR4811	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4812				HR4811 (Lo-16bits)
HR4813	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4814	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4815	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4816	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4817	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4818	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4819	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4820	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4821	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)
HR4827	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)
HR4833	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4838	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4839	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4840	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4841	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4842	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4843	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4844	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4845	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4846	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4847	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4848	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4849	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4850	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4851	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4852	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4853	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4870	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4871	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4872	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4873	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4874	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4875	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4876	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4877	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4886				HR4885 (Lo-16bits)
HR4887	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4888				HR4887 (Lo-16bits)
HR4889	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4890				HR4889 (Lo-16bits)
HR4891	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4892				HR4891 (Lo-16bits)
HR4893	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4894	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4895	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4896	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4897	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4898	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4899	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4900	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4901	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4902	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4903	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4904	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4905	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4906	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4907	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4908	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4909	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4910	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4911	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4912	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4913	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4914	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4915	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4916	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4917	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4918	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4919	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4920	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4921	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4922	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4923	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4924	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4925	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4934				HR4933 (Lo-16bits)
HR4935	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4936				HR4935 (Lo-16bits)
HR4937	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4938				HR4937 (Lo-16bits)
HR4939	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4940				HR4939 (Lo-16bits)
HR4941	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4942				HR4941 (Lo-16bits)
HR4943	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4944				HR4943 (Lo-16bits)
HR4945	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4946				HR4945 (Lo-16bits)
HR4947	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4948				HR4947 (Lo-16bits)
HR4949	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)
HR4961	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4966				HR4965 (Lo-16bits)
HR4967	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4968				HR4967 (Lo-16bits)
HR4969	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4970				HR4969 (Lo-16bits)
HR4971	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4972				HR4971 (Lo-16bits)
HR4973	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)
HR4979	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4990	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4991	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4992	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4993	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4994	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4995	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4996	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4997	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4998				HR4997 (Lo-16bits)
HR4999	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5000				HR4999 (Lo-16bits)
HR5001	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5002				HR5001 (Lo-16bits)
HR5003	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5004				HR5003 (Lo-16bits)
HR5005	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5006				HR5005 (Lo-16bits)
HR5007	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5008				HR5007 (Lo-16bits)
HR5009	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5010				HR5009 (Lo-16bits)
HR5011	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5012				HR5011 (Lo-16bits)
HR5013	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5014	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5015	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5016	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5017	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5018	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5019	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5020	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5021	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5038	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5039	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5040	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5041	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5042	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5043	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5044	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5045	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5046	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5047	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5048	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5049	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5050	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5051	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5052	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5053	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5070	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5071	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5072	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5073	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5074	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5075	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5076	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5077	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5086				HR5085 (Lo-16bits)
HR5087	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5088				HR5087 (Lo-16bits)
HR5089	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5090				HR5089 (Lo-16bits)
HR5091	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5092				HR5091 (Lo-16bits)
HR5093	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5094	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5095	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5096	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5097	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5098	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5099	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5100	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5101	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5102	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5103	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5104	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5105	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5106	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5107	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5108	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5109	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5110	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5111	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5112	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5113	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5114	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR5115	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5116	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5117	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5118	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5119	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5120	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5121	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5122	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5123	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5124	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5125	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5134				HR5133 (Lo-16bits)
HR5135	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5136				HR5135 (Lo-16bits)
HR5137	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5138				HR5137 (Lo-16bits)
HR5139	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5140				HR5139 (Lo-16bits)
HR5141	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5142				HR5141 (Lo-16bits)
HR5143	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5144				HR5143 (Lo-16bits)
HR5145	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5146				HR5145 (Lo-16bits)
HR5147	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5148				HR5147 (Lo-16bits)
HR5149	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5150				HR5149 (Lo-16bits)
HR5151	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5152				HR5151 (Lo-16bits)
HR5153	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5154				HR5153 (Lo-16bits)
HR5155	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5156				HR5155 (Lo-16bits)
HR5157	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)
HR5163	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5170				HR5169 (Lo-16bits)
HR5171	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)
HR5179	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5190	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5191	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5192	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5193	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5194	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5195	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5196	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5197	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)
HR5203	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5214	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5215	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5216	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5217	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5218	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5219	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5220	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5221	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5238	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5239	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5240	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5241	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5242	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5243	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5244	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5245	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5246	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5247	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5248	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5249	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5250	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5251	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5252	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5253	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5254	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5255	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5256	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5257	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5258	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5259	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5260	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5261	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5265	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5289	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5313	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)
HR5319	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5335	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5342				HR5341 (Lo-16bits)
HR5343	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5344				HR5343 (Lo-16bits)
HR5345	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5346				HR5345 (Lo-16bits)
HR5347	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5348				HR5347 (Lo-16bits)
HR5349	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5350				HR5349 (Lo-16bits)
HR5351	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5352				HR5351 (Lo-16bits)
HR5353	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5354				HR5353 (Lo-16bits)
HR5355	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5356				HR5355 (Lo-16bits)
HR5357	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5358	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5359	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5360	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5361	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5362	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5363	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5364	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5365	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5406				HR5405 (Lo-16bits)
HR5407	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5408				HR5407 (Lo-16bits)
HR5409	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5410				HR5409 (Lo-16bits)
HR5411	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5412				HR5411 (Lo-16bits)
HR5413	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5414				HR5413 (Lo-16bits)
HR5415	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5416				HR5415 (Lo-16bits)
HR5417	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5418				HR5417 (Lo-16bits)
HR5419	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5420				HR5419 (Lo-16bits)
HR5421	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5422				HR5421 (Lo-16bits)
HR5423	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5424				HR5423 (Lo-16bits)
HR5425	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5426				HR5425 (Lo-16bits)
HR5427	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5428				HR5427 (Lo-16bits)
HR5429	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5430				HR5429 (Lo-16bits)
HR5431	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5432				HR5431 (Lo-16bits)
HR5433	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5434				HR5433 (Lo-16bits)
HR5435	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5436				HR5435 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5437	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5446	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5447	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5448	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5449	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5450	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5451	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5452	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5453	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5454	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5455	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5456	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5457	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5458	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5459	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5460	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5461	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5463	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5487	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5502				HR5501 (Lo-16bits)
HR5503	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5504				HR5503 (Lo-16bits)
HR5505	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5506				HR5505 (Lo-16bits)
HR5507	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5508				HR5507 (Lo-16bits)
HR5509	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5510	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5511	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5512	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5513	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5514	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5515	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5516	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5517	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5518	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5519	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5520	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5521	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5522	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5523	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5524	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5525	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5526	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5527	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5528	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5529	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5530	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5531	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5532	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5533	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR5534	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5535	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5536	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5537	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5538	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5539	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5540	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5541	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5550				HR5549 (Lo-16bits)
HR5551	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5552				HR5551 (Lo-16bits)
HR5553	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5554				HR5553 (Lo-16bits)
HR5555	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5556				HR5555 (Lo-16bits)
HR5557	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5558				HR5557 (Lo-16bits)
HR5559	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5560				HR5559 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5561	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5562				HR5561 (Lo-16bits)
HR5563	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5564				HR5563 (Lo-16bits)
HR5565	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5566				HR5565 (Lo-16bits)
HR5567	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5568				HR5567 (Lo-16bits)
HR5569	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5570				HR5569 (Lo-16bits)
HR5571	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5572				HR5571 (Lo-16bits)
HR5573	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5591	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)
HR5597	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5598				HR5597 (Lo-16bits)
HR5599	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5606	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5607	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5608	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5609	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5610	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5611	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5612	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5613	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5616				HR5615 (Lo-16bits)
HR5617	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5622				HR5621 (Lo-16bits)
HR5623	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5630	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5631	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5632	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5633	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5634	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5635	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5636	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5637	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5639	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)
HR5647	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5654	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5655	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5656	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5657	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5658	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5659	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5660	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5661	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5662	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5663	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5664	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5665	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5666	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5667	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5668	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5669	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5670	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5671	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5672	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5673	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5674	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5675	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5676	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5677	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5685	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)
HR5691	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)
HR5695	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5709	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)
HR5715	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)
HR5719	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5733	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5757	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5758				HR5757 (Lo-16bits)
HR5759	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5760				HR5759 (Lo-16bits)
HR5761	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5762				HR5761 (Lo-16bits)
HR5763	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5764				HR5763 (Lo-16bits)
HR5765	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5766				HR5765 (Lo-16bits)
HR5767	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5768				HR5767 (Lo-16bits)
HR5769	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5770				HR5769 (Lo-16bits)
HR5771	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5772				HR5771 (Lo-16bits)
HR5773	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5774	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5775	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5776	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5777	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5778	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5779	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

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TN00011

Holding Register	Parameter	Access	Length	
HR5780	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5781	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5807	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5822				HR5821 (Lo-16bits)
HR5823	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5824				HR5823 (Lo-16bits)
HR5825	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5826				HR5825 (Lo-16bits)
HR5827	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5828				HR5827 (Lo-16bits)
HR5829	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5830				HR5829 (Lo-16bits)
HR5831	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5832				HR5831 (Lo-16bits)
HR5833	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5834				HR5833 (Lo-16bits)
HR5835	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5836				HR5835 (Lo-16bits)
HR5837	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5838				HR5837 (Lo-16bits)
HR5839	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5840				HR5839 (Lo-16bits)
HR5841	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5842				HR5841 (Lo-16bits)
HR5843	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5844				HR5843 (Lo-16bits)
HR5845	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5846				HR5845 (Lo-16bits)
HR5847	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5848				HR5847 (Lo-16bits)
HR5849	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5850				HR5849 (Lo-16bits)
HR5851	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5852				HR5851 (Lo-16bits)
HR5853	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	
HR5862	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5863	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5864	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5865	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5866	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5867	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5868	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5869	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5870	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5871	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5872	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5873	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5874	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5875	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5876	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5877	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5918				HR5917 (Lo-16bits)
HR5919	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5920				HR5919 (Lo-16bits)
HR5921	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5922				HR5921 (Lo-16bits)
HR5923	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5924				HR5923 (Lo-16bits)
HR5925	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5926	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5927	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5928	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5929	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5930	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5931	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5932	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5933	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5934	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR5935	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5936	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5937	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5938	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5939	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5940	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5941	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5942	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5943	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5944	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5945	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5946	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5947	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5948	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5949	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5950	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5951	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5952	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5953	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5954	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR5955	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5956	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5957	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5966				HR5965 (Lo-16bits)
HR5967	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5968				HR5967 (Lo-16bits)
HR5969	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5970				HR5969 (Lo-16bits)
HR5971	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5972				HR5971 (Lo-16bits)
HR5973	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5974				HR5973 (Lo-16bits)
HR5975	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5976				HR5975 (Lo-16bits)
HR5977	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5978				HR5977 (Lo-16bits)
HR5979	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5980				HR5979 (Lo-16bits)
HR5981	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5982				HR5981 (Lo-16bits)
HR5983	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5984				HR5983 (Lo-16bits)
HR5985	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5986				HR5985 (Lo-16bits)
HR5987	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5988				HR5987 (Lo-16bits)
HR5989	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5990				HR5989 (Lo-16bits)
HR5991	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)
HR5995	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6014				HR6013 (Lo-16bits)
HR6015	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)
HR6017	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6022	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6023	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6024	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6025	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6026	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6027	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6028	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6029	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6038				HR6037 (Lo-16bits)
HR6039	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6046	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6047	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6048	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6049	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6050	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6051	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6052	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6053	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6063	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6070	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6071	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6072	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6073	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6074	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6075	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6076	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6077	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6078	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6079	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6080	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6081	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6082	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6083	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6084	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6085	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6086	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6087	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6088	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6089	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6090	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6091	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6092	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6093	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6107	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6131	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)
HR6143	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)
HR6151	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6155	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6174				HR6173 (Lo-16bits)
HR6175	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6176				HR6175 (Lo-16bits)
HR6177	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6178				HR6177 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6179	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6180				HR6179 (Lo-16bits)
HR6181	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6182				HR6181 (Lo-16bits)
HR6183	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6184				HR6183 (Lo-16bits)
HR6185	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6186				HR6185 (Lo-16bits)
HR6187	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6188				HR6187 (Lo-16bits)
HR6189	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6190	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6191	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6192	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6193	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6194	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6195	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6196	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6197	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6203	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6229	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6238				HR6237 (Lo-16bits)
HR6239	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6240				HR6239 (Lo-16bits)
HR6241	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6242				HR6241 (Lo-16bits)
HR6243	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6244				HR6243 (Lo-16bits)
HR6245	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6246				HR6245 (Lo-16bits)
HR6247	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6248				HR6247 (Lo-16bits)
HR6249	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6250				HR6249 (Lo-16bits)
HR6251	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6252				HR6251 (Lo-16bits)
HR6253	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6254				HR6253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6255	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6256				HR6255 (Lo-16bits)
HR6257	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6258				HR6257 (Lo-16bits)
HR6259	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6260				HR6259 (Lo-16bits)
HR6261	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6262				HR6261 (Lo-16bits)
HR6263	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6264				HR6263 (Lo-16bits)
HR6265	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6266				HR6265 (Lo-16bits)
HR6267	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6268				HR6267 (Lo-16bits)
HR6269	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6278	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6279	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6280	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6281	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

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TN00011

Holding Register	Parameter	Access	Length	
HR6282	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6283	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6284	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6285	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6286	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6287	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6288	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6289	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6290	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6291	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6292	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6293	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6307	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6331	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6334				HR6333 (Lo-16bits)
HR6335	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6336				HR6335 (Lo-16bits)
HR6337	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6338				HR6337 (Lo-16bits)
HR6339	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6340				HR6339 (Lo-16bits)
HR6341	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6342	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6343	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6344	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6345	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6346	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6347	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6348	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6349	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6350	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6351	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6352	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6353	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6354	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR6355	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6356	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6357	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6358	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6359	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6360	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6361	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6362	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6363	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6364	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6365	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6366	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6367	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6368	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6369	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6370	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6371	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6372	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6373	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6382				HR6381 (Lo-16bits)
HR6383	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6384				HR6383 (Lo-16bits)
HR6385	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6386				HR6385 (Lo-16bits)
HR6387	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6388				HR6387 (Lo-16bits)
HR6389	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6390				HR6389 (Lo-16bits)
HR6391	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6392				HR6391 (Lo-16bits)
HR6393	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6394				HR6393 (Lo-16bits)
HR6395	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6396				HR6395 (Lo-16bits)
HR6397	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6398				HR6397 (Lo-16bits)
HR6399	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6400				HR6399 (Lo-16bits)
HR6401	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6402				HR6401 (Lo-16bits)
HR6403	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6404				HR6403 (Lo-16bits)
HR6405	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6406				HR6405 (Lo-16bits)
HR6407	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6414				HR6413 (Lo-16bits)
HR6415	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)
HR6423	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6436				HR6435 (Lo-16bits)
HR6437	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6438	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6439	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6440	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6441	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6442	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6443	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6444	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6445	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6459	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6462	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6463	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6464	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6465	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6466	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6467	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6468	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6469	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6485	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6486	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6487	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6488	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6489	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6490	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6491	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6492	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6493	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6494	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6495	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6496	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6497	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6498	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6499	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6500	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6501	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6502	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

Holding Register	Parameter	Access	Length	
HR6503	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6504	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6505	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6506	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6507	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6508	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6509	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)
HR6527	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)
HR6535	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)
HR6551	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6590				HR6589 (Lo-16bits)
HR6591	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6592				HR6591 (Lo-16bits)
HR6593	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6594				HR6593 (Lo-16bits)
HR6595	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6596				HR6595 (Lo-16bits)
HR6597	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6598				HR6597 (Lo-16bits)
HR6599	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6600				HR6599 (Lo-16bits)
HR6601	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6602				HR6601 (Lo-16bits)
HR6603	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6604				HR6603 (Lo-16bits)
HR6605	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6606	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6607	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6608	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6609	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6610	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6611	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6612	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6613	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6625	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6654				HR6653 (Lo-16bits)
HR6655	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6656				HR6655 (Lo-16bits)
HR6657	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6658				HR6657 (Lo-16bits)
HR6659	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6660				HR6659 (Lo-16bits)
HR6661	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6662				HR6661 (Lo-16bits)
HR6663	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6664				HR6663 (Lo-16bits)
HR6665	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6666				HR6665 (Lo-16bits)
HR6667	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6668				HR6667 (Lo-16bits)
HR6669	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6670				HR6669 (Lo-16bits)
HR6671	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6672				HR6671 (Lo-16bits)
HR6673	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6674				HR6673 (Lo-16bits)
HR6675	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6676				HR6675 (Lo-16bits)
HR6677	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6678				HR6677 (Lo-16bits)
HR6679	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6680				HR6679 (Lo-16bits)
HR6681	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6682				HR6681 (Lo-16bits)
HR6683	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6684				HR6683 (Lo-16bits)
HR6685	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6694	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6695	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6696	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6697	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6698	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6699	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6700	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6701	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6702	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6703	DEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR6704	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6705	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6706	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6707	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6708	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6709	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6729	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6750				HR6749 (Lo-16bits)
HR6751	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6752				HR6751 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6753	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6754				HR6753 (Lo-16bits)
HR6755	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6756				HR6755 (Lo-16bits)
HR6757	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6758	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6759	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6760	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6761	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6762	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6763	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6764	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6765	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6766	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6767	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6768	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6769	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6770	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6771	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6772	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6773	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6774	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6775	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR6776	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6777	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6778	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6779	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6780	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6781	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6782	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6783	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6784	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6785	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6786	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6787	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6788	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6789	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6798				HR6797 (Lo-16bits)
HR6799	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6800				HR6799 (Lo-16bits)
HR6801	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6802				HR6801 (Lo-16bits)
HR6803	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6804				HR6803 (Lo-16bits)
HR6805	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6806				HR6805 (Lo-16bits)
HR6807	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6808				HR6807 (Lo-16bits)
HR6809	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6810				HR6809 (Lo-16bits)
HR6811	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6812				HR6811 (Lo-16bits)
HR6813	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6822				HR6821 (Lo-16bits)
HR6823	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6830				HR6829 (Lo-16bits)
HR6831	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)
HR6839	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6854	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6855	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6856	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6857	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6858	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6859	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6860	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6861	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6862				HR6861 (Lo-16bits)
HR6863	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6864				HR6863 (Lo-16bits)
HR6865	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6866				HR6865 (Lo-16bits)
HR6867	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6868				HR6867 (Lo-16bits)
HR6869	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6878	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6879	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6880	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6881	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6882	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6883	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6884	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6885	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6886				HR6885 (Lo-16bits)
HR6887	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6888				HR6887 (Lo-16bits)
HR6889	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6890				HR6889 (Lo-16bits)
HR6891	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6892				HR6891 (Lo-16bits)
HR6893	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6894				HR6893 (Lo-16bits)
HR6895	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6896				HR6895 (Lo-16bits)
HR6897	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6898				HR6897 (Lo-16bits)
HR6899	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6900				HR6899 (Lo-16bits)
HR6901	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6902	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6903	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6904	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6905	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6906	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6907	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6908	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6909	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6910	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6911	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6912	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6913	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6914	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6915	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6916	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6917	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6925	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)
HR6959	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6966	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6967	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6968	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6969	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6970	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6971	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6972	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6973	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6982				HR6981 (Lo-16bits)
HR6983	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6984				HR6983 (Lo-16bits)
HR6985	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6986				HR6985 (Lo-16bits)
HR6987	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6988				HR6987 (Lo-16bits)
HR6989	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6990	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6991	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6992	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6993	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6994	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6995	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6996	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6997	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6998	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6999	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7000	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7001	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7002	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7003	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7004	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7005	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7006	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7007	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7008	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7009	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7010	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7011	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7012	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7013	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7014	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7015	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7016	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7017	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7018	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7019	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7020	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7021	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7024				HR7023 (Lo-16bits)
HR7025	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)
HR7029	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)
HR7041	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7054				HR7053 (Lo-16bits)
HR7055	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)
HR7059	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7062				HR7061 (Lo-16bits)
HR7063	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7070				HR7069 (Lo-16bits)
HR7071	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7072				HR7071 (Lo-16bits)
HR7073	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7074				HR7073 (Lo-16bits)
HR7075	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7076				HR7075 (Lo-16bits)
HR7077	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7084				HR7083 (Lo-16bits)
HR7085	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7086	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7087	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7088	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7089	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7090	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7091	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7092	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7093	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7106				HR7105 (Lo-16bits)
HR7107	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7108				HR7107 (Lo-16bits)
HR7109	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7110	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7111	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7112	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7113	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7114	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7115	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7116	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7117	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7118	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7119	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7120	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7121	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7122	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7123	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7124	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7125	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7155	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7174	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7175	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7176	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7177	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7178	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7179	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7180	CUB2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7181	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7194				HR7193 (Lo-16bits)
HR7195	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7198	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7199	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7200	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7201	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7202	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7203	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7204	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7205	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7206	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7207	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7208	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7209	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7210	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7211	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7212	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7213	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7214	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7215	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7216	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7217	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7218	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7219	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7220	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7221	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7222	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7223	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7224	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7225	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7226	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7227	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7228	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7229	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7232				HR7231 (Lo-16bits)
HR7233	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7255	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7285	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7294	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7295	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7296	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7297	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7298	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7299	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7300	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7301	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7311	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7318	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7319	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7320	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7321	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7322	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7323	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7324	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7325	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7326	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7327	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7328	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7329	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7330	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7331	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7332	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7333	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7355	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7381	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7382	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7383	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7384	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7385	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7386	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7387	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7388	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7389	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7406	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7407	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7408	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7409	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7410	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7411	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7412	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7413	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7414	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7415	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7416	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7417	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7418	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7419	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7420	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7421	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7422	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7423	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7424	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7425	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7426	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7427	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7428	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7429	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7430	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7431	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7432	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7433	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7434	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7435	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7436	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7437	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7455	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7485	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7502	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7503	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7504	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7505	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7506	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7507	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7508	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7509	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7511	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7526	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7527	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7528	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7529	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7530	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7531	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7532	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7533	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7534	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7535	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7536	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7537	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7538	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7539	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7540	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7541	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7555	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)
HR7589	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7590	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7591	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7592	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7593	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7594	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7595	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7596	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7597	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7608				HR7607 (Lo-16bits)
HR7609	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7614	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7615	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7616	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7617	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7618	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7619	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7620	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7621	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7622	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7623	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7624	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7625	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7626	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7627	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7628	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7629	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7630	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7631	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR7632	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7633	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7634	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7635	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7636	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7637	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7638	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7639	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7640	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7641	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7642	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7643	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7644	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7645	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7656				HR7655 (Lo-16bits)
HR7657	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7686				HR7685 (Lo-16bits)
HR7687	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7710	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7711	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7712	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7713	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7714	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7715	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7716	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7717	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)
HR7719	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7734	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7735	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7736	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7737	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7738	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7739	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7740	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7741	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7742	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7743	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7744	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7745	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7746	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7747	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7748	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7749	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7750	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7751	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7752	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7753	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7754	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7755	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7756	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7757	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7758				HR7757 (Lo-16bits)
HR7759	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7760				HR7759 (Lo-16bits)
HR7761	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7762				HR7761 (Lo-16bits)
HR7763	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7774	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7775	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7776	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7777	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7778	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7779	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7780	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7781	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7788				HR7787 (Lo-16bits)
HR7789	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7816				HR7815 (Lo-16bits)
HR7817	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7846	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7847	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7848	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7849	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7850	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7851	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7852	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7853	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7854	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7855	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7856	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7857	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7858	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7859	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7860	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7861	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7862	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7863	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7864	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7865	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7866	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7867	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7868	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7869	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7870	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7871	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7872	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7873	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7874	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7875	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7876	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7877	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7891	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7894	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7895	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7896	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7897	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7898	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7899	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7900	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7901	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7917	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7947	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7966	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7967	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7968	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7969	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7970	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7971	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7972	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7973	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7974	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7975	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7976	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7977	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7978	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7979	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7980	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7981	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7982	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7983	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7984	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7985	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7986	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7987	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7988	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7989	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7990	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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TN00011

Holding Register	Parameter	Access	Length	
HR7991	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7992	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7993	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7994	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7995	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7996	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7997	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8014	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8015	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8016	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8017	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8018	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8019	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8020	OV3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR8021	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8032				HR8031 (Lo-16bits)
HR8033	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8034				HR8033 (Lo-16bits)
HR8035	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8036				HR8035 (Lo-16bits)
HR8037	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8038				HR8037 (Lo-16bits)
HR8039	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8047	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8077	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8086	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8087	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8088	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8089	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8090	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8091	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8092	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8093	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8094	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8095	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8096	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8097	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8098	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8099	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8100	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8101	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8102	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8103	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8104	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8105	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8106	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8107	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8108	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8109	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8110	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8111	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8112	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8113	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8114	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8115	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8116	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8117	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8122				HR8121 (Lo-16bits)
HR8123	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)
HR8127	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)
HR8131	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8134	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8135	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8136	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8137	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8138	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8139	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8140	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8141	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8150				HR8149 (Lo-16bits)
HR8151	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8170				HR8169 (Lo-16bits)
HR8171	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8172				HR8171 (Lo-16bits)
HR8173	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8179	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8206	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8207	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8208	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8209	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8210	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8211	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8212	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8213	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8214	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8215	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8216	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8217	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8218	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8219	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8220	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8221	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8222	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8223	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8224	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8225	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8226	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8227	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8228	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8229	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8230	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8231	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8232	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8233	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8234	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8235	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8236	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8237	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8253	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)
HR8257	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8264				HR8263 (Lo-16bits)
HR8265	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8266				HR8265 (Lo-16bits)
HR8267	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8268				HR8267 (Lo-16bits)
HR8269	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8270	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8271	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8272	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8273	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8274	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8275	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8276	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8277	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8282				HR8281 (Lo-16bits)
HR8283	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8310				HR8309 (Lo-16bits)
HR8311	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8340				HR8339 (Lo-16bits)
HR8341	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8342	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8343	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8344	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8345	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8346	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8347	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8348	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8349	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8350	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8351	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8352	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8353	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8354	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8355	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8356	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8357	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8358	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8359	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8360	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8361	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8362	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8363	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8364	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8365	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8366	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8367	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8368	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8369	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8370	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8371	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8372	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8373	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8383	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8398				HR8397 (Lo-16bits)
HR8399	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8400				HR8399 (Lo-16bits)
HR8401	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8402				HR8401 (Lo-16bits)
HR8403	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8404				HR8403 (Lo-16bits)
HR8405	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8406	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8407	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8408	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8409	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8410	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8411	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8412	UV2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR8413	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8439	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8469	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8478	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8479	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8480	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8481	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8482	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8483	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8484	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8485	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8486	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8487	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8488	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8489	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8490	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8491	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8492	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8493	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8494	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8495	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8496	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8497	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8498	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8499	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8500	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8501	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8502	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8503	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8504	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8505	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8506	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8507	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8508	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8509	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8513	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)
HR8527	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)
HR8533	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8536				HR8535 (Lo-16bits)
HR8537	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8538				HR8537 (Lo-16bits)
HR8539	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8540				HR8539 (Lo-16bits)
HR8541	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8542	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8543	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8544	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8545	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8546	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8547	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8548	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8549	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8566				HR8565 (Lo-16bits)
HR8567	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8596				HR8595 (Lo-16bits)
HR8597	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)
HR8599	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8614	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8615	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8616	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8617	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8618	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8619	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8620	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8621	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8622	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8623	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8624	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8625	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8626	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8627	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8628	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8629	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8630	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8631	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8632	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8633	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8634	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8635	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8636	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8637	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8638	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8639	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8640	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8641	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8642	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8643	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8644	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8645	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8666				HR8665 (Lo-16bits)
HR8667	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8668				HR8667 (Lo-16bits)
HR8669	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8672				HR8671 (Lo-16bits)
HR8673	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8674				HR8673 (Lo-16bits)
HR8675	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8676				HR8675 (Lo-16bits)
HR8677	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8678	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8679	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8680	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8681	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8682	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8683	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8684	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8685	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)
HR8689	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8692				HR8691 (Lo-16bits)
HR8693	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8720				HR8719 (Lo-16bits)
HR8721	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)
HR8737	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8750	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8751	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8752	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8753	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8754	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8755	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8756	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8757	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8758	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8759	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8760	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8761	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8762	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8763	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8764	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8765	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8766	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8767	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8768	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8769	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8770	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8771	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8772	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8773	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8774	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8775	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8776	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8777	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8778	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8779	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8780	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8781	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8795	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8808				HR8807 (Lo-16bits)
HR8809	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8810				HR8809 (Lo-16bits)
HR8811	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8812				HR8811 (Lo-16bits)
HR8813	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8814	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8815	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8816	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8817	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8818	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8819	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8820	VUB1:Delay type	R(W)	1	0=DT;1=IDMT

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TN00011

Holding Register	Parameter	Access	Length	
HR8821	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8847	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8850				HR8849 (Lo-16bits)
HR8851	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)
HR8853	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8877	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)
HR8879	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8886	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8887	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8888	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8889	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8890	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8891	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8892	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8893	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8894	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8895	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8896	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8897	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8898	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8899	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8900	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8901	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8902	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8903	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8904	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8905	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8906	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8907	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8908	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8909	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8910	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8911	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8912	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8913	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8914	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8915	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8916	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8917	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8918				HR8917 (Lo-16bits)
HR8919	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8920				HR8919 (Lo-16bits)
HR8921	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8922				HR8921 (Lo-16bits)
HR8923	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8924				HR8923 (Lo-16bits)
HR8925	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8926				HR8925 (Lo-16bits)
HR8927	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8928				HR8927 (Lo-16bits)
HR8929	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8930				HR8929 (Lo-16bits)
HR8931	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8932				HR8931 (Lo-16bits)
HR8933	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8934				HR8933 (Lo-16bits)
HR8935	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8936				HR8935 (Lo-16bits)
HR8937	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8938				HR8937 (Lo-16bits)
HR8939	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8940				HR8939 (Lo-16bits)
HR8941	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8942				HR8941 (Lo-16bits)
HR8943	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8944				HR8943 (Lo-16bits)
HR8945	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8946				HR8945 (Lo-16bits)
HR8947	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8948				HR8947 (Lo-16bits)
HR8949	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8950	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8951	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8952	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8953	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8954	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8955	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8956	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8957	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)
HR8961	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)
HR8973	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8974				HR8973 (Lo-16bits)
HR8975	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8982				HR8981 (Lo-16bits)
HR8983	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8984				HR8983 (Lo-16bits)
HR8985	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8986				HR8985 (Lo-16bits)
HR8987	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8988				HR8987 (Lo-16bits)
HR8989	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9004				HR9003 (Lo-16bits)
HR9005	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9022	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9023	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9024	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9025	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9026	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9027	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9028	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9029	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9030	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9031	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9032	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9033	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9034	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9035	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9036	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9037	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9038	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9039	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9040	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9041	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9042	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9043	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9044	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9045	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9046	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9047	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9048	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR9049	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9050	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9051	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9052	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9053	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9054				HR9053 (Lo-16bits)
HR9055	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9056				HR9055 (Lo-16bits)
HR9057	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9058				HR9057 (Lo-16bits)
HR9059	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9060				HR9059 (Lo-16bits)
HR9061	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9062				HR9061 (Lo-16bits)
HR9063	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9064				HR9063 (Lo-16bits)
HR9065	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9066				HR9065 (Lo-16bits)
HR9067	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9068				HR9067 (Lo-16bits)
HR9069	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9070				HR9069 (Lo-16bits)
HR9071	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9072				HR9071 (Lo-16bits)
HR9073	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9074				HR9073 (Lo-16bits)
HR9075	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9076				HR9075 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9077	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9078				HR9077 (Lo-16bits)
HR9079	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9080				HR9079 (Lo-16bits)
HR9081	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9082				HR9081 (Lo-16bits)
HR9083	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9084				HR9083 (Lo-16bits)
HR9085	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9086	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9087	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9088	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9089	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9090	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9091	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9092	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9093	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9103	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9118				HR9117 (Lo-16bits)
HR9119	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9120				HR9119 (Lo-16bits)
HR9121	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9122				HR9121 (Lo-16bits)
HR9123	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9124				HR9123 (Lo-16bits)
HR9125	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9132				HR9131 (Lo-16bits)
HR9133	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)
HR9145	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9158	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9159	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9160	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9161	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9162	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9163	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9164	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9165	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9166	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9167	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9168	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9169	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9170	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9171	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9172	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9173	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9174	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9175	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9176	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9177	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9178	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9179	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9180	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9181	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9182	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9183	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9184	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9185	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9186	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9187	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9188	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9189	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9194				HR9193 (Lo-16bits)
HR9195	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9196				HR9195 (Lo-16bits)
HR9197	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9198				HR9197 (Lo-16bits)
HR9199	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9200				HR9199 (Lo-16bits)
HR9201	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9202				HR9201 (Lo-16bits)
HR9203	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9204				HR9203 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9205	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9206				HR9205 (Lo-16bits)
HR9207	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9208				HR9207 (Lo-16bits)
HR9209	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9210				HR9209 (Lo-16bits)
HR9211	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9216				HR9215 (Lo-16bits)
HR9217	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9218				HR9217 (Lo-16bits)
HR9219	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9220				HR9219 (Lo-16bits)
HR9221	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9222	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9223	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9224	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9225	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9226	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9227	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9228	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9229	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9231	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)
HR9245	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9258				HR9257 (Lo-16bits)
HR9259	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9262				HR9261 (Lo-16bits)
HR9263	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9264				HR9263 (Lo-16bits)
HR9265	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9266				HR9265 (Lo-16bits)
HR9267	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9268				HR9267 (Lo-16bits)
HR9269	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR9288				HR9287 (Lo-16bits)
HR9289	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9294	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9295	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9296	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9297	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9298	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9299	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9300	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9301	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9302	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9303	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9304	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9305	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9306	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9307	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9308	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9309	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9310	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9311	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9312	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9313	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9314	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9315	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9316	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9317	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9318	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9319	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9320	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9321	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9322	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9323	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9324	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9325	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9326	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9327	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9328	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9329	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9330	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9331	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9332	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9333	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9361	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9366	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9367	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9368	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9369	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9370	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9371	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9372	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9373	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9391	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9406	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9407	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9408	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9409	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9410	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9411	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9412	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9413	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9421	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9446	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9447	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9448	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9449	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9450	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9451	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9452	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9453	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9464				HR9463 (Lo-16bits)
HR9465	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9466				HR9465 (Lo-16bits)
HR9467	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9468				HR9467 (Lo-16bits)
HR9469	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9481	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9486	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9487	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9488	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9489	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9490	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9491	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9492	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9493	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9511	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9526	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9527	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9528	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9529	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9530	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9531	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9532	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9533	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9541	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9566	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9567	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9568	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9569	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9570	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9571	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9572	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9573	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9580				HR9579 (Lo-16bits)
HR9581	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9582				HR9581 (Lo-16bits)
HR9583	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9584				HR9583 (Lo-16bits)
HR9585	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9586				HR9585 (Lo-16bits)
HR9587	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9588				HR9587 (Lo-16bits)
HR9589	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9590				HR9589 (Lo-16bits)
HR9591	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9592				HR9591 (Lo-16bits)
HR9593	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9594				HR9593 (Lo-16bits)
HR9595	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9596				HR9595 (Lo-16bits)
HR9597	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9598				HR9597 (Lo-16bits)
HR9599	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9601	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9602				HR9601 (Lo-16bits)
HR9603	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9604				HR9603 (Lo-16bits)
HR9605	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9606	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9607	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9608	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9609	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9610	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9611	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9612	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9613	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9614				HR9613 (Lo-16bits)
HR9615	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9616				HR9615 (Lo-16bits)
HR9617	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9618				HR9617 (Lo-16bits)
HR9619	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9620				HR9619 (Lo-16bits)
HR9621	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9622				HR9621 (Lo-16bits)
HR9623	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9624				HR9623 (Lo-16bits)
HR9625	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9626				HR9625 (Lo-16bits)
HR9627	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9628				HR9627 (Lo-16bits)
HR9629	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9630				HR9629 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9631	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9632				HR9631 (Lo-16bits)
HR9633	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9634				HR9633 (Lo-16bits)
HR9635	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9636				HR9635 (Lo-16bits)
HR9637	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9638				HR9637 (Lo-16bits)
HR9639	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9640				HR9639 (Lo-16bits)
HR9641	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9642				HR9641 (Lo-16bits)
HR9643	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9644				HR9643 (Lo-16bits)
HR9645	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9660				HR9659 (Lo-16bits)
HR9661	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9689	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9691	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9692	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9693	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9694	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9695	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9696	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9697	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9698	CUB1:I2> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9699	CUB2:I2>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9700	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9701	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9702	OV1:U> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9703	OV2:U>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9704	OV3:U>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9705	OV4:U>>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9706	UV1:U< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9707	UV2:U<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9708	UV3:U<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9709	UV4:U<<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9710	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9711	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9712	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9713	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9714	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled;1=Activated
HR9715	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9743	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9750				HR9749 (Lo-16bits)
HR9751	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9752				HR9751 (Lo-16bits)
HR9753	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9754				HR9753 (Lo-16bits)
HR9755	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9769	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	DIGI:DIs (cpu)	R	2	0b=DI1;1b=DI2;2b=DI3; (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	DIGI:DIs (input option 1)	R	2	0b=DI4;1b=DI5;2b=DI6;3b=DI7;4b=DI8;5b=DI9;6b=DI10;7b=DI1
HR9774				HR9773 (Lo-16bits)
HR9775	DIGI:DIs (input option 2)	R	2	0b=DI12;1b=DI13;2b=DI14;3b=DI15;4b=DI16;5b=DI17;6b=DI18
HR9776				HR9775 (Lo-16bits)
HR9777	DIGI:DIs (input option 3)	R	2	0b=DI20;1b=DI21;2b=DI22;3b=DI23;4b=DI24;5b=DI25;6b=DI26
HR9778				HR9777 (Lo-16bits)
HR9779	DIGI:DIs (input option 4)	R	2	0b=D28;1b=DI29;2b=DI30;3b=DI31;4b=DI32;5b=DI33;6b=DI34;
HR9780				HR9779 (Lo-16bits)
HR9781	DIGI:DIs (input option 5)	R	2	0b=DI36;1b=DI37;2b=DI38;3b=DI39;4b=DI40;5b=DI41;6b=DI42
HR9782				HR9781 (Lo-16bits)
HR9783	DIGI:DIs (input option 6)	R	2	0b=DI44;1b=DI45;2b=DI46;3b=DI47;4b=DI48;5b=DI49;6b=DI50
HR9784				HR9783 (Lo-16bits)
HR9785	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9797	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9802	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9803	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled;1=Activated
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

1.3 M Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware M_v2.1.1.3-233. The firmware is used by the following device types:

- AQ-M210
- AQ-M215

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.3 - 3. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

Holding Register	Parameter	Access	Length	
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR452	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR508	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR538	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR568	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR598	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR628	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR658	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR688	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR718	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR748	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR778	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR807				HR806 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR808	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR838	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR865				HR864 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR889				HR888 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR943				HR942 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR944	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR973				HR972 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR974	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1004	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1034	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1064	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1094	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1124	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1154	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	POW1:Reset 3ph Energies	RW	1	0=-;1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:Dis 1-32	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7;7b=DI8; (Hi-16bits)
HR1250				HR1249 (Lo-16bits)
HR1251	DIGI:Dis 33-64	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38;6b=DI39
HR1252				HR1251 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1253	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b=O8;8b=
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8
HR1256	SGS:Force SG	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1257	SGS:Force SG change	RW	1	0=Disabled;1=Enabled
HR1258	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=SG1...6;6=
HR1259	SGS:Remote SG change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1260	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1261	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1262	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1263	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1264	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1265	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1266	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1267	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1268	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1269	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1271	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1273	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1275	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1277	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1278	DEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1279	DEF1:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1280	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1281	DEF2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1282	DEF2:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;

Holding Register	Parameter	Access	Length	
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1284	DEF3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1285	DEF3:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1287	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1288	DEF4:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1289	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1290	IEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1292	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1296	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1312	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	HOC2:Ih>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1317	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1338	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1342	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1359	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4=CLPU b
HR1380	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloaded;4=C
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Blocked;4b=2
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1385	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1386	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1387	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1389	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1390	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1391	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1394	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1395	TOLF1:Temp C or F deg	RW	1	0=C;1=F
HR1396	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1397	TOLF1:RTD Sensor	R	1	0=Invalid;1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set;1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1401	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=Running

Holding Register	Parameter	Access	Length	
HR1402	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1403	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1404	TOLM1:Time constants	RW	1	0=Single;1=Multiple
HR1405	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1408	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Blocked
HR1411	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1415	OV2:U>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1416	OV3:U>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1417	OV4:U>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1418	UV1:U< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1419	UV2:U<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1420	UV3:U<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1421	UV4:U<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1422	NOV1:U0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1423	NOV1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1425	NOV2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1427	NOV3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1429	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input

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Holding Register	Parameter	Access	Length	
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1443	FRQV1:f>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1444	FRQV1:f>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1445	FRQV1:f>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1446	FRQV1:f< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1447	FRQV1:f<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1448	FRQV1:f<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1449	FRQV1:f<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On high overloa
HR1459	OPW1:P> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1460	UPW1:P< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1461	RPW1:Prev> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1463	SEF1:U03rd< Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1466	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=VHz(1) Blocked;2=VHz(1) Start;3=VHz(1) Alarm;4=
HR1469	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=Alarm
HR1470	ARC1:I/I0 Arc> condition	R	1	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4b=Z3 Trip
HR1471	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1472	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1473	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTBusfail;3=VTLinefail;4=Blocked
HR1474	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live VTS Ok SE
HR1475	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR1476	THD1:THD condition	R	1	0=Normal;1=Start;2=Alarm;3=Blocked
HR1477	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On
HR1478	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo cmd;5=Bk
HR1479	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR1480	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=SYN1 Vco
HR1481				HR1480 (Lo-16bits)
HR1482	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=SYN2 Vco
HR1483				HR1482 (Lo-16bits)
HR1484	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=SYN3 Vco
HR1485				HR1484 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1486	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1487	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1488	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1489	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1490	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1491	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1492	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1493	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1494	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1495	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1496	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR is runni Requested;14b=AR4 Requested;15b=AR5 Requested;16b=Exec
HR1497				HR1496 (Lo-16bits)
HR1498	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Delay;4=AR
HR1499	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set higher than U
HR1500				HR1499 (Lo-16bits)
HR1501	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Operation bl
HR1502				HR1501 (Lo-16bits)
HR1503	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low set Decre
HR1504	VRG1:Voltage meas condition	R	1	0=Not configured;1=System U12;2=System U23;3=System U31;
HR1505	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLT detected
HR1506	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1507	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLT detected
HR1508	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1509	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLT FW;2=FLT REV

Holding Register	Parameter	Access	Length	
HR1510	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=No voltage
HR1511	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1512	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLT detected
HR1513	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1514				HR1513 (Lo-16bits)
HR1515	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1518				HR1517 (Lo-16bits)
HR1519	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=A8;8b=A
HR1520				HR1519 (Lo-16bits)
HR1521	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=A39;7b= (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1524				HR1523 (Lo-16bits)
HR1525	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1530				HR1529 (Lo-16bits)
HR1531	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1532				HR1531 (Lo-16bits)
HR1533	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1535	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1536				HR1535 (Lo-16bits)
HR1537	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1538				HR1537 (Lo-16bits)
HR1539	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1542				HR1541 (Lo-16bits)
HR1543	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1544				HR1543 (Lo-16bits)
HR1545	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1548				HR1547 (Lo-16bits)
HR1549	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1550				HR1549 (Lo-16bits)
HR1551	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1554				HR1553 (Lo-16bits)
HR1555	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1556				HR1555 (Lo-16bits)
HR1557	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1559	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1560				HR1559 (Lo-16bits)
HR1561	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1562				HR1561 (Lo-16bits)
HR1563	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1566				HR1565 (Lo-16bits)
HR1567	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1568				HR1567 (Lo-16bits)
HR1569	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1572				HR1571 (Lo-16bits)
HR1573	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1574				HR1573 (Lo-16bits)
HR1575	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1578				HR1577 (Lo-16bits)
HR1579	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1583	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1584				HR1583 (Lo-16bits)
HR1585	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1590				HR1589 (Lo-16bits)
HR1591	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1592				HR1591 (Lo-16bits)
HR1593	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1594				HR1593 (Lo-16bits)
HR1595	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1596				HR1595 (Lo-16bits)
HR1597	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1598				HR1597 (Lo-16bits)
HR1599	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1600				HR1599 (Lo-16bits)
HR1601	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1602				HR1601 (Lo-16bits)
HR1603	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1606				HR1605 (Lo-16bits)
HR1607	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1608				HR1607 (Lo-16bits)
HR1609	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1610				HR1609 (Lo-16bits)
HR1611	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1612				HR1611 (Lo-16bits)
HR1613	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1614				HR1613 (Lo-16bits)
HR1615	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1616				HR1615 (Lo-16bits)
HR1617	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1618				HR1617 (Lo-16bits)
HR1619	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1620				HR1619 (Lo-16bits)
HR1621	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1622				HR1621 (Lo-16bits)
HR1623	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1624				HR1623 (Lo-16bits)
HR1625	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1626				HR1625 (Lo-16bits)
HR1627	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1630				HR1629 (Lo-16bits)
HR1631	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1632				HR1631 (Lo-16bits)
HR1633	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1634				HR1633 (Lo-16bits)
HR1635	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=VHz(2) Start;2=VHz(2) Alarm;3=VHz(2) Trip;4=VHz(
HR1636	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1639	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1642				HR1641 (Lo-16bits)
HR1643	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1688				HR1687 (Lo-16bits)
HR1689	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1690				HR1689 (Lo-16bits)
HR1691	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1692				HR1691 (Lo-16bits)
HR1693	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1694				HR1693 (Lo-16bits)
HR1695	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1712	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1713	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1714	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1715	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1716	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR1717				HR1716 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1718	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR1719				HR1718 (Lo-16bits)
HR1720	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1721				HR1720 (Lo-16bits)
HR1722	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1723				HR1722 (Lo-16bits)
HR1724	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR1725	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher
HR1726	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked:2=Trip;3=Alarm
HR1727	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	PGS1:PS1 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1730	VJP1:Voltage meas selected	R	1	0=Selection Ok;1=Selection not available
HR1731	PGS1:PS2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1732	PGS1:PS3 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1733	PGS1:PS4 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1734	PGS1:PS5 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1735	PGS1:PS6 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1736	PGS1:PS7 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1737	PGS1:PS8 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1738	PGS1:PS9 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1739	PGS1:PS10 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1740	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1741				HR1740 (Lo-16bits)
HR1742	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1743				HR1742 (Lo-16bits)
HR1744	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1745				HR1744 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1746	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1747				HR1746 (Lo-16bits)
HR1748	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1749				HR1748 (Lo-16bits)
HR1750	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1751				HR1750 (Lo-16bits)
HR1752	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1753				HR1752 (Lo-16bits)
HR1754	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1755				HR1754 (Lo-16bits)
HR1756	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1757				HR1756 (Lo-16bits)
HR1758	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1759				HR1758 (Lo-16bits)
HR1760	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1761				HR1760 (Lo-16bits)
HR1762	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1763				HR1762 (Lo-16bits)
HR1764	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled;1=Activated
HR1767	CTRL:Local/Remote status	R	1	0=Remote;1=Local
HR1768	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1769				HR1768 (Lo-16bits)
HR1770	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1773				HR1772 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1774	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1775				HR1774 (Lo-16bits)
HR1776	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1777				HR1776 (Lo-16bits)
HR1778	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1779				HR1778 (Lo-16bits)
HR1780	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1781				HR1780 (Lo-16bits)
HR1782	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1783				HR1782 (Lo-16bits)
HR1784	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1795				HR1794 (Lo-16bits)
HR1796	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	GSYN:Synchronizer mode	RW	1	0=Disabled;1=Activated
HR1801	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1803	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1829	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1838				HR1837 (Lo-16bits)
HR1839	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1840				HR1839 (Lo-16bits)
HR1841	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	VREC:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1852	VREC:Scale current values to primary	RW	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR1853	VREC:Record fault types (Overcurrrent & Voltage Over/Under) faults	RW	1	0=No;1=Yes
HR1854	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)
HR1862	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	RTB:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1873	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR1874	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)
HR1878	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1879				HR1878 (Lo-16bits)
HR1880	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)
HR1886	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)
HR1888	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR1891				HR1890 (Lo-16bits)
HR1892	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1895				HR1894 (Lo-16bits)
HR1896	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1897				HR1896 (Lo-16bits)
HR1898	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1899				HR1898 (Lo-16bits)
HR1900	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1901				HR1900 (Lo-16bits)
HR1902	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1903				HR1902 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1904	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1907				HR1906 (Lo-16bits)
HR1908	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1909				HR1908 (Lo-16bits)
HR1910	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1919				HR1918 (Lo-16bits)
HR1920	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1921				HR1920 (Lo-16bits)
HR1922	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1923				HR1922 (Lo-16bits)
HR1924	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1925				HR1924 (Lo-16bits)
HR1926	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1927				HR1926 (Lo-16bits)
HR1928	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1929				HR1928 (Lo-16bits)
HR1930	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1931				HR1930 (Lo-16bits)
HR1932	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1933				HR1932 (Lo-16bits)
HR1934	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1935				HR1934 (Lo-16bits)
HR1936	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1937				HR1936 (Lo-16bits)
HR1938	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1939				HR1938 (Lo-16bits)
HR1940	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1941				HR1940 (Lo-16bits)
HR1942	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1943				HR1942 (Lo-16bits)
HR1944	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1945				HR1944 (Lo-16bits)
HR1946	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1947				HR1946 (Lo-16bits)
HR1948	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1949				HR1948 (Lo-16bits)
HR1950	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1951				HR1950 (Lo-16bits)
HR1952	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1953				HR1952 (Lo-16bits)
HR1954	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1955				HR1954 (Lo-16bits)
HR1956	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1957				HR1956 (Lo-16bits)
HR1958	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1959				HR1958 (Lo-16bits)
HR1960	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1961				HR1960 (Lo-16bits)
HR1962	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR1965				HR1964 (Lo-16bits)
HR1966	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1967				HR1966 (Lo-16bits)
HR1968	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1969				HR1968 (Lo-16bits)
HR1970	NOC1:I> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1971	NOC1:I> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1972	NOC1:I> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1973	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1974				HR1973 (Lo-16bits)
HR1975	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1976				HR1975 (Lo-16bits)
HR1977	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1978				HR1977 (Lo-16bits)
HR1979	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	NOC1:Delay type	RW	1	0=DT;1=IDMT
HR1982	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1983				HR1982 (Lo-16bits)
HR1984	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR1985	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1986	NOC1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1987	NOC1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1988	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1989				HR1988 (Lo-16bits)
HR1990	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1991				HR1990 (Lo-16bits)
HR1992	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1993				HR1992 (Lo-16bits)
HR1994	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1995				HR1994 (Lo-16bits)
HR1996	NOC1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1997	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1998	NOC2:I>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1999	NOC2:I>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2000	NOC2:I>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2001	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2002				HR2001 (Lo-16bits)
HR2003	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	NOC2:Delay type	RW	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR2010	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2011				HR2010 (Lo-16bits)
HR2012	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2013	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2014	NOC2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2015	NOC2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2016	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2017				HR2016 (Lo-16bits)
HR2018	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2019				HR2018 (Lo-16bits)
HR2020	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2021				HR2020 (Lo-16bits)
HR2022	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2023				HR2022 (Lo-16bits)
HR2024	NOC2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2025	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2026	NOC3:I>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2027	NOC3:I>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2028	NOC3:I>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2029	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2035	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR2038	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2039				HR2038 (Lo-16bits)
HR2040	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2041	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2042	NOC3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2043	NOC3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2044	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2045				HR2044 (Lo-16bits)
HR2046	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2047				HR2046 (Lo-16bits)
HR2048	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2049				HR2048 (Lo-16bits)
HR2050	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2051				HR2050 (Lo-16bits)
HR2052	NOC3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2053	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2054	NOC4:I>>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR2055	NOC4:I>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2056	NOC4:I>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2057	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)
HR2059	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2061	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR2066	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2067				HR2066 (Lo-16bits)
HR2068	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2069	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2070	NOC4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2071	NOC4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2072	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2073				HR2072 (Lo-16bits)
HR2074	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2075				HR2074 (Lo-16bits)
HR2076	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2077				HR2076 (Lo-16bits)
HR2078	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2079				HR2078 (Lo-16bits)
HR2080	NOC4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2081	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2082	DOC1:Idir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2083	DOC1:Idir> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2084	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2085				HR2084 (Lo-16bits)
HR2086	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2087				HR2086 (Lo-16bits)
HR2088	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2089				HR2088 (Lo-16bits)
HR2090	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2091				HR2090 (Lo-16bits)
HR2092	DOC2:Idir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2093	DOC2:Idir>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2094	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2095				HR2094 (Lo-16bits)
HR2096	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2097				HR2096 (Lo-16bits)
HR2098	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2099				HR2098 (Lo-16bits)
HR2100	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2101				HR2100 (Lo-16bits)
HR2102	DOC3:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2103	DOC3:Idir>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2104	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2105				HR2104 (Lo-16bits)
HR2106	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2107				HR2106 (Lo-16bits)
HR2108	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2109				HR2108 (Lo-16bits)
HR2110	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2111				HR2110 (Lo-16bits)
HR2112	DOC4:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2113	DOC4:Idir>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2114	DOC4:Idir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2115				HR2114 (Lo-16bits)
HR2116	DOC4:Idir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2117				HR2116 (Lo-16bits)
HR2118	DOC4:Idir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2119				HR2118 (Lo-16bits)
HR2120	DOC4:Idir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2121				HR2120 (Lo-16bits)
HR2122	NEF1:I0> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2123	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2124				HR2123 (Lo-16bits)
HR2125	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2126				HR2125 (Lo-16bits)
HR2127	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR2128	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2129				HR2128 (Lo-16bits)
HR2130	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2131	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2132	NEF1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2133	NEF1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2134	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2135				HR2134 (Lo-16bits)
HR2136	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2137				HR2136 (Lo-16bits)
HR2138	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2139				HR2138 (Lo-16bits)
HR2140	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NEF1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2143	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2144	NEF2:I0>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2145	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2146				HR2145 (Lo-16bits)
HR2147	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2148				HR2147 (Lo-16bits)
HR2149	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR2150	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2151				HR2150 (Lo-16bits)
HR2152	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2153	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2154	NEF2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2155	NEF2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2156	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2157				HR2156 (Lo-16bits)
HR2158	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2159				HR2158 (Lo-16bits)
HR2160	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2161				HR2160 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2162	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	NEF2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2165	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2166	NEF3:I0>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2167	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR2172	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2173				HR2172 (Lo-16bits)
HR2174	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2175	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2176	NEF3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2177	NEF3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2178	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2179				HR2178 (Lo-16bits)
HR2180	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2181				HR2180 (Lo-16bits)
HR2182	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2183				HR2182 (Lo-16bits)
HR2184	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	NEF3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2187	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)

Holding Register	Parameter	Access	Length	
HR2188	NEF4:I0>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2189	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR2194	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2195				HR2194 (Lo-16bits)
HR2196	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2197	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2198	NEF4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2199	NEF4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2200	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2201				HR2200 (Lo-16bits)
HR2202	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2203				HR2202 (Lo-16bits)
HR2204	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)
HR2206	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	NEF4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2209	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2210	DEF1:I0dir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2211	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2212				HR2211 (Lo-16bits)
HR2213	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2214				HR2213 (Lo-16bits)
HR2215	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	DEF2:I0dir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2218	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2219				HR2218 (Lo-16bits)
HR2220	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2221				HR2220 (Lo-16bits)
HR2222	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2223				HR2222 (Lo-16bits)
HR2224	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2225	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2226				HR2225 (Lo-16bits)
HR2227	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2228				HR2227 (Lo-16bits)
HR2229	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2230				HR2229 (Lo-16bits)
HR2231	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2232	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2233				HR2232 (Lo-16bits)
HR2234	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2235				HR2234 (Lo-16bits)
HR2236	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2237				HR2236 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2238	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR2239	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2240				HR2239 (Lo-16bits)
HR2241	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2242	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2243	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2244	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2245	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2246	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2247	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2248	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2249	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2250	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2251	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2252	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2253	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2254	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2255	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2256	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2257	TOLM1:TM> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2258	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2259	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2260	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2261	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2267	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2272				HR2271 (Lo-16bits)
HR2273	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2286				HR2285 (Lo-16bits)
HR2287	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2291	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR2298				HR2297 (Lo-16bits)
HR2299	ROC1:Idrw> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2300	ROC1:Idrw> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2301	ROC2:Idrw>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2302	ROC2:Idrw>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2303	ROC3:Idrw>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2304	ROC3:Idrw>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2305	ROC4:Idrw>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2306	ROC4:Idrw>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2307	ROC5:Idrw>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2308	ROC5:Idrw>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2309	ROC6:Idrw>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2310	ROC6:Idrw>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2311	ROC7:Idrw>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2312	ROC7:Idrw>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2313	ROC8:Idrw>>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR2314	ROC8:Idrw>>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2315	ROV1:Urw>/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2316	ROV2:Urw>>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2317	ROV3:Urw>>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2318	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2319	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=Start On (Alarm);3=Start On (Alarm & Tr
HR2320	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2321	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2322	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2323	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2324	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)
HR2328	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2331				HR2330 (Lo-16bits)
HR2332	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2337				HR2336 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2338	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2339				HR2338 (Lo-16bits)
HR2340	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2343				HR2342 (Lo-16bits)
HR2344	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2345				HR2344 (Lo-16bits)
HR2346	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2362	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2363				HR2362 (Lo-16bits)
HR2364	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)
HR2370	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2379				HR2378 (Lo-16bits)
HR2380	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2381				HR2380 (Lo-16bits)
HR2382	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2383				HR2382 (Lo-16bits)
HR2384	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2385				HR2384 (Lo-16bits)
HR2386	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2387				HR2386 (Lo-16bits)
HR2388	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2389				HR2388 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2390	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2391				HR2390 (Lo-16bits)
HR2392	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2393				HR2392 (Lo-16bits)
HR2394	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2395				HR2394 (Lo-16bits)
HR2396	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2397				HR2396 (Lo-16bits)
HR2398	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2401				HR2400 (Lo-16bits)
HR2402	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2403				HR2402 (Lo-16bits)
HR2404	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2417				HR2416 (Lo-16bits)
HR2418	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2419				HR2418 (Lo-16bits)
HR2420	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	TOLT1:TT> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2437	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2438	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2439	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2440	TOLF1:TF> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2441	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2442	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2443	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2444	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2446	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)
HR2456	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2469				HR2468 (Lo-16bits)
HR2470	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)
HR2496	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2498	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2499				HR2498 (Lo-16bits)
HR2500	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)
HR2508	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2515				HR2514 (Lo-16bits)
HR2516	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR2517	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR2518	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2519				HR2518 (Lo-16bits)
HR2520	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2521				HR2520 (Lo-16bits)
HR2522	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2523				HR2522 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2524	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2525				HR2524 (Lo-16bits)
HR2526	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2527				HR2526 (Lo-16bits)
HR2528	ARC1:I/I0 Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2529				HR2528 (Lo-16bits)
HR2530	ARC1:I/I0 Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2531				HR2530 (Lo-16bits)
HR2532	ARC1:I/I0 Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2533				HR2532 (Lo-16bits)
HR2534	ARC1:I/I0 Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2535				HR2534 (Lo-16bits)
HR2536	ARC1:I/I0 Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2537				HR2536 (Lo-16bits)
HR2538	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2539				HR2538 (Lo-16bits)
HR2540	DIGI:DI1 Counter Clear	RW	1	0=-;1=Clear
HR2541	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	DIGI:DI2 Counter Clear	RW	1	0=-;1=Clear
HR2544	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2545				HR2544 (Lo-16bits)
HR2546	DIGI:DI3 Counter Clear	RW	1	0=-;1=Clear
HR2547	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	DIGI:DI4 Counter Clear	RW	1	0=-;1=Clear
HR2550	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2551				HR2550 (Lo-16bits)
HR2552	DIGI:DI5 Counter Clear	RW	1	0=-;1=Clear
HR2553	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	DIGI:DI6 Counter Clear	RW	1	0=-;1=Clear
HR2556	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2557				HR2556 (Lo-16bits)
HR2558	DIGI:DI7 Counter Clear	RW	1	0=-;1=Clear
HR2559	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	DIGI:DI8 Counter Clear	RW	1	0=-;1=Clear
HR2562	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2563				HR2562 (Lo-16bits)
HR2564	DIGI:DI9 Counter Clear	RW	1	0=-;1=Clear
HR2565	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	DIGI:DI10 Counter Clear	RW	1	0=-;1=Clear
HR2568	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2569				HR2568 (Lo-16bits)
HR2570	DIGI:DI11 Counter Clear	RW	1	0=-;1=Clear
HR2571	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	DIGI:DI12 Counter Clear	RW	1	0=-;1=Clear
HR2574	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2575				HR2574 (Lo-16bits)
HR2576	DIGI:DI13 Counter Clear	RW	1	0=-;1=Clear
HR2577	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	DIGI:DI14 Counter Clear	RW	1	0=-;1=Clear
HR2580	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2581				HR2580 (Lo-16bits)
HR2582	DIGI:DI15 Counter Clear	RW	1	0=-;1=Clear
HR2583	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	DIGI:DI16 Counter Clear	RW	1	0=-;1=Clear
HR2586	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2587				HR2586 (Lo-16bits)
HR2588	DIGI:DI17 Counter Clear	RW	1	0=-;1=Clear
HR2589	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)
HR2591	DIGI:DI18 Counter Clear	RW	1	0=-;1=Clear
HR2592	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2593				HR2592 (Lo-16bits)
HR2594	DIGI:DI19 Counter Clear	RW	1	0=-;1=Clear
HR2595	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	DIGI:DI20 Counter Clear	RW	1	0=-;1=Clear
HR2598	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2599				HR2598 (Lo-16bits)
HR2600	DIGI:DI21 Counter Clear	RW	1	0=-;1=Clear
HR2601	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	DIGI:DI22 Counter Clear	RW	1	0=-;1=Clear
HR2604	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	DIGI:DI23 Counter Clear	RW	1	0=-;1=Clear
HR2607	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2608				HR2607 (Lo-16bits)
HR2609	DIGI:DI24 Counter Clear	RW	1	0=-;1=Clear
HR2610	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2611				HR2610 (Lo-16bits)
HR2612	DIGI:DI25 Counter Clear	RW	1	0=-;1=Clear
HR2613	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	DIGI:DI26 Counter Clear	RW	1	0=-;1=Clear
HR2616	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2617				HR2616 (Lo-16bits)
HR2618	DIGI:DI27 Counter Clear	RW	1	0=-;1=Clear
HR2619	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)
HR2621	DIGI:DI28 Counter Clear	RW	1	0=-;1=Clear
HR2622	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2623				HR2622 (Lo-16bits)
HR2624	DIGI:DI29 Counter Clear	RW	1	0=-;1=Clear
HR2625	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	DIGI:DI30 Counter Clear	RW	1	0=-;1=Clear
HR2628	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2629				HR2628 (Lo-16bits)
HR2630	DIGI:DI31 Counter Clear	RW	1	0=-;1=Clear
HR2631	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2632				HR2631 (Lo-16bits)
HR2633	DIGI:DI32 Counter Clear	RW	1	0=-;1=Clear
HR2634	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2635				HR2634 (Lo-16bits)
HR2636	DIGI:DI33 Counter Clear	RW	1	0=-;1=Clear
HR2637	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DIGI:DI34 Counter Clear	RW	1	0=-;1=Clear
HR2640	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2641				HR2640 (Lo-16bits)
HR2642	DIGI:DI35 Counter Clear	RW	1	0=-;1=Clear
HR2643	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DIGI:DI36 Counter Clear	RW	1	0=-;1=Clear
HR2646	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2647				HR2646 (Lo-16bits)
HR2648	DIGI:DI37 Counter Clear	RW	1	0=-;1=Clear
HR2649	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2650				HR2649 (Lo-16bits)
HR2651	DIGI:DI38 Counter Clear	RW	1	0=-;1=Clear
HR2652	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2653				HR2652 (Lo-16bits)
HR2654	DIGI:DI39 Counter Clear	RW	1	0=-;1=Clear
HR2655	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DIGI:DI40 Counter Clear	RW	1	0=-;1=Clear
HR2658	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2659				HR2658 (Lo-16bits)
HR2660	DIGI:DI41 Counter Clear	RW	1	0=-;1=Clear
HR2661	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DIGI:DI42 Counter Clear	RW	1	0=-;1=Clear
HR2664	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2665				HR2664 (Lo-16bits)
HR2666	DIGI:DI43 Counter Clear	RW	1	0=-;1=Clear
HR2667	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DIGI:DI44 Counter Clear	RW	1	0=-;1=Clear
HR2670	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2671				HR2670 (Lo-16bits)
HR2672	DIGI:DI45 Counter Clear	RW	1	0=-;1=Clear
HR2673	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DIGI:DI46 Counter Clear	RW	1	0=-;1=Clear
HR2676	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2677				HR2676 (Lo-16bits)
HR2678	DIGI:DI47 Counter Clear	RW	1	0=-;1=Clear
HR2679	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DIGI:DI48 Counter Clear	RW	1	0=-;1=Clear
HR2682	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2683				HR2682 (Lo-16bits)
HR2684	DIGI:DI49 Counter Clear	RW	1	0=-;1=Clear
HR2685	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DIGI:DI50 Counter Clear	RW	1	0=-;1=Clear
HR2688	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2689				HR2688 (Lo-16bits)
HR2690	DIGI:DI51 Counter Clear	RW	1	0=-;1=Clear
HR2691	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)
HR2703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2710	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2711	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2712	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2713	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2714	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2715	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2716	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2717	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)
HR2727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2728				HR2727 (Lo-16bits)
HR2729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2730				HR2729 (Lo-16bits)
HR2731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)
HR2733	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2734	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2735	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2736	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2737	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2738	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2739	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2740	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2741	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2742	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2743	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2744	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2745	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2746	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2747	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2748	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2749	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2750	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2751	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2752	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2753	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2754	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2755	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2756	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2757	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2758	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2759	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2760	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2761	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2762	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2763	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2764	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2765	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2776				HR2775 (Lo-16bits)
HR2777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2806				HR2805 (Lo-16bits)
HR2807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2830	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2831	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2832	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2833	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2834	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2835	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2836	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2837	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2854	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2855	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2856	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2857	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2858	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2859	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2860	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2861	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2878	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2879	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2880	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2881	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2882	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2883	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2884	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2885	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2886	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2887	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2888	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2889	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2890	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2891	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2892	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2893	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2903	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2910	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2911	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2912	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2913	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2914	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2915	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2916	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2917	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)
HR2927	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2928				HR2927 (Lo-16bits)
HR2929	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2930				HR2929 (Lo-16bits)
HR2931	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2932				HR2931 (Lo-16bits)
HR2933	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2934	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2935	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2936	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2937	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2938	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2939	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2940	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2941	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2942	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2943	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2944	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2945	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2946	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2947	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2948	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2949	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2950	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2951	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2952	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2953	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2954	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2955	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2956	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2957	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2958	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2959	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2960	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2961	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2962	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2963	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2964	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2965	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2974				HR2973 (Lo-16bits)
HR2975	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2976				HR2975 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2977	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2978				HR2977 (Lo-16bits)
HR2979	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2980				HR2979 (Lo-16bits)
HR2981	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2982				HR2981 (Lo-16bits)
HR2983	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2984				HR2983 (Lo-16bits)
HR2985	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2986				HR2985 (Lo-16bits)
HR2987	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2988				HR2987 (Lo-16bits)
HR2989	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3004				HR3003 (Lo-16bits)
HR3005	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3006				HR3005 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3007	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3008				HR3007 (Lo-16bits)
HR3009	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3010				HR3009 (Lo-16bits)
HR3011	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3012				HR3011 (Lo-16bits)
HR3013	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)
HR3015	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3018				HR3017 (Lo-16bits)
HR3019	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3030	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3031	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3032	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3033	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3034	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3035	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3036	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3037	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3038				HR3037 (Lo-16bits)
HR3039	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3040				HR3039 (Lo-16bits)
HR3041	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3042				HR3041 (Lo-16bits)
HR3043	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3044				HR3043 (Lo-16bits)
HR3045	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3046				HR3045 (Lo-16bits)
HR3047	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3048				HR3047 (Lo-16bits)
HR3049	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3050				HR3049 (Lo-16bits)
HR3051	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3052				HR3051 (Lo-16bits)
HR3053	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3054	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3055	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3056	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3057	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3058	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3059	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3060	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3061	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3078	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3079	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3080	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3081	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3082	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3083	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3084	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3085	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3086	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3087	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3088	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3089	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3090	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3091	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3092	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3093	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3103	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3110	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3111	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3112	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3113	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3114	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3115	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3116	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3117	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3126				HR3125 (Lo-16bits)
HR3127	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3128				HR3127 (Lo-16bits)
HR3129	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3130				HR3129 (Lo-16bits)
HR3131	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3132				HR3131 (Lo-16bits)
HR3133	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3134	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3135	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3136	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3137	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3138	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3139	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3140	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3141	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3142	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3143	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3144	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3145	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3146	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3147	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3148	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3149	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3150	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3151	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3152	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR3153	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3154	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3155	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3156	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3157	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3158	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3159	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3160	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3161	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3162	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3163	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3164	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3165	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3177	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3206				HR3205 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3207	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3208				HR3207 (Lo-16bits)
HR3209	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3210				HR3209 (Lo-16bits)
HR3211	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3230	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3231	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3232	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3233	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3234	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3235	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3236	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3237	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3238				HR3237 (Lo-16bits)
HR3239	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3240				HR3239 (Lo-16bits)
HR3241	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3248				HR3247 (Lo-16bits)
HR3249	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3254	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3255	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3256	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3257	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3258	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3259	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3260	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3261	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3278	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3279	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3280	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3281	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3282	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3283	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3284	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3285	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3286	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3287	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3288	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3289	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3290	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3291	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3292	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3293	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3303	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3310	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3311	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3312	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3313	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3314	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3315	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3316	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3317	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)
HR3327	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3330				HR3329 (Lo-16bits)
HR3331	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3334	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3335	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3336	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3337	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3338	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3339	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3340	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3341	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3342	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3343	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3344	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3345	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3346	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3347	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3348	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3349	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3350	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3351	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3352	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3353	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3354	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3355	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3356	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3357	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3358	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3359	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3360	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3361	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3362	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3363	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3364	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3365	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3374				HR3373 (Lo-16bits)
HR3375	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3377	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3382				HR3381 (Lo-16bits)
HR3383	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3384				HR3383 (Lo-16bits)
HR3385	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3386				HR3385 (Lo-16bits)
HR3387	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3388				HR3387 (Lo-16bits)
HR3389	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3390				HR3389 (Lo-16bits)
HR3391	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3392				HR3391 (Lo-16bits)
HR3393	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3394				HR3393 (Lo-16bits)
HR3395	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3396				HR3395 (Lo-16bits)
HR3397	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)
HR3405	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3407	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3410				HR3409 (Lo-16bits)
HR3411	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)
HR3415	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3430	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3431	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3432	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3433	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3434	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3435	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3436	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3437	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3440				HR3439 (Lo-16bits)
HR3441	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3446				HR3445 (Lo-16bits)
HR3447	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3448				HR3447 (Lo-16bits)
HR3449	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3450				HR3449 (Lo-16bits)
HR3451	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3452				HR3451 (Lo-16bits)
HR3453	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3454	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3455	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3456	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3457	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3458	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3459	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3460	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3461	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3470				HR3469 (Lo-16bits)
HR3471	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3472				HR3471 (Lo-16bits)
HR3473	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3474				HR3473 (Lo-16bits)
HR3475	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3476				HR3475 (Lo-16bits)
HR3477	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3478	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3479	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3480	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3481	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3482	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3483	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3484	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3485	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3486	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3487	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3488	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3489	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3490	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3491	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3492	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3493	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3494	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3495	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3496	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3497	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3498	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir

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Holding Register	Parameter	Access	Length	
HR3499	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3500	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3501	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)
HR3519	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3523	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)
HR3547	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)
HR3549	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3550	DOC1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3551	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3552	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3553	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3554	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3555	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3556	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3557	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)
HR3559	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3566				HR3565 (Lo-16bits)
HR3567	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3568				HR3567 (Lo-16bits)
HR3569	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3570				HR3569 (Lo-16bits)
HR3571	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3572				HR3571 (Lo-16bits)
HR3573	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3574	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3575	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3576	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3577	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3578	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3579	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3580	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3581	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3582	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3583	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3584	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3585	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3586	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3587	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3588	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3589	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3590	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3591	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3592	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3593	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3594	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3595	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3596	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3597	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR3598	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3599	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3600	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3601	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3602	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3603	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3604	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3605	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3614				HR3613 (Lo-16bits)
HR3615	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3616				HR3615 (Lo-16bits)
HR3617	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3618				HR3617 (Lo-16bits)
HR3619	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3620				HR3619 (Lo-16bits)
HR3621	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3622				HR3621 (Lo-16bits)
HR3623	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3624				HR3623 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3625	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3626				HR3625 (Lo-16bits)
HR3627	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3628				HR3627 (Lo-16bits)
HR3629	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3630				HR3629 (Lo-16bits)
HR3631	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3632				HR3631 (Lo-16bits)
HR3633	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3634				HR3633 (Lo-16bits)
HR3635	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3636				HR3635 (Lo-16bits)
HR3637	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3652				HR3651 (Lo-16bits)
HR3653	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3655	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)
HR3659	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3670	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3671	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3672	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3673	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3674	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3675	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3676	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3677	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3680				HR3679 (Lo-16bits)
HR3681	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3686				HR3685 (Lo-16bits)
HR3687	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3688				HR3687 (Lo-16bits)
HR3689	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3690				HR3689 (Lo-16bits)
HR3691	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3692				HR3691 (Lo-16bits)
HR3693	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3694	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3695	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3696	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3697	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3698	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3699	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3700	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3701	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3703	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3710				HR3709 (Lo-16bits)
HR3711	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3712				HR3711 (Lo-16bits)
HR3713	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3714				HR3713 (Lo-16bits)
HR3715	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3716				HR3715 (Lo-16bits)
HR3717	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3718	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3719	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3720	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3721	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3722	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3723	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3724	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3725	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3726	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3727	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3728	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3729	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3730	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3731	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3732	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3733	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3734	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3735	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3736	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3737	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3738	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3739	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3740	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3741	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)
HR3777	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3790	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3791	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3792	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3793	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3794	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3795	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3796	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3797	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3799	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3806				HR3805 (Lo-16bits)
HR3807	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3808				HR3807 (Lo-16bits)
HR3809	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3810				HR3809 (Lo-16bits)
HR3811	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3812				HR3811 (Lo-16bits)
HR3813	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3814	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3815	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3816	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3817	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3818	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3819	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3820	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3821	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3822	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3823	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3824	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3825	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3826	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3827	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3828	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3829	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3830	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3831	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3832	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3833	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3834	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3835	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3836	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3837	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3838	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3839	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3840	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3841	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3842	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3843	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR3844	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3845	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)
HR3851	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3862				HR3861 (Lo-16bits)
HR3863	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3864				HR3863 (Lo-16bits)
HR3865	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3866				HR3865 (Lo-16bits)
HR3867	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3874				HR3873 (Lo-16bits)
HR3875	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3904				HR3903 (Lo-16bits)
HR3905	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3910	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3911	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3912	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3913	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3914	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3915	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3916	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3917	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3927	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)
HR3933	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3934	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3935	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3936	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3937	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3938	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3939	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3940	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3941	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3952				HR3951 (Lo-16bits)
HR3953	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3958	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3959	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3960	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3961	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3962	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3963	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3964	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3965	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3966	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3967	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3968	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3969	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3970	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3971	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3972	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3973	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3974	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3975	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3976	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3977	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3978	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3979	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3980	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3981	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4017	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4030	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4031	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4032	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4033	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4034	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4035	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4036	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4037	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4045	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4054	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4055	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4056	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4057	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4058	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4059	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4060	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4061	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4062	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4063	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4064	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4065	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4066	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4067	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4068	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR4069	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4070	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4071	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4072	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4073	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4074	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4075	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4076	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4077	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4078	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4079	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4080	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4081	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4082	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4083	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4084	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4085	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)
HR4089	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4090				HR4089 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4091	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4094				HR4093 (Lo-16bits)
HR4095	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)
HR4099	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)
HR4101	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4108				HR4107 (Lo-16bits)
HR4109	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4112				HR4111 (Lo-16bits)
HR4113	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4114				HR4113 (Lo-16bits)
HR4115	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4116				HR4115 (Lo-16bits)
HR4117	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4120				HR4119 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4121	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)
HR4141	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4150	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4151	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4152	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4153	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4154	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4155	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4156	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4157	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4166				HR4165 (Lo-16bits)
HR4167	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4168				HR4167 (Lo-16bits)
HR4169	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4170				HR4169 (Lo-16bits)
HR4171	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4172				HR4171 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4173	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4174	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4175	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4176	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4177	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4178	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4179	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4180	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4181	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4190				HR4189 (Lo-16bits)
HR4191	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4192				HR4191 (Lo-16bits)
HR4193	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4194				HR4193 (Lo-16bits)
HR4195	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4196				HR4195 (Lo-16bits)
HR4197	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4198	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4199	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4200	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4201	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4202	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4203	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4204	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4205	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4206	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4207	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4208	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4209	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4210	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4211	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4212	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4213	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4214	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4215	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4216	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4217	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4218	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4219	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4220	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4221	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4265	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4270	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4271	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4272	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4273	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4274	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4275	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4276	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4277	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4286				HR4285 (Lo-16bits)
HR4287	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4288				HR4287 (Lo-16bits)
HR4289	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4290				HR4289 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4291	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4292				HR4291 (Lo-16bits)
HR4293	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4294	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4295	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4296	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4297	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4298	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4299	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4300	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4301	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4302	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4303	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4304	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4305	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4306	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4307	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4308	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4309	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4310	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4311	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4312	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4313	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4314	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4315	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4316	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4317	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4318	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4319	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4320	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4321	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4322	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4323	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4324	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4325	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4336				HR4335 (Lo-16bits)
HR4337	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4338				HR4337 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4339	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4340				HR4339 (Lo-16bits)
HR4341	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4342				HR4341 (Lo-16bits)
HR4343	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4366				HR4365 (Lo-16bits)
HR4367	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4368				HR4367 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4369	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4370				HR4369 (Lo-16bits)
HR4371	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4372				HR4371 (Lo-16bits)
HR4373	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4390	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4391	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4392	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4393	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4394	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4395	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4396	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4397	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4414	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4415	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4416	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4417	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4418	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4419	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4420	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4421	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4438	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4439	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4440	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4441	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4442	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4443	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4444	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4445	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4446	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4447	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4448	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4449	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4450	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4451	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4452	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4453	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4470	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4471	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4472	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4473	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4474	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4475	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4476	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4477	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4492				HR4491 (Lo-16bits)
HR4493	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4494	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4495	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4496	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4497	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4498	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4499	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4500	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4501	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4502	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4503	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4504	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4505	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4506	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4507	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4508	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4509	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4510	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4511	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4512	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4513	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4514	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4515	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4516	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4517	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4518	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4519	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4520	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4521	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4522	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4523	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4524	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4525	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4534				HR4533 (Lo-16bits)
HR4535	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4538				HR4537 (Lo-16bits)
HR4539	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4540				HR4539 (Lo-16bits)
HR4541	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4542				HR4541 (Lo-16bits)
HR4543	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4544				HR4543 (Lo-16bits)
HR4545	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)
HR4549	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4566				HR4565 (Lo-16bits)
HR4567	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4568				HR4567 (Lo-16bits)
HR4569	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4570				HR4569 (Lo-16bits)
HR4571	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4572				HR4571 (Lo-16bits)
HR4573	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4590	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4591	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4592	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4593	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4594	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4595	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4596	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4597	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4598				HR4597 (Lo-16bits)
HR4599	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4600				HR4599 (Lo-16bits)
HR4601	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4602				HR4601 (Lo-16bits)
HR4603	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4604				HR4603 (Lo-16bits)
HR4605	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4606				HR4605 (Lo-16bits)
HR4607	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4608				HR4607 (Lo-16bits)
HR4609	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4610				HR4609 (Lo-16bits)
HR4611	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4612				HR4611 (Lo-16bits)
HR4613	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4614	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4615	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4616	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4617	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4618	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4619	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4620	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4621	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4638	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4639	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4640	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4641	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4642	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4643	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4644	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4645	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4646	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4647	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4648	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4649	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4650	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4651	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4652	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4653	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4670	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4671	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4672	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4673	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4674	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4675	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4676	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4677	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4686				HR4685 (Lo-16bits)
HR4687	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4688				HR4687 (Lo-16bits)
HR4689	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4690				HR4689 (Lo-16bits)
HR4691	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4692				HR4691 (Lo-16bits)
HR4693	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4694	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4695	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4696	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4697	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4698	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4699	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4700	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4701	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4702	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4703	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4704	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4705	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4706	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4707	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4708	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4709	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4710	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4711	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4712	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4713	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4714	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4715	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4716	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4717	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4718	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4719	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4720	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4721	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4722	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4723	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4724	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4725	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)
HR4731	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4734				HR4733 (Lo-16bits)
HR4735	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4736				HR4735 (Lo-16bits)
HR4737	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4738				HR4737 (Lo-16bits)
HR4739	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4740				HR4739 (Lo-16bits)
HR4741	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4742				HR4741 (Lo-16bits)
HR4743	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4744				HR4743 (Lo-16bits)
HR4745	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4746				HR4745 (Lo-16bits)
HR4747	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4748				HR4747 (Lo-16bits)
HR4749	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)
HR4753	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4766				HR4765 (Lo-16bits)
HR4767	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4768				HR4767 (Lo-16bits)
HR4769	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4770				HR4769 (Lo-16bits)
HR4771	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4772				HR4771 (Lo-16bits)
HR4773	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4778				HR4777 (Lo-16bits)
HR4779	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)
HR4781	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4790	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4791	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4792	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4793	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4794	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4795	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4796	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4797	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4798				HR4797 (Lo-16bits)
HR4799	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4800				HR4799 (Lo-16bits)
HR4801	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4802				HR4801 (Lo-16bits)
HR4803	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4804				HR4803 (Lo-16bits)
HR4805	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4806				HR4805 (Lo-16bits)
HR4807	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4808				HR4807 (Lo-16bits)
HR4809	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4810				HR4809 (Lo-16bits)
HR4811	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4812				HR4811 (Lo-16bits)
HR4813	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4814	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4815	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4816	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4817	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4818	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4819	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4820	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4821	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)
HR4827	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)
HR4833	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4838	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4839	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4840	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4841	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4842	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4843	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR4844	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4845	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4846	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4847	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4848	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4849	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4850	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4851	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4852	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4853	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4870	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4871	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4872	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4873	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4874	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4875	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4876	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4877	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4886				HR4885 (Lo-16bits)
HR4887	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4888				HR4887 (Lo-16bits)
HR4889	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4890				HR4889 (Lo-16bits)
HR4891	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4892				HR4891 (Lo-16bits)
HR4893	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4894	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4895	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4896	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4897	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4898	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4899	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4900	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4901	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4902	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4903	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4904	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4905	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4906	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4907	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4908	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4909	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4910	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4911	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4912	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4913	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4914	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4915	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4916	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4917	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4918	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4919	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4920	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4921	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4922	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4923	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4924	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4925	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4934				HR4933 (Lo-16bits)
HR4935	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4936				HR4935 (Lo-16bits)
HR4937	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4938				HR4937 (Lo-16bits)
HR4939	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4940				HR4939 (Lo-16bits)
HR4941	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4942				HR4941 (Lo-16bits)
HR4943	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4944				HR4943 (Lo-16bits)
HR4945	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4946				HR4945 (Lo-16bits)
HR4947	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4948				HR4947 (Lo-16bits)
HR4949	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)
HR4961	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4966				HR4965 (Lo-16bits)
HR4967	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4968				HR4967 (Lo-16bits)
HR4969	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4970				HR4969 (Lo-16bits)
HR4971	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4972				HR4971 (Lo-16bits)
HR4973	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)
HR4979	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4990	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4991	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4992	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4993	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4994	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4995	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4996	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4997	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4998				HR4997 (Lo-16bits)
HR4999	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5000				HR4999 (Lo-16bits)
HR5001	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5002				HR5001 (Lo-16bits)
HR5003	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5004				HR5003 (Lo-16bits)
HR5005	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5006				HR5005 (Lo-16bits)
HR5007	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5008				HR5007 (Lo-16bits)
HR5009	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5010				HR5009 (Lo-16bits)
HR5011	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5012				HR5011 (Lo-16bits)
HR5013	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5014	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5015	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5016	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5017	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5018	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5019	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5020	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5021	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5038	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5039	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5040	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5041	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5042	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5043	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5044	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5045	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5046	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5047	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5048	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5049	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5050	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5051	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5052	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5053	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5070	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5071	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5072	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5073	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5074	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5075	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5076	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5077	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5086				HR5085 (Lo-16bits)
HR5087	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5088				HR5087 (Lo-16bits)
HR5089	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5090				HR5089 (Lo-16bits)
HR5091	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5092				HR5091 (Lo-16bits)
HR5093	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5094	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5095	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5096	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5097	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5098	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5099	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5100	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5101	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5102	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5103	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5104	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5105	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5106	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5107	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5108	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5109	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5110	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5111	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5112	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5113	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5114	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR5115	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5116	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5117	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5118	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5119	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5120	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5121	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5122	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5123	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5124	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5125	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5134				HR5133 (Lo-16bits)
HR5135	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5136				HR5135 (Lo-16bits)
HR5137	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5138				HR5137 (Lo-16bits)
HR5139	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5140				HR5139 (Lo-16bits)
HR5141	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5142				HR5141 (Lo-16bits)
HR5143	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5144				HR5143 (Lo-16bits)
HR5145	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5146				HR5145 (Lo-16bits)
HR5147	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5148				HR5147 (Lo-16bits)
HR5149	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5150				HR5149 (Lo-16bits)
HR5151	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5152				HR5151 (Lo-16bits)
HR5153	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5154				HR5153 (Lo-16bits)
HR5155	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5156				HR5155 (Lo-16bits)
HR5157	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)
HR5163	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5170				HR5169 (Lo-16bits)
HR5171	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)
HR5179	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5190	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5191	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5192	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5193	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5194	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5195	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5196	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5197	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)
HR5203	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5214	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5215	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5216	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5217	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5218	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5219	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5220	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5221	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5238	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5239	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5240	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5241	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5242	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5243	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5244	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5245	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5246	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5247	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5248	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5249	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5250	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5251	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5252	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5253	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5254	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5255	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5256	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5257	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5258	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5259	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5260	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5261	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5265	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5289	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5313	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)
HR5319	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5335	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5342				HR5341 (Lo-16bits)
HR5343	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5344				HR5343 (Lo-16bits)
HR5345	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5346				HR5345 (Lo-16bits)
HR5347	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5348				HR5347 (Lo-16bits)
HR5349	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5350				HR5349 (Lo-16bits)
HR5351	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5352				HR5351 (Lo-16bits)
HR5353	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5354				HR5353 (Lo-16bits)
HR5355	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5356				HR5355 (Lo-16bits)
HR5357	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5358	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5359	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5360	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5361	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5362	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5363	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5364	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5365	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5406				HR5405 (Lo-16bits)
HR5407	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5408				HR5407 (Lo-16bits)
HR5409	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5410				HR5409 (Lo-16bits)
HR5411	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5412				HR5411 (Lo-16bits)
HR5413	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5414				HR5413 (Lo-16bits)
HR5415	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5416				HR5415 (Lo-16bits)
HR5417	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5418				HR5417 (Lo-16bits)
HR5419	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5420				HR5419 (Lo-16bits)
HR5421	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5422				HR5421 (Lo-16bits)
HR5423	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5424				HR5423 (Lo-16bits)
HR5425	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5426				HR5425 (Lo-16bits)
HR5427	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5428				HR5427 (Lo-16bits)
HR5429	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5430				HR5429 (Lo-16bits)
HR5431	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5432				HR5431 (Lo-16bits)
HR5433	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5434				HR5433 (Lo-16bits)
HR5435	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5436				HR5435 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5437	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5446	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5447	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5448	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5449	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5450	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5451	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5452	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5453	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5454	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5455	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5456	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5457	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5458	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5459	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5460	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5461	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5463	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5487	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5502				HR5501 (Lo-16bits)
HR5503	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5504				HR5503 (Lo-16bits)
HR5505	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5506				HR5505 (Lo-16bits)
HR5507	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5508				HR5507 (Lo-16bits)
HR5509	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5510	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5511	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5512	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5513	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5514	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5515	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5516	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5517	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5518	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5519	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5520	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5521	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5522	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5523	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5524	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5525	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5526	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5527	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5528	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5529	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5530	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5531	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5532	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5533	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR5534	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5535	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5536	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5537	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5538	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5539	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5540	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5541	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5550				HR5549 (Lo-16bits)
HR5551	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5552				HR5551 (Lo-16bits)
HR5553	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5554				HR5553 (Lo-16bits)
HR5555	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5556				HR5555 (Lo-16bits)
HR5557	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5558				HR5557 (Lo-16bits)
HR5559	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5560				HR5559 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5561	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5562				HR5561 (Lo-16bits)
HR5563	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5564				HR5563 (Lo-16bits)
HR5565	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5566				HR5565 (Lo-16bits)
HR5567	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5568				HR5567 (Lo-16bits)
HR5569	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5570				HR5569 (Lo-16bits)
HR5571	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5572				HR5571 (Lo-16bits)
HR5573	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5591	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)
HR5597	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5598				HR5597 (Lo-16bits)
HR5599	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5606	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5607	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5608	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5609	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5610	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5611	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5612	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5613	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5616				HR5615 (Lo-16bits)
HR5617	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5622				HR5621 (Lo-16bits)
HR5623	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5630	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5631	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5632	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5633	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5634	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5635	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5636	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5637	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5639	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)
HR5647	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5654	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5655	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5656	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5657	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5658	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5659	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5660	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5661	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5662	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5663	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5664	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5665	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5666	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5667	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5668	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5669	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5670	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5671	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5672	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5673	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5674	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5675	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5676	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5677	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5685	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)
HR5691	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)
HR5695	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5709	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)
HR5715	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)
HR5719	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5733	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5757	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5758				HR5757 (Lo-16bits)
HR5759	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5760				HR5759 (Lo-16bits)
HR5761	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5762				HR5761 (Lo-16bits)
HR5763	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5764				HR5763 (Lo-16bits)
HR5765	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5766				HR5765 (Lo-16bits)
HR5767	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5768				HR5767 (Lo-16bits)
HR5769	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5770				HR5769 (Lo-16bits)
HR5771	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5772				HR5771 (Lo-16bits)
HR5773	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5774	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5775	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5776	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5777	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5778	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5779	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

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TN00011

Holding Register	Parameter	Access	Length	
HR5780	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5781	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5807	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5822				HR5821 (Lo-16bits)
HR5823	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5824				HR5823 (Lo-16bits)
HR5825	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5826				HR5825 (Lo-16bits)
HR5827	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5828				HR5827 (Lo-16bits)
HR5829	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5830				HR5829 (Lo-16bits)
HR5831	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5832				HR5831 (Lo-16bits)
HR5833	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5834				HR5833 (Lo-16bits)
HR5835	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5836				HR5835 (Lo-16bits)
HR5837	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5838				HR5837 (Lo-16bits)
HR5839	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5840				HR5839 (Lo-16bits)
HR5841	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5842				HR5841 (Lo-16bits)
HR5843	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5844				HR5843 (Lo-16bits)
HR5845	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5846				HR5845 (Lo-16bits)
HR5847	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5848				HR5847 (Lo-16bits)
HR5849	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5850				HR5849 (Lo-16bits)
HR5851	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5852				HR5851 (Lo-16bits)
HR5853	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	
HR5862	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5863	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5864	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5865	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5866	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5867	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5868	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5869	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5870	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5871	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5872	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5873	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5874	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5875	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5876	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5877	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5918				HR5917 (Lo-16bits)
HR5919	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5920				HR5919 (Lo-16bits)
HR5921	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5922				HR5921 (Lo-16bits)
HR5923	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5924				HR5923 (Lo-16bits)
HR5925	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5926	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5927	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5928	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5929	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5930	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5931	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5932	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5933	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5934	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR5935	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5936	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5937	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5938	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5939	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5940	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5941	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5942	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5943	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5944	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5945	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5946	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5947	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5948	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5949	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5950	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5951	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5952	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5953	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5954	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR5955	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5956	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5957	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5966				HR5965 (Lo-16bits)
HR5967	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5968				HR5967 (Lo-16bits)
HR5969	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5970				HR5969 (Lo-16bits)
HR5971	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5972				HR5971 (Lo-16bits)
HR5973	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5974				HR5973 (Lo-16bits)
HR5975	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5976				HR5975 (Lo-16bits)
HR5977	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5978				HR5977 (Lo-16bits)
HR5979	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5980				HR5979 (Lo-16bits)
HR5981	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5982				HR5981 (Lo-16bits)
HR5983	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5984				HR5983 (Lo-16bits)
HR5985	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5986				HR5985 (Lo-16bits)
HR5987	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5988				HR5987 (Lo-16bits)
HR5989	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5990				HR5989 (Lo-16bits)
HR5991	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)
HR5995	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6014				HR6013 (Lo-16bits)
HR6015	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)
HR6017	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6022	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6023	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6024	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6025	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6026	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6027	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6028	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6029	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6038				HR6037 (Lo-16bits)
HR6039	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6046	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6047	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6048	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6049	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6050	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6051	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6052	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6053	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6063	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6070	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6071	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6072	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6073	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6074	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6075	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6076	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6077	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6078	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6079	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6080	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6081	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6082	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR6083	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6084	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6085	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6086	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6087	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6088	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6089	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6090	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6091	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6092	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6093	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6107	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6131	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)
HR6143	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)
HR6151	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6155	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6174				HR6173 (Lo-16bits)
HR6175	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6176				HR6175 (Lo-16bits)
HR6177	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6178				HR6177 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6179	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6180				HR6179 (Lo-16bits)
HR6181	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6182				HR6181 (Lo-16bits)
HR6183	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6184				HR6183 (Lo-16bits)
HR6185	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6186				HR6185 (Lo-16bits)
HR6187	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6188				HR6187 (Lo-16bits)
HR6189	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6190	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6191	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6192	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6193	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6194	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6195	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6196	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6197	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6203	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6229	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6238				HR6237 (Lo-16bits)
HR6239	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6240				HR6239 (Lo-16bits)
HR6241	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6242				HR6241 (Lo-16bits)
HR6243	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6244				HR6243 (Lo-16bits)
HR6245	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6246				HR6245 (Lo-16bits)
HR6247	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6248				HR6247 (Lo-16bits)
HR6249	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6250				HR6249 (Lo-16bits)
HR6251	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6252				HR6251 (Lo-16bits)
HR6253	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6254				HR6253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6255	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6256				HR6255 (Lo-16bits)
HR6257	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6258				HR6257 (Lo-16bits)
HR6259	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6260				HR6259 (Lo-16bits)
HR6261	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6262				HR6261 (Lo-16bits)
HR6263	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6264				HR6263 (Lo-16bits)
HR6265	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6266				HR6265 (Lo-16bits)
HR6267	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6268				HR6267 (Lo-16bits)
HR6269	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6278	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6279	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6280	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6281	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

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TN00011

Holding Register	Parameter	Access	Length	
HR6282	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6283	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6284	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6285	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6286	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6287	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6288	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6289	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6290	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6291	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6292	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6293	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6307	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6331	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6334				HR6333 (Lo-16bits)
HR6335	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6336				HR6335 (Lo-16bits)
HR6337	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6338				HR6337 (Lo-16bits)
HR6339	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6340				HR6339 (Lo-16bits)
HR6341	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6342	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6343	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6344	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6345	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6346	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6347	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6348	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6349	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6350	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6351	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6352	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6353	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6354	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR6355	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6356	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6357	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6358	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6359	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6360	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6361	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6362	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6363	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6364	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6365	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6366	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6367	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6368	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6369	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6370	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6371	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6372	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6373	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6382				HR6381 (Lo-16bits)
HR6383	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6384				HR6383 (Lo-16bits)
HR6385	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6386				HR6385 (Lo-16bits)
HR6387	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6388				HR6387 (Lo-16bits)
HR6389	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6390				HR6389 (Lo-16bits)
HR6391	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6392				HR6391 (Lo-16bits)
HR6393	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6394				HR6393 (Lo-16bits)
HR6395	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6396				HR6395 (Lo-16bits)
HR6397	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6398				HR6397 (Lo-16bits)
HR6399	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6400				HR6399 (Lo-16bits)
HR6401	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6402				HR6401 (Lo-16bits)
HR6403	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6404				HR6403 (Lo-16bits)
HR6405	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6406				HR6405 (Lo-16bits)
HR6407	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6414				HR6413 (Lo-16bits)
HR6415	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)
HR6423	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6436				HR6435 (Lo-16bits)
HR6437	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6438	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6439	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6440	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6441	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6442	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6443	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6444	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6445	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6459	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6462	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6463	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6464	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6465	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6466	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6467	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6468	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6469	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6485	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6486	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6487	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6488	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6489	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6490	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6491	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6492	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6493	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6494	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6495	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6496	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6497	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6498	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6499	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6500	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6501	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6502	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

Holding Register	Parameter	Access	Length	
HR6503	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6504	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6505	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6506	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6507	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6508	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6509	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)
HR6527	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)
HR6535	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)
HR6551	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6590				HR6589 (Lo-16bits)
HR6591	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6592				HR6591 (Lo-16bits)
HR6593	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6594				HR6593 (Lo-16bits)
HR6595	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6596				HR6595 (Lo-16bits)
HR6597	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6598				HR6597 (Lo-16bits)
HR6599	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6600				HR6599 (Lo-16bits)
HR6601	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6602				HR6601 (Lo-16bits)
HR6603	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6604				HR6603 (Lo-16bits)
HR6605	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6606	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6607	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6608	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6609	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6610	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6611	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6612	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6613	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6625	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6654				HR6653 (Lo-16bits)
HR6655	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6656				HR6655 (Lo-16bits)
HR6657	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6658				HR6657 (Lo-16bits)
HR6659	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6660				HR6659 (Lo-16bits)
HR6661	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6662				HR6661 (Lo-16bits)
HR6663	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6664				HR6663 (Lo-16bits)
HR6665	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6666				HR6665 (Lo-16bits)
HR6667	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6668				HR6667 (Lo-16bits)
HR6669	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6670				HR6669 (Lo-16bits)
HR6671	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6672				HR6671 (Lo-16bits)
HR6673	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6674				HR6673 (Lo-16bits)
HR6675	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6676				HR6675 (Lo-16bits)
HR6677	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6678				HR6677 (Lo-16bits)
HR6679	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6680				HR6679 (Lo-16bits)
HR6681	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6682				HR6681 (Lo-16bits)
HR6683	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6684				HR6683 (Lo-16bits)
HR6685	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6694	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6695	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6696	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6697	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6698	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6699	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6700	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6701	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6702	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6703	DEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR6704	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6705	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6706	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6707	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6708	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6709	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6729	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6750				HR6749 (Lo-16bits)
HR6751	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6752				HR6751 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6753	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6754				HR6753 (Lo-16bits)
HR6755	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6756				HR6755 (Lo-16bits)
HR6757	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6758	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6759	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6760	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6761	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6762	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6763	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6764	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6765	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6766	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6767	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6768	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6769	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6770	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6771	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6772	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6773	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6774	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6775	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR6776	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6777	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6778	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6779	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6780	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6781	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6782	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6783	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6784	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6785	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6786	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6787	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6788	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6789	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6798				HR6797 (Lo-16bits)
HR6799	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6800				HR6799 (Lo-16bits)
HR6801	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6802				HR6801 (Lo-16bits)
HR6803	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6804				HR6803 (Lo-16bits)
HR6805	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6806				HR6805 (Lo-16bits)
HR6807	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6808				HR6807 (Lo-16bits)
HR6809	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6810				HR6809 (Lo-16bits)
HR6811	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6812				HR6811 (Lo-16bits)
HR6813	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6822				HR6821 (Lo-16bits)
HR6823	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6830				HR6829 (Lo-16bits)
HR6831	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)
HR6839	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6854	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6855	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6856	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6857	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6858	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6859	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6860	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6861	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6862				HR6861 (Lo-16bits)
HR6863	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6864				HR6863 (Lo-16bits)
HR6865	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6866				HR6865 (Lo-16bits)
HR6867	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6868				HR6867 (Lo-16bits)
HR6869	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6878	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6879	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6880	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6881	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6882	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6883	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6884	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6885	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6886				HR6885 (Lo-16bits)
HR6887	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6888				HR6887 (Lo-16bits)
HR6889	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6890				HR6889 (Lo-16bits)
HR6891	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6892				HR6891 (Lo-16bits)
HR6893	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6894				HR6893 (Lo-16bits)
HR6895	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6896				HR6895 (Lo-16bits)
HR6897	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6898				HR6897 (Lo-16bits)
HR6899	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6900				HR6899 (Lo-16bits)
HR6901	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6902	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6903	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6904	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6905	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6906	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6907	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6908	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6909	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6910	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6911	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6912	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6913	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6914	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6915	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6916	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6917	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6925	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)
HR6959	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6966	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6967	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6968	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6969	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6970	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6971	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6972	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6973	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6982				HR6981 (Lo-16bits)
HR6983	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6984				HR6983 (Lo-16bits)
HR6985	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6986				HR6985 (Lo-16bits)
HR6987	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6988				HR6987 (Lo-16bits)
HR6989	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6990	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6991	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6992	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6993	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6994	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6995	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6996	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6997	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6998	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6999	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7000	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7001	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7002	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7003	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7004	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7005	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7006	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7007	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7008	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7009	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7010	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7011	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7012	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7013	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7014	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7015	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7016	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7017	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7018	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7019	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7020	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7021	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7024				HR7023 (Lo-16bits)
HR7025	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)
HR7029	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)
HR7041	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7054				HR7053 (Lo-16bits)
HR7055	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)
HR7059	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7062				HR7061 (Lo-16bits)
HR7063	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7070				HR7069 (Lo-16bits)
HR7071	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7072				HR7071 (Lo-16bits)
HR7073	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7074				HR7073 (Lo-16bits)
HR7075	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7076				HR7075 (Lo-16bits)
HR7077	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7084				HR7083 (Lo-16bits)
HR7085	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7086	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7087	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7088	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7089	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7090	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7091	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7092	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7093	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7106				HR7105 (Lo-16bits)
HR7107	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7108				HR7107 (Lo-16bits)
HR7109	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7110	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7111	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7112	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7113	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7114	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7115	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7116	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7117	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7118	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7119	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7120	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7121	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7122	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7123	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7124	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7125	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7155	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7174	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7175	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7176	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7177	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7178	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7179	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7180	CUB2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7181	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7194				HR7193 (Lo-16bits)
HR7195	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7198	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7199	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7200	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7201	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7202	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7203	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7204	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7205	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7206	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7207	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7208	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7209	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7210	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7211	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7212	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7213	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7214	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7215	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7216	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7217	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7218	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7219	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7220	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7221	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7222	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7223	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7224	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7225	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7226	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7227	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7228	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7229	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7232				HR7231 (Lo-16bits)
HR7233	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7255	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7285	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7294	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7295	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7296	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7297	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7298	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7299	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7300	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7301	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7311	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7318	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7319	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7320	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7321	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7322	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7323	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7324	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7325	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7326	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7327	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7328	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7329	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7330	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7331	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7332	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7333	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7355	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7381	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7382	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7383	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7384	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7385	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7386	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7387	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7388	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7389	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7406	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7407	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7408	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7409	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7410	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7411	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7412	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7413	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7414	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7415	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7416	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7417	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7418	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7419	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7420	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7421	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7422	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7423	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7424	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7425	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7426	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7427	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7428	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7429	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7430	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7431	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7432	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7433	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7434	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7435	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7436	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7437	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7455	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7485	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7502	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7503	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7504	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7505	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7506	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7507	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7508	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7509	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7511	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7526	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7527	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7528	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7529	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7530	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7531	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7532	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7533	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7534	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7535	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7536	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7537	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7538	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7539	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7540	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7541	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7555	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)
HR7589	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7590	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7591	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7592	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7593	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7594	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7595	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7596	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7597	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7608				HR7607 (Lo-16bits)
HR7609	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7614	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7615	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7616	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7617	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7618	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7619	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7620	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7621	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7622	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7623	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7624	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7625	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7626	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7627	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7628	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7629	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7630	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7631	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7632	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7633	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7634	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7635	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7636	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7637	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7638	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7639	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7640	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7641	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7642	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7643	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7644	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7645	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7656				HR7655 (Lo-16bits)
HR7657	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7686				HR7685 (Lo-16bits)
HR7687	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7710	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7711	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7712	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7713	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7714	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7715	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7716	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7717	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)
HR7719	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7734	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7735	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7736	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7737	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7738	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR7739	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7740	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7741	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7742	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7743	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7744	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7745	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7746	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7747	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7748	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7749	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7750	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7751	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7752	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7753	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7754	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7755	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7756	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7757	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7758				HR7757 (Lo-16bits)
HR7759	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7760				HR7759 (Lo-16bits)
HR7761	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7762				HR7761 (Lo-16bits)
HR7763	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7774	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7775	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7776	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7777	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7778	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7779	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7780	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7781	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7788				HR7787 (Lo-16bits)
HR7789	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7816				HR7815 (Lo-16bits)
HR7817	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR7846	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7847	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7848	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7849	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7850	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7851	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7852	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7853	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7854	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7855	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7856	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7857	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7858	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7859	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7860	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7861	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7862	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7863	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7864	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7865	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7866	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7867	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7868	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7869	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7870	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7871	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7872	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7873	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7874	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7875	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7876	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7877	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7891	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7894	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7895	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7896	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7897	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7898	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7899	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7900	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7901	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7917	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7947	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7966	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7967	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7968	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7969	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7970	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7971	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7972	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7973	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7974	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7975	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7976	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7977	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7978	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7979	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7980	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7981	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7982	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7983	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7984	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7985	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7986	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7987	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7988	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7989	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7990	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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Holding Register	Parameter	Access	Length	
HR7991	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7992	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7993	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7994	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7995	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7996	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7997	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8014	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8015	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8016	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8017	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8018	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8019	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8020	OV3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR8021	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8032				HR8031 (Lo-16bits)
HR8033	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8034				HR8033 (Lo-16bits)
HR8035	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8036				HR8035 (Lo-16bits)
HR8037	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8038				HR8037 (Lo-16bits)
HR8039	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8047	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8077	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8086	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8087	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8088	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8089	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8090	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8091	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8092	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8093	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8094	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8095	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8096	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8097	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8098	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8099	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8100	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8101	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8102	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8103	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8104	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8105	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8106	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8107	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8108	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8109	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8110	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8111	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8112	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8113	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8114	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8115	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8116	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8117	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8122				HR8121 (Lo-16bits)
HR8123	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)
HR8127	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)
HR8131	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8134	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8135	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8136	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8137	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8138	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8139	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8140	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8141	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8150				HR8149 (Lo-16bits)
HR8151	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8170				HR8169 (Lo-16bits)
HR8171	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8172				HR8171 (Lo-16bits)
HR8173	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8179	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8206	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8207	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8208	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8209	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8210	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8211	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8212	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8213	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8214	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8215	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8216	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8217	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8218	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8219	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8220	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8221	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8222	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8223	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8224	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8225	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8226	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8227	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8228	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8229	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8230	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8231	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8232	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8233	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8234	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8235	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8236	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8237	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8253	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)
HR8257	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8264				HR8263 (Lo-16bits)
HR8265	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8266				HR8265 (Lo-16bits)
HR8267	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8268				HR8267 (Lo-16bits)
HR8269	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8270	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8271	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8272	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8273	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8274	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8275	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8276	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8277	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8282				HR8281 (Lo-16bits)
HR8283	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8310				HR8309 (Lo-16bits)
HR8311	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8340				HR8339 (Lo-16bits)
HR8341	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8342	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8343	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8344	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8345	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8346	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8347	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8348	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8349	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8350	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8351	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8352	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8353	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8354	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8355	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8356	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8357	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8358	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8359	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8360	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8361	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8362	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8363	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8364	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8365	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8366	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8367	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8368	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8369	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8370	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8371	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8372	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8373	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8383	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8398				HR8397 (Lo-16bits)
HR8399	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8400				HR8399 (Lo-16bits)
HR8401	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8402				HR8401 (Lo-16bits)
HR8403	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8404				HR8403 (Lo-16bits)
HR8405	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8406	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8407	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8408	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8409	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8410	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8411	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8412	UV2:Delay type	R(W)	1	0=DT;1=IDMT

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TN00011

Holding Register	Parameter	Access	Length	
HR8413	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8439	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8469	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8478	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8479	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8480	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8481	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8482	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8483	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8484	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8485	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8486	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8487	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8488	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8489	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8490	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8491	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8492	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8493	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8494	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8495	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8496	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8497	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8498	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8499	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8500	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8501	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8502	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8503	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8504	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8505	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8506	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8507	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8508	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8509	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8513	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)
HR8527	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)
HR8533	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8536				HR8535 (Lo-16bits)
HR8537	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8538				HR8537 (Lo-16bits)
HR8539	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8540				HR8539 (Lo-16bits)
HR8541	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8542	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8543	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8544	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8545	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8546	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8547	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8548	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8549	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8566				HR8565 (Lo-16bits)
HR8567	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8596				HR8595 (Lo-16bits)
HR8597	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)
HR8599	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8614	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8615	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8616	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8617	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8618	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8619	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8620	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8621	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8622	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8623	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8624	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8625	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8626	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8627	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8628	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8629	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8630	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8631	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8632	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8633	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8634	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8635	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8636	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8637	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8638	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8639	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8640	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8641	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8642	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8643	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8644	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8645	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8666				HR8665 (Lo-16bits)
HR8667	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8668				HR8667 (Lo-16bits)
HR8669	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8672				HR8671 (Lo-16bits)
HR8673	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8674				HR8673 (Lo-16bits)
HR8675	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8676				HR8675 (Lo-16bits)
HR8677	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8678	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8679	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8680	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8681	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8682	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8683	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8684	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8685	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)
HR8689	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8692				HR8691 (Lo-16bits)
HR8693	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8720				HR8719 (Lo-16bits)
HR8721	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)
HR8737	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8750	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8751	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8752	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8753	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8754	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8755	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8756	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8757	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8758	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8759	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8760	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8761	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8762	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8763	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8764	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8765	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8766	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8767	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8768	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8769	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8770	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8771	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8772	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8773	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8774	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8775	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8776	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8777	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8778	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8779	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8780	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8781	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8795	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8808				HR8807 (Lo-16bits)
HR8809	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8810				HR8809 (Lo-16bits)
HR8811	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8812				HR8811 (Lo-16bits)
HR8813	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8814	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8815	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8816	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8817	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8818	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8819	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8820	VUB1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR8821	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8847	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8850				HR8849 (Lo-16bits)
HR8851	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)
HR8853	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8877	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)
HR8879	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8886	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8887	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8888	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8889	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8890	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8891	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8892	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8893	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8894	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8895	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8896	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8897	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8898	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8899	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8900	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8901	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8902	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8903	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8904	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8905	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8906	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8907	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8908	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8909	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8910	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8911	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8912	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8913	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8914	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8915	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8916	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8917	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8918				HR8917 (Lo-16bits)
HR8919	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8920				HR8919 (Lo-16bits)
HR8921	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8922				HR8921 (Lo-16bits)
HR8923	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8924				HR8923 (Lo-16bits)
HR8925	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8926				HR8925 (Lo-16bits)
HR8927	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8928				HR8927 (Lo-16bits)
HR8929	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8930				HR8929 (Lo-16bits)
HR8931	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8932				HR8931 (Lo-16bits)
HR8933	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8934				HR8933 (Lo-16bits)
HR8935	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8936				HR8935 (Lo-16bits)
HR8937	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8938				HR8937 (Lo-16bits)
HR8939	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8940				HR8939 (Lo-16bits)
HR8941	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8942				HR8941 (Lo-16bits)
HR8943	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8944				HR8943 (Lo-16bits)
HR8945	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8946				HR8945 (Lo-16bits)
HR8947	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8948				HR8947 (Lo-16bits)
HR8949	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8950	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8951	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8952	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8953	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8954	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8955	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8956	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8957	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)
HR8961	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)
HR8973	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8974				HR8973 (Lo-16bits)
HR8975	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8982				HR8981 (Lo-16bits)
HR8983	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8984				HR8983 (Lo-16bits)
HR8985	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8986				HR8985 (Lo-16bits)
HR8987	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8988				HR8987 (Lo-16bits)
HR8989	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9004				HR9003 (Lo-16bits)
HR9005	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9022	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9023	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9024	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9025	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9026	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9027	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9028	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9029	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9030	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9031	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9032	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9033	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9034	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9035	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9036	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9037	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9038	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9039	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9040	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9041	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9042	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9043	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9044	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9045	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9046	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9047	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9048	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR9049	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9050	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9051	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9052	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9053	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9054				HR9053 (Lo-16bits)
HR9055	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9056				HR9055 (Lo-16bits)
HR9057	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9058				HR9057 (Lo-16bits)
HR9059	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9060				HR9059 (Lo-16bits)
HR9061	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9062				HR9061 (Lo-16bits)
HR9063	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9064				HR9063 (Lo-16bits)
HR9065	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9066				HR9065 (Lo-16bits)
HR9067	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9068				HR9067 (Lo-16bits)
HR9069	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9070				HR9069 (Lo-16bits)
HR9071	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9072				HR9071 (Lo-16bits)
HR9073	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9074				HR9073 (Lo-16bits)
HR9075	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9076				HR9075 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9077	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9078				HR9077 (Lo-16bits)
HR9079	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9080				HR9079 (Lo-16bits)
HR9081	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9082				HR9081 (Lo-16bits)
HR9083	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9084				HR9083 (Lo-16bits)
HR9085	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9086	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9087	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9088	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9089	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9090	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9091	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9092	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9093	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9103	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9118				HR9117 (Lo-16bits)
HR9119	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9120				HR9119 (Lo-16bits)
HR9121	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9122				HR9121 (Lo-16bits)
HR9123	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9124				HR9123 (Lo-16bits)
HR9125	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9132				HR9131 (Lo-16bits)
HR9133	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)
HR9145	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9158	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9159	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9160	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9161	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9162	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9163	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9164	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9165	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9166	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9167	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9168	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9169	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9170	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9171	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9172	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9173	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9174	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9175	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9176	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9177	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9178	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9179	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9180	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9181	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9182	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9183	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9184	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9185	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9186	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9187	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9188	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9189	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9194				HR9193 (Lo-16bits)
HR9195	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9196				HR9195 (Lo-16bits)
HR9197	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9198				HR9197 (Lo-16bits)
HR9199	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9200				HR9199 (Lo-16bits)
HR9201	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9202				HR9201 (Lo-16bits)
HR9203	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9204				HR9203 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9205	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9206				HR9205 (Lo-16bits)
HR9207	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9208				HR9207 (Lo-16bits)
HR9209	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9210				HR9209 (Lo-16bits)
HR9211	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9216				HR9215 (Lo-16bits)
HR9217	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9218				HR9217 (Lo-16bits)
HR9219	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9220				HR9219 (Lo-16bits)
HR9221	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9222	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9223	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9224	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9225	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9226	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9227	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9228	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9229	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9231	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)
HR9245	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9258				HR9257 (Lo-16bits)
HR9259	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9262				HR9261 (Lo-16bits)
HR9263	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9264				HR9263 (Lo-16bits)
HR9265	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9266				HR9265 (Lo-16bits)
HR9267	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9268				HR9267 (Lo-16bits)
HR9269	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9288				HR9287 (Lo-16bits)
HR9289	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9294	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9295	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9296	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9297	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9298	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9299	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9300	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9301	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9302	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9303	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9304	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9305	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9306	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9307	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9308	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9309	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9310	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9311	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9312	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9313	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9314	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9315	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9316	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9317	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9318	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9319	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9320	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9321	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9322	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9323	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9324	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9325	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9326	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9327	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9328	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9329	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9330	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9331	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9332	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9333	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9361	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9366	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9367	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9368	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9369	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9370	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9371	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9372	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9373	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9391	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9406	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9407	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9408	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9409	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9410	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9411	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9412	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9413	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9421	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9446	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9447	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9448	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9449	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9450	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9451	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9452	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9453	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9464				HR9463 (Lo-16bits)
HR9465	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9466				HR9465 (Lo-16bits)
HR9467	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9468				HR9467 (Lo-16bits)
HR9469	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9481	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9486	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9487	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9488	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9489	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9490	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9491	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9492	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9493	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9511	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9526	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9527	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9528	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9529	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9530	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9531	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9532	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9533	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9541	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9566	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9567	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9568	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9569	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9570	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9571	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9572	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9573	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9580				HR9579 (Lo-16bits)
HR9581	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9582				HR9581 (Lo-16bits)
HR9583	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9584				HR9583 (Lo-16bits)
HR9585	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9586				HR9585 (Lo-16bits)
HR9587	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9588				HR9587 (Lo-16bits)
HR9589	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9590				HR9589 (Lo-16bits)
HR9591	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9592				HR9591 (Lo-16bits)
HR9593	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9594				HR9593 (Lo-16bits)
HR9595	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9596				HR9595 (Lo-16bits)
HR9597	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9598				HR9597 (Lo-16bits)
HR9599	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9601	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9602				HR9601 (Lo-16bits)
HR9603	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9604				HR9603 (Lo-16bits)
HR9605	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9606	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9607	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9608	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9609	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9610	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9611	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9612	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9613	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9614				HR9613 (Lo-16bits)
HR9615	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9616				HR9615 (Lo-16bits)
HR9617	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9618				HR9617 (Lo-16bits)
HR9619	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9620				HR9619 (Lo-16bits)
HR9621	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9622				HR9621 (Lo-16bits)
HR9623	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9624				HR9623 (Lo-16bits)
HR9625	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9626				HR9625 (Lo-16bits)
HR9627	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9628				HR9627 (Lo-16bits)
HR9629	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9630				HR9629 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9631	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9632				HR9631 (Lo-16bits)
HR9633	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9634				HR9633 (Lo-16bits)
HR9635	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9636				HR9635 (Lo-16bits)
HR9637	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9638				HR9637 (Lo-16bits)
HR9639	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9640				HR9639 (Lo-16bits)
HR9641	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9642				HR9641 (Lo-16bits)
HR9643	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9644				HR9643 (Lo-16bits)
HR9645	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9660				HR9659 (Lo-16bits)
HR9661	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9689	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9691	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9692	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9693	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9694	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9695	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9696	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9697	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9698	CUB1:I2> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9699	CUB2:I2>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9700	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9701	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9702	OV1:U> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9703	OV2:U>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9704	OV3:U>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9705	OV4:U>>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9706	UV1:U< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9707	UV2:U<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9708	UV3:U<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9709	UV4:U<<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9710	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9711	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9712	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9713	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9714	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled;1=Activated
HR9715	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9743	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9750				HR9749 (Lo-16bits)
HR9751	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9752				HR9751 (Lo-16bits)
HR9753	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9754				HR9753 (Lo-16bits)
HR9755	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9769	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	DIGI:DIs (cpu)	R	2	0b=DI1;1b=DI2;2b=DI3; (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	DIGI:DIs (input option 1)	R	2	0b=DI4;1b=DI5;2b=DI6;3b=DI7;4b=DI8;5b=DI9;6b=DI10;7b=DI1
HR9774				HR9773 (Lo-16bits)
HR9775	DIGI:DIs (input option 2)	R	2	0b=DI12;1b=DI13;2b=DI14;3b=DI15;4b=DI16;5b=DI17;6b=DI18
HR9776				HR9775 (Lo-16bits)
HR9777	DIGI:DIs (input option 3)	R	2	0b=DI20;1b=DI21;2b=DI22;3b=DI23;4b=DI24;5b=DI25;6b=DI26
HR9778				HR9777 (Lo-16bits)
HR9779	DIGI:DIs (input option 4)	R	2	0b=D28;1b=DI29;2b=DI30;3b=DI31;4b=DI32;5b=DI33;6b=DI34;
HR9780				HR9779 (Lo-16bits)
HR9781	DIGI:DIs (input option 5)	R	2	0b=DI36;1b=DI37;2b=DI38;3b=DI39;4b=DI40;5b=DI41;6b=DI42
HR9782				HR9781 (Lo-16bits)
HR9783	DIGI:DIs (input option 6)	R	2	0b=DI44;1b=DI45;2b=DI46;3b=DI47;4b=DI48;5b=DI49;6b=DI50
HR9784				HR9783 (Lo-16bits)
HR9785	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9797	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9802	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9803	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled;1=Activated
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

1.4 R Modbus map

The following table lists modbus holding registers for AQ-R215 devices with firmware R_v2.1.1.3-233.

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.4 - 4. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

Holding Register	Parameter	Access	Length	
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pri.Res.curr.I01 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pri.Res.curr.I02 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pri.Pha.curr.IL1 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Pha.curr.IL2 3h.	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Pri.Pha.curr.IL3 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:Pri.Res.curr.I01 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pri.Res.curr.I02 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR67				HR66 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR68	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR97				HR96 (Lo-16bits)
HR98	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR456	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR481				HR480 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR482	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U1 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:U1 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:U1 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:U2 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:U2 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS3:U2 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:U3 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U3 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U3 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U4 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U4 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U4 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U1 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:U1 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U1 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U2 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U2 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:U2 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U3 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U3 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U3 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U4 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U4 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:U4 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR901				HR900 (Lo-16bits)
HR902	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR929				HR928 (Lo-16bits)
HR930	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR953				HR952 (Lo-16bits)
HR954	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR977				HR976 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR978	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1187				HR1186 (Lo-16bits)
HR1188	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1217				HR1216 (Lo-16bits)
HR1218	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)
HR1228	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1229				HR1228 (Lo-16bits)
HR1230	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)
HR1232	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)
HR1234	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1244	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)
HR1252	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)
HR1256	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)
HR1258	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1259				HR1258 (Lo-16bits)
HR1260	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1268	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)
HR1276	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1279				HR1278 (Lo-16bits)
HR1280	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1281				HR1280 (Lo-16bits)
HR1282	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)
HR1288	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1291				HR1290 (Lo-16bits)
HR1292	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	POW1:Reset 3ph Energies	RW	1	0=-;1=Reset
HR1301	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1302				HR1301 (Lo-16bits)
HR1303	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1304				HR1303 (Lo-16bits)
HR1305	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1306				HR1305 (Lo-16bits)
HR1307	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1308				HR1307 (Lo-16bits)
HR1309	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1310				HR1309 (Lo-16bits)
HR1311	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1312				HR1311 (Lo-16bits)
HR1313	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1314				HR1313 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1315	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1316				HR1315 (Lo-16bits)
HR1317	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1338				HR1337 (Lo-16bits)
HR1339	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1340				HR1339 (Lo-16bits)
HR1341	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1342				HR1341 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1343	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1344				HR1343 (Lo-16bits)
HR1345	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1346				HR1345 (Lo-16bits)
HR1347	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1348				HR1347 (Lo-16bits)
HR1349	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1350				HR1349 (Lo-16bits)
HR1351	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1352				HR1351 (Lo-16bits)
HR1353	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1354				HR1353 (Lo-16bits)
HR1355	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1356				HR1355 (Lo-16bits)
HR1357	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1358				HR1357 (Lo-16bits)
HR1359	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	DIGI:Dis 1-32	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7;7b=DI8; (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	DIGI:Dis 33-64	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38;6b=DI39
HR1366				HR1365 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1367	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b=O8;8b=
HR1368				HR1367 (Lo-16bits)
HR1369	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8
HR1370	SGS:Force SG	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1371	SGS:Force SG change	RW	1	0=Disabled;1=Enabled
HR1372	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=SG1...6;6=
HR1373	SGS:Remote SG change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1374	CTRL:Local/Remote status	R	1	0=Remote;1=Local
HR1375	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)
HR1381	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1382				HR1381 (Lo-16bits)
HR1383	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1393	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1402				HR1401 (Lo-16bits)
HR1403	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)
HR1405	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)
HR1409	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1410				HR1409 (Lo-16bits)
HR1411	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1417	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)
HR1419	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1426	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1427				HR1426 (Lo-16bits)
HR1428	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1429	NOC1:I> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR1430	NOC1:I> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1431	NOC1:I> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1432	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1433				HR1432 (Lo-16bits)
HR1434	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1435				HR1434 (Lo-16bits)
HR1436	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1437				HR1436 (Lo-16bits)
HR1438	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1439				HR1438 (Lo-16bits)
HR1440	NOC1:Delay type	RW	1	0=DT;1=IDMT
HR1441	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1442				HR1441 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1443	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1444	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1445	NOC1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1446	NOC1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1447	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1448				HR1447 (Lo-16bits)
HR1449	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1450				HR1449 (Lo-16bits)
HR1451	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1452				HR1451 (Lo-16bits)
HR1453	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1454				HR1453 (Lo-16bits)
HR1455	NOC1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1456	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1457	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1458	NOC2:I>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1459	NOC2:I>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1460	NOC2:I>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1461	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1462				HR1461 (Lo-16bits)
HR1463	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1464				HR1463 (Lo-16bits)
HR1465	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1466				HR1465 (Lo-16bits)
HR1467	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1469	NOC2:Delay type	RW	1	0=DT;1=IDMT
HR1470	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1471				HR1470 (Lo-16bits)
HR1472	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1473	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1474	NOC2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1475	NOC2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1476	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1477				HR1476 (Lo-16bits)
HR1478	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1479				HR1478 (Lo-16bits)
HR1480	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1481				HR1480 (Lo-16bits)
HR1482	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1483				HR1482 (Lo-16bits)
HR1484	NOC2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1485	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1486	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1487	NOC3:I>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1488	NOC3:I>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1489	NOC3:I>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1490	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1491				HR1490 (Lo-16bits)
HR1492	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1493				HR1492 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1494	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1495				HR1494 (Lo-16bits)
HR1496	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1497				HR1496 (Lo-16bits)
HR1498	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR1499	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1500				HR1499 (Lo-16bits)
HR1501	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1502	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1503	NOC3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1504	NOC3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1505	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1506				HR1505 (Lo-16bits)
HR1507	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1508				HR1507 (Lo-16bits)
HR1509	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1510				HR1509 (Lo-16bits)
HR1511	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1512				HR1511 (Lo-16bits)
HR1513	NOC3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1514	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1515	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1516	NOC4:I>>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1517	NOC4:I>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1518	NOC4:I>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1519	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1520				HR1519 (Lo-16bits)
HR1521	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1524				HR1523 (Lo-16bits)
HR1525	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR1528	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1529				HR1528 (Lo-16bits)
HR1530	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1531	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1532	NOC4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1533	NOC4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1534	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1535				HR1534 (Lo-16bits)
HR1536	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1539				HR1538 (Lo-16bits)
HR1540	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1541				HR1540 (Lo-16bits)
HR1542	NOC4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1543	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1544	ROC1:Idrw> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1545	ROC1:Idrw> Current direction	R	1	0=Undefined;1=Forward;2=Reverse

Holding Register	Parameter	Access	Length	
HR1546	ROC2:Idrw>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1547	ROC2:Idrw>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1548	ROC3:Idrw>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1549	ROC3:Idrw>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1550	ROC4:Idrw>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1551	ROC4:Idrw>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1552	ROC5:Idrw>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1553	ROC5:Idrw>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1554	ROC6:Idrw>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1555	ROC6:Idrw>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1556	ROC7:Idrw>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1557	ROC7:Idrw>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1558	ROC8:Idrw>>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1559	ROC8:Idrw>>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR1560	ROV1:Urw>/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1561	ROV2:Urw>>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1562	ROV3:Urw>>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1563	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1564	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=Start On (Alarm);3=Start On (Alarm & Tr
HR1565	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1566	DOC1:Idir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1567	DOC1:Idir> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1568	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1569				HR1568 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1570	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1571				HR1570 (Lo-16bits)
HR1572	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1575				HR1574 (Lo-16bits)
HR1576	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1577	DOC2:Idir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1578	DOC2:Idir>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1579	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1584				HR1583 (Lo-16bits)
HR1585	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1588	DOC3:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1589	DOC3:Idir>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1590	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1593				HR1592 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1594	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1595				HR1594 (Lo-16bits)
HR1596	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1597				HR1596 (Lo-16bits)
HR1598	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1599	DOC4:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1600	DOC4:Idir>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1601	DOC4:Idir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1602				HR1601 (Lo-16bits)
HR1603	DOC4:Idir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	DOC4:Idir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1606				HR1605 (Lo-16bits)
HR1607	DOC4:Idir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1608				HR1607 (Lo-16bits)
HR1609	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1610	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1611	NEF1:I0> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1612	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR1617	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1618				HR1617 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1619	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1620	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1621	NEF1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1622	NEF1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1623	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1624				HR1623 (Lo-16bits)
HR1625	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1626				HR1625 (Lo-16bits)
HR1627	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1630				HR1629 (Lo-16bits)
HR1631	NEF1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1632	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1633	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1634	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1635	NEF2:I0>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1636	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1639				HR1638 (Lo-16bits)
HR1640	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR1641	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1644	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR1645	NEF2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1646	NEF2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1647	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	NEF2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1656	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1657	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1658	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1659	NEF3:I0>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1660	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1661				HR1660 (Lo-16bits)
HR1662	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1663				HR1662 (Lo-16bits)
HR1664	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR1665	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1668	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1669	NEF3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1670	NEF3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR1671	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	NEF3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1680	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1681	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1682	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1683	NEF4:I0>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1684	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1685				HR1684 (Lo-16bits)
HR1686	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1687				HR1686 (Lo-16bits)
HR1688	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR1689	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1690				HR1689 (Lo-16bits)
HR1691	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1692	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1693	NEF4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1694	NEF4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1695	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1698				HR1697 (Lo-16bits)
HR1699	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	NEF4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR1704	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR1705	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1706	DEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1707	DEF1:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1708	DEF1:I0dir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1709	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)
HR1713	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1716	DEF2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1717	DEF2:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1718	DEF2:I0dir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1719	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR1720				HR1719 (Lo-16bits)
HR1721	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1722				HR1721 (Lo-16bits)
HR1723	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1724				HR1723 (Lo-16bits)
HR1725	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1726	DEF3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1727	DEF3:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1728	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1729	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1732				HR1731 (Lo-16bits)
HR1733	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1736	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1737	DEF4:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1738	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1739	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1742				HR1741 (Lo-16bits)
HR1743	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1744				HR1743 (Lo-16bits)
HR1745	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1746	IEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1747	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1748	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR1749	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1750	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1751	HOC1:Ih> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1752	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1753				HR1752 (Lo-16bits)
HR1754	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1755				HR1754 (Lo-16bits)
HR1756	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1757				HR1756 (Lo-16bits)
HR1758	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1759				HR1758 (Lo-16bits)
HR1760	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1761				HR1760 (Lo-16bits)
HR1762	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1763				HR1762 (Lo-16bits)
HR1764	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1767				HR1766 (Lo-16bits)
HR1768	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1769				HR1768 (Lo-16bits)
HR1770	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	HOC2:Ih>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1773	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1776				HR1775 (Lo-16bits)
HR1777	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1778				HR1777 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1779	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1780				HR1779 (Lo-16bits)
HR1781	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1784				HR1783 (Lo-16bits)
HR1785	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1790				HR1789 (Lo-16bits)
HR1791	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)
HR1793	HOC3:Ih>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1794	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1795				HR1794 (Lo-16bits)
HR1796	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1801				HR1800 (Lo-16bits)
HR1802	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1803				HR1802 (Lo-16bits)
HR1804	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1805				HR1804 (Lo-16bits)
HR1806	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1807				HR1806 (Lo-16bits)
HR1808	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1809				HR1808 (Lo-16bits)
HR1810	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1811				HR1810 (Lo-16bits)
HR1812	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1813				HR1812 (Lo-16bits)
HR1814	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1815	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4=CLPU b
HR1836	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1837	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1838	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloaded;4=C

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Holding Register	Parameter	Access	Length	
HR1839	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Blocked;4b=2
HR1840				HR1839 (Lo-16bits)
HR1841	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1846	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1847	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1848	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1849	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1850	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1851	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1852	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1853	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1854	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1855	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1856	TOLF1:Temp C or F deg	RW	1	0=C;1=F
HR1857	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1858	TOLF1:RTD Sensor	R	1	0=Invalid;1=OK
HR1859	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set;1=Estimate
HR1860	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1861	TOLM1:TM> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR1862	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR1863	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR1864	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR1865	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=Running
HR1866	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1867	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1868	TOLM1:Time constants	RW	1	0=Single;1=Multiple

Holding Register	Parameter	Access	Length	
HR1869	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1870	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1871	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1872	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1873	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1874	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Blocked
HR1875	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1876	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1877	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1878	OV1:U> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1879	OV2:U>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1880	OV3:U>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1881	OV4:U>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1882	UV1:U< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1883	UV2:U<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1884	UV3:U<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1885	UV4:U<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1886	NOV1:U0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1887	NOV1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1888	NOV2:U0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1889	NOV2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1890	NOV3:U0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1891	NOV3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1892	NOV4:U0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1893	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1894	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1895	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR1896	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1897	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1898	PGS1:PS1 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1899	PGS1:PS2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1900	PGS1:PS3 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1901	PGS1:PS4 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1902	PGS1:PS5 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1903	PGS1:PS6 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1904	PGS1:PS7 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1905	PGS1:PS8 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1906	PGS1:PS9 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1907	PGS1:PS10 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1908	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1909	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1910	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1911	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1912	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1913	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1914	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1915	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1916	FRQV1:f> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1917	FRQV1:f>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1918	FRQV1:f>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1919	FRQV1:f>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1920	FRQV1:f< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1921	FRQV1:f<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1922	FRQV1:f<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

Holding Register	Parameter	Access	Length	
HR1923	FRQV1:f<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1924	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1925	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1926	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1927	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1928	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1929	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1930	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1931	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1932	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR1933	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On high overloa
HR1936	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1937	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1938	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1939	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1940	OPW1:P> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1941	UPW1:P< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1942	RPW1:Prev> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1943	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1944	SEF1:U03rd< Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1945	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1946	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1947	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1948	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1949	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1950	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=VHz(1) Blocked;2=VHz(1) Start;3=VHz(1) Alarm;4=

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Holding Register	Parameter	Access	Length	
HR1951	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=VHz(2) Start;2=VHz(2) Alarm;3=VHz(2) Trip;4=VHz(
HR1952	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=Alarm
HR1953	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1954				HR1953 (Lo-16bits)
HR1955	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1958	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1959				HR1958 (Lo-16bits)
HR1960	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1961				HR1960 (Lo-16bits)
HR1962	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1965	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1972	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1973				HR1972 (Lo-16bits)
HR1974	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1975				HR1974 (Lo-16bits)
HR1976	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1977				HR1976 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1978	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1979	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1982				HR1981 (Lo-16bits)
HR1983	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1984				HR1983 (Lo-16bits)
HR1985	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1986	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1987				HR1986 (Lo-16bits)
HR1988	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1989				HR1988 (Lo-16bits)
HR1990	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1991				HR1990 (Lo-16bits)
HR1992	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR1993	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1994				HR1993 (Lo-16bits)
HR1995	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1996				HR1995 (Lo-16bits)
HR1997	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1998				HR1997 (Lo-16bits)
HR1999	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR2000				HR1999 (Lo-16bits)
HR2001	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2002	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2003				HR2002 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2004	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2005				HR2004 (Lo-16bits)
HR2006	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2007				HR2006 (Lo-16bits)
HR2008	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR2009				HR2008 (Lo-16bits)
HR2010	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2011	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2018	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2019				HR2018 (Lo-16bits)
HR2020	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2021				HR2020 (Lo-16bits)
HR2022	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2023				HR2022 (Lo-16bits)
HR2024	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2025	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2030				HR2029 (Lo-16bits)
HR2031	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2032	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2033				HR2032 (Lo-16bits)
HR2034	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2035				HR2034 (Lo-16bits)
HR2036	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2037				HR2036 (Lo-16bits)
HR2038	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2039	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2044				HR2043 (Lo-16bits)
HR2045	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2046	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2047				HR2046 (Lo-16bits)
HR2048	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2049				HR2048 (Lo-16bits)
HR2050	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2051				HR2050 (Lo-16bits)
HR2052	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2053	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2054				HR2053 (Lo-16bits)
HR2055	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2056				HR2055 (Lo-16bits)
HR2057	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)
HR2059	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)
HR2061	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2062	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2063				HR2062 (Lo-16bits)
HR2064	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2065				HR2064 (Lo-16bits)
HR2066	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2067				HR2066 (Lo-16bits)
HR2068	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR2069				HR2068 (Lo-16bits)
HR2070	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2071	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2072				HR2071 (Lo-16bits)
HR2073	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2074				HR2073 (Lo-16bits)
HR2075	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2081	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)
HR2091	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2092				HR2091 (Lo-16bits)
HR2093	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2094				HR2093 (Lo-16bits)
HR2095	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2096				HR2095 (Lo-16bits)
HR2097	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2098				HR2097 (Lo-16bits)
HR2099	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2105	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2116				HR2115 (Lo-16bits)
HR2117	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2118				HR2117 (Lo-16bits)
HR2119	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2120				HR2119 (Lo-16bits)
HR2121	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2122				HR2121 (Lo-16bits)
HR2123	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2124				HR2123 (Lo-16bits)
HR2125	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2126				HR2125 (Lo-16bits)
HR2127	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2128				HR2127 (Lo-16bits)
HR2129	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2130				HR2129 (Lo-16bits)
HR2131	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2134				HR2133 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2135	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2136				HR2135 (Lo-16bits)
HR2137	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2138				HR2137 (Lo-16bits)
HR2139	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2140				HR2139 (Lo-16bits)
HR2141	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2142				HR2141 (Lo-16bits)
HR2143	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2144				HR2143 (Lo-16bits)
HR2145	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2146				HR2145 (Lo-16bits)
HR2147	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2148				HR2147 (Lo-16bits)
HR2149	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2150				HR2149 (Lo-16bits)
HR2151	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2152				HR2151 (Lo-16bits)
HR2153	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2154				HR2153 (Lo-16bits)
HR2155	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2158				HR2157 (Lo-16bits)
HR2159	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2165	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2178				HR2177 (Lo-16bits)
HR2179	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2180				HR2179 (Lo-16bits)
HR2181	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2182				HR2181 (Lo-16bits)
HR2183	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2184				HR2183 (Lo-16bits)
HR2185	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2186				HR2185 (Lo-16bits)
HR2187	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2195	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)
HR2201	ARC1:I/I0 Arc> condition	R	1	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4b=Z3 Trip
HR2202	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR2203	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR2204	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTBusfail;3=VTLinefail;4=Blocked
HR2205	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live VTS Ok SE
HR2206	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR2207	THD1:THD condition	R	1	0=Normal;1=Start;2=Alarm;3=Blocked
HR2208	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On
HR2209	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo cmd;5=BK
HR2210	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR2211	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2212				HR2211 (Lo-16bits)
HR2213	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2214				HR2213 (Lo-16bits)
HR2215	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2218				HR2217 (Lo-16bits)
HR2219	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2220				HR2219 (Lo-16bits)
HR2221	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=SYN1 Vco
HR2222				HR2221 (Lo-16bits)
HR2223	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=SYN2 Vco
HR2224				HR2223 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2225	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=SYN3 Vco
HR2226				HR2225 (Lo-16bits)
HR2227	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR2228				HR2227 (Lo-16bits)
HR2229	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR2230				HR2229 (Lo-16bits)
HR2231	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR2232				HR2231 (Lo-16bits)
HR2233	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked:2=Trip;3=Alarm
HR2234	VJP1:Voltage meas selected	R	1	0=Selection Ok;1=Selection not available
HR2235	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2236	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2237	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2238	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2239	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2240	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2241	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2242	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2243	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2244	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2245	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2246	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2247	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad

Holding Register	Parameter	Access	Length	
HR2248	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2249	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2250	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled;1=Activated
HR2251	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR is runni Requested;14b=AR4 Requested;15b=AR5 Requested;16b=Exec
HR2252				HR2251 (Lo-16bits)
HR2253	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Delay;4=AR
HR2254	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set higher than U
HR2255				HR2254 (Lo-16bits)
HR2256	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Operation bl
HR2257				HR2256 (Lo-16bits)
HR2258	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low set Decre
HR2259	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	VRG1:Voltage meas condition	R	1	0=Not configured;1=System U12;2=System U23;3=System U31;
HR2264	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR2265				HR2264 (Lo-16bits)
HR2266	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR2267				HR2266 (Lo-16bits)
HR2268	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR2269	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher
HR2270	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2271				HR2270 (Lo-16bits)
HR2272	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2273				HR2272 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2274	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2275				HR2274 (Lo-16bits)
HR2276	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2277				HR2276 (Lo-16bits)
HR2278	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2279				HR2278 (Lo-16bits)
HR2280	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2281				HR2280 (Lo-16bits)
HR2282	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2283				HR2282 (Lo-16bits)
HR2284	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2285				HR2284 (Lo-16bits)
HR2286	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2287				HR2286 (Lo-16bits)
HR2288	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2289				HR2288 (Lo-16bits)
HR2290	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2291				HR2290 (Lo-16bits)
HR2292	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2293				HR2292 (Lo-16bits)
HR2294	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2295				HR2294 (Lo-16bits)
HR2296	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2297				HR2296 (Lo-16bits)
HR2298	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2299				HR2298 (Lo-16bits)
HR2300	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2301				HR2300 (Lo-16bits)
HR2302	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2303				HR2302 (Lo-16bits)
HR2304	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2305				HR2304 (Lo-16bits)
HR2306	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2307				HR2306 (Lo-16bits)
HR2308	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2309				HR2308 (Lo-16bits)
HR2310	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2311				HR2310 (Lo-16bits)
HR2312	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2313				HR2312 (Lo-16bits)
HR2314	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2315				HR2314 (Lo-16bits)
HR2316	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2317				HR2316 (Lo-16bits)
HR2318	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2319				HR2318 (Lo-16bits)
HR2320	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2321				HR2320 (Lo-16bits)
HR2322	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2323				HR2322 (Lo-16bits)
HR2324	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2328	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2331				HR2330 (Lo-16bits)
HR2332	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2337				HR2336 (Lo-16bits)
HR2338	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2339				HR2338 (Lo-16bits)
HR2340	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2343				HR2342 (Lo-16bits)
HR2344	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2345				HR2344 (Lo-16bits)
HR2346	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLT detected
HR2351	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLT detected
HR2352	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLT detected
HR2353	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLT detected

Holding Register	Parameter	Access	Length	
HR2354	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLT FW;2=FLT REV
HR2355	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=No voltage
HR2356	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLT detected
HR2357	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLT detected
HR2358	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=A8;8b=A
HR2359				HR2358 (Lo-16bits)
HR2360	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=A39;7b= (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	GSYN:Synchronizer mode	RW	1	0=Disabled;1=Activated
HR2363	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR2364				HR2363 (Lo-16bits)
HR2365	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR2366				HR2365 (Lo-16bits)
HR2367	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR2368				HR2367 (Lo-16bits)
HR2369	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR2370				HR2369 (Lo-16bits)
HR2371	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR2372				HR2371 (Lo-16bits)
HR2373	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR2374				HR2373 (Lo-16bits)
HR2375	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR2376				HR2375 (Lo-16bits)
HR2377	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR2378				HR2377 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2379	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR2380				HR2379 (Lo-16bits)
HR2381	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR2382				HR2381 (Lo-16bits)
HR2383	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR2384				HR2383 (Lo-16bits)
HR2385	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR2386				HR2385 (Lo-16bits)
HR2387	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)
HR2391	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)
HR2393	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2402				HR2401 (Lo-16bits)
HR2403	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2404				HR2403 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2405	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2406				HR2405 (Lo-16bits)
HR2407	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2408				HR2407 (Lo-16bits)
HR2409	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2410				HR2409 (Lo-16bits)
HR2411	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2412				HR2411 (Lo-16bits)
HR2413	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2414				HR2413 (Lo-16bits)
HR2415	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2416				HR2415 (Lo-16bits)
HR2417	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2418				HR2417 (Lo-16bits)
HR2419	VREC:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR2420	VREC:Scale current values to primary	RW	1	0=No;1=Yes
HR2421	VREC:Record fault types (Overcurrrent & Voltage Over/Under) faults	RW	1	0=No;1=Yes
HR2422	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2430	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR2437				HR2436 (Lo-16bits)
HR2438	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR2439				HR2438 (Lo-16bits)
HR2440	RTB:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR2441	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR2442	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2443				HR2442 (Lo-16bits)
HR2444	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)
HR2446	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2456	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2481				HR2480 (Lo-16bits)
HR2482	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	TOLT1:TT> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2495	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2496	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2497	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2498	TOLF1:TF> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2499	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2500	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2501	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2502	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2507				HR2506 (Lo-16bits)
HR2508	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2515				HR2514 (Lo-16bits)
HR2516	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2517				HR2516 (Lo-16bits)
HR2518	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2519				HR2518 (Lo-16bits)
HR2520	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2521				HR2520 (Lo-16bits)
HR2522	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2523				HR2522 (Lo-16bits)
HR2524	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2525				HR2524 (Lo-16bits)
HR2526	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2527				HR2526 (Lo-16bits)
HR2528	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2529				HR2528 (Lo-16bits)
HR2530	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2531				HR2530 (Lo-16bits)
HR2532	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2533				HR2532 (Lo-16bits)
HR2534	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2535				HR2534 (Lo-16bits)
HR2536	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2537				HR2536 (Lo-16bits)
HR2538	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2539				HR2538 (Lo-16bits)
HR2540	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2541				HR2540 (Lo-16bits)
HR2542	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2543				HR2542 (Lo-16bits)
HR2544	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2545				HR2544 (Lo-16bits)
HR2546	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2547				HR2546 (Lo-16bits)
HR2548	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2549				HR2548 (Lo-16bits)
HR2550	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2551				HR2550 (Lo-16bits)
HR2552	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2553				HR2552 (Lo-16bits)
HR2554	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2555				HR2554 (Lo-16bits)
HR2556	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2557				HR2556 (Lo-16bits)
HR2558	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2559				HR2558 (Lo-16bits)
HR2560	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2561				HR2560 (Lo-16bits)
HR2562	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2563				HR2562 (Lo-16bits)
HR2564	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2565				HR2564 (Lo-16bits)
HR2566	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2567				HR2566 (Lo-16bits)
HR2568	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2569				HR2568 (Lo-16bits)
HR2570	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2571				HR2570 (Lo-16bits)
HR2572	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2573				HR2572 (Lo-16bits)
HR2574	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR2575	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR2576	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2577				HR2576 (Lo-16bits)
HR2578	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2579				HR2578 (Lo-16bits)
HR2580	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2581				HR2580 (Lo-16bits)
HR2582	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2583				HR2582 (Lo-16bits)
HR2584	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2585				HR2584 (Lo-16bits)
HR2586	ARC1:I/I0 Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2587				HR2586 (Lo-16bits)
HR2588	ARC1:I/I0 Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2589				HR2588 (Lo-16bits)
HR2590	ARC1:I/I0 Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2591				HR2590 (Lo-16bits)
HR2592	ARC1:I/I0 Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2593				HR2592 (Lo-16bits)
HR2594	ARC1:I/I0 Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2595				HR2594 (Lo-16bits)
HR2596	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2597				HR2596 (Lo-16bits)
HR2598	DIGI:DI1 Counter Clear	RW	1	0=-;1=Clear
HR2599	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2600				HR2599 (Lo-16bits)
HR2601	DIGI:DI2 Counter Clear	RW	1	0=-;1=Clear
HR2602	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2603				HR2602 (Lo-16bits)
HR2604	DIGI:DI3 Counter Clear	RW	1	0=-;1=Clear
HR2605	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2606				HR2605 (Lo-16bits)
HR2607	DIGI:DI4 Counter Clear	RW	1	0=-;1=Clear
HR2608	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2609				HR2608 (Lo-16bits)
HR2610	DIGI:DI5 Counter Clear	RW	1	0=-;1=Clear
HR2611	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2612				HR2611 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2613	DIGI:DI6 Counter Clear	RW	1	0=-;1=Clear
HR2614	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2615				HR2614 (Lo-16bits)
HR2616	DIGI:DI7 Counter Clear	RW	1	0=-;1=Clear
HR2617	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2618				HR2617 (Lo-16bits)
HR2619	DIGI:DI8 Counter Clear	RW	1	0=-;1=Clear
HR2620	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2621				HR2620 (Lo-16bits)
HR2622	DIGI:DI9 Counter Clear	RW	1	0=-;1=Clear
HR2623	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2624				HR2623 (Lo-16bits)
HR2625	DIGI:DI10 Counter Clear	RW	1	0=-;1=Clear
HR2626	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2627				HR2626 (Lo-16bits)
HR2628	DIGI:DI11 Counter Clear	RW	1	0=-;1=Clear
HR2629	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2630				HR2629 (Lo-16bits)
HR2631	DIGI:DI12 Counter Clear	RW	1	0=-;1=Clear
HR2632	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2633				HR2632 (Lo-16bits)
HR2634	DIGI:DI13 Counter Clear	RW	1	0=-;1=Clear
HR2635	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2636				HR2635 (Lo-16bits)
HR2637	DIGI:DI14 Counter Clear	RW	1	0=-;1=Clear
HR2638	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2639				HR2638 (Lo-16bits)
HR2640	DIGI:DI15 Counter Clear	RW	1	0=-;1=Clear
HR2641	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2642				HR2641 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2643	DIGI:DI16 Counter Clear	RW	1	0=-;1=Clear
HR2644	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2645				HR2644 (Lo-16bits)
HR2646	DIGI:DI17 Counter Clear	RW	1	0=-;1=Clear
HR2647	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2648				HR2647 (Lo-16bits)
HR2649	DIGI:DI18 Counter Clear	RW	1	0=-;1=Clear
HR2650	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2651				HR2650 (Lo-16bits)
HR2652	DIGI:DI19 Counter Clear	RW	1	0=-;1=Clear
HR2653	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2654				HR2653 (Lo-16bits)
HR2655	DIGI:DI20 Counter Clear	RW	1	0=-;1=Clear
HR2656	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2657				HR2656 (Lo-16bits)
HR2658	DIGI:DI21 Counter Clear	RW	1	0=-;1=Clear
HR2659	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2660				HR2659 (Lo-16bits)
HR2661	DIGI:DI22 Counter Clear	RW	1	0=-;1=Clear
HR2662	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2663				HR2662 (Lo-16bits)
HR2664	DIGI:DI23 Counter Clear	RW	1	0=-;1=Clear
HR2665	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	DIGI:DI24 Counter Clear	RW	1	0=-;1=Clear
HR2668	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2669				HR2668 (Lo-16bits)
HR2670	DIGI:DI25 Counter Clear	RW	1	0=-;1=Clear
HR2671	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2673	DIGI:DI26 Counter Clear	RW	1	0=-;1=Clear
HR2674	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2675				HR2674 (Lo-16bits)
HR2676	DIGI:DI27 Counter Clear	RW	1	0=-;1=Clear
HR2677	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	DIGI:DI28 Counter Clear	RW	1	0=-;1=Clear
HR2680	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2681				HR2680 (Lo-16bits)
HR2682	DIGI:DI29 Counter Clear	RW	1	0=-;1=Clear
HR2683	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	DIGI:DI30 Counter Clear	RW	1	0=-;1=Clear
HR2686	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2687				HR2686 (Lo-16bits)
HR2688	DIGI:DI31 Counter Clear	RW	1	0=-;1=Clear
HR2689	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	DIGI:DI32 Counter Clear	RW	1	0=-;1=Clear
HR2692	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2693				HR2692 (Lo-16bits)
HR2694	DIGI:DI33 Counter Clear	RW	1	0=-;1=Clear
HR2695	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DIGI:DI34 Counter Clear	RW	1	0=-;1=Clear
HR2698	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2699				HR2698 (Lo-16bits)
HR2700	DIGI:DI35 Counter Clear	RW	1	0=-;1=Clear
HR2701	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2703	DIGI:DI36 Counter Clear	RW	1	0=-;1=Clear
HR2704	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2705				HR2704 (Lo-16bits)
HR2706	DIGI:DI37 Counter Clear	RW	1	0=-;1=Clear
HR2707	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	DIGI:DI38 Counter Clear	RW	1	0=-;1=Clear
HR2710	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2711				HR2710 (Lo-16bits)
HR2712	DIGI:DI39 Counter Clear	RW	1	0=-;1=Clear
HR2713	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DIGI:DI40 Counter Clear	RW	1	0=-;1=Clear
HR2716	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2717				HR2716 (Lo-16bits)
HR2718	DIGI:DI41 Counter Clear	RW	1	0=-;1=Clear
HR2719	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DIGI:DI42 Counter Clear	RW	1	0=-;1=Clear
HR2722	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2723				HR2722 (Lo-16bits)
HR2724	DIGI:DI43 Counter Clear	RW	1	0=-;1=Clear
HR2725	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)
HR2727	DIGI:DI44 Counter Clear	RW	1	0=-;1=Clear
HR2728	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2729				HR2728 (Lo-16bits)
HR2730	DIGI:DI45 Counter Clear	RW	1	0=-;1=Clear
HR2731	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2733	DIGI:DI46 Counter Clear	RW	1	0=-;1=Clear
HR2734	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2735				HR2734 (Lo-16bits)
HR2736	DIGI:DI47 Counter Clear	RW	1	0=-;1=Clear
HR2737	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2738				HR2737 (Lo-16bits)
HR2739	DIGI:DI48 Counter Clear	RW	1	0=-;1=Clear
HR2740	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2741				HR2740 (Lo-16bits)
HR2742	DIGI:DI49 Counter Clear	RW	1	0=-;1=Clear
HR2743	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2744				HR2743 (Lo-16bits)
HR2745	DIGI:DI50 Counter Clear	RW	1	0=-;1=Clear
HR2746	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2747				HR2746 (Lo-16bits)
HR2748	DIGI:DI51 Counter Clear	RW	1	0=-;1=Clear
HR2749	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2750				HR2749 (Lo-16bits)
HR2751	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2752				HR2751 (Lo-16bits)
HR2753	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2754				HR2753 (Lo-16bits)
HR2755	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2756				HR2755 (Lo-16bits)
HR2757	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2763	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2768	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2769	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2770	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2771	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2772	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2773	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2774	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2775	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2789	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2792	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2793	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2794	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2795	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2796	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2797	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2798	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2799	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2800	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2801	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2802	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2803	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2804	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2805	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2806	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2807	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2808	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2809	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2810	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2811	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2812	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2813	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2814	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2815	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2816	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2817	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2818	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2819	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2820	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2821	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2822	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2823	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2830				HR2829 (Lo-16bits)
HR2831	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2832				HR2831 (Lo-16bits)
HR2833	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2837	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)
HR2861	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2867	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2878				HR2877 (Lo-16bits)
HR2879	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2880				HR2879 (Lo-16bits)
HR2881	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2882				HR2881 (Lo-16bits)
HR2883	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2884				HR2883 (Lo-16bits)
HR2885	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2886				HR2885 (Lo-16bits)
HR2887	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2888	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2889	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2890	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2891	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2892	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2893	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2894	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2895	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2910				HR2909 (Lo-16bits)
HR2911	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2912	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2913	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2914	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2915	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2916	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2917	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2918	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2919	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)
HR2927	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2928				HR2927 (Lo-16bits)
HR2929	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2930				HR2929 (Lo-16bits)
HR2931	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2932				HR2931 (Lo-16bits)
HR2933	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2934				HR2933 (Lo-16bits)
HR2935	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2936	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2937	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2938	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2939	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2940	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2941	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2942	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2943	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2944	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2945	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2946	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2947	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2948	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2949	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2950	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2951	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2952				HR2951 (Lo-16bits)
HR2953	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2954				HR2953 (Lo-16bits)
HR2955	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2956				HR2955 (Lo-16bits)
HR2957	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2958				HR2957 (Lo-16bits)
HR2959	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2960				HR2959 (Lo-16bits)
HR2961	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2962				HR2961 (Lo-16bits)
HR2963	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2964				HR2963 (Lo-16bits)
HR2965	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2968	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2969	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2970	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2971	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2972	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2973	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2974	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2975	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2976				HR2975 (Lo-16bits)
HR2977	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2978				HR2977 (Lo-16bits)
HR2979	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2980				HR2979 (Lo-16bits)
HR2981	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2982				HR2981 (Lo-16bits)
HR2983	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2984				HR2983 (Lo-16bits)
HR2985	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2986				HR2985 (Lo-16bits)
HR2987	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2988				HR2987 (Lo-16bits)
HR2989	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2990				HR2989 (Lo-16bits)
HR2991	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2992	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2993	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2994	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2995	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2996	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2997	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2998	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2999	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3000	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3001	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3002	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3003	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3004	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3005	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3006	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3007	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3008	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3009	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3010	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3011	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3012	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR3013	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3014	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3015	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3016	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3017	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3018	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3019	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3020	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3021	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3022	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3023	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3030				HR3029 (Lo-16bits)
HR3031	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3032				HR3031 (Lo-16bits)
HR3033	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3034				HR3033 (Lo-16bits)
HR3035	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3036				HR3035 (Lo-16bits)
HR3037	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3038				HR3037 (Lo-16bits)
HR3039	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3040				HR3039 (Lo-16bits)
HR3041	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3042				HR3041 (Lo-16bits)
HR3043	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3044				HR3043 (Lo-16bits)
HR3045	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3046				HR3045 (Lo-16bits)
HR3047	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3048				HR3047 (Lo-16bits)
HR3049	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3050				HR3049 (Lo-16bits)
HR3051	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3052				HR3051 (Lo-16bits)
HR3053	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3054				HR3053 (Lo-16bits)
HR3055	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3056				HR3055 (Lo-16bits)
HR3057	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3058				HR3057 (Lo-16bits)
HR3059	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3060				HR3059 (Lo-16bits)
HR3061	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3068				HR3067 (Lo-16bits)
HR3069	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3078				HR3077 (Lo-16bits)
HR3079	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3080				HR3079 (Lo-16bits)
HR3081	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3082				HR3081 (Lo-16bits)
HR3083	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3084				HR3083 (Lo-16bits)
HR3085	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3086				HR3085 (Lo-16bits)
HR3087	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3088	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3089	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3090	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3091	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3092	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3093	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3094	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3095	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)
HR3103	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3110				HR3109 (Lo-16bits)
HR3111	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3112	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3113	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3114	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3115	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3116	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3117	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3118	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3119	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3126				HR3125 (Lo-16bits)
HR3127	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3128				HR3127 (Lo-16bits)
HR3129	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3130				HR3129 (Lo-16bits)
HR3131	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3132				HR3131 (Lo-16bits)
HR3133	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3134				HR3133 (Lo-16bits)
HR3135	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3136	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3137	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3138	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3139	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3140	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3141	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3142	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3143	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3144	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3145	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3146	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3147	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3148	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3149	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3150	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3151	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3152				HR3151 (Lo-16bits)
HR3153	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3154				HR3153 (Lo-16bits)
HR3155	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3156				HR3155 (Lo-16bits)
HR3157	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3164				HR3163 (Lo-16bits)
HR3165	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3168	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3169	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3170	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3171	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3172	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3173	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3174	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3175	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3190				HR3189 (Lo-16bits)
HR3191	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3192	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3193	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3194	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3195	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3196	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3197	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3198	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3199	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3200	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3201	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3202	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3203	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3204	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3205	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3206	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3207	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3208	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3209	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3210	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3211	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3212	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3213	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3214	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3215	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3216	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3217	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3218	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3219	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3220	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3221	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3222	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3223	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3230				HR3229 (Lo-16bits)
HR3231	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3232				HR3231 (Lo-16bits)
HR3233	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3234				HR3233 (Lo-16bits)
HR3235	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3236				HR3235 (Lo-16bits)
HR3237	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3238				HR3237 (Lo-16bits)
HR3239	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3240				HR3239 (Lo-16bits)
HR3241	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3248				HR3247 (Lo-16bits)
HR3249	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3254				HR3253 (Lo-16bits)
HR3255	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3256				HR3255 (Lo-16bits)
HR3257	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3258				HR3257 (Lo-16bits)
HR3259	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3268				HR3267 (Lo-16bits)
HR3269	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3288	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3289	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3290	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3291	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3292	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3293	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3294	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3295	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3312	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3313	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3314	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3315	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3316	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3317	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3318	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3319	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)
HR3327	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3330				HR3329 (Lo-16bits)
HR3331	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3336	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3337	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3338	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3339	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3340	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3341	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3342	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3343	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3344	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3345	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3346	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3347	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3348	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3349	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3350	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3351	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3352				HR3351 (Lo-16bits)
HR3353	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3354				HR3353 (Lo-16bits)
HR3355	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3364				HR3363 (Lo-16bits)
HR3365	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3368	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3369	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3370	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3371	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3372	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3373	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3374	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3375	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)
HR3377	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3382				HR3381 (Lo-16bits)
HR3383	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3384				HR3383 (Lo-16bits)
HR3385	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3386				HR3385 (Lo-16bits)
HR3387	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3388				HR3387 (Lo-16bits)
HR3389	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3390				HR3389 (Lo-16bits)
HR3391	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3392	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3393	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3394	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3395	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3396	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3397	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3398	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3399	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3400	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3401	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3402	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3403	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3404	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3405	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3406	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3407	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3408	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3409	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3410	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3411	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3412	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3413	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3414	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3415	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3416	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3417	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3418	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3419	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3420	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3421	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3422	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3423	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3430				HR3429 (Lo-16bits)
HR3431	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3432				HR3431 (Lo-16bits)
HR3433	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3434				HR3433 (Lo-16bits)
HR3435	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3436				HR3435 (Lo-16bits)
HR3437	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3438				HR3437 (Lo-16bits)
HR3439	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3440				HR3439 (Lo-16bits)
HR3441	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3446				HR3445 (Lo-16bits)
HR3447	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3448				HR3447 (Lo-16bits)
HR3449	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3450				HR3449 (Lo-16bits)
HR3451	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3452				HR3451 (Lo-16bits)
HR3453	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3454				HR3453 (Lo-16bits)
HR3455	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3456				HR3455 (Lo-16bits)
HR3457	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3458				HR3457 (Lo-16bits)
HR3459	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3460				HR3459 (Lo-16bits)
HR3461	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3468				HR3467 (Lo-16bits)
HR3469	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3470				HR3469 (Lo-16bits)
HR3471	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3472				HR3471 (Lo-16bits)
HR3473	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3474				HR3473 (Lo-16bits)
HR3475	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3476				HR3475 (Lo-16bits)
HR3477	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3478				HR3477 (Lo-16bits)
HR3479	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3480				HR3479 (Lo-16bits)
HR3481	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3482				HR3481 (Lo-16bits)
HR3483	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3484				HR3483 (Lo-16bits)
HR3485	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3486				HR3485 (Lo-16bits)
HR3487	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3488	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3489	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3490	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3491	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3492	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3493	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3494	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3495	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3496				HR3495 (Lo-16bits)
HR3497	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3498				HR3497 (Lo-16bits)
HR3499	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3500				HR3499 (Lo-16bits)
HR3501	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3512	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3513	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3514	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3515	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3516	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3517	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3518	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3519	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)
HR3523	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3536	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3537	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3538	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3539	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3540	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3541	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3542	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3543	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3544	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3545	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3546	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3547	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3548	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3549	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3550	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3551	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3552	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3553	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3554	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3555	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3556	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3557	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3558	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir

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Holding Register	Parameter	Access	Length	
HR3559	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3566				HR3565 (Lo-16bits)
HR3567	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3568				HR3567 (Lo-16bits)
HR3569	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3570				HR3569 (Lo-16bits)
HR3571	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3572				HR3571 (Lo-16bits)
HR3573	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3574				HR3573 (Lo-16bits)
HR3575	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3576				HR3575 (Lo-16bits)
HR3577	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3578				HR3577 (Lo-16bits)
HR3579	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3580				HR3579 (Lo-16bits)
HR3581	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3582				HR3581 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3583	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3584				HR3583 (Lo-16bits)
HR3585	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3586				HR3585 (Lo-16bits)
HR3587	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3588				HR3587 (Lo-16bits)
HR3589	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3590				HR3589 (Lo-16bits)
HR3591	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3592				HR3591 (Lo-16bits)
HR3593	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3594				HR3593 (Lo-16bits)
HR3595	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3596				HR3595 (Lo-16bits)
HR3597	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3598				HR3597 (Lo-16bits)
HR3599	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3600				HR3599 (Lo-16bits)
HR3601	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3602				HR3601 (Lo-16bits)
HR3603	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3604				HR3603 (Lo-16bits)
HR3605	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3608	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3609	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3610	DOC1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3611	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3612	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3613	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3614	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3615	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3616				HR3615 (Lo-16bits)
HR3617	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3618				HR3617 (Lo-16bits)
HR3619	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3620				HR3619 (Lo-16bits)
HR3621	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3622				HR3621 (Lo-16bits)
HR3623	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3624				HR3623 (Lo-16bits)
HR3625	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3626				HR3625 (Lo-16bits)
HR3627	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3628				HR3627 (Lo-16bits)
HR3629	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3630				HR3629 (Lo-16bits)
HR3631	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3632	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3633	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3634	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3635	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3636	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3637	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3638	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3639	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3640	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3641	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3642	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3643	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3644	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3645	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3646	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3647	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3648	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3649	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3650	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3651	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3652	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3653	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3654	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3655	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3656	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3657	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3658	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3659	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3660	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3661	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3662	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3663	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3670				HR3669 (Lo-16bits)
HR3671	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3672				HR3671 (Lo-16bits)
HR3673	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3674				HR3673 (Lo-16bits)
HR3675	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3676				HR3675 (Lo-16bits)
HR3677	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3680				HR3679 (Lo-16bits)
HR3681	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3686				HR3685 (Lo-16bits)
HR3687	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3688				HR3687 (Lo-16bits)
HR3689	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3690				HR3689 (Lo-16bits)
HR3691	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3692				HR3691 (Lo-16bits)
HR3693	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3694				HR3693 (Lo-16bits)
HR3695	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3696				HR3695 (Lo-16bits)
HR3697	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3698				HR3697 (Lo-16bits)
HR3699	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3700				HR3699 (Lo-16bits)
HR3701	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)
HR3703	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3710				HR3709 (Lo-16bits)
HR3711	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3712				HR3711 (Lo-16bits)
HR3713	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3714				HR3713 (Lo-16bits)
HR3715	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3716				HR3715 (Lo-16bits)
HR3717	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3718				HR3717 (Lo-16bits)
HR3719	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3720				HR3719 (Lo-16bits)
HR3721	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3722				HR3721 (Lo-16bits)
HR3723	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3724				HR3723 (Lo-16bits)
HR3725	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3726				HR3725 (Lo-16bits)
HR3727	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3728	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3729	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3730	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3731	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3732	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3733	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3734	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3735	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3736				HR3735 (Lo-16bits)
HR3737	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3738				HR3737 (Lo-16bits)
HR3739	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3740				HR3739 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3741	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3746				HR3745 (Lo-16bits)
HR3747	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3752	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3753	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3754	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3755	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3756	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3757	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3758	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3759	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3765	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3770				HR3769 (Lo-16bits)
HR3771	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3776	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3777	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3778	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3779	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3780	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3781	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3782	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3783	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3784	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3785	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3786	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3787	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3788	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3789	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3790	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3791	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3792	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3793	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3794	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3795	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3796	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3797	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3798	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3799	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3806				HR3805 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3807	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3808				HR3807 (Lo-16bits)
HR3809	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3810				HR3809 (Lo-16bits)
HR3811	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3812				HR3811 (Lo-16bits)
HR3813	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3814				HR3813 (Lo-16bits)
HR3815	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3816				HR3815 (Lo-16bits)
HR3817	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3818				HR3817 (Lo-16bits)
HR3819	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3820				HR3819 (Lo-16bits)
HR3821	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3822				HR3821 (Lo-16bits)
HR3823	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3824				HR3823 (Lo-16bits)
HR3825	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3826				HR3825 (Lo-16bits)
HR3827	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3828				HR3827 (Lo-16bits)
HR3829	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3830				HR3829 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3831	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3832				HR3831 (Lo-16bits)
HR3833	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3834				HR3833 (Lo-16bits)
HR3835	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3836				HR3835 (Lo-16bits)
HR3837	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3838				HR3837 (Lo-16bits)
HR3839	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3840				HR3839 (Lo-16bits)
HR3841	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3842				HR3841 (Lo-16bits)
HR3843	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3844				HR3843 (Lo-16bits)
HR3845	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3848	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3849	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3850	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3851	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3852	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3853	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3854	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3855	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3860				HR3859 (Lo-16bits)
HR3861	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3862				HR3861 (Lo-16bits)
HR3863	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3864				HR3863 (Lo-16bits)
HR3865	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3866				HR3865 (Lo-16bits)
HR3867	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3872	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3873	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3874	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3875	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3876	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3877	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3878	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3879	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3880	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3881	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3882	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3883	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3884	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3885	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3886	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3887	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3888	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3889	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3890	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3891	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3892	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3893	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3894	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3895	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3896	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3897	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3898	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3899	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3900	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3901	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3902	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3903	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3906				HR3905 (Lo-16bits)
HR3907	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3910				HR3909 (Lo-16bits)
HR3911	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3912				HR3911 (Lo-16bits)
HR3913	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3914				HR3913 (Lo-16bits)
HR3915	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3916				HR3915 (Lo-16bits)
HR3917	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)
HR3927	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)
HR3933	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3934				HR3933 (Lo-16bits)
HR3935	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3936				HR3935 (Lo-16bits)
HR3937	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3938				HR3937 (Lo-16bits)
HR3939	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3940				HR3939 (Lo-16bits)
HR3941	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)
HR3965	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3966				HR3965 (Lo-16bits)
HR3967	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3968	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3969	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3970	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3971	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3972	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3973	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3974	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3975	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3976				HR3975 (Lo-16bits)
HR3977	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3978				HR3977 (Lo-16bits)
HR3979	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3980				HR3979 (Lo-16bits)
HR3981	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3989	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3992	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3993	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3994	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3995	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3996	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3997	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3998	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3999	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4015	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4016	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4017	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4018	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4019	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4020	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4021	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4022	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4023	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4024	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4025	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4026	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4027	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4028	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4029	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4030	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4031	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4032	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4033	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4034	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4035	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4036	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4037	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4038	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4039	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4054				HR4053 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4055	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4056				HR4055 (Lo-16bits)
HR4057	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4058				HR4057 (Lo-16bits)
HR4059	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4060				HR4059 (Lo-16bits)
HR4061	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4062				HR4061 (Lo-16bits)
HR4063	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4064				HR4063 (Lo-16bits)
HR4065	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4066				HR4065 (Lo-16bits)
HR4067	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4068				HR4067 (Lo-16bits)
HR4069	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4070				HR4069 (Lo-16bits)
HR4071	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4072				HR4071 (Lo-16bits)
HR4073	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4074				HR4073 (Lo-16bits)
HR4075	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4076				HR4075 (Lo-16bits)
HR4077	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4078				HR4077 (Lo-16bits)
HR4079	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4080				HR4079 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4081	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4082				HR4081 (Lo-16bits)
HR4083	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4084				HR4083 (Lo-16bits)
HR4085	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4088	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4089	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4090	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4091	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4092	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4093	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4094	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4095	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)
HR4099	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)
HR4101	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4108				HR4107 (Lo-16bits)
HR4109	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4112	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4113	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4114	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4115	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4116	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4117	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4118	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4119	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4120	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4121	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4122	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4123	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4124	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4125	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4126	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4127	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4128	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4129	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4130	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4131	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4132	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4133	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4134	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4135	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4136	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4137	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4138	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4139	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4140	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4141	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4142	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4143	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4150				HR4149 (Lo-16bits)
HR4151	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4152				HR4151 (Lo-16bits)
HR4153	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4154				HR4153 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4155	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4156				HR4155 (Lo-16bits)
HR4157	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4166				HR4165 (Lo-16bits)
HR4167	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4168				HR4167 (Lo-16bits)
HR4169	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4170				HR4169 (Lo-16bits)
HR4171	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4172				HR4171 (Lo-16bits)
HR4173	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4174				HR4173 (Lo-16bits)
HR4175	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4176				HR4175 (Lo-16bits)
HR4177	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4178				HR4177 (Lo-16bits)
HR4179	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4180				HR4179 (Lo-16bits)
HR4181	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4185	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4190				HR4189 (Lo-16bits)
HR4191	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4192				HR4191 (Lo-16bits)
HR4193	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4194				HR4193 (Lo-16bits)
HR4195	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4196				HR4195 (Lo-16bits)
HR4197	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4198				HR4197 (Lo-16bits)
HR4199	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4200				HR4199 (Lo-16bits)
HR4201	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4202				HR4201 (Lo-16bits)
HR4203	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4204				HR4203 (Lo-16bits)
HR4205	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4206				HR4205 (Lo-16bits)
HR4207	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4208	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4209	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4210	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4211	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4212	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4213	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4214	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4215	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4216				HR4215 (Lo-16bits)
HR4217	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4218				HR4217 (Lo-16bits)
HR4219	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4220				HR4219 (Lo-16bits)
HR4221	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4232	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4233	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4234	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4235	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4236	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4237	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4238	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4239	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4256	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4257	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4258	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4259	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4260	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4261	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4262	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4263	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4264	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4265	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4266	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4267	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4268	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4269	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4270	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4271	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4272	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4273	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4274	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4275	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4276	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4277	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4278	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4279	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4286				HR4285 (Lo-16bits)
HR4287	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4288				HR4287 (Lo-16bits)
HR4289	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4290				HR4289 (Lo-16bits)
HR4291	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4292				HR4291 (Lo-16bits)
HR4293	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4294				HR4293 (Lo-16bits)
HR4295	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4296				HR4295 (Lo-16bits)
HR4297	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4300				HR4299 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4301	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4302				HR4301 (Lo-16bits)
HR4303	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4310				HR4309 (Lo-16bits)
HR4311	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4312				HR4311 (Lo-16bits)
HR4313	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4314				HR4313 (Lo-16bits)
HR4315	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4316				HR4315 (Lo-16bits)
HR4317	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4328	DOC4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4329	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4330	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4331	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4332	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4333	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4334	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4335	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4336				HR4335 (Lo-16bits)
HR4337	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4338				HR4337 (Lo-16bits)
HR4339	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4340				HR4339 (Lo-16bits)
HR4341	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4342				HR4341 (Lo-16bits)
HR4343	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4352	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4353	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4354	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4355	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4356	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4357	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4358	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4359	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4360	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4361	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4362	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4363	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4364	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4365	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4366	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4367	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4368	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4369	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4370	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4371	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4372	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4373	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4374	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4375	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4376	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4377	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4378	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4379	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4380	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4381	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4382	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4383	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4390				HR4389 (Lo-16bits)
HR4391	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4394				HR4393 (Lo-16bits)
HR4395	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4396				HR4395 (Lo-16bits)
HR4397	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4403	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)
HR4415	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4418				HR4417 (Lo-16bits)
HR4419	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4420				HR4419 (Lo-16bits)
HR4421	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4433	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)
HR4445	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4448	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4449	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4450	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4451	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4452	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4453	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4454	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4455	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4458				HR4457 (Lo-16bits)
HR4459	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4472	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4473	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4474	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4475	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4476	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4477	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4478	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4479	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4481	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)
HR4493	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4496	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4497	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4498	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4499	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4500	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4501	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4502	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4503	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4504	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4505	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4506	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4507	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4508	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4509	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4510	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4511	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4512				HR4511 (Lo-16bits)
HR4513	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4514				HR4513 (Lo-16bits)
HR4515	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4516				HR4515 (Lo-16bits)
HR4517	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4518				HR4517 (Lo-16bits)
HR4519	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4522				HR4521 (Lo-16bits)
HR4523	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4524				HR4523 (Lo-16bits)
HR4525	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4528	NEF1:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4529	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4530	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4531	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4532	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4533	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4534	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4535	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4538				HR4537 (Lo-16bits)
HR4539	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4540				HR4539 (Lo-16bits)
HR4541	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4542				HR4541 (Lo-16bits)
HR4543	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4544				HR4543 (Lo-16bits)
HR4545	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)
HR4549	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4552	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4553	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4554	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4555	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4556	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4557	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4558	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4559	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4560	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4561	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4562	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4563	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4564	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4565	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4566	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4567	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4568	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4569	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4570	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4571	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4572	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4573	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4574	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4575	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4576	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4577	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4578	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4579	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4580	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4581	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4582	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4583	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4590				HR4589 (Lo-16bits)
HR4591	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4592				HR4591 (Lo-16bits)
HR4593	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4594				HR4593 (Lo-16bits)
HR4595	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4596				HR4595 (Lo-16bits)
HR4597	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4598				HR4597 (Lo-16bits)
HR4599	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4600				HR4599 (Lo-16bits)
HR4601	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4602				HR4601 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4603	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4604				HR4603 (Lo-16bits)
HR4605	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4606				HR4605 (Lo-16bits)
HR4607	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4608				HR4607 (Lo-16bits)
HR4609	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4610				HR4609 (Lo-16bits)
HR4611	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4612				HR4611 (Lo-16bits)
HR4613	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4614				HR4613 (Lo-16bits)
HR4615	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4616				HR4615 (Lo-16bits)
HR4617	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4618				HR4617 (Lo-16bits)
HR4619	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4620				HR4619 (Lo-16bits)
HR4621	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4633	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4638				HR4637 (Lo-16bits)
HR4639	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4640				HR4639 (Lo-16bits)
HR4641	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4642				HR4641 (Lo-16bits)
HR4643	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4644				HR4643 (Lo-16bits)
HR4645	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4646				HR4645 (Lo-16bits)
HR4647	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4648	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4649	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4650	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4651	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4652	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4653	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4654	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4655	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4658				HR4657 (Lo-16bits)
HR4659	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4666				HR4665 (Lo-16bits)
HR4667	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4670				HR4669 (Lo-16bits)
HR4671	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4672	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4673	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4674	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4675	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4676	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4677	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4678	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4679	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4681	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4686				HR4685 (Lo-16bits)
HR4687	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4688				HR4687 (Lo-16bits)
HR4689	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4690				HR4689 (Lo-16bits)
HR4691	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4692				HR4691 (Lo-16bits)
HR4693	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4694				HR4693 (Lo-16bits)
HR4695	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4696	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4697	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4698	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4699	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4700	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4701	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4702	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4703	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4704	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4705	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4706	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4707	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4708	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4709	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4710	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4711	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4712				HR4711 (Lo-16bits)
HR4713	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4714				HR4713 (Lo-16bits)
HR4715	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4716				HR4715 (Lo-16bits)
HR4717	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4718				HR4717 (Lo-16bits)
HR4719	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4720				HR4719 (Lo-16bits)
HR4721	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4722				HR4721 (Lo-16bits)
HR4723	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4724				HR4723 (Lo-16bits)
HR4725	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4728	NEF2:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4729	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4730	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4731	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4732	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4733	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4734	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4735	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4736				HR4735 (Lo-16bits)
HR4737	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4738				HR4737 (Lo-16bits)
HR4739	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4740				HR4739 (Lo-16bits)
HR4741	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4742				HR4741 (Lo-16bits)
HR4743	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4744				HR4743 (Lo-16bits)
HR4745	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4746				HR4745 (Lo-16bits)
HR4747	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4748				HR4747 (Lo-16bits)
HR4749	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4752	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4753	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4754	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4755	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4756	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4757	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4758	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4759	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4760	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4761	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4762	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4763	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4764	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4765	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4766	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4767	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4768	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4769	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4770	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4771	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4772	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4773	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4774	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4775	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4776	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4777	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4778	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4779	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4780	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4781	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4782	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4783	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4790				HR4789 (Lo-16bits)
HR4791	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4792				HR4791 (Lo-16bits)
HR4793	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4794				HR4793 (Lo-16bits)
HR4795	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4796				HR4795 (Lo-16bits)
HR4797	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4798				HR4797 (Lo-16bits)
HR4799	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4800				HR4799 (Lo-16bits)
HR4801	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4802				HR4801 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4803	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4804				HR4803 (Lo-16bits)
HR4805	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4806				HR4805 (Lo-16bits)
HR4807	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4808				HR4807 (Lo-16bits)
HR4809	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4810				HR4809 (Lo-16bits)
HR4811	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4812				HR4811 (Lo-16bits)
HR4813	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4814				HR4813 (Lo-16bits)
HR4815	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4816				HR4815 (Lo-16bits)
HR4817	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4818				HR4817 (Lo-16bits)
HR4819	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4820				HR4819 (Lo-16bits)
HR4821	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)
HR4827	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4833	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4838				HR4837 (Lo-16bits)
HR4839	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4840				HR4839 (Lo-16bits)
HR4841	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4842				HR4841 (Lo-16bits)
HR4843	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4844				HR4843 (Lo-16bits)
HR4845	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4846				HR4845 (Lo-16bits)
HR4847	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4848	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4849	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4850	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4851	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4852	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4853	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4854	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4855	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4858				HR4857 (Lo-16bits)
HR4859	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4866				HR4865 (Lo-16bits)
HR4867	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4870				HR4869 (Lo-16bits)
HR4871	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4872	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4873	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4874	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4875	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4876	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4877	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4878	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4879	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4881	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4886				HR4885 (Lo-16bits)
HR4887	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4888				HR4887 (Lo-16bits)
HR4889	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4890				HR4889 (Lo-16bits)
HR4891	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4892				HR4891 (Lo-16bits)
HR4893	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4894				HR4893 (Lo-16bits)
HR4895	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4896	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4897	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4898	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4899	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4900	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4901	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4902	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4903	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4904	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4905	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4906	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4907	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4908	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4909	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4910	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4911	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4912				HR4911 (Lo-16bits)
HR4913	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4914				HR4913 (Lo-16bits)
HR4915	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4916				HR4915 (Lo-16bits)
HR4917	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4918				HR4917 (Lo-16bits)
HR4919	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4920				HR4919 (Lo-16bits)
HR4921	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4922				HR4921 (Lo-16bits)
HR4923	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4924				HR4923 (Lo-16bits)
HR4925	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4928	NEF3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4929	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4930	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4931	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4932	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4933	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4934	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4935	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4936				HR4935 (Lo-16bits)
HR4937	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4938				HR4937 (Lo-16bits)
HR4939	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4940				HR4939 (Lo-16bits)
HR4941	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4942				HR4941 (Lo-16bits)
HR4943	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4944				HR4943 (Lo-16bits)
HR4945	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4946				HR4945 (Lo-16bits)
HR4947	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4948				HR4947 (Lo-16bits)
HR4949	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4952	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4953	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4954	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR4955	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4956	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4957	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4958	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4959	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4960	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4961	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4962	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4963	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4964	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4965	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4966	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4967	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4968	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4969	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4970	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4971	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4972	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4973	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4974	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4975	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4976	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4977	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4978	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4979	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4980	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4981	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4982	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4983	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4990				HR4989 (Lo-16bits)
HR4991	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4992				HR4991 (Lo-16bits)
HR4993	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4994				HR4993 (Lo-16bits)
HR4995	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4996				HR4995 (Lo-16bits)
HR4997	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4998				HR4997 (Lo-16bits)
HR4999	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5000				HR4999 (Lo-16bits)
HR5001	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5002				HR5001 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5003	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5004				HR5003 (Lo-16bits)
HR5005	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5006				HR5005 (Lo-16bits)
HR5007	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5008				HR5007 (Lo-16bits)
HR5009	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5010				HR5009 (Lo-16bits)
HR5011	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5012				HR5011 (Lo-16bits)
HR5013	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5014				HR5013 (Lo-16bits)
HR5015	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5016				HR5015 (Lo-16bits)
HR5017	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5018				HR5017 (Lo-16bits)
HR5019	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5020				HR5019 (Lo-16bits)
HR5021	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5033	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5038				HR5037 (Lo-16bits)
HR5039	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5040				HR5039 (Lo-16bits)
HR5041	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5042				HR5041 (Lo-16bits)
HR5043	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5044				HR5043 (Lo-16bits)
HR5045	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5046				HR5045 (Lo-16bits)
HR5047	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5048	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5049	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5050	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5051	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5052	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5053	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5054	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5055	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5058				HR5057 (Lo-16bits)
HR5059	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5066				HR5065 (Lo-16bits)
HR5067	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5070				HR5069 (Lo-16bits)
HR5071	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5072	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5073	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5074	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5075	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5076	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5077	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5078	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5079	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5081	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5086				HR5085 (Lo-16bits)
HR5087	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5088				HR5087 (Lo-16bits)
HR5089	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5090				HR5089 (Lo-16bits)
HR5091	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5092				HR5091 (Lo-16bits)
HR5093	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5094				HR5093 (Lo-16bits)
HR5095	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5096	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5097	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5098	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5099	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5100	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5101	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5102	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5103	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5104	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5105	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5106	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5107	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5108	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5109	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5110	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5111	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5112				HR5111 (Lo-16bits)
HR5113	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5114				HR5113 (Lo-16bits)
HR5115	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5116				HR5115 (Lo-16bits)
HR5117	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5118				HR5117 (Lo-16bits)
HR5119	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5120				HR5119 (Lo-16bits)
HR5121	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5122				HR5121 (Lo-16bits)
HR5123	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5124				HR5123 (Lo-16bits)
HR5125	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5128	NEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR5129	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5130	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5131	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5132	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5133	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5134	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5135	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5136				HR5135 (Lo-16bits)
HR5137	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5138				HR5137 (Lo-16bits)
HR5139	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5140				HR5139 (Lo-16bits)
HR5141	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5142				HR5141 (Lo-16bits)
HR5143	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5144				HR5143 (Lo-16bits)
HR5145	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5146				HR5145 (Lo-16bits)
HR5147	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5148				HR5147 (Lo-16bits)
HR5149	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5150				HR5149 (Lo-16bits)
HR5151	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5152	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5153	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5154	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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TN00011

Holding Register	Parameter	Access	Length	
HR5155	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5156	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5157	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5158	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5159	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5160	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5161	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5162	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5163	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5164	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5165	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5166	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5167	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5168	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5169	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5170	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5171	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5172	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5173	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5174	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5175	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR5176	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5177	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5178	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5179	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5180	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5181	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5182	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5183	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5190				HR5189 (Lo-16bits)
HR5191	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5192				HR5191 (Lo-16bits)
HR5193	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5194				HR5193 (Lo-16bits)
HR5195	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5196				HR5195 (Lo-16bits)
HR5197	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5203	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5214				HR5213 (Lo-16bits)
HR5215	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5216				HR5215 (Lo-16bits)
HR5217	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5218				HR5217 (Lo-16bits)
HR5219	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5220				HR5219 (Lo-16bits)
HR5221	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5233	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5238				HR5237 (Lo-16bits)
HR5239	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5240				HR5239 (Lo-16bits)
HR5241	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5242				HR5241 (Lo-16bits)
HR5243	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5244				HR5243 (Lo-16bits)
HR5245	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5246				HR5245 (Lo-16bits)
HR5247	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5248	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5249	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5250	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5251	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5252	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5253	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5254	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5255	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5256				HR5255 (Lo-16bits)
HR5257	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5258				HR5257 (Lo-16bits)
HR5259	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5260				HR5259 (Lo-16bits)
HR5261	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)
HR5265	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5272	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5273	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5274	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5275	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5276	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5277	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5278	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5279	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5281	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)
HR5289	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5296	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5297	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5298	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5299	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5300	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5301	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5302	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5303	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5304	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR5305	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5306	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5307	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5308	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5309	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5310	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5311	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5312	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5313	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5314	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5315	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5316	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5317	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5318	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5319	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5327	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)
HR5335	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5342				HR5341 (Lo-16bits)
HR5343	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5344				HR5343 (Lo-16bits)
HR5345	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5346				HR5345 (Lo-16bits)
HR5347	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5348				HR5347 (Lo-16bits)
HR5349	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5350				HR5349 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5351	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5352				HR5351 (Lo-16bits)
HR5353	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5354				HR5353 (Lo-16bits)
HR5355	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5356				HR5355 (Lo-16bits)
HR5357	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5358				HR5357 (Lo-16bits)
HR5359	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5360				HR5359 (Lo-16bits)
HR5361	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5362				HR5361 (Lo-16bits)
HR5363	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5364				HR5363 (Lo-16bits)
HR5365	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5375	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5406				HR5405 (Lo-16bits)
HR5407	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5408				HR5407 (Lo-16bits)
HR5409	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5410				HR5409 (Lo-16bits)
HR5411	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5412				HR5411 (Lo-16bits)
HR5413	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5414				HR5413 (Lo-16bits)
HR5415	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5416	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5417	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5418	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5419	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5420	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5421	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5422	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5423	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5424				HR5423 (Lo-16bits)
HR5425	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5426				HR5425 (Lo-16bits)
HR5427	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5428				HR5427 (Lo-16bits)
HR5429	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5430				HR5429 (Lo-16bits)
HR5431	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5432				HR5431 (Lo-16bits)
HR5433	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5434				HR5433 (Lo-16bits)
HR5435	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5436				HR5435 (Lo-16bits)
HR5437	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5445	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5446				HR5445 (Lo-16bits)
HR5447	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5448				HR5447 (Lo-16bits)
HR5449	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5450				HR5449 (Lo-16bits)
HR5451	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5452				HR5451 (Lo-16bits)
HR5453	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5454				HR5453 (Lo-16bits)
HR5455	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5456				HR5455 (Lo-16bits)
HR5457	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5458				HR5457 (Lo-16bits)
HR5459	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5460				HR5459 (Lo-16bits)
HR5461	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)
HR5463	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5469	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)
HR5487	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5499	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5502				HR5501 (Lo-16bits)
HR5503	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5504	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5505	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5506	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5507	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5508	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5509	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5510	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5511	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5512	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5513	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5514	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5515	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5516	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5517	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5518	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5519	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5520				HR5519 (Lo-16bits)
HR5521	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5522				HR5521 (Lo-16bits)
HR5523	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5524				HR5523 (Lo-16bits)
HR5525	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5526				HR5525 (Lo-16bits)
HR5527	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5528				HR5527 (Lo-16bits)
HR5529	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5530				HR5529 (Lo-16bits)
HR5531	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5532				HR5531 (Lo-16bits)
HR5533	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5534				HR5533 (Lo-16bits)
HR5535	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5536				HR5535 (Lo-16bits)
HR5537	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5538				HR5537 (Lo-16bits)
HR5539	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5540				HR5539 (Lo-16bits)
HR5541	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5550				HR5549 (Lo-16bits)
HR5551	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5552				HR5551 (Lo-16bits)
HR5553	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5554				HR5553 (Lo-16bits)
HR5555	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5556				HR5555 (Lo-16bits)
HR5557	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5558				HR5557 (Lo-16bits)
HR5559	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5560				HR5559 (Lo-16bits)
HR5561	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5562				HR5561 (Lo-16bits)
HR5563	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5564				HR5563 (Lo-16bits)
HR5565	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5566				HR5565 (Lo-16bits)
HR5567	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5568	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5569	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5570	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5571	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5572	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5573	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5574	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5575	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5576	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5577	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5578	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5579	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5580	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5581	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5582	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5583	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5584	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5585	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5586	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5587	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5588	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5589	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5590	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5591	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5592	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5593	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR5594	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5595	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5596	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5597	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5598	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5599	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5606				HR5605 (Lo-16bits)
HR5607	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5608				HR5607 (Lo-16bits)
HR5609	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5610				HR5609 (Lo-16bits)
HR5611	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5612				HR5611 (Lo-16bits)
HR5613	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5616				HR5615 (Lo-16bits)
HR5617	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5622				HR5621 (Lo-16bits)
HR5623	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5630				HR5629 (Lo-16bits)
HR5631	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5632				HR5631 (Lo-16bits)
HR5633	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5634				HR5633 (Lo-16bits)
HR5635	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5636				HR5635 (Lo-16bits)
HR5637	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)
HR5639	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)
HR5647	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5652				HR5651 (Lo-16bits)
HR5653	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5654				HR5653 (Lo-16bits)
HR5655	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5656				HR5655 (Lo-16bits)
HR5657	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5658				HR5657 (Lo-16bits)
HR5659	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5660				HR5659 (Lo-16bits)
HR5661	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5662				HR5661 (Lo-16bits)
HR5663	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5664	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5665	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5666	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5667	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5668	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5669	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5670	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5671	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5672				HR5671 (Lo-16bits)
HR5673	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5674				HR5673 (Lo-16bits)
HR5675	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5676				HR5675 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5677	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)
HR5685	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5688	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5689	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5690	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5691	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5692	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5693	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5694	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5695	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5701	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)
HR5709	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5712	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5713	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5714	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5715	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5716	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5717	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5718	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5719	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5720	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5721	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5722	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5723	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5724	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5725	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5726	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5727	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5728	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5729	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5730	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5731	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5732	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5733	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5734	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5735	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)
HR5757	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5758				HR5757 (Lo-16bits)
HR5759	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5760				HR5759 (Lo-16bits)
HR5761	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5762				HR5761 (Lo-16bits)
HR5763	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5764				HR5763 (Lo-16bits)
HR5765	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5766				HR5765 (Lo-16bits)
HR5767	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5768				HR5767 (Lo-16bits)
HR5769	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5770				HR5769 (Lo-16bits)
HR5771	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5772				HR5771 (Lo-16bits)
HR5773	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5774				HR5773 (Lo-16bits)
HR5775	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5776				HR5775 (Lo-16bits)
HR5777	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5778				HR5777 (Lo-16bits)
HR5779	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5780				HR5779 (Lo-16bits)
HR5781	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)
HR5807	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5822				HR5821 (Lo-16bits)
HR5823	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5824				HR5823 (Lo-16bits)
HR5825	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5826				HR5825 (Lo-16bits)
HR5827	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5828				HR5827 (Lo-16bits)
HR5829	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5830				HR5829 (Lo-16bits)
HR5831	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5832	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5833	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5834	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5835	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5836	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5837	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5838	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5839	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5840				HR5839 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5841	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5842				HR5841 (Lo-16bits)
HR5843	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5844				HR5843 (Lo-16bits)
HR5845	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5846				HR5845 (Lo-16bits)
HR5847	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5848				HR5847 (Lo-16bits)
HR5849	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5850				HR5849 (Lo-16bits)
HR5851	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5852				HR5851 (Lo-16bits)
HR5853	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5862				HR5861 (Lo-16bits)
HR5863	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5864				HR5863 (Lo-16bits)
HR5865	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5866				HR5865 (Lo-16bits)
HR5867	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5868				HR5867 (Lo-16bits)
HR5869	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5870				HR5869 (Lo-16bits)
HR5871	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5872				HR5871 (Lo-16bits)
HR5873	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5874				HR5873 (Lo-16bits)
HR5875	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5876				HR5875 (Lo-16bits)
HR5877	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5918				HR5917 (Lo-16bits)
HR5919	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5920	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5921	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	
HR5922	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5923	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5924	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5925	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5926	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5927	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5928	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5929	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5930	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5931	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5932	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5933	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5934	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5935	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5936				HR5935 (Lo-16bits)
HR5937	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5938				HR5937 (Lo-16bits)
HR5939	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5940				HR5939 (Lo-16bits)
HR5941	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5942				HR5941 (Lo-16bits)
HR5943	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5944				HR5943 (Lo-16bits)
HR5945	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5946				HR5945 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5947	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5948				HR5947 (Lo-16bits)
HR5949	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5950				HR5949 (Lo-16bits)
HR5951	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5952				HR5951 (Lo-16bits)
HR5953	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5954				HR5953 (Lo-16bits)
HR5955	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5956				HR5955 (Lo-16bits)
HR5957	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5966				HR5965 (Lo-16bits)
HR5967	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5968				HR5967 (Lo-16bits)
HR5969	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5970				HR5969 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5971	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5972				HR5971 (Lo-16bits)
HR5973	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5974				HR5973 (Lo-16bits)
HR5975	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5976				HR5975 (Lo-16bits)
HR5977	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5978				HR5977 (Lo-16bits)
HR5979	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5980				HR5979 (Lo-16bits)
HR5981	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5982				HR5981 (Lo-16bits)
HR5983	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5984	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5985	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5986	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5987	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5988	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5989	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5990	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5991	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5992	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5993	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5994	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5995	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR5996	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5997	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5998	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5999	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6000	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6001	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6002	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6003	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6004	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6005	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6006	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6007	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6008	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6009	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6010	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6011	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6012	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6013	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6014	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6015	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6017	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6022				HR6021 (Lo-16bits)
HR6023	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6024				HR6023 (Lo-16bits)
HR6025	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6026				HR6025 (Lo-16bits)
HR6027	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6028				HR6027 (Lo-16bits)
HR6029	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6038				HR6037 (Lo-16bits)
HR6039	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6046				HR6045 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6047	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6048				HR6047 (Lo-16bits)
HR6049	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6050				HR6049 (Lo-16bits)
HR6051	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6052				HR6051 (Lo-16bits)
HR6053	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)
HR6063	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6070				HR6069 (Lo-16bits)
HR6071	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6072				HR6071 (Lo-16bits)
HR6073	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6074				HR6073 (Lo-16bits)
HR6075	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6076				HR6075 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6077	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6078				HR6077 (Lo-16bits)
HR6079	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6080	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6081	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6082	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6083	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6084	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6085	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6086	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6087	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6088				HR6087 (Lo-16bits)
HR6089	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6090				HR6089 (Lo-16bits)
HR6091	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6092				HR6091 (Lo-16bits)
HR6093	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6100				HR6099 (Lo-16bits)
HR6101	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6104	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6105	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6106	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6107	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6108	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6109	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6110	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6111	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6126				HR6125 (Lo-16bits)
HR6127	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6128	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6129	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6130	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6131	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6132	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6133	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6134	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6135	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6136	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6137	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6138	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6139	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6140	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6141	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6142	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6143	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

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Holding Register	Parameter	Access	Length	
HR6144	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6145	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6146	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6147	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6148	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6149	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6150	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6151	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)
HR6155	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6169	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6174				HR6173 (Lo-16bits)
HR6175	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6176				HR6175 (Lo-16bits)
HR6177	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6178				HR6177 (Lo-16bits)
HR6179	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6180				HR6179 (Lo-16bits)
HR6181	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6182				HR6181 (Lo-16bits)
HR6183	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6184				HR6183 (Lo-16bits)
HR6185	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6186				HR6185 (Lo-16bits)
HR6187	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6188				HR6187 (Lo-16bits)
HR6189	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6190				HR6189 (Lo-16bits)
HR6191	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6192				HR6191 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6193	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6194				HR6193 (Lo-16bits)
HR6195	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6196				HR6195 (Lo-16bits)
HR6197	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)
HR6203	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6217	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)
HR6229	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6238				HR6237 (Lo-16bits)
HR6239	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6240				HR6239 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6241	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6242				HR6241 (Lo-16bits)
HR6243	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6244				HR6243 (Lo-16bits)
HR6245	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6246				HR6245 (Lo-16bits)
HR6247	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6248	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6249	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6250	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6251	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6252	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6253	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6254	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6255	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6256				HR6255 (Lo-16bits)
HR6257	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6258				HR6257 (Lo-16bits)
HR6259	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6260				HR6259 (Lo-16bits)
HR6261	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6262				HR6261 (Lo-16bits)
HR6263	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6264				HR6263 (Lo-16bits)
HR6265	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6266				HR6265 (Lo-16bits)
HR6267	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6268				HR6267 (Lo-16bits)
HR6269	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6278				HR6277 (Lo-16bits)
HR6279	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6280				HR6279 (Lo-16bits)
HR6281	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6282				HR6281 (Lo-16bits)
HR6283	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6284				HR6283 (Lo-16bits)
HR6285	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6286				HR6285 (Lo-16bits)
HR6287	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6288				HR6287 (Lo-16bits)
HR6289	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6290				HR6289 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6291	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6292				HR6291 (Lo-16bits)
HR6293	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)
HR6307	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)
HR6331	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6334				HR6333 (Lo-16bits)
HR6335	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6336	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6337	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6338	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6339	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6340	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6341	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6342	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6343	DEF3:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR6344	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6345	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6346	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6347	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6348	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6349	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6350	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6351	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6352				HR6351 (Lo-16bits)
HR6353	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6354				HR6353 (Lo-16bits)
HR6355	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6356				HR6355 (Lo-16bits)
HR6357	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6358				HR6357 (Lo-16bits)
HR6359	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6360				HR6359 (Lo-16bits)
HR6361	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6362				HR6361 (Lo-16bits)
HR6363	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6364				HR6363 (Lo-16bits)
HR6365	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6366				HR6365 (Lo-16bits)
HR6367	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6368				HR6367 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6369	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6370				HR6369 (Lo-16bits)
HR6371	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6372				HR6371 (Lo-16bits)
HR6373	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6382				HR6381 (Lo-16bits)
HR6383	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6384				HR6383 (Lo-16bits)
HR6385	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6386				HR6385 (Lo-16bits)
HR6387	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6388				HR6387 (Lo-16bits)
HR6389	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6390				HR6389 (Lo-16bits)
HR6391	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6392				HR6391 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6393	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6394				HR6393 (Lo-16bits)
HR6395	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6396				HR6395 (Lo-16bits)
HR6397	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6398				HR6397 (Lo-16bits)
HR6399	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6400	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6401	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6402	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6403	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6404	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6405	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6406	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6407	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6408	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6409	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6410	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6411	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6412	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6413	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6414	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6415	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6416	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR6417	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6418	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6419	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6420	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6421	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6422	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6423	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6424	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6425	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6426	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6427	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6428	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6429	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6430	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6431	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6436				HR6435 (Lo-16bits)
HR6437	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6438				HR6437 (Lo-16bits)
HR6439	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6440				HR6439 (Lo-16bits)
HR6441	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6442				HR6441 (Lo-16bits)
HR6443	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6444				HR6443 (Lo-16bits)
HR6445	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)
HR6459	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6462				HR6461 (Lo-16bits)
HR6463	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6464				HR6463 (Lo-16bits)
HR6465	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6466				HR6465 (Lo-16bits)
HR6467	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6468				HR6467 (Lo-16bits)
HR6469	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6470				HR6469 (Lo-16bits)
HR6471	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)
HR6485	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6486				HR6485 (Lo-16bits)
HR6487	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6488				HR6487 (Lo-16bits)
HR6489	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6490				HR6489 (Lo-16bits)
HR6491	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6492				HR6491 (Lo-16bits)
HR6493	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6494				HR6493 (Lo-16bits)
HR6495	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6496	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6497	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6498	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6499	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6500	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6501	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6502	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6503	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6504				HR6503 (Lo-16bits)
HR6505	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6506				HR6505 (Lo-16bits)
HR6507	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6508				HR6507 (Lo-16bits)
HR6509	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6520	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6521	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6522	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6523	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6524	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6525	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6526	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6527	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6528				HR6527 (Lo-16bits)
HR6529	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)
HR6535	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6544	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6545	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6546	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6547	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6548	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6549	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6550	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6551	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6552	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6553	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6554	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6555	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6556	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6557	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6558	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6559	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6560	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6561	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6562	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6563	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6564	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6565	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

Holding Register	Parameter	Access	Length	
HR6566	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6567	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6590				HR6589 (Lo-16bits)
HR6591	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6592				HR6591 (Lo-16bits)
HR6593	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6594				HR6593 (Lo-16bits)
HR6595	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6596				HR6595 (Lo-16bits)
HR6597	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6598				HR6597 (Lo-16bits)
HR6599	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6600				HR6599 (Lo-16bits)
HR6601	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6602				HR6601 (Lo-16bits)
HR6603	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6604				HR6603 (Lo-16bits)
HR6605	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6606				HR6605 (Lo-16bits)
HR6607	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6608				HR6607 (Lo-16bits)
HR6609	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6610				HR6609 (Lo-16bits)
HR6611	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6612				HR6611 (Lo-16bits)
HR6613	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)
HR6625	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6654				HR6653 (Lo-16bits)
HR6655	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6656				HR6655 (Lo-16bits)
HR6657	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6658				HR6657 (Lo-16bits)
HR6659	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6660				HR6659 (Lo-16bits)
HR6661	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6662				HR6661 (Lo-16bits)
HR6663	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6664	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6665	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6666	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6667	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6668	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6669	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6670	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6671	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6672				HR6671 (Lo-16bits)
HR6673	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6674				HR6673 (Lo-16bits)
HR6675	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6676				HR6675 (Lo-16bits)
HR6677	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6678				HR6677 (Lo-16bits)
HR6679	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6680				HR6679 (Lo-16bits)
HR6681	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6682				HR6681 (Lo-16bits)
HR6683	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6684				HR6683 (Lo-16bits)
HR6685	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6694				HR6693 (Lo-16bits)
HR6695	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6696				HR6695 (Lo-16bits)
HR6697	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6698				HR6697 (Lo-16bits)
HR6699	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6700				HR6699 (Lo-16bits)
HR6701	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6702				HR6701 (Lo-16bits)
HR6703	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6704				HR6703 (Lo-16bits)
HR6705	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6706				HR6705 (Lo-16bits)
HR6707	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6708				HR6707 (Lo-16bits)
HR6709	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)
HR6729	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6741	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6750				HR6749 (Lo-16bits)
HR6751	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6752	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6753	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6754	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6755	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6756	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6757	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6758	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6759	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6760	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6761	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6762	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6763	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6764	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6765	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6766	DEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR6767	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6768				HR6767 (Lo-16bits)
HR6769	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6770				HR6769 (Lo-16bits)
HR6771	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6772				HR6771 (Lo-16bits)
HR6773	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6774				HR6773 (Lo-16bits)
HR6775	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6776				HR6775 (Lo-16bits)
HR6777	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6778				HR6777 (Lo-16bits)
HR6779	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6780				HR6779 (Lo-16bits)
HR6781	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6782				HR6781 (Lo-16bits)
HR6783	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6784				HR6783 (Lo-16bits)
HR6785	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6786				HR6785 (Lo-16bits)
HR6787	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6788				HR6787 (Lo-16bits)
HR6789	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6791	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6798				HR6797 (Lo-16bits)
HR6799	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6800				HR6799 (Lo-16bits)
HR6801	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6802				HR6801 (Lo-16bits)
HR6803	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6804				HR6803 (Lo-16bits)
HR6805	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6806				HR6805 (Lo-16bits)
HR6807	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6808				HR6807 (Lo-16bits)
HR6809	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6810				HR6809 (Lo-16bits)
HR6811	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6812				HR6811 (Lo-16bits)
HR6813	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6815	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6816	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6817	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6818	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6819	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6820	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6821	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6822	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6823	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6824	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6825	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6826	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6827	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6828	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6829	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6830	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6831	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6832	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6833	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6834	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6835	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6836	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6837	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR6838	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6839	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6840	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6841	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6842	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6843	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6844	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6845	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6846	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6847	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6854				HR6853 (Lo-16bits)
HR6855	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6856				HR6855 (Lo-16bits)
HR6857	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6858				HR6857 (Lo-16bits)
HR6859	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6860				HR6859 (Lo-16bits)
HR6861	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6862				HR6861 (Lo-16bits)
HR6863	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6864				HR6863 (Lo-16bits)
HR6865	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6866				HR6865 (Lo-16bits)
HR6867	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6868				HR6867 (Lo-16bits)
HR6869	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6878				HR6877 (Lo-16bits)
HR6879	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6880				HR6879 (Lo-16bits)
HR6881	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6882				HR6881 (Lo-16bits)
HR6883	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6884				HR6883 (Lo-16bits)
HR6885	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6886				HR6885 (Lo-16bits)
HR6887	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6888				HR6887 (Lo-16bits)
HR6889	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6890				HR6889 (Lo-16bits)
HR6891	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6892				HR6891 (Lo-16bits)
HR6893	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6894				HR6893 (Lo-16bits)
HR6895	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6896				HR6895 (Lo-16bits)
HR6897	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6898				HR6897 (Lo-16bits)
HR6899	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6900				HR6899 (Lo-16bits)
HR6901	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6902				HR6901 (Lo-16bits)
HR6903	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6904				HR6903 (Lo-16bits)
HR6905	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6906				HR6905 (Lo-16bits)
HR6907	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6908				HR6907 (Lo-16bits)
HR6909	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6910				HR6909 (Lo-16bits)
HR6911	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6912	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6913	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6914	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6915	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6916	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6917	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6918	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6919	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6920				HR6919 (Lo-16bits)
HR6921	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)
HR6925	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6936	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6937	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6938	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6939	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6940	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6941	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6942	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6943	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6952				HR6951 (Lo-16bits)
HR6953	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)
HR6959	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6960	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6961	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6962	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6963	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6964	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6965	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6966	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6967	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6968	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6969	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6970	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6971	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6972	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6973	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6974	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6975	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6982				HR6981 (Lo-16bits)
HR6983	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6984				HR6983 (Lo-16bits)
HR6985	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6986				HR6985 (Lo-16bits)
HR6987	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6988				HR6987 (Lo-16bits)
HR6989	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6990				HR6989 (Lo-16bits)
HR6991	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6992				HR6991 (Lo-16bits)
HR6993	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6994				HR6993 (Lo-16bits)
HR6995	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6996				HR6995 (Lo-16bits)
HR6997	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6998				HR6997 (Lo-16bits)
HR6999	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7000				HR6999 (Lo-16bits)
HR7001	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7002				HR7001 (Lo-16bits)
HR7003	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7004				HR7003 (Lo-16bits)
HR7005	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7006				HR7005 (Lo-16bits)
HR7007	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7008				HR7007 (Lo-16bits)
HR7009	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7010				HR7009 (Lo-16bits)
HR7011	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7012				HR7011 (Lo-16bits)
HR7013	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7014				HR7013 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7015	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7016				HR7015 (Lo-16bits)
HR7017	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7018				HR7017 (Lo-16bits)
HR7019	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7020				HR7019 (Lo-16bits)
HR7021	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7024	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7025	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7026	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7027	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7028	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7029	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7030	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR7031	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7041	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7048	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7049	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7050	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7051	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7052	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7053	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7054	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7055	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7056	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7057	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7058	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7059	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7060	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7061	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7062	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7063	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7064	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR7065	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7066	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7067	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7068	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7069	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7070	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7071	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7072	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7073	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7074	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7075	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7076	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7077	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7078	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7079	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7084				HR7083 (Lo-16bits)
HR7085	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7088				HR7087 (Lo-16bits)
HR7089	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7106				HR7105 (Lo-16bits)
HR7107	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7108				HR7107 (Lo-16bits)
HR7109	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7110				HR7109 (Lo-16bits)
HR7111	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7112				HR7111 (Lo-16bits)
HR7113	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7114				HR7113 (Lo-16bits)
HR7115	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7116				HR7115 (Lo-16bits)
HR7117	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7118				HR7117 (Lo-16bits)
HR7119	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7120				HR7119 (Lo-16bits)
HR7121	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7122				HR7121 (Lo-16bits)
HR7123	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7124				HR7123 (Lo-16bits)
HR7125	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7144	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7145	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7146	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7147	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7148	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7149	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7150	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7151	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)
HR7155	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7168	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7169	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7170	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7171	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7172	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7173	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7174	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7175	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7176	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7177	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7178	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7179	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7180	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7181	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7182	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7183	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7189	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7194				HR7193 (Lo-16bits)
HR7195	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7198				HR7197 (Lo-16bits)
HR7199	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7200				HR7199 (Lo-16bits)
HR7201	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7202				HR7201 (Lo-16bits)
HR7203	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7206				HR7205 (Lo-16bits)
HR7207	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7208				HR7207 (Lo-16bits)
HR7209	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7210				HR7209 (Lo-16bits)
HR7211	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7212				HR7211 (Lo-16bits)
HR7213	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7214				HR7213 (Lo-16bits)
HR7215	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7216				HR7215 (Lo-16bits)
HR7217	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7218				HR7217 (Lo-16bits)
HR7219	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7220				HR7219 (Lo-16bits)
HR7221	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7222				HR7221 (Lo-16bits)
HR7223	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7224				HR7223 (Lo-16bits)
HR7225	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7226				HR7225 (Lo-16bits)
HR7227	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7228				HR7227 (Lo-16bits)
HR7229	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7232	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7233	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7234	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7235	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7236	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7237	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7238	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7239	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7242				HR7241 (Lo-16bits)
HR7243	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)
HR7255	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7256	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7257	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7258	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7259	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7260	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7261	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7262	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7263	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7264	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7265	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7266	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7267	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7268	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7269	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7270	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7271	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7272	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7273	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7274	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7275	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7276	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7277	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7278	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7279	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7280	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7281	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7282	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7283	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7284	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7285	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7286	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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TN00011

Holding Register	Parameter	Access	Length	
HR7287	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7317	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7324				HR7323 (Lo-16bits)
HR7325	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7326				HR7325 (Lo-16bits)
HR7327	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7328				HR7327 (Lo-16bits)
HR7329	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7330				HR7329 (Lo-16bits)
HR7331	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7332				HR7331 (Lo-16bits)
HR7333	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7347	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7352	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7353	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7354	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7355	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7356	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7357	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7358	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7359	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7373	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7376	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7377	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7378	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7379	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7380	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7381	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7382	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7383	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7384	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7385	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7386	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7387	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7388	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7389	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7390	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7391	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7417	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)
HR7427	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)
HR7429	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)
HR7431	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7432				HR7431 (Lo-16bits)
HR7433	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7434				HR7433 (Lo-16bits)
HR7435	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7436				HR7435 (Lo-16bits)
HR7437	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7440	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7441	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7442	CUB3:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7443	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7444	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7445	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7446	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7447	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)
HR7455	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7464	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7465	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7466	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7467	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7468	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7469	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7470	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7471	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7472	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7473	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7474	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7475	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7476	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7477	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7478	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7479	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7480	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7481	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7482	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7483	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7484	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7485	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7486	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7487	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7488	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7489	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7490	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7491	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7492	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7493	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7494	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7495	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7502				HR7501 (Lo-16bits)
HR7503	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)
HR7511	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7518				HR7517 (Lo-16bits)
HR7519	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7526				HR7525 (Lo-16bits)
HR7527	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7528				HR7527 (Lo-16bits)
HR7529	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7530				HR7529 (Lo-16bits)
HR7531	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7532				HR7531 (Lo-16bits)
HR7533	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7534				HR7533 (Lo-16bits)
HR7535	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7536				HR7535 (Lo-16bits)
HR7537	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)
HR7539	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7548				HR7547 (Lo-16bits)
HR7549	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7560	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7561	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7562	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7563	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7564	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7565	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7566	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7567	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7574				HR7573 (Lo-16bits)
HR7575	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7582				HR7581 (Lo-16bits)
HR7583	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7584	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7585	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7586	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7587	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7588	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7589	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7590	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7591	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7592	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7593	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7594	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7595	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7596	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7597	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7598	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7599	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7617	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7618				HR7617 (Lo-16bits)
HR7619	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7620				HR7619 (Lo-16bits)
HR7621	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7622				HR7621 (Lo-16bits)
HR7623	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7624				HR7623 (Lo-16bits)
HR7625	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7630				HR7629 (Lo-16bits)
HR7631	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7632				HR7631 (Lo-16bits)
HR7633	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7634				HR7633 (Lo-16bits)
HR7635	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7636				HR7635 (Lo-16bits)
HR7637	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7638				HR7637 (Lo-16bits)
HR7639	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7640				HR7639 (Lo-16bits)
HR7641	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7642				HR7641 (Lo-16bits)
HR7643	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7644				HR7643 (Lo-16bits)
HR7645	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7648	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7649	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7650	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7651	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7652	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7653	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7654	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7655	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7670				HR7669 (Lo-16bits)
HR7671	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7672	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7673	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7674	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7675	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7676	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7677	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7678	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7679	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7680	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7681	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7682	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7683	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7684	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7685	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7686	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7687	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7688	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7689	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7690	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7691	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7692	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR7693	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7694	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7695	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7696	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7697	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7698	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7699	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7700	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7701	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7702	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7703	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7710				HR7709 (Lo-16bits)
HR7711	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7712				HR7711 (Lo-16bits)
HR7713	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7714				HR7713 (Lo-16bits)
HR7715	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7716				HR7715 (Lo-16bits)
HR7717	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7718				HR7717 (Lo-16bits)
HR7719	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7744				HR7743 (Lo-16bits)
HR7745	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7746				HR7745 (Lo-16bits)
HR7747	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7748				HR7747 (Lo-16bits)
HR7749	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7750				HR7749 (Lo-16bits)
HR7751	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7752				HR7751 (Lo-16bits)
HR7753	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7754				HR7753 (Lo-16bits)
HR7755	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7756				HR7755 (Lo-16bits)
HR7757	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7758				HR7757 (Lo-16bits)
HR7759	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7760				HR7759 (Lo-16bits)
HR7761	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7762				HR7761 (Lo-16bits)
HR7763	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7768	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7769	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7770	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7771	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7772	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7773	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7774	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7775	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7792	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7793	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7794	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7795	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7796	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7797	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7798	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR7799	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7800	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7801	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7802	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7803	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7804	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7805	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7806	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7807	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7808	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7809	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7810	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7811	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7812	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7813	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7814	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7815	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7821	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7832	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7833	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7834	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7835	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7836	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7837	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7838	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7839	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7846				HR7845 (Lo-16bits)
HR7847	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7848				HR7847 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7849	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7850				HR7849 (Lo-16bits)
HR7851	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7852				HR7851 (Lo-16bits)
HR7853	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7854				HR7853 (Lo-16bits)
HR7855	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7856				HR7855 (Lo-16bits)
HR7857	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7858				HR7857 (Lo-16bits)
HR7859	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7860				HR7859 (Lo-16bits)
HR7861	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)
HR7865	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)
HR7875	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7878				HR7877 (Lo-16bits)
HR7879	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)
HR7891	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7894				HR7893 (Lo-16bits)
HR7895	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7896				HR7895 (Lo-16bits)
HR7897	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7898				HR7897 (Lo-16bits)
HR7899	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7900				HR7899 (Lo-16bits)
HR7901	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7904	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7905	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7906	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7907	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7908	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7909	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7910	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7911	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7912	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7913	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7914	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7915	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7916	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7917	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7918	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7919	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7920	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7921	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7922	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7923	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7924	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7925	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7926	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7927	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7928	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7929	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7930	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7931	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7932	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7933	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7934	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7935	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7951	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7952	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7953	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7954	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7955	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7956	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7957	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7958	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7959	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7966				HR7965 (Lo-16bits)
HR7967	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7968				HR7967 (Lo-16bits)
HR7969	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7970				HR7969 (Lo-16bits)
HR7971	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7972				HR7971 (Lo-16bits)
HR7973	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7974				HR7973 (Lo-16bits)
HR7975	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7976				HR7975 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7977	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7978				HR7977 (Lo-16bits)
HR7979	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7980				HR7979 (Lo-16bits)
HR7981	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7982				HR7981 (Lo-16bits)
HR7983	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7984				HR7983 (Lo-16bits)
HR7985	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7986				HR7985 (Lo-16bits)
HR7987	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7988				HR7987 (Lo-16bits)
HR7989	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7990				HR7989 (Lo-16bits)
HR7991	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7992				HR7991 (Lo-16bits)
HR7993	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7994				HR7993 (Lo-16bits)
HR7995	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7996				HR7995 (Lo-16bits)
HR7997	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8007	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8024	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8025	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8026	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8027	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8028	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8029	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8030	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8031	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8032	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8033	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8034	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8035	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8036	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8037	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8038	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8039	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8040	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8041	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8042	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8043	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8044	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8045	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8046	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8047	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8048	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8049	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8050	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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TN00011

Holding Register	Parameter	Access	Length	
HR8051	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8052	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8053	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8054	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8055	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8072	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8073	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8074	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8075	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8076	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8077	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8078	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8079	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8081	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)
HR8103	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8104				HR8103 (Lo-16bits)
HR8105	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8106				HR8105 (Lo-16bits)
HR8107	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8108				HR8107 (Lo-16bits)
HR8109	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8110				HR8109 (Lo-16bits)
HR8111	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8112				HR8111 (Lo-16bits)
HR8113	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8114				HR8113 (Lo-16bits)
HR8115	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8116				HR8115 (Lo-16bits)
HR8117	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8122				HR8121 (Lo-16bits)
HR8123	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)
HR8127	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)
HR8131	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8134				HR8133 (Lo-16bits)
HR8135	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8136				HR8135 (Lo-16bits)
HR8137	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8138				HR8137 (Lo-16bits)
HR8139	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8144	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8145	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8146	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8147	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8148	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8149	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8150	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8151	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8152	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8153	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8154	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8155	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8156	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8157	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8158	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8159	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8160	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8161	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8162	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8163	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8164	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8165	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8166	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8167	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8168	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8169	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8170	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8171	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8172	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8173	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8174	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8175	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8181	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8192	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8193	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8194	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8195	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8196	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8197	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8198	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8199	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8209	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)
HR8229	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8238				HR8237 (Lo-16bits)
HR8239	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)
HR8253	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)
HR8257	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8264	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8265	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8266	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8267	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8268	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8269	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8270	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8271	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8272	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8273	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8274	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8275	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8276	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8277	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8278	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8279	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8280	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8281	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8282	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8283	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8284	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8285	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8286	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8287	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8288	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8289	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8290	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8291	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8292	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8293	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8294	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8295	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8311	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8328	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8329	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8330	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8331	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8332	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8333	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8334	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8335	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8340				HR8339 (Lo-16bits)
HR8341	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8368				HR8367 (Lo-16bits)
HR8369	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8398				HR8397 (Lo-16bits)
HR8399	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8400	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8401	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8402	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8403	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8404	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8405	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8406	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8407	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8408	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8409	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8410	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8411	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8412	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8413	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8414	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8415	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8416	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8417	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8418	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8419	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8420	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8421	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8422	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8423	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8424	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8425	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8426	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8427	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8428	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8429	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8430	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8431	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8441	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8464	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8465	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8466	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8467	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8468	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8469	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8470	UV2:Delay type	R(W)	1	0=DT;1=IDMT

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TN00011

Holding Register	Parameter	Access	Length	
HR8471	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8497	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8527	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)
HR8533	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8536	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8537	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8538	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8539	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8540	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8541	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8542	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8543	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8544	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8545	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8546	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8547	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8548	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8549	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8550	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8551	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8552	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8553	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8554	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8555	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8556	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8557	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8558	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8559	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8560	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8561	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8562	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8563	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8564	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8565	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8566	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8567	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8571	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8596				HR8595 (Lo-16bits)
HR8597	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8598				HR8597 (Lo-16bits)
HR8599	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8600	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8601	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8602	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8603	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8604	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8605	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8606	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8607	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8624				HR8623 (Lo-16bits)
HR8625	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8654				HR8653 (Lo-16bits)
HR8655	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8666				HR8665 (Lo-16bits)
HR8667	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8668				HR8667 (Lo-16bits)
HR8669	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8672	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8673	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8674	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8675	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8676	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8677	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8678	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8679	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8680	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8681	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8682	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8683	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8684	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8685	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8686	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8687	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8688	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8689	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8690	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8691	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8692	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8693	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8694	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8695	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8696	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8697	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8698	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8699	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8700	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8701	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8702	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8703	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8724				HR8723 (Lo-16bits)
HR8725	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8736	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8737	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8738	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8739	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8740	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8741	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8742	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8743	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8750				HR8749 (Lo-16bits)
HR8751	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8754				HR8753 (Lo-16bits)
HR8755	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8778				HR8777 (Lo-16bits)
HR8779	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8808	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8809	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8810	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8811	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8812	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8813	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8814	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8815	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8816	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8817	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8818	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8819	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8820	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8821	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8822	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8823	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8824	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8825	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8826	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8827	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8828	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8829	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8830	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8831	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8832	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8833	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8834	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8835	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8836	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8837	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8838	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8839	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)
HR8847	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8850				HR8849 (Lo-16bits)
HR8851	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8853	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8872	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8873	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8874	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8875	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8876	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8877	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8878	VUB1:Delay type	R(W)	1	0=DT;1=IDMT

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TN00011

Holding Register	Parameter	Access	Length	
HR8879	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8886				HR8885 (Lo-16bits)
HR8887	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8888				HR8887 (Lo-16bits)
HR8889	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8890				HR8889 (Lo-16bits)
HR8891	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8892				HR8891 (Lo-16bits)
HR8893	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8894				HR8893 (Lo-16bits)
HR8895	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8896				HR8895 (Lo-16bits)
HR8897	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8898				HR8897 (Lo-16bits)
HR8899	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8900				HR8899 (Lo-16bits)
HR8901	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8902				HR8901 (Lo-16bits)
HR8903	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8904				HR8903 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8905	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8906				HR8905 (Lo-16bits)
HR8907	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8908				HR8907 (Lo-16bits)
HR8909	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8910				HR8909 (Lo-16bits)
HR8911	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8912				HR8911 (Lo-16bits)
HR8913	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8914				HR8913 (Lo-16bits)
HR8915	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8916				HR8915 (Lo-16bits)
HR8917	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8918				HR8917 (Lo-16bits)
HR8919	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8920				HR8919 (Lo-16bits)
HR8921	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8922				HR8921 (Lo-16bits)
HR8923	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8924				HR8923 (Lo-16bits)
HR8925	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8926				HR8925 (Lo-16bits)
HR8927	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8928				HR8927 (Lo-16bits)
HR8929	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8930				HR8929 (Lo-16bits)
HR8931	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8932				HR8931 (Lo-16bits)
HR8933	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8934				HR8933 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8935	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8936				HR8935 (Lo-16bits)
HR8937	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8938				HR8937 (Lo-16bits)
HR8939	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8940				HR8939 (Lo-16bits)
HR8941	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8942				HR8941 (Lo-16bits)
HR8943	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8944	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8945	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8946	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8947	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8948	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8949	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8950	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8951	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8952	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8953	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8954	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8955	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8956	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8957	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8958	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8959	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8960	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8961	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8962	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8963	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8964	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8965	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8966	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8967	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8968	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8969	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8970	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8971	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8972	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8973	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8974	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8975	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8980				HR8979 (Lo-16bits)
HR8981	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8982				HR8981 (Lo-16bits)
HR8983	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8984				HR8983 (Lo-16bits)
HR8985	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8986				HR8985 (Lo-16bits)
HR8987	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8988				HR8987 (Lo-16bits)
HR8989	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9004				HR9003 (Lo-16bits)
HR9005	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9006				HR9005 (Lo-16bits)
HR9007	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9008	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9009	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9010	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9011	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9012	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9013	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9014	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR9015	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9022				HR9021 (Lo-16bits)
HR9023	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9024				HR9023 (Lo-16bits)
HR9025	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9026				HR9025 (Lo-16bits)
HR9027	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9028				HR9027 (Lo-16bits)
HR9029	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9030				HR9029 (Lo-16bits)
HR9031	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9032				HR9031 (Lo-16bits)
HR9033	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9034				HR9033 (Lo-16bits)
HR9035	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9036				HR9035 (Lo-16bits)
HR9037	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9038				HR9037 (Lo-16bits)
HR9039	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9040				HR9039 (Lo-16bits)
HR9041	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9042				HR9041 (Lo-16bits)
HR9043	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9044				HR9043 (Lo-16bits)
HR9045	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9046				HR9045 (Lo-16bits)
HR9047	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9048				HR9047 (Lo-16bits)
HR9049	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9050				HR9049 (Lo-16bits)
HR9051	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9052				HR9051 (Lo-16bits)
HR9053	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9054				HR9053 (Lo-16bits)
HR9055	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9056				HR9055 (Lo-16bits)
HR9057	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9058				HR9057 (Lo-16bits)
HR9059	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9060				HR9059 (Lo-16bits)
HR9061	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9062				HR9061 (Lo-16bits)
HR9063	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9064				HR9063 (Lo-16bits)
HR9065	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9066				HR9065 (Lo-16bits)
HR9067	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9068				HR9067 (Lo-16bits)
HR9069	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9070				HR9069 (Lo-16bits)
HR9071	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9072				HR9071 (Lo-16bits)
HR9073	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9074				HR9073 (Lo-16bits)
HR9075	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9076				HR9075 (Lo-16bits)
HR9077	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9078				HR9077 (Lo-16bits)
HR9079	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9080	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9081	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9082	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9083	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9084	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9085	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9086	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9087	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9088	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9089	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9090	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9091	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9092	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9093	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9094	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9095	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9096	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9097	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9098	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9099	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9100	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9101	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9102	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9103	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9104	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9105	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9106	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR9107	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9108	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9109	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9110	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9111	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9118				HR9117 (Lo-16bits)
HR9119	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9120				HR9119 (Lo-16bits)
HR9121	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9122				HR9121 (Lo-16bits)
HR9123	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9124				HR9123 (Lo-16bits)
HR9125	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9132				HR9131 (Lo-16bits)
HR9133	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9135	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9144	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9145	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9146	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9147	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9148	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9149	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9150	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9151	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9158				HR9157 (Lo-16bits)
HR9159	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9160				HR9159 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9161	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9162				HR9161 (Lo-16bits)
HR9163	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9164				HR9163 (Lo-16bits)
HR9165	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9166				HR9165 (Lo-16bits)
HR9167	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9168				HR9167 (Lo-16bits)
HR9169	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9170				HR9169 (Lo-16bits)
HR9171	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9172				HR9171 (Lo-16bits)
HR9173	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9174				HR9173 (Lo-16bits)
HR9175	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9176				HR9175 (Lo-16bits)
HR9177	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9178				HR9177 (Lo-16bits)
HR9179	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9180				HR9179 (Lo-16bits)
HR9181	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9182				HR9181 (Lo-16bits)
HR9183	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9184				HR9183 (Lo-16bits)
HR9185	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9186				HR9185 (Lo-16bits)
HR9187	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9188				HR9187 (Lo-16bits)
HR9189	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9190				HR9189 (Lo-16bits)
HR9191	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9194				HR9193 (Lo-16bits)
HR9195	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9196				HR9195 (Lo-16bits)
HR9197	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9198				HR9197 (Lo-16bits)
HR9199	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9200				HR9199 (Lo-16bits)
HR9201	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9202				HR9201 (Lo-16bits)
HR9203	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9204				HR9203 (Lo-16bits)
HR9205	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9206				HR9205 (Lo-16bits)
HR9207	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9208				HR9207 (Lo-16bits)
HR9209	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9210				HR9209 (Lo-16bits)
HR9211	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9216	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9217	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9218	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9219	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9220	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9221	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9222	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9223	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9224	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9225	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9226	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9227	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9228	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9229	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9230	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9231	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9232	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9233	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9234	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9235	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9236	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9237	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9238	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9239	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9240	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9241	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9242	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9243	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9244	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9245	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9246	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9247	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9262				HR9261 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9263	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9264				HR9263 (Lo-16bits)
HR9265	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9266				HR9265 (Lo-16bits)
HR9267	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9268				HR9267 (Lo-16bits)
HR9269	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9280	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9281	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9282	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9283	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9284	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9285	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9286	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9287	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9288				HR9287 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9289	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9294				HR9293 (Lo-16bits)
HR9295	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9296				HR9295 (Lo-16bits)
HR9297	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9302				HR9301 (Lo-16bits)
HR9303	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9304				HR9303 (Lo-16bits)
HR9305	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9306				HR9305 (Lo-16bits)
HR9307	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9308				HR9307 (Lo-16bits)
HR9309	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9310				HR9309 (Lo-16bits)
HR9311	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9316				HR9315 (Lo-16bits)
HR9317	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9320				HR9319 (Lo-16bits)
HR9321	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9322				HR9321 (Lo-16bits)
HR9323	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9324				HR9323 (Lo-16bits)
HR9325	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9326				HR9325 (Lo-16bits)
HR9327	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9328				HR9327 (Lo-16bits)
HR9329	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9346				HR9345 (Lo-16bits)
HR9347	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9352	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9353	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9354	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9355	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9356	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9357	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9358	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9359	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9360	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9361	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9362	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9363	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9364	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9365	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9366	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9367	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9368	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9369	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9370	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9371	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9372	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9373	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9374	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9375	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9376	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9377	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9378	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9379	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9380	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9381	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9382	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9383	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9384	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9385	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9386	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9387	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9388	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9389	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9390	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9391	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9419	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9424	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9425	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9426	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9427	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9428	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9429	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9430	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9431	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9449	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9464	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9465	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9466	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9467	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9468	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9469	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9470	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9471	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9479	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9504	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9505	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9506	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9507	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9508	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9509	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9510	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9511	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9526				HR9525 (Lo-16bits)
HR9527	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9528				HR9527 (Lo-16bits)
HR9529	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9530				HR9529 (Lo-16bits)
HR9531	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9539	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9544	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9545	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9546	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9547	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9548	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9549	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9550	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9551	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9566				HR9565 (Lo-16bits)
HR9567	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9568				HR9567 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9569	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9570				HR9569 (Lo-16bits)
HR9571	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9572				HR9571 (Lo-16bits)
HR9573	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9580				HR9579 (Lo-16bits)
HR9581	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9582				HR9581 (Lo-16bits)
HR9583	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9584	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9585	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9586	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9587	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9588	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9589	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9590	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9591	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9592				HR9591 (Lo-16bits)
HR9593	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9594				HR9593 (Lo-16bits)
HR9595	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9596				HR9595 (Lo-16bits)
HR9597	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9598				HR9597 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9599	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)
HR9601	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9602				HR9601 (Lo-16bits)
HR9603	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9604				HR9603 (Lo-16bits)
HR9605	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9606				HR9605 (Lo-16bits)
HR9607	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9608				HR9607 (Lo-16bits)
HR9609	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9610				HR9609 (Lo-16bits)
HR9611	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9612				HR9611 (Lo-16bits)
HR9613	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9614				HR9613 (Lo-16bits)
HR9615	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9616				HR9615 (Lo-16bits)
HR9617	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9618				HR9617 (Lo-16bits)
HR9619	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9620				HR9619 (Lo-16bits)
HR9621	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9622				HR9621 (Lo-16bits)
HR9623	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9624	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9625	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9626	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9627	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9628	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9629	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9630	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9631	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9632				HR9631 (Lo-16bits)
HR9633	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9634				HR9633 (Lo-16bits)
HR9635	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9636				HR9635 (Lo-16bits)
HR9637	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9638				HR9637 (Lo-16bits)
HR9639	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9640				HR9639 (Lo-16bits)
HR9641	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9642				HR9641 (Lo-16bits)
HR9643	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9644				HR9643 (Lo-16bits)
HR9645	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9659	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9664	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9665	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9666	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9667	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9668	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9669	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9670	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9671	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9689	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9690				HR9689 (Lo-16bits)
HR9691	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)
HR9697	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9718				HR9717 (Lo-16bits)
HR9719	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)
HR9743	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9747	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9748	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9749	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9750	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9751	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9752	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9753	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9754	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9755	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9756	CUB1:I2> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9757	CUB2:I2>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9758	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9759	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9760	OV1:U> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9761	OV2:U>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9762	OV3:U>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9763	OV4:U>>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9764	UV1:U< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9765	UV2:U<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9766	UV3:U<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9767	UV4:U<<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9768	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9769	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9770	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9771	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9772	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled;1=Activated
HR9773	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9774				HR9773 (Lo-16bits)
HR9775	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9776				HR9775 (Lo-16bits)
HR9777	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9778				HR9777 (Lo-16bits)
HR9779	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9780				HR9779 (Lo-16bits)
HR9781	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9782				HR9781 (Lo-16bits)
HR9783	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9784				HR9783 (Lo-16bits)
HR9785	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)
HR9797	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9801	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9802				HR9801 (Lo-16bits)
HR9803	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9804				HR9803 (Lo-16bits)
HR9805	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9806				HR9805 (Lo-16bits)
HR9807	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9808				HR9807 (Lo-16bits)
HR9809	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9810				HR9809 (Lo-16bits)
HR9811	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9812				HR9811 (Lo-16bits)
HR9813	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9814				HR9813 (Lo-16bits)
HR9815	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9816				HR9815 (Lo-16bits)
HR9817	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9818				HR9817 (Lo-16bits)
HR9819	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9820				HR9819 (Lo-16bits)
HR9821	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9822				HR9821 (Lo-16bits)
HR9823	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9824				HR9823 (Lo-16bits)
HR9825	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9826				HR9825 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9827	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9828				HR9827 (Lo-16bits)
HR9829	DIGI:DIs (cpu)	R	2	0b=DI1;1b=DI2;2b=DI3; (Hi-16bits)
HR9830				HR9829 (Lo-16bits)
HR9831	DIGI:DIs (input option 1)	R	2	0b=DI4;1b=DI5;2b=DI6;3b=DI7;4b=DI8;5b=DI9;6b=DI10;7b=DI1
HR9832				HR9831 (Lo-16bits)
HR9833	DIGI:DIs (input option 2)	R	2	0b=DI12;1b=DI13;2b=DI14;3b=DI15;4b=DI16;5b=DI17;6b=DI18
HR9834				HR9833 (Lo-16bits)
HR9835	DIGI:DIs (input option 3)	R	2	0b=DI20;1b=DI21;2b=DI22;3b=DI23;4b=DI24;5b=DI25;6b=DI26
HR9836				HR9835 (Lo-16bits)
HR9837	DIGI:DIs (input option 4)	R	2	0b=D28;1b=DI29;2b=DI30;3b=DI31;4b=DI32;5b=DI33;6b=DI34;
HR9838				HR9837 (Lo-16bits)
HR9839	DIGI:DIs (input option 5)	R	2	0b=DI36;1b=DI37;2b=DI38;3b=DI39;4b=DI40;5b=DI41;6b=DI42
HR9840				HR9839 (Lo-16bits)
HR9841	DIGI:DIs (input option 6)	R	2	0b=DI44;1b=DI45;2b=DI46;3b=DI47;4b=DI48;5b=DI49;6b=DI50
HR9842				HR9841 (Lo-16bits)
HR9843	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9844				HR9843 (Lo-16bits)
HR9845	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9846				HR9845 (Lo-16bits)
HR9847	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9848				HR9847 (Lo-16bits)
HR9849	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9850				HR9849 (Lo-16bits)
HR9851	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9852				HR9851 (Lo-16bits)
HR9853	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9854				HR9853 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9855	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9856				HR9855 (Lo-16bits)
HR9857	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9858				HR9857 (Lo-16bits)
HR9859	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9860	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9861	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled;1=Activated
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

1.5 G Modbus map

The following table lists Modbus holding registers for AQ-G215 devices with firmware G_v2.1.1.3-233.

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.5 - 5. Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

Holding Register	Parameter	Access	Length	
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR454	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1206	POW1:Reset 3ph Energies	RW	1	0=-;1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:Dis 1-32	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7;7b=DI8; (Hi-16bits)
HR1250				HR1249 (Lo-16bits)
HR1251	DIGI:Dis 33-64	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38;6b=DI39
HR1252				HR1251 (Lo-16bits)
HR1253	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b=O8;8b=
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8

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Holding Register	Parameter	Access	Length	
HR1256	SGS:Force SG	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1257	SGS:Force SG change	RW	1	0=Disabled;1=Enabled
HR1258	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=SG1...6;6=
HR1259	SGS:Remote SG change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7=SG7;8=
HR1260	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1261	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1262	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1263	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1264	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1265	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1266	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1267	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1268	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1269	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1271	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1273	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1275	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=C-G-F
HR1277	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1278	DEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1279	DEF1:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1280	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1281	DEF2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1282	DEF2:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1284	DEF3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1285	DEF3:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;

Holding Register	Parameter	Access	Length	
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start;5=Unear
HR1287	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1288	DEF4:I0 Direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1289	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1290	IEF1:U0> Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1292	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1296	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1316	HOC2:Ih>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1317	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1338	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)
HR1342	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1346	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1359	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4=CLPU b
HR1380	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloaded;4=C
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Blocked;4b=2
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1385	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1386	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and angle diff
HR1387	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1389	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1390	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1391	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1394	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Load norma
HR1395	TOLF1:Temp C or F deg	RW	1	0=C;1=F
HR1396	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1397	TOLF1:RTD Sensor	R	1	0=Invalid;1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set;1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=Trip On;5=
HR1401	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=Running
HR1402	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1403	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1404	TOLM1:Time constants	RW	1	0=Single;1=Multiple

Holding Register	Parameter	Access	Length	
HR1405	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1408	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Blocked
HR1411	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1415	OV2:U>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1416	OV3:U>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1417	OV4:U>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1418	UV1:U< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1419	UV2:U<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1420	UV3:U<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1421	UV4:U<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Undervolt Blk On
HR1422	NOV1:U0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1423	NOV1:U0> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1425	NOV2:U0>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1427	NOV3:U0>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1429	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1443	FRQV1:f>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1444	FRQV1:f>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1445	FRQV1:f>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1446	FRQV1:f< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1447	FRQV1:f<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1448	FRQV1:f<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1449	FRQV1:f<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On high overloa

Holding Register	Parameter	Access	Length	
HR1459	OPW1:P> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1460	UPW1:P< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1461	RPW1:Prev> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1463	SEF1:U03rd< Measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1466	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=VHz(1) Blocked;2=VHz(1) Start;3=VHz(1) Alarm;4=
HR1469	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=Alarm
HR1470	ARC1:I/I0 Arc> condition	R	1	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4b=Z3 Trip
HR1471	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1472	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm;2=Blocked
HR1473	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTBusfail;3=VTLinefail;4=Blocked
HR1474	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live VTS Ok SE
HR1475	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR1476	THD1:THD condition	R	1	0=Normal;1=Start;2=Alarm;3=Blocked
HR1477	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On
HR1478	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo cmd;5=BK
HR1479	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR1480	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=SYN1 Vco
HR1481				HR1480 (Lo-16bits)
HR1482	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=SYN2 Vco
HR1483				HR1482 (Lo-16bits)
HR1484	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=SYN3 Vco
HR1485				HR1484 (Lo-16bits)
HR1486	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad

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TN00011

Holding Register	Parameter	Access	Length	
HR1487	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1488	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1489	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1490	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1491	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1492	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1493	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1494	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1495	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1496	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR is runni Requested;14b=AR4 Requested;15b=AR5 Requested;16b=Exec
HR1497				HR1496 (Lo-16bits)
HR1498	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Delay;4=AR
HR1499	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set higher than U
HR1500				HR1499 (Lo-16bits)
HR1501	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Operation bl
HR1502				HR1501 (Lo-16bits)
HR1503	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low set Decre
HR1504	VRG1:Voltage meas condition	R	1	0=Not configured;1=System U12;2=System U23;3=System U31;
HR1505	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLT detected
HR1506	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1507	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLT detected
HR1508	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1509	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLT FW;2=FLT REV
HR1510	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=No voltage

Holding Register	Parameter	Access	Length	
HR1511	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLT detected
HR1512	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLT detected
HR1513	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1514				HR1513 (Lo-16bits)
HR1515	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1518				HR1517 (Lo-16bits)
HR1519	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=A8;8b=A
HR1520				HR1519 (Lo-16bits)
HR1521	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=A39;7b= (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1524				HR1523 (Lo-16bits)
HR1525	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1530				HR1529 (Lo-16bits)
HR1531	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1532				HR1531 (Lo-16bits)
HR1533	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1536				HR1535 (Lo-16bits)
HR1537	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1538				HR1537 (Lo-16bits)
HR1539	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1542				HR1541 (Lo-16bits)
HR1543	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1544				HR1543 (Lo-16bits)
HR1545	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1548				HR1547 (Lo-16bits)
HR1549	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1550				HR1549 (Lo-16bits)
HR1551	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1554				HR1553 (Lo-16bits)
HR1555	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1556				HR1555 (Lo-16bits)
HR1557	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1560				HR1559 (Lo-16bits)
HR1561	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1562				HR1561 (Lo-16bits)
HR1563	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1566				HR1565 (Lo-16bits)
HR1567	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1568				HR1567 (Lo-16bits)
HR1569	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1572				HR1571 (Lo-16bits)
HR1573	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1574				HR1573 (Lo-16bits)
HR1575	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1578				HR1577 (Lo-16bits)
HR1579	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1584				HR1583 (Lo-16bits)
HR1585	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1590				HR1589 (Lo-16bits)
HR1591	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1592				HR1591 (Lo-16bits)
HR1593	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1594				HR1593 (Lo-16bits)
HR1595	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1596				HR1595 (Lo-16bits)
HR1597	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1598				HR1597 (Lo-16bits)
HR1599	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1600				HR1599 (Lo-16bits)
HR1601	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1602				HR1601 (Lo-16bits)
HR1603	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1606				HR1605 (Lo-16bits)
HR1607	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1608				HR1607 (Lo-16bits)
HR1609	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1610				HR1609 (Lo-16bits)
HR1611	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1612				HR1611 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1613	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1614				HR1613 (Lo-16bits)
HR1615	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1616				HR1615 (Lo-16bits)
HR1617	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1618				HR1617 (Lo-16bits)
HR1619	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1620				HR1619 (Lo-16bits)
HR1621	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1622				HR1621 (Lo-16bits)
HR1623	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1624				HR1623 (Lo-16bits)
HR1625	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1626				HR1625 (Lo-16bits)
HR1627	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1630				HR1629 (Lo-16bits)
HR1631	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1632				HR1631 (Lo-16bits)
HR1633	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1634				HR1633 (Lo-16bits)
HR1635	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=VHz(2) Start;2=VHz(2) Alarm;3=VHz(2) Trip;4=VHz(
HR1636	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1639	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1643	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1671	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1688				HR1687 (Lo-16bits)
HR1689	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1690				HR1689 (Lo-16bits)
HR1691	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1692				HR1691 (Lo-16bits)
HR1693	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1694				HR1693 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1695	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1712	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1713	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1714	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1715	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1716	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR1717				HR1716 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1718	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR1719				HR1718 (Lo-16bits)
HR1720	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1721				HR1720 (Lo-16bits)
HR1722	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1723				HR1722 (Lo-16bits)
HR1724	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR1725	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher
HR1726	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked:2=Trip;3=Alarm
HR1727	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	PGS1:PS1 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1730	VJP1:Voltage meas selected	R	1	0=Selection Ok;1=Selection not available
HR1731	PGS1:PS2 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1732	PGS1:PS3 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1733	PGS1:PS4 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1734	PGS1:PS5 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1735	PGS1:PS6 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1736	PGS1:PS7 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1737	PGS1:PS8 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1738	PGS1:PS9 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1739	PGS1:PS10 >/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1740	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1741				HR1740 (Lo-16bits)
HR1742	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1743				HR1742 (Lo-16bits)
HR1744	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1745				HR1744 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1746	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1747				HR1746 (Lo-16bits)
HR1748	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1749				HR1748 (Lo-16bits)
HR1750	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1751				HR1750 (Lo-16bits)
HR1752	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1753				HR1752 (Lo-16bits)
HR1754	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1755				HR1754 (Lo-16bits)
HR1756	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1757				HR1756 (Lo-16bits)
HR1758	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1759				HR1758 (Lo-16bits)
HR1760	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1761				HR1760 (Lo-16bits)
HR1762	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1763				HR1762 (Lo-16bits)
HR1764	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled;1=Activated
HR1767	CTRL:Local/Remote status	R	1	0=Remote;1=Local
HR1768	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1769				HR1768 (Lo-16bits)
HR1770	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1773				HR1772 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1774	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1775				HR1774 (Lo-16bits)
HR1776	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1777				HR1776 (Lo-16bits)
HR1778	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1779				HR1778 (Lo-16bits)
HR1780	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1781				HR1780 (Lo-16bits)
HR1782	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1783				HR1782 (Lo-16bits)
HR1784	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1795				HR1794 (Lo-16bits)
HR1796	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	GSYN:Synchronizer mode	RW	1	0=Disabled;1=Activated
HR1801	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1803	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1829	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1838				HR1837 (Lo-16bits)
HR1839	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1840				HR1839 (Lo-16bits)
HR1841	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	VREC:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1852	VREC:Scale current values to primary	RW	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR1853	VREC:Record fault types (Overcurrrent & Voltage Over/Under) faults	RW	1	0=No;1=Yes
HR1854	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)
HR1862	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	RTB:Measurement value recorder mode	RW	1	0=Disabled;1=Activated
HR1873	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR1874	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)
HR1878	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1879				HR1878 (Lo-16bits)
HR1880	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)
HR1886	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)
HR1888	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR1891				HR1890 (Lo-16bits)
HR1892	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1895				HR1894 (Lo-16bits)
HR1896	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1897				HR1896 (Lo-16bits)
HR1898	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1899				HR1898 (Lo-16bits)
HR1900	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1901				HR1900 (Lo-16bits)
HR1902	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1903				HR1902 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1904	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1907				HR1906 (Lo-16bits)
HR1908	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1909				HR1908 (Lo-16bits)
HR1910	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1919				HR1918 (Lo-16bits)
HR1920	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1921				HR1920 (Lo-16bits)
HR1922	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1923				HR1922 (Lo-16bits)
HR1924	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR1925				HR1924 (Lo-16bits)
HR1926	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1927				HR1926 (Lo-16bits)
HR1928	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1929				HR1928 (Lo-16bits)
HR1930	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1931				HR1930 (Lo-16bits)
HR1932	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1933				HR1932 (Lo-16bits)
HR1934	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1935				HR1934 (Lo-16bits)
HR1936	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1937				HR1936 (Lo-16bits)
HR1938	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1939				HR1938 (Lo-16bits)
HR1940	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1941				HR1940 (Lo-16bits)
HR1942	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1943				HR1942 (Lo-16bits)
HR1944	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1945				HR1944 (Lo-16bits)
HR1946	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1947				HR1946 (Lo-16bits)
HR1948	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1949				HR1948 (Lo-16bits)
HR1950	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1951				HR1950 (Lo-16bits)
HR1952	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1953				HR1952 (Lo-16bits)
HR1954	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1955				HR1954 (Lo-16bits)
HR1956	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1957				HR1956 (Lo-16bits)
HR1958	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1959				HR1958 (Lo-16bits)
HR1960	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1961				HR1960 (Lo-16bits)
HR1962	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR1965				HR1964 (Lo-16bits)
HR1966	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR1967				HR1966 (Lo-16bits)
HR1968	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR1969				HR1968 (Lo-16bits)
HR1970	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1971	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1972	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1973	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1974	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1975				HR1974 (Lo-16bits)
HR1976	NOC1:I> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR1977	NOC1:I> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR1978	NOC1:I> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR1979	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1982				HR1981 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1983	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1984				HR1983 (Lo-16bits)
HR1985	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1986				HR1985 (Lo-16bits)
HR1987	NOC1:Delay type	RW	1	0=DT;1=IDMT
HR1988	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1989				HR1988 (Lo-16bits)
HR1990	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR1991	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR1992	NOC1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR1993	NOC1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR1994	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1995				HR1994 (Lo-16bits)
HR1996	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1997				HR1996 (Lo-16bits)
HR1998	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1999				HR1998 (Lo-16bits)
HR2000	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2001				HR2000 (Lo-16bits)
HR2002	NOC1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2003	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2004	NOC2:I>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri
HR2005	NOC2:I>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2006	NOC2:I>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2007	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2009	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	NOC2:Delay type	RW	1	0=DT;1=IDMT
HR2016	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2017				HR2016 (Lo-16bits)
HR2018	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2019	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2020	NOC2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2021	NOC2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2022	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2023				HR2022 (Lo-16bits)
HR2024	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2025				HR2024 (Lo-16bits)
HR2026	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2027				HR2026 (Lo-16bits)
HR2028	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2029				HR2028 (Lo-16bits)
HR2030	NOC2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2031	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2032	NOC3:I>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Trip
HR2033	NOC3:I>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2034	NOC3:I>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C

Holding Register	Parameter	Access	Length	
HR2035	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR2044	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2045				HR2044 (Lo-16bits)
HR2046	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2047	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2048	NOC3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2049	NOC3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2050	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2051				HR2050 (Lo-16bits)
HR2052	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2053				HR2052 (Lo-16bits)
HR2054	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2055				HR2054 (Lo-16bits)
HR2056	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2057				HR2056 (Lo-16bits)
HR2058	NOC3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2059	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2060	NOC4:I>>>> Phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Trip B;6=Tri

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Holding Register	Parameter	Access	Length	
HR2061	NOC4:I>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2062	NOC4:I>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2063	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2068				HR2067 (Lo-16bits)
HR2069	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2070				HR2069 (Lo-16bits)
HR2071	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR2072	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2073				HR2072 (Lo-16bits)
HR2074	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2075	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2076	NOC4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2077	NOC4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2078	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2079				HR2078 (Lo-16bits)
HR2080	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2081				HR2080 (Lo-16bits)
HR2082	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2083				HR2082 (Lo-16bits)
HR2084	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2085				HR2084 (Lo-16bits)
HR2086	NOC4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=

Holding Register	Parameter	Access	Length	
HR2087	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2088	DOC1:Idir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2089	DOC1:Idir> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2090	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2091				HR2090 (Lo-16bits)
HR2092	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2093				HR2092 (Lo-16bits)
HR2094	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2095				HR2094 (Lo-16bits)
HR2096	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2097				HR2096 (Lo-16bits)
HR2098	DOC2:Idir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2099	DOC2:Idir>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2100	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2101				HR2100 (Lo-16bits)
HR2102	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2103				HR2102 (Lo-16bits)
HR2104	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2105				HR2104 (Lo-16bits)
HR2106	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2107				HR2106 (Lo-16bits)
HR2108	DOC3:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2109	DOC3:Idir>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2110	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2111				HR2110 (Lo-16bits)
HR2112	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2113				HR2112 (Lo-16bits)
HR2114	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2115				HR2114 (Lo-16bits)
HR2116	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2117				HR2116 (Lo-16bits)
HR2118	DOC4:Idir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2119	DOC4:Idir>>>> Last fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-C
HR2120	DOC4:Idir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2121				HR2120 (Lo-16bits)
HR2122	DOC4:Idir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2123				HR2122 (Lo-16bits)
HR2124	DOC4:Idir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2125				HR2124 (Lo-16bits)
HR2126	DOC4:Idir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2127				HR2126 (Lo-16bits)
HR2128	NEF1:I0> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2129	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2130				HR2129 (Lo-16bits)
HR2131	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR2134	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2135				HR2134 (Lo-16bits)
HR2136	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2137	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2138	NEF1:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2139	NEF1:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2140	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	NEF1:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2149	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2150	NEF2:I0>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2151	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2152				HR2151 (Lo-16bits)
HR2153	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2154				HR2153 (Lo-16bits)
HR2155	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR2156	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2157				HR2156 (Lo-16bits)
HR2158	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2159	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2160	NEF2:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2161	NEF2:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2162	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2165				HR2164 (Lo-16bits)
HR2166	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2167				HR2166 (Lo-16bits)
HR2168	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2169				HR2168 (Lo-16bits)
HR2170	NEF2:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2171	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2172	NEF3:I0>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2173	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR2178	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2179				HR2178 (Lo-16bits)
HR2180	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2181	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2182	NEF3:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2183	NEF3:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2184	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2187				HR2186 (Lo-16bits)
HR2188	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2189				HR2188 (Lo-16bits)
HR2190	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2191				HR2190 (Lo-16bits)
HR2192	NEF3:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2193	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2194	NEF4:I0>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2195	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR2200	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2201				HR2200 (Lo-16bits)
HR2202	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2203	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2204	NEF4:Delay char. ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2205	NEF4:Non standard Delay char.	RW	1	0=RI-type;1=RD-type
HR2206	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2209				HR2208 (Lo-16bits)
HR2210	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2211				HR2210 (Lo-16bits)
HR2212	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2213				HR2212 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2214	NEF4:Select SG	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7=SG8;8=
HR2215	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2216	DEF1:I0dir> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2217	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2218				HR2217 (Lo-16bits)
HR2219	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2220				HR2219 (Lo-16bits)
HR2221	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2222				HR2221 (Lo-16bits)
HR2223	DEF2:I0dir>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2224	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2225				HR2224 (Lo-16bits)
HR2226	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2227				HR2226 (Lo-16bits)
HR2228	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2229				HR2228 (Lo-16bits)
HR2230	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip
HR2231	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2232				HR2231 (Lo-16bits)
HR2233	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults;1=Start;2=Trip

Holding Register	Parameter	Access	Length	
HR2238	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2239				HR2238 (Lo-16bits)
HR2240	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2241				HR2240 (Lo-16bits)
HR2242	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2243				HR2242 (Lo-16bits)
HR2244	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2245	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2246	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2247	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2248	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2249	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2250	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2251	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2252	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2253	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2254	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2255	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2256	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2257	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2258	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2259	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR2260	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR2261	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2266				HR2265 (Lo-16bits)
HR2267	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2272				HR2271 (Lo-16bits)
HR2273	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2286				HR2285 (Lo-16bits)
HR2287	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2290				HR2289 (Lo-16bits)
HR2291	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2298				HR2297 (Lo-16bits)
HR2299	ROC1:Idrw> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2300	ROC1:Idrw> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2301	ROC2:Idrw>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2302	ROC2:Idrw>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2303	ROC3:Idrw>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2304	ROC3:Idrw>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2305	ROC4:Idrw>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2306	ROC4:Idrw>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2307	ROC5:Idrw>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2308	ROC5:Idrw>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2309	ROC6:Idrw>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2310	ROC6:Idrw>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2311	ROC7:Idrw>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2312	ROC7:Idrw>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2313	ROC8:Idrw>>>>>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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Holding Register	Parameter	Access	Length	
HR2314	ROC8:Idrw>>>>>>>> Current direction	R	1	0=Undefined;1=Forward;2=Reverse
HR2315	ROV1:Urw>/< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2316	ROV2:Urw>>/<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2317	ROV3:Urw>>>/<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2318	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR2319	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=Start On (Alarm);3=Start On (Alarm & Tr
HR2320	TOLM1:TM> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2321	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2322	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2323	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2324	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)
HR2328	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2331				HR2330 (Lo-16bits)
HR2332	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2337				HR2336 (Lo-16bits)
HR2338	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2339				HR2338 (Lo-16bits)
HR2340	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2343				HR2342 (Lo-16bits)
HR2344	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2345				HR2344 (Lo-16bits)
HR2346	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2363				HR2362 (Lo-16bits)
HR2364	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)
HR2370	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2379				HR2378 (Lo-16bits)
HR2380	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2381				HR2380 (Lo-16bits)
HR2382	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2383				HR2382 (Lo-16bits)
HR2384	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2385				HR2384 (Lo-16bits)
HR2386	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2387				HR2386 (Lo-16bits)
HR2388	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2389				HR2388 (Lo-16bits)
HR2390	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2391				HR2390 (Lo-16bits)
HR2392	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2393				HR2392 (Lo-16bits)
HR2394	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2395				HR2394 (Lo-16bits)
HR2396	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2397				HR2396 (Lo-16bits)
HR2398	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2401				HR2400 (Lo-16bits)
HR2402	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2403				HR2402 (Lo-16bits)
HR2404	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2417				HR2416 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2418	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2419				HR2418 (Lo-16bits)
HR2420	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	TOLT1:TT> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2437	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2438	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2439	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2440	TOLF1:TF> Setting alarm	R	1	0=SF setting ok;1=Service factor set fault. Override to 1.0
HR2441	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault. Override to 1.0
HR2442	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok;1=Nominal current set fault. Override
HR2443	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient k
HR2444	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)
HR2446	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2447				HR2446 (Lo-16bits)
HR2448	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)
HR2456	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2471				HR2470 (Lo-16bits)
HR2472	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)
HR2496	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)
HR2498	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2499				HR2498 (Lo-16bits)
HR2500	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)
HR2508	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR2515				HR2514 (Lo-16bits)
HR2516	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR2517	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR2518	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2519				HR2518 (Lo-16bits)
HR2520	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2521				HR2520 (Lo-16bits)
HR2522	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2523				HR2522 (Lo-16bits)
HR2524	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2525				HR2524 (Lo-16bits)
HR2526	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2527				HR2526 (Lo-16bits)
HR2528	ARC1:I/I0 Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2529				HR2528 (Lo-16bits)
HR2530	ARC1:I/I0 Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2531				HR2530 (Lo-16bits)
HR2532	ARC1:I/I0 Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2533				HR2532 (Lo-16bits)
HR2534	ARC1:I/I0 Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2535				HR2534 (Lo-16bits)
HR2536	ARC1:I/I0 Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2537				HR2536 (Lo-16bits)
HR2538	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2539				HR2538 (Lo-16bits)
HR2540	DIGI:DI1 Counter Clear	RW	1	0=-;1=Clear
HR2541	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	DIGI:DI2 Counter Clear	RW	1	0=-;1=Clear
HR2544	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2545				HR2544 (Lo-16bits)
HR2546	DIGI:DI3 Counter Clear	RW	1	0=-;1=Clear
HR2547	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	DIGI:DI4 Counter Clear	RW	1	0=-;1=Clear
HR2550	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2551				HR2550 (Lo-16bits)
HR2552	DIGI:DI5 Counter Clear	RW	1	0=-;1=Clear

Holding Register	Parameter	Access	Length	
HR2553	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	DIGI:DI6 Counter Clear	RW	1	0=-;1=Clear
HR2556	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2557				HR2556 (Lo-16bits)
HR2558	DIGI:DI7 Counter Clear	RW	1	0=-;1=Clear
HR2559	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	DIGI:DI8 Counter Clear	RW	1	0=-;1=Clear
HR2562	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2563				HR2562 (Lo-16bits)
HR2564	DIGI:DI9 Counter Clear	RW	1	0=-;1=Clear
HR2565	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	DIGI:DI10 Counter Clear	RW	1	0=-;1=Clear
HR2568	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2569				HR2568 (Lo-16bits)
HR2570	DIGI:DI11 Counter Clear	RW	1	0=-;1=Clear
HR2571	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	DIGI:DI12 Counter Clear	RW	1	0=-;1=Clear
HR2574	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2575				HR2574 (Lo-16bits)
HR2576	DIGI:DI13 Counter Clear	RW	1	0=-;1=Clear
HR2577	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	DIGI:DI14 Counter Clear	RW	1	0=-;1=Clear
HR2580	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2581				HR2580 (Lo-16bits)
HR2582	DIGI:DI15 Counter Clear	RW	1	0=-;1=Clear

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Holding Register	Parameter	Access	Length	
HR2583	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	DIGI:DI16 Counter Clear	RW	1	0=-;1=Clear
HR2586	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2587				HR2586 (Lo-16bits)
HR2588	DIGI:DI17 Counter Clear	RW	1	0=-;1=Clear
HR2589	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)
HR2591	DIGI:DI18 Counter Clear	RW	1	0=-;1=Clear
HR2592	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2593				HR2592 (Lo-16bits)
HR2594	DIGI:DI19 Counter Clear	RW	1	0=-;1=Clear
HR2595	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	DIGI:DI20 Counter Clear	RW	1	0=-;1=Clear
HR2598	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2599				HR2598 (Lo-16bits)
HR2600	DIGI:DI21 Counter Clear	RW	1	0=-;1=Clear
HR2601	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	DIGI:DI22 Counter Clear	RW	1	0=-;1=Clear
HR2604	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	DIGI:DI23 Counter Clear	RW	1	0=-;1=Clear
HR2607	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2608				HR2607 (Lo-16bits)
HR2609	DIGI:DI24 Counter Clear	RW	1	0=-;1=Clear
HR2610	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2611				HR2610 (Lo-16bits)
HR2612	DIGI:DI25 Counter Clear	RW	1	0=-;1=Clear

Holding Register	Parameter	Access	Length	
HR2613	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	DIGI:DI26 Counter Clear	RW	1	0=-;1=Clear
HR2616	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2617				HR2616 (Lo-16bits)
HR2618	DIGI:DI27 Counter Clear	RW	1	0=-;1=Clear
HR2619	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)
HR2621	DIGI:DI28 Counter Clear	RW	1	0=-;1=Clear
HR2622	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2623				HR2622 (Lo-16bits)
HR2624	DIGI:DI29 Counter Clear	RW	1	0=-;1=Clear
HR2625	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	DIGI:DI30 Counter Clear	RW	1	0=-;1=Clear
HR2628	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2629				HR2628 (Lo-16bits)
HR2630	DIGI:DI31 Counter Clear	RW	1	0=-;1=Clear
HR2631	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2632				HR2631 (Lo-16bits)
HR2633	DIGI:DI32 Counter Clear	RW	1	0=-;1=Clear
HR2634	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2635				HR2634 (Lo-16bits)
HR2636	DIGI:DI33 Counter Clear	RW	1	0=-;1=Clear
HR2637	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DIGI:DI34 Counter Clear	RW	1	0=-;1=Clear
HR2640	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2641				HR2640 (Lo-16bits)
HR2642	DIGI:DI35 Counter Clear	RW	1	0=-;1=Clear

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Holding Register	Parameter	Access	Length	
HR2643	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DIGI:DI36 Counter Clear	RW	1	0=-;1=Clear
HR2646	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2647				HR2646 (Lo-16bits)
HR2648	DIGI:DI37 Counter Clear	RW	1	0=-;1=Clear
HR2649	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2650				HR2649 (Lo-16bits)
HR2651	DIGI:DI38 Counter Clear	RW	1	0=-;1=Clear
HR2652	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2653				HR2652 (Lo-16bits)
HR2654	DIGI:DI39 Counter Clear	RW	1	0=-;1=Clear
HR2655	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DIGI:DI40 Counter Clear	RW	1	0=-;1=Clear
HR2658	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2659				HR2658 (Lo-16bits)
HR2660	DIGI:DI41 Counter Clear	RW	1	0=-;1=Clear
HR2661	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DIGI:DI42 Counter Clear	RW	1	0=-;1=Clear
HR2664	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2665				HR2664 (Lo-16bits)
HR2666	DIGI:DI43 Counter Clear	RW	1	0=-;1=Clear
HR2667	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DIGI:DI44 Counter Clear	RW	1	0=-;1=Clear
HR2670	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2671				HR2670 (Lo-16bits)
HR2672	DIGI:DI45 Counter Clear	RW	1	0=-;1=Clear

Holding Register	Parameter	Access	Length	
HR2673	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DIGI:DI46 Counter Clear	RW	1	0=-;1=Clear
HR2676	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2677				HR2676 (Lo-16bits)
HR2678	DIGI:DI47 Counter Clear	RW	1	0=-;1=Clear
HR2679	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DIGI:DI48 Counter Clear	RW	1	0=-;1=Clear
HR2682	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2683				HR2682 (Lo-16bits)
HR2684	DIGI:DI49 Counter Clear	RW	1	0=-;1=Clear
HR2685	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DIGI:DI50 Counter Clear	RW	1	0=-;1=Clear
HR2688	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2689				HR2688 (Lo-16bits)
HR2690	DIGI:DI51 Counter Clear	RW	1	0=-;1=Clear
HR2691	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2710	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2711	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2712	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2713	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2714	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2715	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2716	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2717	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)
HR2727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2728				HR2727 (Lo-16bits)
HR2729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2730				HR2729 (Lo-16bits)
HR2731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)
HR2733	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2734	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2735	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2736	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2737	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2738	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2739	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2740	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2741	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2742	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2743	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2744	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2745	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2746	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2747	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2748	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2749	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2750	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2751	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2752	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR2753	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2754	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2755	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2756	NOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2757	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2758	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2759	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2760	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2761	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2762	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2763	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2764	NOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2765	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2830	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2831	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2832	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2833	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2834	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2835	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2836	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2837	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2854	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2855	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2856	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2857	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2858	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2859	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2860	NOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR2861	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2878	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2879	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2880	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2881	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2882	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2883	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2884	NOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR2885	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2886	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2887	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2888	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2889	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2890	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2891	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2892	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2893	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2903	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2910	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2911	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2912	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2913	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2914	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2915	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2916	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2917	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)
HR2927	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2928				HR2927 (Lo-16bits)
HR2929	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2930				HR2929 (Lo-16bits)
HR2931	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2932				HR2931 (Lo-16bits)
HR2933	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2934	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2935	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2936	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2937	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2938	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2939	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2940	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2941	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2942	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2943	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2944	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2945	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2946	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2947	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2948	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2949	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2950	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2951	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2952	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR2953	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2954	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2955	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2956	NOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR2957	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2958	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2959	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2960	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2961	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2962	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2963	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2964	NOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR2965	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2974				HR2973 (Lo-16bits)
HR2975	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2976				HR2975 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2977	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2978				HR2977 (Lo-16bits)
HR2979	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2980				HR2979 (Lo-16bits)
HR2981	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2982				HR2981 (Lo-16bits)
HR2983	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2984				HR2983 (Lo-16bits)
HR2985	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2986				HR2985 (Lo-16bits)
HR2987	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2988				HR2987 (Lo-16bits)
HR2989	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3004				HR3003 (Lo-16bits)
HR3005	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3006				HR3005 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3007	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3008				HR3007 (Lo-16bits)
HR3009	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3010				HR3009 (Lo-16bits)
HR3011	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3012				HR3011 (Lo-16bits)
HR3013	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)
HR3015	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3018				HR3017 (Lo-16bits)
HR3019	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3030	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3031	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3032	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3033	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3034	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3035	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3036	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3037	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3038				HR3037 (Lo-16bits)
HR3039	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3040				HR3039 (Lo-16bits)
HR3041	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3042				HR3041 (Lo-16bits)
HR3043	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3044				HR3043 (Lo-16bits)
HR3045	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3046				HR3045 (Lo-16bits)
HR3047	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3048				HR3047 (Lo-16bits)
HR3049	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3050				HR3049 (Lo-16bits)
HR3051	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3052				HR3051 (Lo-16bits)
HR3053	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3054	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3055	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3056	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3057	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3058	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3059	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3060	NOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3061	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3078	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3079	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3080	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3081	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3082	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3083	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3084	NOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3085	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3086	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3087	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3088	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3089	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3090	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3091	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3092	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3093	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3103	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3110	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3111	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3112	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3113	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3114	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3115	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3116	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3117	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3126				HR3125 (Lo-16bits)
HR3127	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3128				HR3127 (Lo-16bits)
HR3129	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3130				HR3129 (Lo-16bits)
HR3131	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3132				HR3131 (Lo-16bits)
HR3133	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3134	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3135	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3136	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3137	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3138	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3139	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3140	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3141	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3142	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3143	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3144	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3145	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3146	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3147	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3148	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3149	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3150	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3151	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3152	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR3153	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3154	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3155	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3156	NOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3157	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3158	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3159	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3160	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3161	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3162	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3163	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3164	NOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3165	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3177	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3206				HR3205 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3207	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3208				HR3207 (Lo-16bits)
HR3209	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3210				HR3209 (Lo-16bits)
HR3211	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3230	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3231	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3232	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3233	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3234	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3235	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3236	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3237	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3238				HR3237 (Lo-16bits)
HR3239	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3240				HR3239 (Lo-16bits)
HR3241	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3248				HR3247 (Lo-16bits)
HR3249	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	NOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3254	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3255	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3256	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3257	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3258	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3259	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3260	NOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3261	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3278	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3279	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3280	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3281	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3282	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3283	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3284	NOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3285	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3286	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3287	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3288	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3289	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3290	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3291	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3292	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3293	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3303	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3310	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3311	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3312	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3313	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3314	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3315	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3316	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3317	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)
HR3327	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3330				HR3329 (Lo-16bits)
HR3331	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3334	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3335	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3336	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3337	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3338	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3339	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3340	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3341	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3342	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3343	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3344	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3345	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3346	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3347	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3348	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3349	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3350	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3351	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3352	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR3353	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3354	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3355	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3356	NOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3357	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3358	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3359	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3360	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3361	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3362	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3363	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3364	NOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3365	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3374				HR3373 (Lo-16bits)
HR3375	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3377	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3382				HR3381 (Lo-16bits)
HR3383	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3384				HR3383 (Lo-16bits)
HR3385	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3386				HR3385 (Lo-16bits)
HR3387	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3388				HR3387 (Lo-16bits)
HR3389	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3390				HR3389 (Lo-16bits)
HR3391	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3392				HR3391 (Lo-16bits)
HR3393	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3394				HR3393 (Lo-16bits)
HR3395	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3396				HR3395 (Lo-16bits)
HR3397	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)
HR3405	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3407	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3410				HR3409 (Lo-16bits)
HR3411	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)
HR3415	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3430	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3431	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3432	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3433	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3434	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3435	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3436	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3437	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3440				HR3439 (Lo-16bits)
HR3441	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3446				HR3445 (Lo-16bits)
HR3447	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3448				HR3447 (Lo-16bits)
HR3449	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3450				HR3449 (Lo-16bits)
HR3451	NOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3452				HR3451 (Lo-16bits)
HR3453	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3454	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3455	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3456	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3457	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3458	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3459	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3460	NOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3461	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3470				HR3469 (Lo-16bits)
HR3471	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3472				HR3471 (Lo-16bits)
HR3473	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3474				HR3473 (Lo-16bits)
HR3475	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3476				HR3475 (Lo-16bits)
HR3477	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3478	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3479	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3480	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3481	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3482	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3483	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3484	NOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3485	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3486	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3487	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3488	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3489	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3490	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3491	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3492	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3493	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3494	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3495	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3496	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3497	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3498	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir

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Holding Register	Parameter	Access	Length	
HR3499	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3500	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3501	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)
HR3519	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3523	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)
HR3547	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)
HR3549	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3550	DOC1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3551	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3552	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3553	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3554	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3555	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3556	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3557	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)
HR3559	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3566				HR3565 (Lo-16bits)
HR3567	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3568				HR3567 (Lo-16bits)
HR3569	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3570				HR3569 (Lo-16bits)
HR3571	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3572				HR3571 (Lo-16bits)
HR3573	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3574	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3575	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3576	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3577	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3578	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3579	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3580	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3581	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3582	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3583	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3584	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3585	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3586	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3587	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3588	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3589	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3590	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3591	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3592	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3593	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3594	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3595	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3596	DOC1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3597	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3598	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3599	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3600	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3601	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3602	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3603	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3604	DOC1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3605	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3614				HR3613 (Lo-16bits)
HR3615	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3616				HR3615 (Lo-16bits)
HR3617	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3618				HR3617 (Lo-16bits)
HR3619	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3620				HR3619 (Lo-16bits)
HR3621	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3622				HR3621 (Lo-16bits)
HR3623	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3624				HR3623 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3625	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3626				HR3625 (Lo-16bits)
HR3627	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3628				HR3627 (Lo-16bits)
HR3629	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3630				HR3629 (Lo-16bits)
HR3631	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3632				HR3631 (Lo-16bits)
HR3633	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3634				HR3633 (Lo-16bits)
HR3635	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3636				HR3635 (Lo-16bits)
HR3637	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3652				HR3651 (Lo-16bits)
HR3653	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3655	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)
HR3659	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3670	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3671	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3672	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3673	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3674	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3675	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3676	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3677	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3680				HR3679 (Lo-16bits)
HR3681	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3686				HR3685 (Lo-16bits)
HR3687	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3688				HR3687 (Lo-16bits)
HR3689	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3690				HR3689 (Lo-16bits)
HR3691	DOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3692				HR3691 (Lo-16bits)
HR3693	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3694	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3695	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3696	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3697	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3698	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3699	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3700	DOC1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3701	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3703	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3710				HR3709 (Lo-16bits)
HR3711	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3712				HR3711 (Lo-16bits)
HR3713	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3714				HR3713 (Lo-16bits)
HR3715	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3716				HR3715 (Lo-16bits)
HR3717	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3718	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3719	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3720	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3721	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3722	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3723	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3724	DOC1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3725	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3726	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3727	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3728	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3729	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3730	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3731	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3732	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3733	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3734	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3735	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3736	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3737	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3738	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3739	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3740	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3741	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)
HR3777	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3790	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3791	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3792	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3793	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3794	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3795	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3796	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3797	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3799	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3806				HR3805 (Lo-16bits)
HR3807	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3808				HR3807 (Lo-16bits)
HR3809	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3810				HR3809 (Lo-16bits)
HR3811	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3812				HR3811 (Lo-16bits)
HR3813	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3814	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3815	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3816	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3817	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3818	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3819	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3820	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3821	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3822	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3823	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR3824	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3825	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3826	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3827	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3828	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3829	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3830	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3831	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3832	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3833	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3834	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3835	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3836	DOC2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR3837	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3838	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3839	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3840	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3841	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3842	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3843	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3844	DOC2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR3845	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)
HR3851	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3862				HR3861 (Lo-16bits)
HR3863	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3864				HR3863 (Lo-16bits)
HR3865	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3866				HR3865 (Lo-16bits)
HR3867	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3874				HR3873 (Lo-16bits)
HR3875	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3904				HR3903 (Lo-16bits)
HR3905	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3910	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3911	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3912	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3913	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3914	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3915	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3916	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3917	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3927	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)
HR3933	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3934	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3935	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3936	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3937	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3938	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3939	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3940	DOC2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR3941	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR3952				HR3951 (Lo-16bits)
HR3953	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3958	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3959	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3960	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3961	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3962	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3963	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3964	DOC2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR3965	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3966	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3967	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3968	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3969	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3970	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3971	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3972	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3973	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3974	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3975	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3976	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3977	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3978	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3979	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3980	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3981	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4017	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4030	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4031	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4032	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4033	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4034	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4035	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4036	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR4037	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4045	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4054	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4055	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4056	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4057	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4058	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4059	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4060	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4061	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4062	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4063	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4064	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4065	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4066	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4067	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4068	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR4069	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4070	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4071	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4072	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4073	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4074	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4075	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4076	DOC3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4077	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4078	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4079	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4080	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4081	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4082	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4083	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4084	DOC3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4085	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)
HR4089	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4090				HR4089 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4091	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4094				HR4093 (Lo-16bits)
HR4095	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)
HR4099	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)
HR4101	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4108				HR4107 (Lo-16bits)
HR4109	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4112				HR4111 (Lo-16bits)
HR4113	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4114				HR4113 (Lo-16bits)
HR4115	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4116				HR4115 (Lo-16bits)
HR4117	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4120				HR4119 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4121	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)
HR4141	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4150	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4151	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4152	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4153	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4154	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4155	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4156	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4157	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4166				HR4165 (Lo-16bits)
HR4167	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4168				HR4167 (Lo-16bits)
HR4169	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4170				HR4169 (Lo-16bits)
HR4171	DOC3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4172				HR4171 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4173	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4174	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4175	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4176	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4177	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4178	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4179	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4180	DOC3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4181	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4190				HR4189 (Lo-16bits)
HR4191	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4192				HR4191 (Lo-16bits)
HR4193	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4194				HR4193 (Lo-16bits)
HR4195	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4196				HR4195 (Lo-16bits)
HR4197	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4198	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4199	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4200	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4201	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4202	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4203	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4204	DOC3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4205	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4206	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4207	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4208	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4209	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4210	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4211	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4212	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4213	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4214	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR4215	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4216	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4217	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4218	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4219	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4220	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4221	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4265	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4270	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4271	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4272	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4273	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4274	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4275	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4276	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4277	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4286				HR4285 (Lo-16bits)
HR4287	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4288				HR4287 (Lo-16bits)
HR4289	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4290				HR4289 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4291	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4292				HR4291 (Lo-16bits)
HR4293	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4294	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4295	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4296	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4297	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4298	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4299	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4300	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4301	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4302	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4303	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4304	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4305	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4306	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4307	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4308	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4309	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4310	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4311	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4312	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4313	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4314	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4315	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4316	DOC4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4317	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4318	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4319	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4320	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4321	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4322	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4323	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4324	DOC4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4325	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4336				HR4335 (Lo-16bits)
HR4337	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4338				HR4337 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4339	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4340				HR4339 (Lo-16bits)
HR4341	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4342				HR4341 (Lo-16bits)
HR4343	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4366				HR4365 (Lo-16bits)
HR4367	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4368				HR4367 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4369	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4370				HR4369 (Lo-16bits)
HR4371	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4372				HR4371 (Lo-16bits)
HR4373	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4390	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4391	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4392	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4393	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4394	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4395	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4396	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4397	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4414	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4415	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4416	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4417	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4418	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4419	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4420	DOC4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4421	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4438	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4439	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4440	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4441	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4442	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4443	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4444	DOC4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4445	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4446	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4447	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4448	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4449	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4450	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4451	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4452	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4453	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4470	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4471	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4472	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4473	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4474	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4475	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4476	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4477	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4492				HR4491 (Lo-16bits)
HR4493	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4494	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4495	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4496	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4497	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4498	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4499	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4500	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4501	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4502	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4503	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4504	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4505	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4506	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4507	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4508	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4509	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4510	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4511	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4512	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4513	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4514	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4515	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4516	NEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4517	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4518	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4519	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4520	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4521	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4522	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4523	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4524	NEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4525	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4534				HR4533 (Lo-16bits)
HR4535	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4538				HR4537 (Lo-16bits)
HR4539	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4540				HR4539 (Lo-16bits)
HR4541	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4542				HR4541 (Lo-16bits)
HR4543	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4544				HR4543 (Lo-16bits)
HR4545	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)
HR4549	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4566				HR4565 (Lo-16bits)
HR4567	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4568				HR4567 (Lo-16bits)
HR4569	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4570				HR4569 (Lo-16bits)
HR4571	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4572				HR4571 (Lo-16bits)
HR4573	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4590	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4591	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4592	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4593	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4594	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4595	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4596	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4597	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4598				HR4597 (Lo-16bits)
HR4599	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4600				HR4599 (Lo-16bits)
HR4601	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4602				HR4601 (Lo-16bits)
HR4603	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4604				HR4603 (Lo-16bits)
HR4605	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4606				HR4605 (Lo-16bits)
HR4607	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4608				HR4607 (Lo-16bits)
HR4609	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4610				HR4609 (Lo-16bits)
HR4611	NEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4612				HR4611 (Lo-16bits)
HR4613	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4614	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4615	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4616	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4617	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4618	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4619	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4620	NEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4621	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4638	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4639	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4640	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4641	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4642	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4643	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4644	NEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4645	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4646	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4647	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4648	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4649	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4650	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4651	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4652	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4653	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4670	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4671	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4672	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4673	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4674	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4675	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4676	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4677	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4686				HR4685 (Lo-16bits)
HR4687	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4688				HR4687 (Lo-16bits)
HR4689	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4690				HR4689 (Lo-16bits)
HR4691	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4692				HR4691 (Lo-16bits)
HR4693	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4694	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4695	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4696	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4697	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4698	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4699	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4700	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4701	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4702	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4703	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4704	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4705	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4706	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4707	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4708	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4709	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4710	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4711	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4712	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4713	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4714	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4715	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4716	NEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4717	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4718	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4719	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4720	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4721	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4722	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4723	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4724	NEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4725	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)
HR4731	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4734				HR4733 (Lo-16bits)
HR4735	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4736				HR4735 (Lo-16bits)
HR4737	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4738				HR4737 (Lo-16bits)
HR4739	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4740				HR4739 (Lo-16bits)
HR4741	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4742				HR4741 (Lo-16bits)
HR4743	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4744				HR4743 (Lo-16bits)
HR4745	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4746				HR4745 (Lo-16bits)
HR4747	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4748				HR4747 (Lo-16bits)
HR4749	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)
HR4753	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4766				HR4765 (Lo-16bits)
HR4767	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4768				HR4767 (Lo-16bits)
HR4769	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4770				HR4769 (Lo-16bits)
HR4771	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4772				HR4771 (Lo-16bits)
HR4773	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4778				HR4777 (Lo-16bits)
HR4779	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)
HR4781	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4790	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4791	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4792	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4793	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4794	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4795	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4796	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4797	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4798				HR4797 (Lo-16bits)
HR4799	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4800				HR4799 (Lo-16bits)
HR4801	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4802				HR4801 (Lo-16bits)
HR4803	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4804				HR4803 (Lo-16bits)
HR4805	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4806				HR4805 (Lo-16bits)
HR4807	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4808				HR4807 (Lo-16bits)
HR4809	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4810				HR4809 (Lo-16bits)
HR4811	NEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4812				HR4811 (Lo-16bits)
HR4813	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4814	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4815	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4816	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4817	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4818	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4819	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4820	NEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR4821	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)
HR4827	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)
HR4833	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4838	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4839	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4840	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4841	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4842	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4843	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4844	NEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR4845	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4846	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4847	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4848	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4849	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4850	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4851	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4852	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4853	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4870	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4871	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4872	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4873	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4874	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4875	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4876	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4877	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4886				HR4885 (Lo-16bits)
HR4887	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4888				HR4887 (Lo-16bits)
HR4889	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4890				HR4889 (Lo-16bits)
HR4891	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4892				HR4891 (Lo-16bits)
HR4893	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4894	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4895	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4896	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4897	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4898	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4899	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4900	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4901	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4902	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4903	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4904	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4905	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4906	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4907	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4908	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4909	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4910	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4911	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4912	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4913	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4914	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR4915	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4916	NEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR4917	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4918	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4919	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4920	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4921	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4922	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4923	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4924	NEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR4925	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4934				HR4933 (Lo-16bits)
HR4935	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4936				HR4935 (Lo-16bits)
HR4937	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4938				HR4937 (Lo-16bits)
HR4939	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4940				HR4939 (Lo-16bits)
HR4941	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4942				HR4941 (Lo-16bits)
HR4943	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4944				HR4943 (Lo-16bits)
HR4945	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4946				HR4945 (Lo-16bits)
HR4947	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4948				HR4947 (Lo-16bits)
HR4949	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)
HR4961	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4966				HR4965 (Lo-16bits)
HR4967	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4968				HR4967 (Lo-16bits)
HR4969	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4970				HR4969 (Lo-16bits)
HR4971	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4972				HR4971 (Lo-16bits)
HR4973	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)
HR4979	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4990	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4991	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4992	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4993	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4994	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4995	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4996	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4997	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4998				HR4997 (Lo-16bits)
HR4999	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5000				HR4999 (Lo-16bits)
HR5001	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5002				HR5001 (Lo-16bits)
HR5003	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5004				HR5003 (Lo-16bits)
HR5005	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5006				HR5005 (Lo-16bits)
HR5007	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5008				HR5007 (Lo-16bits)
HR5009	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5010				HR5009 (Lo-16bits)
HR5011	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5012				HR5011 (Lo-16bits)
HR5013	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5014	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5015	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5016	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5017	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5018	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5019	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5020	NEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5021	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5038	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5039	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5040	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5041	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5042	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5043	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5044	NEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5045	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5046	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5047	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5048	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5049	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5050	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5051	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5052	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5053	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5070	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5071	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5072	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5073	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5074	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5075	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5076	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR5077	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5086				HR5085 (Lo-16bits)
HR5087	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5088				HR5087 (Lo-16bits)
HR5089	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5090				HR5089 (Lo-16bits)
HR5091	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5092				HR5091 (Lo-16bits)
HR5093	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5094	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5095	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5096	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5097	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5098	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5099	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5100	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5101	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5102	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5103	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5104	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5105	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5106	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5107	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5108	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5109	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5110	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5111	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5112	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5113	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5114	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

Holding Register	Parameter	Access	Length	
HR5115	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5116	NEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5117	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5118	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5119	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5120	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5121	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5122	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5123	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5124	NEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5125	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5134				HR5133 (Lo-16bits)
HR5135	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5136				HR5135 (Lo-16bits)
HR5137	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5138				HR5137 (Lo-16bits)
HR5139	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5140				HR5139 (Lo-16bits)
HR5141	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5142				HR5141 (Lo-16bits)
HR5143	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5144				HR5143 (Lo-16bits)
HR5145	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5146				HR5145 (Lo-16bits)
HR5147	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5148				HR5147 (Lo-16bits)
HR5149	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5150				HR5149 (Lo-16bits)
HR5151	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5152				HR5151 (Lo-16bits)
HR5153	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5154				HR5153 (Lo-16bits)
HR5155	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5156				HR5155 (Lo-16bits)
HR5157	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)
HR5163	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5170				HR5169 (Lo-16bits)
HR5171	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)
HR5179	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5190	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5191	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5192	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5193	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5194	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5195	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR5196	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5197	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)
HR5203	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5214	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5215	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5216	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5217	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5218	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5219	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5220	NEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5221	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5238	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5239	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5240	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5241	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5242	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5243	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR5244	NEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5245	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5246	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5247	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5248	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5249	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5250	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5251	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5252	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5253	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5254	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5255	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5256	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5257	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5258	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5259	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5260	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5261	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5265	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5289	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5313	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)
HR5319	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5335	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5342				HR5341 (Lo-16bits)
HR5343	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5344				HR5343 (Lo-16bits)
HR5345	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5346				HR5345 (Lo-16bits)
HR5347	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5348				HR5347 (Lo-16bits)
HR5349	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5350				HR5349 (Lo-16bits)
HR5351	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5352				HR5351 (Lo-16bits)
HR5353	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5354				HR5353 (Lo-16bits)
HR5355	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5356				HR5355 (Lo-16bits)
HR5357	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5358	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5359	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5360	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5361	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5362	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5363	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5364	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5365	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5406				HR5405 (Lo-16bits)
HR5407	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5408				HR5407 (Lo-16bits)
HR5409	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5410				HR5409 (Lo-16bits)
HR5411	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5412				HR5411 (Lo-16bits)
HR5413	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5414				HR5413 (Lo-16bits)
HR5415	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5416				HR5415 (Lo-16bits)
HR5417	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5418				HR5417 (Lo-16bits)
HR5419	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5420				HR5419 (Lo-16bits)
HR5421	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5422				HR5421 (Lo-16bits)
HR5423	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5424				HR5423 (Lo-16bits)
HR5425	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5426				HR5425 (Lo-16bits)
HR5427	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5428				HR5427 (Lo-16bits)
HR5429	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5430				HR5429 (Lo-16bits)
HR5431	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5432				HR5431 (Lo-16bits)
HR5433	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5434				HR5433 (Lo-16bits)
HR5435	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5436				HR5435 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5437	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5446	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5447	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5448	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5449	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5450	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5451	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5452	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5453	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5454	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5455	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5456	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5457	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5458	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5459	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5460	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5461	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5463	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5487	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5502				HR5501 (Lo-16bits)
HR5503	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5504				HR5503 (Lo-16bits)
HR5505	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5506				HR5505 (Lo-16bits)
HR5507	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5508				HR5507 (Lo-16bits)
HR5509	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5510	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5511	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5512	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5513	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5514	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5515	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5516	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5517	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5518	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5519	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5520	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5521	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5522	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5523	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5524	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5525	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5526	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5527	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5528	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5529	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5530	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5531	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5532	DEF1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5533	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR5534	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5535	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5536	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5537	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5538	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5539	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5540	DEF1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5541	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5550				HR5549 (Lo-16bits)
HR5551	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5552				HR5551 (Lo-16bits)
HR5553	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5554				HR5553 (Lo-16bits)
HR5555	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5556				HR5555 (Lo-16bits)
HR5557	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5558				HR5557 (Lo-16bits)
HR5559	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5560				HR5559 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5561	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5562				HR5561 (Lo-16bits)
HR5563	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5564				HR5563 (Lo-16bits)
HR5565	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5566				HR5565 (Lo-16bits)
HR5567	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5568				HR5567 (Lo-16bits)
HR5569	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5570				HR5569 (Lo-16bits)
HR5571	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5572				HR5571 (Lo-16bits)
HR5573	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5591	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)
HR5597	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5598				HR5597 (Lo-16bits)
HR5599	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5606	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5607	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5608	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5609	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5610	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5611	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5612	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5613	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5616				HR5615 (Lo-16bits)
HR5617	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5622				HR5621 (Lo-16bits)
HR5623	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5630	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5631	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5632	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5633	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5634	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5635	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5636	DEF1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR5637	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5639	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)
HR5647	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5654	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5655	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5656	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5657	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5658	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5659	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5660	DEF1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR5661	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5662	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5663	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5664	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5665	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5666	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5667	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5668	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5669	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5670	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5671	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5672	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5673	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5674	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5675	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5676	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR5677	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5685	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)
HR5691	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)
HR5695	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5709	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)
HR5715	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)
HR5719	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5733	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5757	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5758				HR5757 (Lo-16bits)
HR5759	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5760				HR5759 (Lo-16bits)
HR5761	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5762				HR5761 (Lo-16bits)
HR5763	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5764				HR5763 (Lo-16bits)
HR5765	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5766				HR5765 (Lo-16bits)
HR5767	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5768				HR5767 (Lo-16bits)
HR5769	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5770				HR5769 (Lo-16bits)
HR5771	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5772				HR5771 (Lo-16bits)
HR5773	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5774	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5775	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5776	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5777	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5778	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5779	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

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Holding Register	Parameter	Access	Length	
HR5780	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5781	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5807	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Unearth. Pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5822				HR5821 (Lo-16bits)
HR5823	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5824				HR5823 (Lo-16bits)
HR5825	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5826				HR5825 (Lo-16bits)
HR5827	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5828				HR5827 (Lo-16bits)
HR5829	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5830				HR5829 (Lo-16bits)
HR5831	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5832				HR5831 (Lo-16bits)
HR5833	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5834				HR5833 (Lo-16bits)
HR5835	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5836				HR5835 (Lo-16bits)
HR5837	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5838				HR5837 (Lo-16bits)
HR5839	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5840				HR5839 (Lo-16bits)
HR5841	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5842				HR5841 (Lo-16bits)
HR5843	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5844				HR5843 (Lo-16bits)
HR5845	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5846				HR5845 (Lo-16bits)
HR5847	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5848				HR5847 (Lo-16bits)
HR5849	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5850				HR5849 (Lo-16bits)
HR5851	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5852				HR5851 (Lo-16bits)
HR5853	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	
HR5862	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5863	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5864	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5865	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5866	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5867	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5868	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR5869	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5870	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5871	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5872	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5873	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5874	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5875	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5876	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5877	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5918				HR5917 (Lo-16bits)
HR5919	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5920				HR5919 (Lo-16bits)
HR5921	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5922				HR5921 (Lo-16bits)
HR5923	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5924				HR5923 (Lo-16bits)
HR5925	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5926	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5927	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5928	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5929	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5930	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5931	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5932	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5933	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5934	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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TN00011

Holding Register	Parameter	Access	Length	
HR5935	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5936	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5937	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5938	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5939	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5940	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5941	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5942	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5943	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5944	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5945	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5946	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5947	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5948	DEF2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR5949	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5950	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5951	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5952	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5953	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5954	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR5955	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5956	DEF2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR5957	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5966				HR5965 (Lo-16bits)
HR5967	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5968				HR5967 (Lo-16bits)
HR5969	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5970				HR5969 (Lo-16bits)
HR5971	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5972				HR5971 (Lo-16bits)
HR5973	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5974				HR5973 (Lo-16bits)
HR5975	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5976				HR5975 (Lo-16bits)
HR5977	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5978				HR5977 (Lo-16bits)
HR5979	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5980				HR5979 (Lo-16bits)
HR5981	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5982				HR5981 (Lo-16bits)
HR5983	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5984				HR5983 (Lo-16bits)
HR5985	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5986				HR5985 (Lo-16bits)
HR5987	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5988				HR5987 (Lo-16bits)
HR5989	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5990				HR5989 (Lo-16bits)
HR5991	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)
HR5995	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6014				HR6013 (Lo-16bits)
HR6015	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)
HR6017	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6022	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6023	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6024	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6025	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6026	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6027	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6028	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6029	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6038				HR6037 (Lo-16bits)
HR6039	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6046	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6047	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6048	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6049	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6050	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6051	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6052	DEF2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6053	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6063	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6070	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6071	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6072	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6073	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6074	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6075	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6076	DEF2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6077	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6078	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6079	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6080	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6081	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6082	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR6083	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6084	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6085	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6086	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6087	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6088	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6089	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6090	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6091	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6092	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6093	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6107	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6131	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)
HR6143	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)
HR6151	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6155	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6174				HR6173 (Lo-16bits)
HR6175	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6176				HR6175 (Lo-16bits)
HR6177	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6178				HR6177 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6179	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6180				HR6179 (Lo-16bits)
HR6181	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6182				HR6181 (Lo-16bits)
HR6183	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6184				HR6183 (Lo-16bits)
HR6185	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6186				HR6185 (Lo-16bits)
HR6187	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6188				HR6187 (Lo-16bits)
HR6189	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6190	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6191	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6192	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6193	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6194	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6195	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6196	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6197	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6203	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Unearth. Pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6229	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6238				HR6237 (Lo-16bits)
HR6239	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6240				HR6239 (Lo-16bits)
HR6241	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6242				HR6241 (Lo-16bits)
HR6243	DEF3:Unearth. Pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6244				HR6243 (Lo-16bits)
HR6245	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6246				HR6245 (Lo-16bits)
HR6247	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6248				HR6247 (Lo-16bits)
HR6249	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6250				HR6249 (Lo-16bits)
HR6251	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6252				HR6251 (Lo-16bits)
HR6253	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6254				HR6253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6255	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6256				HR6255 (Lo-16bits)
HR6257	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6258				HR6257 (Lo-16bits)
HR6259	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6260				HR6259 (Lo-16bits)
HR6261	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6262				HR6261 (Lo-16bits)
HR6263	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6264				HR6263 (Lo-16bits)
HR6265	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6266				HR6265 (Lo-16bits)
HR6267	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6268				HR6267 (Lo-16bits)
HR6269	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6278	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6279	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6280	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6281	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi

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TN00011

Holding Register	Parameter	Access	Length	
HR6282	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6283	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6284	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6285	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6286	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6287	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6288	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6289	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6290	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6291	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6292	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6293	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6307	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6331	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6334				HR6333 (Lo-16bits)
HR6335	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6336				HR6335 (Lo-16bits)
HR6337	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6338				HR6337 (Lo-16bits)
HR6339	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6340				HR6339 (Lo-16bits)
HR6341	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6342	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6343	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6344	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6345	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6346	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6347	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6348	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6349	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6350	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6351	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6352	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6353	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6354	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR6355	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6356	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6357	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6358	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6359	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6360	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6361	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6362	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6363	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6364	DEF3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6365	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6366	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6367	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6368	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6369	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6370	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6371	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6372	DEF3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6373	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6382				HR6381 (Lo-16bits)
HR6383	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6384				HR6383 (Lo-16bits)
HR6385	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6386				HR6385 (Lo-16bits)
HR6387	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6388				HR6387 (Lo-16bits)
HR6389	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6390				HR6389 (Lo-16bits)
HR6391	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6392				HR6391 (Lo-16bits)
HR6393	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6394				HR6393 (Lo-16bits)
HR6395	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6396				HR6395 (Lo-16bits)
HR6397	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6398				HR6397 (Lo-16bits)
HR6399	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6400				HR6399 (Lo-16bits)
HR6401	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6402				HR6401 (Lo-16bits)
HR6403	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6404				HR6403 (Lo-16bits)
HR6405	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6406				HR6405 (Lo-16bits)
HR6407	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6414				HR6413 (Lo-16bits)
HR6415	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)
HR6423	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6436				HR6435 (Lo-16bits)
HR6437	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6438	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6439	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6440	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6441	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6442	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6443	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6444	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6445	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6459	DEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6462	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6463	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6464	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6465	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6466	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6467	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6468	DEF3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6469	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6485	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6486	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6487	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6488	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6489	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6490	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6491	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6492	DEF3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6493	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6494	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6495	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6496	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6497	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6498	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6499	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6500	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6501	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6502	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou

Holding Register	Parameter	Access	Length	
HR6503	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6504	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6505	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6506	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6507	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6508	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Watt];2=Grou
HR6509	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)
HR6527	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)
HR6535	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)
HR6551	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6590				HR6589 (Lo-16bits)
HR6591	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6592				HR6591 (Lo-16bits)
HR6593	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6594				HR6593 (Lo-16bits)
HR6595	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6596				HR6595 (Lo-16bits)
HR6597	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6598				HR6597 (Lo-16bits)
HR6599	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6600				HR6599 (Lo-16bits)
HR6601	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6602				HR6601 (Lo-16bits)
HR6603	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6604				HR6603 (Lo-16bits)
HR6605	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6606	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6607	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6608	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6609	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6610	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6611	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6612	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6613	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6625	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Unearth. Pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6654				HR6653 (Lo-16bits)
HR6655	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6656				HR6655 (Lo-16bits)
HR6657	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6658				HR6657 (Lo-16bits)
HR6659	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6660				HR6659 (Lo-16bits)
HR6661	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6662				HR6661 (Lo-16bits)
HR6663	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6664				HR6663 (Lo-16bits)
HR6665	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6666				HR6665 (Lo-16bits)
HR6667	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6668				HR6667 (Lo-16bits)
HR6669	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6670				HR6669 (Lo-16bits)
HR6671	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6672				HR6671 (Lo-16bits)
HR6673	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6674				HR6673 (Lo-16bits)
HR6675	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6676				HR6675 (Lo-16bits)
HR6677	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6678				HR6677 (Lo-16bits)
HR6679	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6680				HR6679 (Lo-16bits)
HR6681	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6682				HR6681 (Lo-16bits)
HR6683	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6684				HR6683 (Lo-16bits)
HR6685	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6694	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6695	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6696	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6697	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6698	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6699	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6700	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sinfi
HR6701	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6702	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6703	DEF4:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR6704	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6705	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6706	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6707	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6708	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6709	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6729	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6750				HR6749 (Lo-16bits)
HR6751	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6752				HR6751 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6753	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6754				HR6753 (Lo-16bits)
HR6755	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6756				HR6755 (Lo-16bits)
HR6757	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6758	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6759	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6760	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6761	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6762	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6763	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6764	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6765	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6766	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6767	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6768	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6769	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6770	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6771	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6772	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6773	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6774	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6775	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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TN00011

Holding Register	Parameter	Access	Length	
HR6776	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6777	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6778	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6779	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6780	DEF4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR6781	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6782	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6783	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6784	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6785	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6786	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6787	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6788	DEF4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type
HR6789	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6798				HR6797 (Lo-16bits)
HR6799	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6800				HR6799 (Lo-16bits)
HR6801	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6802				HR6801 (Lo-16bits)
HR6803	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6804				HR6803 (Lo-16bits)
HR6805	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6806				HR6805 (Lo-16bits)
HR6807	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6808				HR6807 (Lo-16bits)
HR6809	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6810				HR6809 (Lo-16bits)
HR6811	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6812				HR6811 (Lo-16bits)
HR6813	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6822				HR6821 (Lo-16bits)
HR6823	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6830				HR6829 (Lo-16bits)
HR6831	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)
HR6839	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6854	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6855	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6856	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6857	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6858	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6859	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6860	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6861	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6862				HR6861 (Lo-16bits)
HR6863	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6864				HR6863 (Lo-16bits)
HR6865	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6866				HR6865 (Lo-16bits)
HR6867	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6868				HR6867 (Lo-16bits)
HR6869	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6878	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6879	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6880	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR6881	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6882	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6883	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6884	DEF4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR6885	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6886				HR6885 (Lo-16bits)
HR6887	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6888				HR6887 (Lo-16bits)
HR6889	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6890				HR6889 (Lo-16bits)
HR6891	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6892				HR6891 (Lo-16bits)
HR6893	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6894				HR6893 (Lo-16bits)
HR6895	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6896				HR6895 (Lo-16bits)
HR6897	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6898				HR6897 (Lo-16bits)
HR6899	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6900				HR6899 (Lo-16bits)
HR6901	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6902	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6903	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6904	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6905	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6906	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6907	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6908	DEF4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR6909	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6910	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6911	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6912	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6913	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6914	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6915	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6916	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6917	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6925	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)
HR6959	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6966	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6967	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6968	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6969	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6970	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6971	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6972	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6973	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6982				HR6981 (Lo-16bits)
HR6983	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6984				HR6983 (Lo-16bits)
HR6985	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6986				HR6985 (Lo-16bits)
HR6987	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6988				HR6987 (Lo-16bits)
HR6989	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6990	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6991	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6992	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6993	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6994	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6995	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6996	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6997	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6998	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6999	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7000	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7001	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7002	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7003	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7004	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7005	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7006	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7007	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7008	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7009	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7010	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7011	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7012	CUB1:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7013	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7014	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7015	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7016	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7017	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7018	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7019	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7020	CUB1:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7021	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7024				HR7023 (Lo-16bits)
HR7025	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)
HR7029	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)
HR7041	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7054				HR7053 (Lo-16bits)
HR7055	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)
HR7059	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7062				HR7061 (Lo-16bits)
HR7063	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7070				HR7069 (Lo-16bits)
HR7071	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7072				HR7071 (Lo-16bits)
HR7073	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7074				HR7073 (Lo-16bits)
HR7075	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7076				HR7075 (Lo-16bits)
HR7077	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7084				HR7083 (Lo-16bits)
HR7085	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7086	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7087	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7088	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7089	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7090	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7091	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7092	CUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7093	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7106				HR7105 (Lo-16bits)
HR7107	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7108				HR7107 (Lo-16bits)
HR7109	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7110	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7111	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7112	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7113	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7114	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7115	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7116	CUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7117	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7118	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7119	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7120	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7121	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7122	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7123	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7124	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7125	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7155	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7174	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7175	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7176	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7177	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7178	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7179	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7180	CUB2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7181	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7194				HR7193 (Lo-16bits)
HR7195	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7198	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7199	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7200	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7201	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7202	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7203	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7204	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7205	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7206	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7207	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7208	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7209	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7210	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7211	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7212	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7213	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7214	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7215	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7216	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7217	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7218	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7219	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7220	CUB2:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7221	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7222	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7223	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7224	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7225	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7226	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7227	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7228	CUB2:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7229	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7232				HR7231 (Lo-16bits)
HR7233	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7255	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7285	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7294	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7295	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7296	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7297	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7298	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7299	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7300	CUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7301	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7311	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7318	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7319	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7320	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7321	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7322	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7323	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7324	CUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7325	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7326	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7327	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7328	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7329	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7330	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7331	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7332	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7333	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7355	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7381	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7382	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7383	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7384	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7385	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7386	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7387	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7388	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7389	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7406	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7407	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7408	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7409	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7410	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7411	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7412	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7413	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7414	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7415	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7416	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7417	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7418	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7419	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7420	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7421	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7422	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7423	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7424	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7425	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7426	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7427	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7428	CUB3:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7429	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7430	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7431	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7432	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7433	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7434	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7435	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7436	CUB3:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7437	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7455	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7485	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7502	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7503	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7504	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7505	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7506	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7507	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7508	CUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7509	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7511	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7526	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7527	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7528	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7529	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7530	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7531	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7532	CUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7533	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7534	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7535	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7536	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7537	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7538	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7539	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7540	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7541	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7555	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)
HR7589	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7590	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7591	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7592	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7593	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7594	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7595	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7596	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7597	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7608				HR7607 (Lo-16bits)
HR7609	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7614	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7615	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7616	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7617	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7618	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7619	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7620	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7621	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7622	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7623	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7624	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7625	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7626	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7627	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7628	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7629	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7630	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7631	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V

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Holding Register	Parameter	Access	Length	
HR7632	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7633	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7634	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7635	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7636	CUB4:Delay char. ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI;5=IEEE V
HR7637	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7638	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7639	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7640	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7641	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7642	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7643	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7644	CUB4:Non standard Delay char.	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7645	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7656				HR7655 (Lo-16bits)
HR7657	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7686				HR7685 (Lo-16bits)
HR7687	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7710	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7711	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7712	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7713	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7714	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7715	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7716	CUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7717	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)
HR7719	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7734	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7735	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7736	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7737	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7738	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7739	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7740	CUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7741	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7742	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7743	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7744	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7745	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7746	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7747	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7748	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7749	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7750	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7751	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7752	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7753	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7754	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7755	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7756	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7757	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7758				HR7757 (Lo-16bits)
HR7759	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7760				HR7759 (Lo-16bits)
HR7761	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7762				HR7761 (Lo-16bits)
HR7763	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7774	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7775	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7776	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7777	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7778	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7779	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7780	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7781	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7788				HR7787 (Lo-16bits)
HR7789	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7816				HR7815 (Lo-16bits)
HR7817	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR7846	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7847	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7848	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7849	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7850	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7851	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7852	OV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7853	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7854	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7855	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7856	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7857	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7858	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7859	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7860	OV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7861	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7862	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7863	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7864	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7865	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7866	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7867	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7868	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7869	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7870	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7871	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7872	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7873	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7874	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7875	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7876	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7877	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7891	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7894	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7895	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7896	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7897	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7898	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7899	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7900	OV2:Delay type	R(W)	1	0=DT;1=IDMT
HR7901	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7917	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7947	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7966	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7967	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7968	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7969	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7970	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7971	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR7972	OV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7973	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7974	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7975	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7976	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7977	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7978	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7979	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7980	OV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR7981	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7982	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7983	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7984	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7985	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7986	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7987	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7988	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7989	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7990	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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Holding Register	Parameter	Access	Length	
HR7991	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7992	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7993	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7994	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7995	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7996	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7997	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8014	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8015	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8016	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8017	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8018	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8019	OV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8020	OV3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR8021	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8032				HR8031 (Lo-16bits)
HR8033	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8034				HR8033 (Lo-16bits)
HR8035	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8036				HR8035 (Lo-16bits)
HR8037	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8038				HR8037 (Lo-16bits)
HR8039	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8047	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8077	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8086	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8087	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8088	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8089	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8090	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8091	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8092	OV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8093	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8094	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8095	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8096	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8097	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8098	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8099	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8100	OV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8101	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8102	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8103	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8104	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8105	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8106	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8107	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8108	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8109	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8110	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8111	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8112	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8113	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8114	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8115	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8116	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8117	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8122				HR8121 (Lo-16bits)
HR8123	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)
HR8127	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)
HR8131	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8134	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8135	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8136	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8137	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8138	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8139	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8140	OV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8141	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8150				HR8149 (Lo-16bits)
HR8151	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8170				HR8169 (Lo-16bits)
HR8171	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8172				HR8171 (Lo-16bits)
HR8173	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8179	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8206	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8207	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8208	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8209	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8210	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8211	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8212	OV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8213	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8214	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8215	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8216	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8217	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8218	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8219	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8220	OV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8221	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8222	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8223	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8224	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8225	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8226	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8227	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8228	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8229	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8230	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8231	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8232	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8233	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8234	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8235	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8236	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8237	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8253	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)
HR8257	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8264				HR8263 (Lo-16bits)
HR8265	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8266				HR8265 (Lo-16bits)
HR8267	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8268				HR8267 (Lo-16bits)
HR8269	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8270	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8271	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8272	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8273	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8274	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8275	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8276	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8277	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8282				HR8281 (Lo-16bits)
HR8283	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8310				HR8309 (Lo-16bits)
HR8311	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8340				HR8339 (Lo-16bits)
HR8341	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8342	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8343	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8344	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8345	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8346	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8347	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8348	UV1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8349	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8350	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8351	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8352	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8353	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8354	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8355	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8356	UV1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8357	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8358	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8359	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8360	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8361	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8362	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8363	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8364	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8365	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8366	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8367	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8368	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8369	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8370	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8371	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8372	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8373	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8383	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8398				HR8397 (Lo-16bits)
HR8399	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8400				HR8399 (Lo-16bits)
HR8401	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8402				HR8401 (Lo-16bits)
HR8403	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8404				HR8403 (Lo-16bits)
HR8405	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8406	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8407	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8408	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8409	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8410	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8411	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8412	UV2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR8413	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8439	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8469	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8478	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8479	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8480	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8481	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8482	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8483	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8484	UV2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8485	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8486	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8487	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8488	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8489	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8490	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8491	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8492	UV2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8493	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8494	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8495	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8496	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8497	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8498	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8499	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8500	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8501	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8502	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8503	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8504	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8505	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8506	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8507	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8508	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8509	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8513	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)
HR8527	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)
HR8533	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8536				HR8535 (Lo-16bits)
HR8537	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8538				HR8537 (Lo-16bits)
HR8539	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8540				HR8539 (Lo-16bits)
HR8541	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8542	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8543	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8544	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8545	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8546	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8547	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8548	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8549	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8566				HR8565 (Lo-16bits)
HR8567	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8596				HR8595 (Lo-16bits)
HR8597	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)
HR8599	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8614	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8615	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8616	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8617	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8618	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8619	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8620	UV3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8621	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8622	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8623	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8624	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8625	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8626	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8627	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8628	UV3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8629	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8630	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8631	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8632	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8633	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8634	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8635	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8636	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8637	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8638	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8639	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8640	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8641	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8642	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8643	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8644	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8645	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8666				HR8665 (Lo-16bits)
HR8667	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8668				HR8667 (Lo-16bits)
HR8669	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8672				HR8671 (Lo-16bits)
HR8673	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8674				HR8673 (Lo-16bits)
HR8675	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8676				HR8675 (Lo-16bits)
HR8677	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8678	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8679	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8680	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8681	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8682	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8683	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8684	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8685	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)
HR8689	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8692				HR8691 (Lo-16bits)
HR8693	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8720				HR8719 (Lo-16bits)
HR8721	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)
HR8737	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8750	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8751	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8752	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8753	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8754	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8755	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8756	UV4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8757	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8758	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8759	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8760	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8761	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8762	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8763	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8764	UV4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8765	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8766	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8767	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8768	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8769	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8770	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8771	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8772	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8773	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8774	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8775	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8776	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8777	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8778	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8779	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8780	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8781	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8795	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8808				HR8807 (Lo-16bits)
HR8809	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8810				HR8809 (Lo-16bits)
HR8811	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8812				HR8811 (Lo-16bits)
HR8813	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8814	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8815	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8816	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8817	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8818	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8819	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8820	VUB1:Delay type	R(W)	1	0=DT;1=IDMT

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TN00011

Holding Register	Parameter	Access	Length	
HR8821	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8847	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8850				HR8849 (Lo-16bits)
HR8851	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)
HR8853	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8877	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)
HR8879	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8886	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8887	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8888	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8889	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8890	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8891	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8892	VUB1:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR8893	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8894	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8895	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8896	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8897	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8898	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8899	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8900	VUB1:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR8901	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8902	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8903	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8904	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8905	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8906	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8907	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8908	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8909	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8910	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8911	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8912	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8913	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8914	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8915	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8916	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8917	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8918				HR8917 (Lo-16bits)
HR8919	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8920				HR8919 (Lo-16bits)
HR8921	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8922				HR8921 (Lo-16bits)
HR8923	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8924				HR8923 (Lo-16bits)
HR8925	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8926				HR8925 (Lo-16bits)
HR8927	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8928				HR8927 (Lo-16bits)
HR8929	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8930				HR8929 (Lo-16bits)
HR8931	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8932				HR8931 (Lo-16bits)
HR8933	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8934				HR8933 (Lo-16bits)
HR8935	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8936				HR8935 (Lo-16bits)
HR8937	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8938				HR8937 (Lo-16bits)
HR8939	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8940				HR8939 (Lo-16bits)
HR8941	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8942				HR8941 (Lo-16bits)
HR8943	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8944				HR8943 (Lo-16bits)
HR8945	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8946				HR8945 (Lo-16bits)
HR8947	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8948				HR8947 (Lo-16bits)
HR8949	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8950	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8951	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8952	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8953	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8954	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8955	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8956	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8957	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)
HR8961	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)
HR8973	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8974				HR8973 (Lo-16bits)
HR8975	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8982				HR8981 (Lo-16bits)
HR8983	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8984				HR8983 (Lo-16bits)
HR8985	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8986				HR8985 (Lo-16bits)
HR8987	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8988				HR8987 (Lo-16bits)
HR8989	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9004				HR9003 (Lo-16bits)
HR9005	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9022	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9023	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9024	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9025	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9026	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9027	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9028	VUB2:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9029	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9030	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9031	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9032	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9033	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9034	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9035	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9036	VUB2:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9037	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9038	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9039	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9040	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9041	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9042	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9043	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9044	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9045	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9046	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9047	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9048	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR9049	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9050	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9051	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9052	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9053	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9054				HR9053 (Lo-16bits)
HR9055	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9056				HR9055 (Lo-16bits)
HR9057	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9058				HR9057 (Lo-16bits)
HR9059	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9060				HR9059 (Lo-16bits)
HR9061	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9062				HR9061 (Lo-16bits)
HR9063	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9064				HR9063 (Lo-16bits)
HR9065	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9066				HR9065 (Lo-16bits)
HR9067	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9068				HR9067 (Lo-16bits)
HR9069	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9070				HR9069 (Lo-16bits)
HR9071	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9072				HR9071 (Lo-16bits)
HR9073	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9074				HR9073 (Lo-16bits)
HR9075	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9076				HR9075 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9077	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9078				HR9077 (Lo-16bits)
HR9079	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9080				HR9079 (Lo-16bits)
HR9081	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9082				HR9081 (Lo-16bits)
HR9083	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9084				HR9083 (Lo-16bits)
HR9085	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9086	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9087	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9088	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9089	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9090	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9091	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9092	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR9093	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9103	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9118				HR9117 (Lo-16bits)
HR9119	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9120				HR9119 (Lo-16bits)
HR9121	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9122				HR9121 (Lo-16bits)
HR9123	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9124				HR9123 (Lo-16bits)
HR9125	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9132				HR9131 (Lo-16bits)
HR9133	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)
HR9145	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9158	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9159	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9160	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9161	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9162	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9163	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9164	VUB3:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9165	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9166	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9167	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9168	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9169	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9170	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9171	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9172	VUB3:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9173	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9174	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9175	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9176	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9177	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9178	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9179	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9180	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9181	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9182	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9183	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9184	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9185	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9186	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9187	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9188	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9189	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9194				HR9193 (Lo-16bits)
HR9195	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9196				HR9195 (Lo-16bits)
HR9197	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9198				HR9197 (Lo-16bits)
HR9199	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9200				HR9199 (Lo-16bits)
HR9201	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9202				HR9201 (Lo-16bits)
HR9203	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9204				HR9203 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9205	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9206				HR9205 (Lo-16bits)
HR9207	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9208				HR9207 (Lo-16bits)
HR9209	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9210				HR9209 (Lo-16bits)
HR9211	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9216				HR9215 (Lo-16bits)
HR9217	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9218				HR9217 (Lo-16bits)
HR9219	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9220				HR9219 (Lo-16bits)
HR9221	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9222	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9223	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9224	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9225	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9226	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9227	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9228	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9229	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9231	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)
HR9245	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9258				HR9257 (Lo-16bits)
HR9259	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9262				HR9261 (Lo-16bits)
HR9263	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9264				HR9263 (Lo-16bits)
HR9265	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9266				HR9265 (Lo-16bits)
HR9267	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9268				HR9267 (Lo-16bits)
HR9269	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9288				HR9287 (Lo-16bits)
HR9289	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9294	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9295	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9296	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9297	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9298	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9299	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9300	VUB4:Delayed Pick-up release	R(W)	1	0=No;1=Yes
HR9301	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9302	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9303	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9304	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9305	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9306	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9307	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes
HR9308	VUB4:Op.Time calc reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9309	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9310	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9311	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9312	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9313	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9314	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9315	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9316	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9317	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9318	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9319	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9320	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9321	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9322	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9323	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9324	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9325	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9326	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9327	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9328	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9329	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9330	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9331	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9332	FRQV1:f> Used in SG	R(W)	1	0=No;1=Yes
HR9333	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9361	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9366	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9367	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9368	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9369	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9370	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9371	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9372	FRQV1:f>> Used in SG	R(W)	1	0=No;1=Yes
HR9373	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9391	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9406	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9407	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9408	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9409	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9410	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9411	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9412	FRQV1:f>>> Used in SG	R(W)	1	0=No;1=Yes
HR9413	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9421	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9446	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9447	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9448	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9449	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9450	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9451	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9452	FRQV1:f>>>> Used in SG	R(W)	1	0=No;1=Yes
HR9453	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9464				HR9463 (Lo-16bits)
HR9465	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9466				HR9465 (Lo-16bits)
HR9467	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9468				HR9467 (Lo-16bits)
HR9469	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9481	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9486	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9487	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9488	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9489	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9490	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9491	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9492	FRQV1:f< Used in SG	R(W)	1	0=No;1=Yes
HR9493	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9511	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9526	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9527	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9528	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9529	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9530	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9531	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9532	FRQV1:f<< Used in SG	R(W)	1	0=No;1=Yes
HR9533	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9541	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9566	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9567	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9568	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9569	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9570	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR9571	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9572	FRQV1:f<<< Used in SG	R(W)	1	0=No;1=Yes
HR9573	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9580				HR9579 (Lo-16bits)
HR9581	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9582				HR9581 (Lo-16bits)
HR9583	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9584				HR9583 (Lo-16bits)
HR9585	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9586				HR9585 (Lo-16bits)
HR9587	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9588				HR9587 (Lo-16bits)
HR9589	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9590				HR9589 (Lo-16bits)
HR9591	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9592				HR9591 (Lo-16bits)
HR9593	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9594				HR9593 (Lo-16bits)
HR9595	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9596				HR9595 (Lo-16bits)
HR9597	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9598				HR9597 (Lo-16bits)
HR9599	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9601	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9602				HR9601 (Lo-16bits)
HR9603	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9604				HR9603 (Lo-16bits)
HR9605	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9606	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9607	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9608	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9609	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9610	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9611	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9612	FRQV1:f<<<< Used in SG	R(W)	1	0=No;1=Yes
HR9613	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9614				HR9613 (Lo-16bits)
HR9615	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9616				HR9615 (Lo-16bits)
HR9617	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9618				HR9617 (Lo-16bits)
HR9619	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9620				HR9619 (Lo-16bits)
HR9621	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9622				HR9621 (Lo-16bits)
HR9623	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9624				HR9623 (Lo-16bits)
HR9625	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9626				HR9625 (Lo-16bits)
HR9627	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9628				HR9627 (Lo-16bits)
HR9629	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9630				HR9629 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9631	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9632				HR9631 (Lo-16bits)
HR9633	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9634				HR9633 (Lo-16bits)
HR9635	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9636				HR9635 (Lo-16bits)
HR9637	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9638				HR9637 (Lo-16bits)
HR9639	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9640				HR9639 (Lo-16bits)
HR9641	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9642				HR9641 (Lo-16bits)
HR9643	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9644				HR9643 (Lo-16bits)
HR9645	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9660				HR9659 (Lo-16bits)
HR9661	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9689	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9691	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9692	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9693	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9694	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9695	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9696	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9697	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9698	CUB1:I2> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9699	CUB2:I2>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9700	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9701	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled;1=Activated
HR9702	OV1:U> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9703	OV2:U>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9704	OV3:U>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9705	OV4:U>>>> [59] mode	R(W)	1	0=Disabled;1=Activated
HR9706	UV1:U< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9707	UV2:U<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9708	UV3:U<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9709	UV4:U<<<< [27] mode	R(W)	1	0=Disabled;1=Activated
HR9710	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9711	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9712	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated
HR9713	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9714	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled;1=Activated
HR9715	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9743	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9750				HR9749 (Lo-16bits)
HR9751	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9752				HR9751 (Lo-16bits)
HR9753	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9754				HR9753 (Lo-16bits)
HR9755	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9769	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	DIGI:DIs (cpu)	R	2	0b=DI1;1b=DI2;2b=DI3; (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	DIGI:DIs (input option 1)	R	2	0b=DI4;1b=DI5;2b=DI6;3b=DI7;4b=DI8;5b=DI9;6b=DI10;7b=DI1
HR9774				HR9773 (Lo-16bits)
HR9775	DIGI:DIs (input option 2)	R	2	0b=DI12;1b=DI13;2b=DI14;3b=DI15;4b=DI16;5b=DI17;6b=DI18
HR9776				HR9775 (Lo-16bits)
HR9777	DIGI:DIs (input option 3)	R	2	0b=DI20;1b=DI21;2b=DI22;3b=DI23;4b=DI24;5b=DI25;6b=DI26
HR9778				HR9777 (Lo-16bits)
HR9779	DIGI:DIs (input option 4)	R	2	0b=D28;1b=DI29;2b=DI30;3b=DI31;4b=DI32;5b=DI33;6b=DI34;
HR9780				HR9779 (Lo-16bits)
HR9781	DIGI:DIs (input option 5)	R	2	0b=DI36;1b=DI37;2b=DI38;3b=DI39;4b=DI40;5b=DI41;6b=DI42
HR9782				HR9781 (Lo-16bits)
HR9783	DIGI:DIs (input option 6)	R	2	0b=DI44;1b=DI45;2b=DI46;3b=DI47;4b=DI48;5b=DI49;6b=DI50
HR9784				HR9783 (Lo-16bits)
HR9785	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9797	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9802	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled;1=Activated
HR9803	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled;1=Activated
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANCEL=0xA0

2 AQ-25x frame Modbus map

The following table lists Modbus holding registers for AQ-25x frame units with firmware v2.5.7.1-245. For AQ-21x frame Modbus map refer to chapter *AQ-21x frame Modbus map*.

NOTICE!
Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 2 - 6. AQ-25x frame Modbus map

Holding Register	Parameter	Access	Length	
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED

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Holding Register	Parameter	Access	Length	
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pha.angle IL1	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pha.angle IL2	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL3	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:Calculated I0 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Positive sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Negative sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Positive sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Negative sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR81				HR80 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR111				HR110 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR112	MEAS1:IL1 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR141				HR140 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR142	MEAS1:IL1 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL2 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR171				HR170 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR172	MEAS1:IL2 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR201				HR200 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR202	MEAS1:IL2 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR231				HR230 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR232	MEAS1:IL3 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR261				HR260 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR262	MEAS1:IL3 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR291				HR290 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR292	MEAS1:I01 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR321				HR320 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR322	MEAS1:I01 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR351				HR350 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR352	MEAS1:I02 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR381				HR380 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR382	MEAS1:I02 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR411				HR410 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS2:Pri.calc.I"0	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Calculated I"0 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR441				HR440 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR442	MEAS2:I" Pri.Positive sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:I" Pri.Negative sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:I" Pri.Zero sequence curr.	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:I" Positive sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:I" Negative sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:I" Zero sequence curr.angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR469				HR468 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:I"L1 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:I"L1 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I"L1 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I"L1 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I"L1 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I"L1 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR499				HR498 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR500	MEAS2:I"L1 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I"L1 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I"L1 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I"L1 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:I"L1 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR529				HR528 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR530	MEAS2:I"L1 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L2 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L2 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L2 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L2 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L2 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR559				HR558 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR560	MEAS2:I"L2 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L2 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L2 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L2 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L2 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L2 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L2 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L2 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR589				HR588 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR590	MEAS2:I"L2 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L3 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I"L3 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L3 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L3 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR619				HR618 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR620	MEAS2:I"L3 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L3 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L3 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L3 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L3 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L3 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L3 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L3 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L3 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L3 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L3 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR649				HR648 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR650	MEAS2:I"L3 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR679				HR678 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR680	MEAS2:I"01 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"01 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"01 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR709				HR708 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR710	MEAS2:I"01 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR739				HR738 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR740	MEAS2:I"02 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"02 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"02 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR769				HR768 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR770	MEAS2:I"02 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1 Volt pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR799				HR798 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR800	MEAS3:U1 Volt sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2 Volt pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2 Volt sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3 Volt pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS3:U3 Volt sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4 Volt pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4 Volt sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1 Volt TRMS pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1 Volt TRMS sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2 Volt TRMS pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS3:U2 Volt TRMS sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3 Volt TRMS pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3 Volt TRMS sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4 Volt TRMS pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4 Volt TRMS sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR829				HR828 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR830	MEAS3:Positive sequence volt.pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Positive sequence volt.sec	R	2	FLOAT [0.00...480.00] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Negative sequence volt.pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Negative sequence volt.sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS3:Zero sequence Volt.pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero sequence Volt.sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc.Volt pri	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq volt.Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Negative sequence volt.Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero sequence volt.Angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR859				HR858 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:System volt UL31 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:System volt UL1 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR889				HR888 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:System volt U0 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0.00...120.00] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag (kV)	R	2	FLOAT [0.0...1000.0] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR919				HR918 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U1 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:U1 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:U1 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR949				HR948 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR950	MEAS3:U1 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U1 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:U1 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:U1 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR979				HR978 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR980	MEAS3:U1 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U2 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U2 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U2 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1010	MEAS3:U2 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U2 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U2 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U2 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR1040	MEAS3:U2 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U3 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U3 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1070	MEAS3:U3 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U3 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U3 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U3 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1100	MEAS3:U3 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 fundamental	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U4 7th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U4 9th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1130	MEAS3:U4 12th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U4 14th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U4 22nd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23rd harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U4 24th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1160	MEAS3:U4 27th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 29th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30th harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31st harmonic	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	POW1:Active Energy (P) Export/ Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/ Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	POW1:Reset 3ph energies	RW	1	0=-;1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:DIs	R	2	0b=DI1;1b=DI2;2b=DI3;3b=DI4;4b=DI5;5b=DI6;6b=DI7
HR1250				HR1249 (Lo-16bits)
HR1251	DIGI:DIs	R	2	0b=DI33;1b=DI34;2b=DI35;3b=DI36;4b=DI37;5b=DI38
HR1252				HR1251 (Lo-16bits)
HR1253	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR1256	SGS:Force setting group	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7
HR1257	SGS:Force setting group change	RW	1	0=Disabled;1=Enabled
HR1258	SGS:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=S
HR1259	SGS:Remote setting group change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7
HR1260	LOGIC:Reset Latches	RW	1	0=-;1=Reset
HR1261	NOC1:I> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1262	NOC2:I>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1263	NOC3:I>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1264	NOC4:I>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1265	DOC1:Idir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1266	DOC2:Idir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1267	DOC3:Idir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1268	DOC4:Idir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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TN00011

Holding Register	Parameter	Access	Length	
HR1269	NEF1:I0> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=
HR1271	NEF2:I0>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=
HR1273	NEF3:I0>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=
HR1275	NEF4:I0>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=-;1=A-G-R;2=B-G-F;3=C-G-R;4=A-G-F;5=B-G-R;6=
HR1277	DEF1:I0dir> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start
HR1278	DEF1:U0> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1279	DEF1:I0 direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1280	DEF2:I0dir>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start
HR1281	DEF2:U0>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1282	DEF2:I0 direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start
HR1284	DEF3:U0>>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1285	DEF3:I0 direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Unearthed Start
HR1287	DEF4:U0>>>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1288	DEF4:I0 direction now	R	1	0=Reverse;1=Forward;2=Undefined;
HR1289	IEF1:I0Int> condition	R	1	0=Normal;1=StartFWD;2=StartREV;3=Trip;4=Blocked;
HR1290	IEF1:U0> measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1292	CUB2:I2>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1296	HOC1:L1 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	HOC1:I02 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	HOC2:Ih>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1317	HOC2:L1 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	HOC2:L1 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1329	HOC2:I01 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1338	HOC3:L1 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)
HR1342	HOC3:L2 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	HOC3:L3 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	HOC3:I01 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked

Holding Register	Parameter	Access	Length	
HR1359	HOC4:L1 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 harmonic (p.u.)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 harmonic (%)	R	2	FLOAT [0...100000.00] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	CLP1:CLP condition	R	1	0=Normal;1=Curr low;2=Overcurrent On;3=CLPU On;4
HR1380	CBF1:CBFP condition	R	1	0=Normal;1=Start;2=ReTrip;3=CBFP On;4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad;1=Energizing;2=Normal load;3=Overloa
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Bloc
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and ang
HR1385	DIF1:L2 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and ang
HR1386	DIF1:L3 Condition	R	1	0=No Diff;1=Magnitude diff;2=Angle diff;3=Mag.and ang
HR1387	REF1:I0d> condition	R	1	0=Normal;1=Trip;2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=T

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TN00011

Holding Register	Parameter	Access	Length	
HR1389	TOLT1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Lo
HR1390	TOLT1:Temp C or F deg	RW	1	0=C;1=F
HR1391	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set;1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid;1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=T
HR1394	TOLF1:Thermal status	R	1	0=Light / No load;1=High overload;2=Overloading;3=Lo
HR1395	TOLF1:Temp C or F deg	R(W)	1	0=C;1=F
HR1396	TOLF1:Ambient Temp Sel	R(W)	1	0=Manual Set;1=RTD Meas
HR1397	TOLF1:RTD Sensor	R(W)	1	0=Invalid;1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	R(W)	1	0=Set;1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped;1=Starting;2=Running;3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On;3=Inhibit On;4=T
HR1401	TOLM1:Motor status	R	1	0=Stopped;1=Stalled;2=JustStopped;3=Overloading;4=
HR1402	TOLM1:Temp C or F deg	RW	1	0=C;1=F
HR1403	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing;1=NPS Biasing in use
HR1404	TOLM1:Time constants	RW	1	0=Single;1=Multiple
HR1405	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually;1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	RW	1	0=Fixed;1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	RW	1	0=A;1=B;2=F;3=H;4=Manual set
HR1408	TOLM1:Amb. T Selection	RW	1	0=Manual set;1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid;1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal;1=Start; 2=Set time Trip;3=M cap Trip;4=Bloc
HR1411	FSP1:N> condition	R	1	0=Normal;1=Alarm On;2=Inhibits;3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1415	OV2:U>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1416	OV3:U>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1417	OV4:U>>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1418	UV1:U< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked;4=Undervolt Blk

Holding Register	Parameter	Access	Length	
HR1419	UV2:U<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked;4=Undervolt Blk C
HR1420	UV3:U<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked;4=Undervolt Blk C
HR1421	UV4:U<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked;4=Undervolt Blk C
HR1422	NOV1:U0> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1423	NOV1:U0> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1425	NOV2:U0>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1427	NOV3:U0>>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1429	NOV4:U0>>>> measuring now	R	1	0=No U0 avail!;1=U0Calc;2=U3 Input;3=U4 Input
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1443	FRQV1:f>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1444	FRQV1:f>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1445	FRQV1:f>>>> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1446	FRQV1:f< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1447	FRQV1:f<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1448	FRQV1:f<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked

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Holding Register	Parameter	Access	Length	
HR1449	FRQV1:f<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad;1=Normal load;2=Overloaded;3=On hi
HR1459	OPW1:P> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1460	UPW1:P< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1461	RPW1:Prev> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1463	SEF1:U03rd< measuring now	R	1	0=No U0 avail!;1=U3 Input;2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1466	VOC1:Iv> condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail.;1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal;1=Start;;2=Alarm;3=Trip;4=Blocked
HR1469	UPF1:PF< condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=Alarm Start;5=A
HR1470	ARC1:I/I0 Arc> condition	R	2	0b=Z1 Trip;1b=Z1 Blocked;2b=Z2 Trip;3b=Z2 Blocked;4
HR1471				HR1470 (Lo-16bits)
HR1472	CTS1:CTS1 condition	R	1	0=Normal;1=Alarm; 2=Blocked
HR1473	CTS2:CTS2 condition	R	1	0=Normal;1=Alarm; 2=Blocked
HR1474	VTS1:VTS condition	R	1	0=Normal;1=Start;2=VTLinefail;3=VTBusfail;4=Blocked
HR1475	VTS1:Bus voltages	R	1	0=Bus dead;1=Bus Live VTS Ok SEQ Ok;2=Bus Live V
HR1476	FLX1:Fault locator condition	R	1	0=Idle;1=Triggered;2=Calc ready
HR1477	THD1:THD condition	R	1	0=Normal;1=Start; 2=Alarm; 3=Blocked
HR1478	CBW1:CBW condition	R	1	0=Normal;1=Alarm1 On;2=Alarm2 On

Holding Register	Parameter	Access	Length	
HR1479	NJK1:U0> RECL condition	R	1	0=Normal;1=Requested;2=Blocked;3=Active;4=BK clo
HR1480	SOF1:SOTF condition	R	1	0=Normal;1=Init;2=Active;3=Trip;4=Blocked
HR1481	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked;1b=SYN1 Ok;2b=SYN1 Bypass;3b=
HR1482				HR1481 (Lo-16bits)
HR1483	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked;1b=SYN2 Ok;2b=SYN2 Bypass;3b=
HR1484				HR1483 (Lo-16bits)
HR1485	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked;1b=SYN3 Ok;2b=SYN3 Bypass;3b=
HR1486				HR1485 (Lo-16bits)
HR1487	OBJ1:Object1 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1488	OBJ2:Object2 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1489	OBJ3:Object3 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1490	OBJ4:Object4 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1491	OBJ5:Object5 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1492	CIN1:Ind.1 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1493	CIN2:Ind.2 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1494	CIN3:Ind.3 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1495	CIN4:Ind.4 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1496	CIN5:Ind.5 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1497	AR1:AR Status	R	2	0b=AR is inhibit;1b=AR is ready;2b=AR is locked;3b=AR Requested;16b=Executing Shot1;17b=Executing Shot2;
HR1498				HR1497 (Lo-16bits)
HR1499	AR1:Timer active	R	1	0=-;1=AR Lockout;2=AR Reset Reclaim;3=AR Start Del
HR1500	VRG1:Vreg settings condition	R	2	0b=U>> set too low;1b=U< set too high;2b=U> set high
HR1501				HR1500 (Lo-16bits)
HR1502	VRG1:Vreg condition	R	2	0b=Raise command on;1b=Lower command on;2b=Op
HR1503				HR1502 (Lo-16bits)
HR1504	VRG1:Vreg timer active	R	1	0=-;1=Fine tune Decrease;2=Fine tune Increase;3=Low
HR1505	VRG1:Voltage measurement condition	R	1	0=Not configured;1=System U12;2=System U23;3=Sys
HR1506	EMON1:I> (50) Monitor condition	R	1	0=Normal;1=FLTdetected
HR1507	EMON1:I0> (50N) Monitor condition	R	1	0=Normal;1=FLTdetected

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Holding Register	Parameter	Access	Length	
HR1508	EMON1:Idir> (67) Monitor condition	R	1	0=Normal;1=FLTdetected
HR1509	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal;1=FLTdetected
HR1510	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal;1=FLTFW;2=FLTREV
HR1511	EMON1:U< (27) Monitor condition	R	1	0=Normal;1=Undervoltage;2=Novoltage
HR1512	EMON1:U0> (59N) Monitor condition	R	1	0=Normal;1=FLTdetected
HR1513	EMON1:FL (21FL) Monitor condition	R	1	0=Normal;1=FLTdetected
HR1514	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1515				HR1514 (Lo-16bits)
HR1516	DIF1:REFHV> condition	R	2	0b=HV REF Blocked;1b=HV REF Trip (Hi-16bits)
HR1517				HR1516 (Lo-16bits)
HR1518	DIF1:REFLV> condition	R	2	0b=LV REF Blocked;1b=LV REF Trip (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	ALARM:Alarms 1-32	R	2	0b=A1;1b=A2;2b=A3;3b=A4;4b=A5;5b=A6;6b=A7;7b=
HR1521				HR1520 (Lo-16bits)
HR1522	ALARM:Alarms 33-64	R	2	0b=A33;1b=A34;2b=A35;3b=A36;4b=A37;5b=A38;6b=
HR1523				HR1522 (Lo-16bits)
HR1524	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1527				HR1526 (Lo-16bits)
HR1528	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1529				HR1528 (Lo-16bits)
HR1530	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1531				HR1530 (Lo-16bits)
HR1532	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1533				HR1532 (Lo-16bits)
HR1534	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1535				HR1534 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1536	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1539				HR1538 (Lo-16bits)
HR1540	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1541				HR1540 (Lo-16bits)
HR1542	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1543				HR1542 (Lo-16bits)
HR1544	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1545				HR1544 (Lo-16bits)
HR1546	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1547				HR1546 (Lo-16bits)
HR1548	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1551				HR1550 (Lo-16bits)
HR1552	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1553				HR1552 (Lo-16bits)
HR1554	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1557				HR1556 (Lo-16bits)
HR1558	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1559				HR1558 (Lo-16bits)
HR1560	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1561				HR1560 (Lo-16bits)
HR1562	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR1563				HR1562 (Lo-16bits)
HR1564	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR1565				HR1564 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1566	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1569				HR1568 (Lo-16bits)
HR1570	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1571				HR1570 (Lo-16bits)
HR1572	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1575				HR1574 (Lo-16bits)
HR1576	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1577				HR1576 (Lo-16bits)
HR1578	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1581				HR1580 (Lo-16bits)
HR1582	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1583				HR1582 (Lo-16bits)
HR1584	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1587				HR1586 (Lo-16bits)
HR1588	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1589				HR1588 (Lo-16bits)
HR1590	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1593				HR1592 (Lo-16bits)
HR1594	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1595				HR1594 (Lo-16bits)
HR1596	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1597				HR1596 (Lo-16bits)
HR1598	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)
HR1600	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1605				HR1604 (Lo-16bits)
HR1606	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1617				HR1616 (Lo-16bits)
HR1618	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1625				HR1624 (Lo-16bits)
HR1626	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1627				HR1626 (Lo-16bits)
HR1628	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)
HR1630	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1635				HR1634 (Lo-16bits)
HR1636	VHZ1:V/Hz(2) > condition	R	1	0=Normal;1=Start;;2=Alarm;3=Trip;4=Blocked
HR1637	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1638				HR1637 (Lo-16bits)
HR1639	UIM2:Z<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1640	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1641				HR1640 (Lo-16bits)
HR1642	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1643				HR1642 (Lo-16bits)
HR1644	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1645				HR1644 (Lo-16bits)
HR1646	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1647				HR1646 (Lo-16bits)
HR1648	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1649				HR1648 (Lo-16bits)
HR1650	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1651				HR1650 (Lo-16bits)
HR1652	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1653				HR1652 (Lo-16bits)
HR1654	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1655				HR1654 (Lo-16bits)
HR1656	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1657				HR1656 (Lo-16bits)
HR1658	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1659				HR1658 (Lo-16bits)
HR1660	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1661				HR1660 (Lo-16bits)
HR1662	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1663				HR1662 (Lo-16bits)
HR1664	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1665				HR1664 (Lo-16bits)
HR1666	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1667				HR1666 (Lo-16bits)
HR1668	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1669				HR1668 (Lo-16bits)
HR1670	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1671				HR1670 (Lo-16bits)
HR1672	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1673				HR1672 (Lo-16bits)
HR1674	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1675				HR1674 (Lo-16bits)
HR1676	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1677				HR1676 (Lo-16bits)
HR1678	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1679				HR1678 (Lo-16bits)
HR1680	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1681				HR1680 (Lo-16bits)
HR1682	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1683				HR1682 (Lo-16bits)
HR1684	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1685				HR1684 (Lo-16bits)
HR1686	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1687				HR1686 (Lo-16bits)
HR1688	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1691				HR1690 (Lo-16bits)
HR1692	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1693				HR1692 (Lo-16bits)
HR1694	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1695				HR1694 (Lo-16bits)
HR1696	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1697				HR1696 (Lo-16bits)
HR1698	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1699				HR1698 (Lo-16bits)
HR1700	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1701				HR1700 (Lo-16bits)
HR1702	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1703				HR1702 (Lo-16bits)
HR1704	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1705				HR1704 (Lo-16bits)
HR1706	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1707				HR1706 (Lo-16bits)
HR1708	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1709				HR1708 (Lo-16bits)
HR1710	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1711				HR1710 (Lo-16bits)
HR1712	OBJ6:Object6 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1713	OBJ7:Object7 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1714	OBJ8:Object8 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad

Holding Register	Parameter	Access	Length	
HR1715	OBJ9:Object9 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1716	OBJ10:Object10 Breaker Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR1717	VRG1:Voltage now	R	2	FLOAT [0.00...140.00] (Hi-16bits)
HR1718				HR1717 (Lo-16bits)
HR1719	VRG1:Voltage difference to set target	R	2	FLOAT [-100.00...140.00] (Hi-16bits)
HR1720				HR1719 (Lo-16bits)
HR1721	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1722				HR1721 (Lo-16bits)
HR1723	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1724				HR1723 (Lo-16bits)
HR1725	VRG1:Control mode	R	1	0=Auto;1=Manual (loc&rem)
HR1726	VRG1:Remote tap control input	RW	1	0=Stop;1=Lower;2=Higher
HR1727	VJP1:<DELTA><alpha> > condition	R	1	0=Normal;1=Blocked;2=Trip;3=Alarm
HR1728	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1729				HR1728 (Lo-16bits)
HR1730	PGS1:PS1 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1731	VJP1:Selected voltage measurement	R	1	0=Selection Ok;1=Selection not available
HR1732	PGS1:PS2 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1733	PGS1:PS3 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1734	PGS1:PS4 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1735	PGS1:PS5 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1736	PGS1:PS6 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1737	PGS1:PS7 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1738	PGS1:PS8 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1739	PGS1:PS9 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1740	PGS1:PS10 >/< Condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR1741	ASC1:Curve1 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1742				HR1741 (Lo-16bits)
HR1743	ASC1:Curve1 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1744				HR1743 (Lo-16bits)
HR1745	ASC1:Curve2 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1746				HR1745 (Lo-16bits)
HR1747	ASC1:Curve2 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1748				HR1747 (Lo-16bits)
HR1749	ASC1:Curve3 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1750				HR1749 (Lo-16bits)
HR1751	ASC1:Curve3 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1752				HR1751 (Lo-16bits)
HR1753	ASC1:Curve4 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1754				HR1753 (Lo-16bits)
HR1755	ASC1:Curve4 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR1756				HR1755 (Lo-16bits)
HR1757	GMAG:Command AVR (remote)	RW	1	0=Off;1=On
HR1758	GMAG:Command FCR (remote)	RW	1	0=Off;1=On
HR1759	GMAG:Command MVAR (remote)	RW	1	0=Off;1=On
HR1760	GMAG:Command PF (remote)	RW	1	0=Off;1=On
HR1761	GMAG:Unload Q (remote)	RW	1	0=Off;1=On
HR1762	GMAG:Reset (remote)	RW	1	0=Off;1=On
HR1763	GMAG:AVR Ref execute (remote)	RW	1	0=Off;1=On
HR1764	GMAG:AVR Ref value (remote)	RW	2	FLOAT [0...450000] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	GMAG:FCR Ref execute (remote)	RW	1	0=Off;1=On
HR1767	GMAG:FCR Ref value (remote)	RW	2	FLOAT [0.00...100.00] (Hi-16bits)
HR1768				HR1767 (Lo-16bits)
HR1769	GMAG:MVar Ref execute (remote)	RW	1	0=Off;1=On
HR1770	GMAG:MVar Ref value (MVar) (remote)	RW	2	FLOAT [-1000.0000...1000.0000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	GMAG:PF Ref execute (remote)	RW	1	0=Off;1=On
HR1773	GMAG:PF Ref value (remote)	RW	2	FLOAT [-1.00...1.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1774				HR1773 (Lo-16bits)
HR1775	GMAG:Open FB command (remote)	RW	1	0=Off;1=On
HR1776	GMAG:Close FB command (remote)	RW	1	0=Off;1=On
HR1777	GMAG:AVR speed 90 (remote)	RW	1	0=Off;1=On
HR1778	GMAG:Ind GB closed (remote)	RW	1	0=Off;1=On
HR1779	GMAG:Island mode (remote)	RW	1	0=Off;1=On
HR1780	GMAG:Command voltage matching (remote)	RW	1	0=Off;1=On
HR1781	GMAG:Grid voltage (remote)	RW	2	FLOAT [0.00...500.00] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	GMAG:AVR Ref start (remote)	RW	1	0=Off;1=On
HR1784	GMAG:Field current (remote)	R	2	FLOAT [-500.00...500.00] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	GMAG:AVR Ref actual (remote)	R	2	FLOAT [-500.00...500.00] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	GMAG:FCR Ref actual (remote)	R	2	FLOAT [-500.00...500.00] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	GMAG:MVAR Ref actual (remote)	R	2	FLOAT [-500.00...500.00] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	GMAG:PF Ref actual (remote)	R	2	FLOAT [-500.00...500.00] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	GMAG:Mode MVAR (remote)	R	1	0=Off;1=On
HR1795	GMAG:Mode PF (remote)	R	1	0=Off;1=On
HR1796	DIGI:DIs	R	2	0b=DI65;1b=DI66;2b=DI67;3b=DI68;4b=DI69;5b=DI70
HR1797				HR1796 (Lo-16bits)
HR1798	DIGI:DIs	R	2	0b=DI97;1b=DI98;2b=DI99;3b=DI100;4b=DI101;5b=DI (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	RELE:Outputs status	R	2	0b=O1;1b=O2;2b=O3;3b=O4;4b=O5;5b=O6;6b=O7;7b
HR1801				HR1800 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1802	GMAG:Command Excitation to Remote control	RW	1	0=Off;1=On
HR1803	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GMAG:Command Excitation to Remote control Received	R	1	0=No;1=Yes
HR1806	GMAG:Command AVR (remote) Received	R	1	0=No;1=Yes
HR1807	GMAG:Command FCR (remote) Received	R	1	0=No;1=Yes
HR1808	GMAG:Command MVAR (remote) Received	R	1	0=No;1=Yes
HR1809	GMAG:Command PF (remote) Received	R	1	0=No;1=Yes
HR1810	GMAG:Reset remote Received indications	RW	1	0=-;1=Reset
HR1811	GMAG:GMAG FCR Mode	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	GMAG:GMAG AVR Mode	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GMAG:Unload Q (remote) Received	R	1	0=No;1=Yes
HR1816	GMAG:Reset (remote) Received	R	1	0=No;1=Yes
HR1817	GMAG:AVR Ref execute (remote) Received	R	1	0=No;1=Yes
HR1818	GMAG:AVR Ref value (remote) Received	R	2	FLOAT [0...450000] (Hi-16bits)
HR1819				HR1818 (Lo-16bits)
HR1820	GMAG:FCR Ref execute (remote) Received	R	1	0=No;1=Yes
HR1821	GMAG:FCR Ref value (remote) Received	R	2	FLOAT [0.00...100.00] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GMAG:MVar Ref execute (remote) Received	R	1	0=No;1=Yes
HR1824	GMAG:MVar Ref value (remote) Received	R	2	FLOAT [-1000.00...1000.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1825				HR1824 (Lo-16bits)
HR1826	GMAG:PF Ref execute (remote) Received	R	1	0=No;1=Yes
HR1827	GMAG:PF Ref value (remote) Received	R	2	FLOAT [-1.00...1.00] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	GMAG:Open FB command (remote) Received	R	1	0=No;1=Yes
HR1830	GMAG:Close FB command (remote) Received	R	1	0=No;1=Yes
HR1831	GMAG:AVR speed 90 (remote) Received	R	1	0=No;1=Yes
HR1832	GMAG:Ind GB closed (remote) Received	R	1	0=No;1=Yes
HR1833	GMAG:Island mode (remote) Received	R	1	0=No;1=Yes
HR1834	GMAG:Command voltage matching (remote) Received	R	1	0=No;1=Yes
HR1835	GMAG:Grid voltage (remote) Received	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	GMAG:AVR Ref start (remote) Received	R	1	0=No;1=Yes
HR1838	IAE1:I>U< I.A.E. condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR1839	GSYN:Synchronizer mode	R(W)	1	0=Disabled; 1=Activated
HR1840	GSYN:Magnitude difference	R	2	FLOAT [-200.000...200.000] (Hi-16bits)
HR1841				HR1840 (Lo-16bits)
HR1842	GSYN:Frequency difference	R	2	FLOAT [-100.000...100.000] (Hi-16bits)
HR1843				HR1842 (Lo-16bits)
HR1844	GSYN:Angle difference	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR1845				HR1844 (Lo-16bits)
HR1846	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1847				HR1846 (Lo-16bits)
HR1848	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1849				HR1848 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR1850	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1851				HR1850 (Lo-16bits)
HR1852	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1853				HR1852 (Lo-16bits)
HR1854	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)
HR1862	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200.000...200.000] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100.000...100.000] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	GSYN:Angle difference fine tune	RW	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR1873				HR1872 (Lo-16bits)
HR1874	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1878	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1879				HR1878 (Lo-16bits)
HR1880	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200.000...200.000] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100.000...100.000] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)
HR1886	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)
HR1888	GSYN:Synchronizing time left	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	VREC:Fault register mode	R(W)	1	0=Disabled; 1=Activated
HR1891	VREC:Scale current values to primary	RW	1	0=No;1=Yes
HR1892	VREC:Fault register trigger	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	VREC:Record fault types (Overcurrrent & Voltage Over/ Under) faults	RW	1	0=No;1=Yes
HR1895	VREC:VREC Magnitude 1	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1896				HR1895 (Lo-16bits)
HR1897	VREC:VREC Magnitude 2	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1898				HR1897 (Lo-16bits)
HR1899	VREC:VREC Magnitude 3	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1900				HR1899 (Lo-16bits)
HR1901	VREC:VREC Magnitude 4	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1902				HR1901 (Lo-16bits)
HR1903	VREC:VREC Magnitude 5	R	2	FLOAT [0.000...1800.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1904				HR1903 (Lo-16bits)
HR1905	VREC:VREC Magnitude 6	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1906				HR1905 (Lo-16bits)
HR1907	VREC:VREC Magnitude 7	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1908				HR1907 (Lo-16bits)
HR1909	VREC:VREC Magnitude 8	R	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR1910				HR1909 (Lo-16bits)
HR1911	RTB:Real time bus measurements	R(W)	1	0=Disabled; 1=Activated
HR1912	RTB:Scale current values to primary	RW	1	0=No;1=Yes
HR1913	RTB:Magnitude 1	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1914				HR1913 (Lo-16bits)
HR1915	RTB:Magnitude 2	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1916				HR1915 (Lo-16bits)
HR1917	RTB:Magnitude 3	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1918				HR1917 (Lo-16bits)
HR1919	RTB:Magnitude 4	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1920				HR1919 (Lo-16bits)
HR1921	RTB:Magnitude 5	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1922				HR1921 (Lo-16bits)
HR1923	RTB:Magnitude 6	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1924				HR1923 (Lo-16bits)
HR1925	RTB:Magnitude 7	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1926				HR1925 (Lo-16bits)
HR1927	RTB:Magnitude 8	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	AOUT:mA In (I option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	AOUT:mA In (I option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	AOUT:mA Out Channel 1 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR1934				HR1933 (Lo-16bits)
HR1935	AOUT:mA Out Channel 1 Outputs now (I option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	AOUT:mA Out Channel 2 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	AOUT:mA Out Channel 2 Outputs now (I option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	AOUT:mA Out Channel 3 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	AOUT:mA Out Channel 3 Outputs now (I option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	AOUT:mA Out Channel 4 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	AOUT:mA Out Channel 4 Outputs now (I option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1948				HR1947 (Lo-16bits)
HR1949	AOUT:mA Out Channel 5 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1950				HR1949 (Lo-16bits)
HR1951	AOUT:mA Out Channel 5 Outputs now (I option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1952				HR1951 (Lo-16bits)
HR1953	AOUT:mA Out Channel 6 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1954				HR1953 (Lo-16bits)
HR1955	AOUT:mA Out Channel 6 Outputs now (I option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	AOUT:mA Out Channel 7 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR1958				HR1957 (Lo-16bits)
HR1959	AOUT:mA Out Channel 7 Outputs now (I option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	AOUT:mA Out Channel 8 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR1962				HR1961 (Lo-16bits)
HR1963	AOUT:mA Out Channel 8 Outputs now (I option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1972				HR1971 (Lo-16bits)
HR1973	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1974				HR1973 (Lo-16bits)
HR1975	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1976				HR1975 (Lo-16bits)
HR1977	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1978				HR1977 (Lo-16bits)
HR1979	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1982				HR1981 (Lo-16bits)
HR1983	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1984				HR1983 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR1985	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1986				HR1985 (Lo-16bits)
HR1987	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1988				HR1987 (Lo-16bits)
HR1989	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1990				HR1989 (Lo-16bits)
HR1991	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1992				HR1991 (Lo-16bits)
HR1993	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1994				HR1993 (Lo-16bits)
HR1995	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1996				HR1995 (Lo-16bits)
HR1997	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR1998				HR1997 (Lo-16bits)
HR1999	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2000				HR1999 (Lo-16bits)
HR2001	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2002				HR2001 (Lo-16bits)
HR2003	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2015	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	GSYN:Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	GSYN:Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	GMAG:Read MVar Ref from	RW	1	0=Value from comm Bus;1=Value from AI
HR2036	GMAG:MVar Ref selection	RW	1	0=Scaled input 1;1=Scaled input 2;2=Scaled input 3;3=
HR2037	GMAG:MVar Ref change threshold (AI)	RW	2	FLOAT [0...50.0] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	GMAG:Update MVar Ref automatically when threshold exceeds	RW	1	0=No;1=Yes
HR2040	PWR1:PQS>/< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2041	PWR2:PQS>>/<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2042	PWR3:PQS>>>/<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR2043	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked

Holding Register	Parameter	Access	Length	
HR2044	CIN6:Ind.6 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2045	CIN7:Ind.7 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2046	CIN8:Ind.8 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2047	CIN9:Ind.9 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2048	CIN10:Ind.10 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2049	CIN11:Ind.11 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2050	CIN12:Ind.12 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2051	CIN13:Ind.13 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2052	CIN14:Ind.14 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2053	CIN15:Ind.15 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2054	CIN16:Ind.16 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2055	CIN17:Ind.17 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2056	CIN18:Ind.18 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2057	CIN19:Ind.19 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2058	CIN20:Ind.20 Object Status	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR2059	RHC1:Counter is now	R	1	0=Idle;1=Counting
HR2060	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR2061				HR2060 (Lo-16bits)
HR2062	GST1:Generating time (Running hours)	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2063				HR2062 (Lo-16bits)
HR2064	NOC1:I> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2065	NOC1:I> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2066	NOC1:I> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2067				HR2066 (Lo-16bits)
HR2068	NOC1:I> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2069				HR2068 (Lo-16bits)
HR2070	NOC1:I> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2071				HR2070 (Lo-16bits)
HR2072	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2073				HR2072 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2074	NOC1:Delay type	RW	1	0=DT;1=IDMT
HR2075	NOC1:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2078	NOC1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2079	NOC1:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2080	NOC1:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2081	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC1:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC1:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC1:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC1:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2090	NOC1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2091	NOC2:I>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2092	NOC2:I>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2093	NOC2:I>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2094				HR2093 (Lo-16bits)
HR2095	NOC2:I>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2096				HR2095 (Lo-16bits)
HR2097	NOC2:I>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2098				HR2097 (Lo-16bits)
HR2099	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2101	NOC2:Delay type	RW	1	0=DT;1=IDMT
HR2102	NOC2:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2103				HR2102 (Lo-16bits)
HR2104	NOC2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2105	NOC2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2106	NOC2:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2107	NOC2:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2108	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2109				HR2108 (Lo-16bits)
HR2110	NOC2:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2111				HR2110 (Lo-16bits)
HR2112	NOC2:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2113				HR2112 (Lo-16bits)
HR2114	NOC2:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2115				HR2114 (Lo-16bits)
HR2116	NOC2:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2117	NOC2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2118	NOC3:I>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2119	NOC3:I>>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2120	NOC3:I>>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2121				HR2120 (Lo-16bits)
HR2122	NOC3:I>>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2123				HR2122 (Lo-16bits)
HR2124	NOC3:I>>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2125				HR2124 (Lo-16bits)
HR2126	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2127				HR2126 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2128	NOC3:Delay type	RW	1	0=DT;1=IDMT
HR2129	NOC3:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2130				HR2129 (Lo-16bits)
HR2131	NOC3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2132	NOC3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2133	NOC3:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2134	NOC3:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2135	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2136				HR2135 (Lo-16bits)
HR2137	NOC3:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2138				HR2137 (Lo-16bits)
HR2139	NOC3:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2140				HR2139 (Lo-16bits)
HR2141	NOC3:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2142				HR2141 (Lo-16bits)
HR2143	NOC3:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2144	NOC3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2145	NOC4:I>>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2146	NOC4:I>>>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2147	NOC4:I>>>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2148				HR2147 (Lo-16bits)
HR2149	NOC4:I>>>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2150				HR2149 (Lo-16bits)
HR2151	NOC4:I>>>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2152				HR2151 (Lo-16bits)
HR2153	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2154				HR2153 (Lo-16bits)
HR2155	NOC4:Delay type	RW	1	0=DT;1=IDMT
HR2156	NOC4:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2157				HR2156 (Lo-16bits)
HR2158	NOC4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2159	NOC4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2160	NOC4:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2161	NOC4:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2162	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	NOC4:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2165				HR2164 (Lo-16bits)
HR2166	NOC4:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2167				HR2166 (Lo-16bits)
HR2168	NOC4:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2169				HR2168 (Lo-16bits)
HR2170	NOC4:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2171	NOC4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2172	DOC1:Idir> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2173	DOC1:Idir> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2174	DOC1:Idir> Latest fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2175				HR2174 (Lo-16bits)
HR2176	DOC1:Idir> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2177				HR2176 (Lo-16bits)
HR2178	DOC1:Idir> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2179				HR2178 (Lo-16bits)
HR2180	DOC1:Idir> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2181				HR2180 (Lo-16bits)
HR2182	DOC2:Idir>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2183	DOC2:Idir>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2184	DOC2:Idir>> Latest fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	DOC2:Idir>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2187				HR2186 (Lo-16bits)
HR2188	DOC2:Idir>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2189				HR2188 (Lo-16bits)
HR2190	DOC2:Idir>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2191				HR2190 (Lo-16bits)
HR2192	DOC3:Idir>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2193	DOC3:Idir>>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2194	DOC3:Idir>>> Latest fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2195				HR2194 (Lo-16bits)
HR2196	DOC3:Idir>>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2197				HR2196 (Lo-16bits)
HR2198	DOC3:Idir>>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2199				HR2198 (Lo-16bits)
HR2200	DOC3:Idir>>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2201				HR2200 (Lo-16bits)
HR2202	DOC4:Idir>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2203	DOC4:Idir>>>> Latest fault type	R	1	0=-;1=A-G;2=B-G;3=A-B;4=C-G;5=A-C;6=B-C;7=A-B-
HR2204	DOC4:Idir>>>> Latest fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2206	DOC4:Idir>>>> Latest fault IA current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	DOC4:Idir>>>> Latest fault IB current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2209				HR2208 (Lo-16bits)
HR2210	DOC4:Idir>>>> Latest fault IC current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2211				HR2210 (Lo-16bits)
HR2212	NEF1:I0> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2213	NEF1:I0> Latest fault I0 current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2214				HR2213 (Lo-16bits)
HR2215	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	NEF1:Delay type	RW	1	0=DT;1=IDMT
HR2218	NEF1:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2219				HR2218 (Lo-16bits)
HR2220	NEF1:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2221	NEF1:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2222	NEF1:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2223	NEF1:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2224	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2225				HR2224 (Lo-16bits)
HR2226	NEF1:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2227				HR2226 (Lo-16bits)
HR2228	NEF1:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2229				HR2228 (Lo-16bits)
HR2230	NEF1:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2231				HR2230 (Lo-16bits)
HR2232	NEF1:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7

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Holding Register	Parameter	Access	Length	
HR2233	NEF1:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2234	NEF2:I0>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2235	NEF2:I0>> Latest fault I0 current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NEF2:Delay type	RW	1	0=DT;1=IDMT
HR2240	NEF2:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2241				HR2240 (Lo-16bits)
HR2242	NEF2:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2243	NEF2:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2244	NEF2:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2245	NEF2:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2246	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2247				HR2246 (Lo-16bits)
HR2248	NEF2:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2249				HR2248 (Lo-16bits)
HR2250	NEF2:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2251				HR2250 (Lo-16bits)
HR2252	NEF2:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2253				HR2252 (Lo-16bits)
HR2254	NEF2:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2255	NEF2:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2256	NEF3:I0>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2257	NEF3:I0>>> Latest fault I0 current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2261	NEF3:Delay type	RW	1	0=DT;1=IDMT
HR2262	NEF3:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2263				HR2262 (Lo-16bits)
HR2264	NEF3:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2265	NEF3:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2266	NEF3:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2267	NEF3:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2268	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2269				HR2268 (Lo-16bits)
HR2270	NEF3:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2271				HR2270 (Lo-16bits)
HR2272	NEF3:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2273				HR2272 (Lo-16bits)
HR2274	NEF3:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2275				HR2274 (Lo-16bits)
HR2276	NEF3:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2277	NEF3:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2278	NEF4:I0>>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2279	NEF4:I0>>>> Latest fault I0 current (pri)	R	2	FLOAT [0.00...1000000.00] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	NEF4:Delay type	RW	1	0=DT;1=IDMT
HR2284	NEF4:Definite (min) operating time delay	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2285				HR2284 (Lo-16bits)
HR2286	NEF4:Delay curve series	RW	1	0=IEC;1=IEEE;2=Non standard
HR2287	NEF4:Delay characteristics IEC	RW	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR2288	NEF4:Delay characteristics ANSI/ IEEE	RW	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2289	NEF4:Non-standard delay characteristics	RW	1	0=RI-type;1=RD-type
HR2290	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2291				HR2290 (Lo-16bits)
HR2292	NEF4:IDMT constant A	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2293				HR2292 (Lo-16bits)
HR2294	NEF4:IDMT constant B	RW	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2295				HR2294 (Lo-16bits)
HR2296	NEF4:IDMT constant C	RW	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2297				HR2296 (Lo-16bits)
HR2298	NEF4:Select setting group	RW	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR2299	NEF4:Write settings to selected SG	RW	1	0=-;1=Write (autoreset)
HR2300	DEF1:I0dir> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2301	DEF1:I0dir> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2302				HR2301 (Lo-16bits)
HR2303	DEF1:I0dir> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2304				HR2303 (Lo-16bits)
HR2305	DEF1:I0dir> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	DEF2:I0dir>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2308	DEF2:I0dir>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2309				HR2308 (Lo-16bits)
HR2310	DEF2:I0dir>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2311				HR2310 (Lo-16bits)
HR2312	DEF2:I0dir>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2313				HR2312 (Lo-16bits)
HR2314	DEF3:I0dir>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip

Holding Register	Parameter	Access	Length	
HR2315	DEF3:I0dir>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	DEF3:I0dir>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	DEF3:I0dir>>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	DEF4:I0dir>>>> Latest fault ended with	R	1	0=No Faults;1=Start;2=Trip
HR2322	DEF4:I0dir>>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2323				HR2322 (Lo-16bits)
HR2324	DEF4:I0dir>>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	DEF4:I0dir>>>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)
HR2328	GMAG:MVar Ref value (kVar) (remote)	RW	2	FLOAT [-1000.0000...1000.0000] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	GMAG:Mode FCR (remote)	R	1	0=Off;1=On
HR2331	GMAG:Mode AVR (remote)	R	1	0=Off;1=On
HR2332	ALARM:Alarms 65-96	R	2	0b=A65;1b=A66;2b=A67;3b=A68;4b=A69;5b=A70;6b=
HR2333				HR2332 (Lo-16bits)
HR2334	ALARM:Alarms 97-128	R	2	0b=A97;1b=A98;2b=A99;3b=A100;4b=A101;5b=A102
HR2335				HR2334 (Lo-16bits)
HR2336	VUB1:U1/2 >/< Measurement condition	R	1	0=Seq Volt available;1=No Seq volts available
HR2337	VUB2:U1/2 >>/<< Measurement condition	R	1	0=Seq Volt available;1=No Seq volts available
HR2338	VUB3:U1/2 >>>/<<< Measurement condition	R	1	0=Seq Volt available;1=No Seq volts available
HR2339	VUB4:U1/2 >>>>/<<<< Measurement condition	R	1	0=Seq Volt available;1=No Seq volts available
HR2340	VREC:Reset last fault values from memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2341				HR2340 (Lo-16bits)
HR2342	NOC1:I> phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Tri
HR2343	NOC2:I>> phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Tri
HR2344	NOC3:I>>> phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Tri
HR2345	NOC4:I>>>> phases condition	R	1	0=Normal;1=Start A;2=Start B;3=Start C;4=Trip A;5=Tri
HR2346	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	VRG1:Target voltage (UTGT)	RW	2	FLOAT [70.00...140.00] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2363				HR2362 (Lo-16bits)
HR2364	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)
HR2370	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2371				HR2370 (Lo-16bits)
HR2372	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2379				HR2378 (Lo-16bits)
HR2380	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2381				HR2380 (Lo-16bits)
HR2382	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2383				HR2382 (Lo-16bits)
HR2384	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2385				HR2384 (Lo-16bits)
HR2386	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2387				HR2386 (Lo-16bits)
HR2388	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2389				HR2388 (Lo-16bits)
HR2390	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2391				HR2390 (Lo-16bits)
HR2392	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2393				HR2392 (Lo-16bits)
HR2394	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2395				HR2394 (Lo-16bits)
HR2396	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2397				HR2396 (Lo-16bits)
HR2398	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2401				HR2400 (Lo-16bits)
HR2402	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2403				HR2402 (Lo-16bits)
HR2404	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	VRG1:U>/< time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2417				HR2416 (Lo-16bits)
HR2418	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2419				HR2418 (Lo-16bits)
HR2420	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2431				HR2430 (Lo-16bits)
HR2432	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2437				HR2436 (Lo-16bits)
HR2438	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2439				HR2438 (Lo-16bits)
HR2440	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.10...30.00] (Hi-16bits)
HR2441				HR2440 (Lo-16bits)
HR2442	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2443				HR2442 (Lo-16bits)
HR2444	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)
HR2446	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)
HR2456	IMERX:Positive sequence Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	IMERX:Positive sequence Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2460	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)
HR2462	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000.00...100000.00] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	POW1:3PH Power factor	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	POW2:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	POW2:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	POW2:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2484	POW2:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)
HR2486	POW2:Active Energy (P) Export/ Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	POW2:Active Energy (P) Export/ Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	POW2:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	POW2:Imported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	POW2:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)
HR2496	POW2:Imported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)
HR2498	POW2:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2499				HR2498 (Lo-16bits)
HR2500	POW2:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	POW2:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	POW2:Exported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	POW2:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR2508	POW2:Exported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)
HR2510	POW2:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	POW2:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	POW2:Reset 3ph energies	RW	1	0=-;1=Reset
HR2515	POW2:POW2 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2516				HR2515 (Lo-16bits)
HR2517	POW2:POW2 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	POW2:POW2 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	POW2:POW2 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	POW2:POW2 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	POW2:POW2 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	POW2:POW2 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2528				HR2527 (Lo-16bits)
HR2529	POW2:POW2 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2530				HR2529 (Lo-16bits)
HR2531	POW2:3PH Power factor	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2532				HR2531 (Lo-16bits)
HR2533	LOGIC1:Logic out 33-64	RW	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2534				HR2533 (Lo-16bits)
HR2535	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)
HR2537	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2552				HR2551 (Lo-16bits)
HR2553	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2556				HR2555 (Lo-16bits)
HR2557	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2558				HR2557 (Lo-16bits)
HR2559	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2564				HR2563 (Lo-16bits)
HR2565	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	SYN1:Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	SYN1:Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2576				HR2575 (Lo-16bits)
HR2577	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2580				HR2579 (Lo-16bits)
HR2581	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2582				HR2581 (Lo-16bits)
HR2583	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2586				HR2585 (Lo-16bits)
HR2587	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2588				HR2587 (Lo-16bits)
HR2589	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)
HR2591	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2592				HR2591 (Lo-16bits)
HR2593	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR2594				HR2593 (Lo-16bits)
HR2595	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2598				HR2597 (Lo-16bits)
HR2599	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2600				HR2599 (Lo-16bits)
HR2601	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2604				HR2603 (Lo-16bits)
HR2605	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2606	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2607	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2608	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2609	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2610	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2611	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2612	NOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR2613	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2616				HR2615 (Lo-16bits)
HR2617	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2618				HR2617 (Lo-16bits)
HR2619	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)
HR2621	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR2622				HR2621 (Lo-16bits)
HR2623	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2624				HR2623 (Lo-16bits)
HR2625	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	NOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2628				HR2627 (Lo-16bits)
HR2629	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2630	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2631	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2632	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2633	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2634	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2635	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2636	NOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2637	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2638	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2639	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2640	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2641	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2642	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2643	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2644	NOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2645	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2646	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2647	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2648	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

Holding Register	Parameter	Access	Length	
HR2649	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2650	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2651	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2652	NOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2653	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2654	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2655	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2656	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2657	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2658	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2659	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2660	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2661	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2670				HR2669 (Lo-16bits)
HR2671	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2673	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2688				HR2687 (Lo-16bits)
HR2689	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	NOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2703	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2726	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2727	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2728	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2729	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2730	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2731	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2732	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2733	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2734				HR2733 (Lo-16bits)
HR2735	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2736				HR2735 (Lo-16bits)
HR2737	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2738				HR2737 (Lo-16bits)
HR2739	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2740				HR2739 (Lo-16bits)
HR2741	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2742				HR2741 (Lo-16bits)
HR2743	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2744				HR2743 (Lo-16bits)
HR2745	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2746				HR2745 (Lo-16bits)
HR2747	NOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2748				HR2747 (Lo-16bits)
HR2749	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2750	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2751	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2752	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2753	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2754	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR2755	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2756	NOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2757	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)
HR2763	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2774	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2775	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2776	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2777	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2778	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2779	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2780	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2781	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2782	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2783	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2784	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2785	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2786	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2787	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2788	NOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2789	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2806	NOC2:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR2807	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2808	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2809	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2810	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2811	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2812	NOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR2813	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2830	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2831	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2832	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR2833	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2834	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2835	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2836	NOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR2837	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2838	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2839	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2840	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2841	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2842	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2843	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2844	NOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR2845	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2846	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2847	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2848	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2849	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2850	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2851	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2852	NOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR2853	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2854	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2855	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2856	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR2857	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2858	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2859	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2860	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR2861	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2878				HR2877 (Lo-16bits)
HR2879	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2880				HR2879 (Lo-16bits)
HR2881	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2882				HR2881 (Lo-16bits)
HR2883	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2884				HR2883 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2885	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2886				HR2885 (Lo-16bits)
HR2887	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2888				HR2887 (Lo-16bits)
HR2889	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2890				HR2889 (Lo-16bits)
HR2891	NOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2892				HR2891 (Lo-16bits)
HR2893	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2910				HR2909 (Lo-16bits)
HR2911	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2912				HR2911 (Lo-16bits)
HR2913	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2914				HR2913 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2915	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2916				HR2915 (Lo-16bits)
HR2917	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2926	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2927	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2928	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2929	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2930	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2931	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2932	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR2933	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2934				HR2933 (Lo-16bits)
HR2935	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2936				HR2935 (Lo-16bits)
HR2937	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2938				HR2937 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR2939	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2940				HR2939 (Lo-16bits)
HR2941	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2942				HR2941 (Lo-16bits)
HR2943	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2944				HR2943 (Lo-16bits)
HR2945	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2946				HR2945 (Lo-16bits)
HR2947	NOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2948				HR2947 (Lo-16bits)
HR2949	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2950	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2951	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2952	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2953	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2954	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2955	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2956	NOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR2957	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2958				HR2957 (Lo-16bits)
HR2959	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2960				HR2959 (Lo-16bits)
HR2961	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2962				HR2961 (Lo-16bits)
HR2963	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2964				HR2963 (Lo-16bits)
HR2965	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR2967	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2974	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2975	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2976	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2977	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2978	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2979	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2980	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR2981	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2982	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2983	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2984	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2985	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2986	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2987	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR2988	NOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR2989	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3004				HR3003 (Lo-16bits)
HR3005	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3006	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3007	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3008	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3009	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3010	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3011	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3012	NOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3013	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)
HR3015	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3018				HR3017 (Lo-16bits)
HR3019	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3030	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3031	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3032	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3033	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3034	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3035	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3036	NOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3037	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3038	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3039	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3040	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3041	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3042	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3043	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3044	NOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

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Holding Register	Parameter	Access	Length	
HR3045	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3046	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3047	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3048	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3049	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3050	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3051	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3052	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3053	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3054	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3055	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3056	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3057	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3058	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3059	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3060	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3061	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3064				HR3063 (Lo-16bits)
HR3065	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3067	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3078				HR3077 (Lo-16bits)
HR3079	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3080				HR3079 (Lo-16bits)
HR3081	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3082				HR3081 (Lo-16bits)
HR3083	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3084				HR3083 (Lo-16bits)
HR3085	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3086				HR3085 (Lo-16bits)
HR3087	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3088				HR3087 (Lo-16bits)
HR3089	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3090				HR3089 (Lo-16bits)
HR3091	NOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3092				HR3091 (Lo-16bits)
HR3093	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3097	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)
HR3103	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3110				HR3109 (Lo-16bits)
HR3111	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3112				HR3111 (Lo-16bits)
HR3113	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3114				HR3113 (Lo-16bits)
HR3115	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3116				HR3115 (Lo-16bits)
HR3117	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3126	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3127	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3128	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3129	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3130	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3131	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3132	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3133	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3134				HR3133 (Lo-16bits)
HR3135	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3136				HR3135 (Lo-16bits)
HR3137	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3138				HR3137 (Lo-16bits)
HR3139	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)
HR3143	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)
HR3147	NOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3148				HR3147 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3149	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3150	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3151	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3152	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3153	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3154	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3155	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3156	NOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3157	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)
HR3165	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3174	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3175	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3176	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3177	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3178	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3179	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3180	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3181	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3182	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3183	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3184	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3185	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3186	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3187	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3188	NOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3189	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3201	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3206	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3207	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3208	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3209	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3210	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3211	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3212	NOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR3213	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3227	NOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3230	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3231	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3232	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3233	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3234	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3235	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3236	NOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3237	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3238	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3239	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3240	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3241	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3242	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3243	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3244	NOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3245	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3246	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3247	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3248	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3249	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3250	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3251	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3252	NOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

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Holding Register	Parameter	Access	Length	
HR3253	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3254	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3255	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3256	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3257	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3258	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3259	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3260	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3261	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3279	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	NOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3309	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)
HR3315	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3326	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3327	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3328	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3329	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3330	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3331	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3332	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3333	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3335	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)
HR3347	NOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3348				HR3347 (Lo-16bits)
HR3349	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3350	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3351	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3352	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3353	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3354	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3355	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3356	NOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3357	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3362				HR3361 (Lo-16bits)
HR3363	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3374	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3375	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3376	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3377	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3378	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3379	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3380	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3381	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3382	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3383	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3384	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3385	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR3386	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3387	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3388	NOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3389	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3390	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3391	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3392	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3393	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3394	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3395	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3396	DOC1:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3397	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)
HR3405	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)
HR3407	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3410				HR3409 (Lo-16bits)
HR3411	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3415	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	DOC1:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3430				HR3429 (Lo-16bits)
HR3431	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3432				HR3431 (Lo-16bits)
HR3433	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3434				HR3433 (Lo-16bits)
HR3435	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3436				HR3435 (Lo-16bits)
HR3437	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3440				HR3439 (Lo-16bits)
HR3441	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3445	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3446	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3447	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3448	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3449	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3450	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3451	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3452	DOC1:Delay type	R(W)	1	0=DT;1=IDMT
HR3453	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3454				HR3453 (Lo-16bits)
HR3455	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3456				HR3455 (Lo-16bits)
HR3457	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3458				HR3457 (Lo-16bits)
HR3459	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3460				HR3459 (Lo-16bits)
HR3461	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)
HR3467	DOC1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3470	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR3471	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3472	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3473	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3474	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3475	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3476	DOC1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3477	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3478	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3479	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3480	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3481	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3482	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3483	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3484	DOC1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3485	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3486	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3487	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3488	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3489	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3490	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3491	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3492	DOC1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3493	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3494	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3495	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR3496	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3497	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3498	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3499	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3500	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3501	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)
HR3519	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)
HR3523	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)
HR3547	DOC1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)
HR3549	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3550				HR3549 (Lo-16bits)
HR3551	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3552				HR3551 (Lo-16bits)
HR3553	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR3554				HR3553 (Lo-16bits)
HR3555	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3556				HR3555 (Lo-16bits)
HR3557	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)
HR3559	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3566	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3567	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3568	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3569	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3570	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3571	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3572	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3573	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3574				HR3573 (Lo-16bits)
HR3575	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3576				HR3575 (Lo-16bits)
HR3577	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3578				HR3577 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3579	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3580				HR3579 (Lo-16bits)
HR3581	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3582				HR3581 (Lo-16bits)
HR3583	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3584				HR3583 (Lo-16bits)
HR3585	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3586				HR3585 (Lo-16bits)
HR3587	DOC1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3588				HR3587 (Lo-16bits)
HR3589	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3590	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3591	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3592	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3593	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3594	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3595	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3596	DOC1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3597	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3598				HR3597 (Lo-16bits)
HR3599	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3600				HR3599 (Lo-16bits)
HR3601	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3602				HR3601 (Lo-16bits)
HR3603	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3604				HR3603 (Lo-16bits)
HR3605	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3607	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3614	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3615	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3616	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3617	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3618	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3619	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3620	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3621	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3622	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3623	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3624	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3625	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3626	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3627	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3628	DOC1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR3629	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3630	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3631	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3632	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3633	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3634	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3635	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3636	DOC2:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3637	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3652				HR3651 (Lo-16bits)
HR3653	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)
HR3655	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3659	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC2:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3670				HR3669 (Lo-16bits)
HR3671	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3672				HR3671 (Lo-16bits)
HR3673	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3674				HR3673 (Lo-16bits)
HR3675	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3676				HR3675 (Lo-16bits)
HR3677	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3680				HR3679 (Lo-16bits)
HR3681	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)
HR3685	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3686	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3687	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3688	DOC2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR3689	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3690	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3691	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3692	DOC2:Delay type	R(W)	1	0=DT;1=IDMT
HR3693	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3694				HR3693 (Lo-16bits)
HR3695	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3696				HR3695 (Lo-16bits)
HR3697	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3698				HR3697 (Lo-16bits)
HR3699	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3700				HR3699 (Lo-16bits)
HR3701	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)
HR3703	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3710	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3711	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3712	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3713	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3714	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR3715	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3716	DOC2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3717	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3718	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3719	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3720	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3721	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3722	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3723	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3724	DOC2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3725	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3726	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3727	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3728	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3729	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3730	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3731	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3732	DOC2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3733	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3734	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3735	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3736	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3737	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR3738	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3739	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3740	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3741	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3767	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)
HR3777	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3790				HR3789 (Lo-16bits)
HR3791	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3792				HR3791 (Lo-16bits)
HR3793	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3794				HR3793 (Lo-16bits)
HR3795	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3796				HR3795 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3797	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)
HR3799	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3806	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3807	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3808	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3809	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3810	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3811	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3812	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR3813	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3814				HR3813 (Lo-16bits)
HR3815	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3816				HR3815 (Lo-16bits)
HR3817	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3818				HR3817 (Lo-16bits)
HR3819	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3820				HR3819 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR3821	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3822				HR3821 (Lo-16bits)
HR3823	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3824				HR3823 (Lo-16bits)
HR3825	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3826				HR3825 (Lo-16bits)
HR3827	DOC2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR3828				HR3827 (Lo-16bits)
HR3829	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3830	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3831	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3832	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3833	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3834	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3835	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3836	DOC2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR3837	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3838				HR3837 (Lo-16bits)
HR3839	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3840				HR3839 (Lo-16bits)
HR3841	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3842				HR3841 (Lo-16bits)
HR3843	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3844				HR3843 (Lo-16bits)
HR3845	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)
HR3847	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3849	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)
HR3851	DOC2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3854	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3855	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3856	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3857	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3858	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3859	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3860	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR3861	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3862	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3863	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3864	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3865	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3866	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3867	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3868	DOC2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR3869	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3870	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir

Holding Register	Parameter	Access	Length	
HR3871	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3872	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3873	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3874	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3875	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3876	DOC3:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR3877	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR3901	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC3:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3910				HR3909 (Lo-16bits)
HR3911	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3912				HR3911 (Lo-16bits)
HR3913	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3914				HR3913 (Lo-16bits)
HR3915	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3916				HR3915 (Lo-16bits)
HR3917	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3926	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3927	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3928	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3929	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3930	DOC3:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR3931	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3932	DOC3:Delay type	R(W)	1	0=DT;1=IDMT
HR3933	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3934				HR3933 (Lo-16bits)
HR3935	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3936				HR3935 (Lo-16bits)
HR3937	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3938				HR3937 (Lo-16bits)
HR3939	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3940				HR3939 (Lo-16bits)
HR3941	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3950	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3951	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3952	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3953	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3954	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3955	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR3956	DOC3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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Holding Register	Parameter	Access	Length	
HR3957	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3958	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3959	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3960	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3961	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3962	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3963	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3964	DOC3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR3965	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3966	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3967	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3968	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3969	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3970	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3971	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3972	DOC3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR3973	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3974	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3975	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3976	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3977	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3978	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3979	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR3980	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR3981	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4030				HR4029 (Lo-16bits)
HR4031	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4032				HR4031 (Lo-16bits)
HR4033	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4034				HR4033 (Lo-16bits)
HR4035	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4036				HR4035 (Lo-16bits)
HR4037	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4046	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4047	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4048	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4049	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4050	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4051	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4052	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4053	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4054				HR4053 (Lo-16bits)
HR4055	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4056				HR4055 (Lo-16bits)
HR4057	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4058				HR4057 (Lo-16bits)
HR4059	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4060				HR4059 (Lo-16bits)
HR4061	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4062				HR4061 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4063	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4064				HR4063 (Lo-16bits)
HR4065	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4066				HR4065 (Lo-16bits)
HR4067	DOC3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4068				HR4067 (Lo-16bits)
HR4069	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4070	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4071	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4072	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4073	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4074	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4075	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4076	DOC3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4077	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4078				HR4077 (Lo-16bits)
HR4079	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4080				HR4079 (Lo-16bits)
HR4081	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4082				HR4081 (Lo-16bits)
HR4083	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4084				HR4083 (Lo-16bits)
HR4085	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)
HR4089	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4090				HR4089 (Lo-16bits)
HR4091	DOC3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4094	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4095	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4096	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4097	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4098	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4099	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4100	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4101	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4102	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4103	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4104	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4105	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4106	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4107	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4108	DOC3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4109	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4110	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4111	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4112	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4113	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir

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Holding Register	Parameter	Access	Length	
HR4114	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4115	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4116	DOC4:Characteristic direction	R(W)	1	0=Directional;1=NonDir
HR4117	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4120				HR4119 (Lo-16bits)
HR4121	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1.0...170.0] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)
HR4141	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4144				HR4143 (Lo-16bits)
HR4145	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC4:Operating sector center	R(W)	2	FLOAT [-180.0...180.0] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4150				HR4149 (Lo-16bits)
HR4151	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4152				HR4151 (Lo-16bits)
HR4153	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4154				HR4153 (Lo-16bits)
HR4155	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4156				HR4155 (Lo-16bits)
HR4157	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.10...40.00] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4166	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4167	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4168	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4169	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4170	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4171	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4172	DOC4:Delay type	R(W)	1	0=DT;1=IDMT
HR4173	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4174				HR4173 (Lo-16bits)
HR4175	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4176				HR4175 (Lo-16bits)
HR4177	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4178				HR4177 (Lo-16bits)
HR4179	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4180				HR4179 (Lo-16bits)
HR4181	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4190	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4191	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4192	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4193	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4194	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4195	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4196	DOC4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4197	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4198	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4199	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4200	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR4201	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4202	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4203	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4204	DOC4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4205	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4206	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4207	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4208	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4209	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4210	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4211	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4212	DOC4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4213	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4214	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4215	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4216	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4217	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4218	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4219	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4220	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4221	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4223	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4253	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)
HR4265	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4270				HR4269 (Lo-16bits)
HR4271	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4272				HR4271 (Lo-16bits)
HR4273	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4274				HR4273 (Lo-16bits)
HR4275	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4276				HR4275 (Lo-16bits)
HR4277	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4283	DOC4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4286	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4287	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4288	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4289	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4290	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4291	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4292	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4293	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4294				HR4293 (Lo-16bits)
HR4295	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4296				HR4295 (Lo-16bits)
HR4297	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4300				HR4299 (Lo-16bits)
HR4301	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4302				HR4301 (Lo-16bits)
HR4303	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4306				HR4305 (Lo-16bits)
HR4307	DOC4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4310	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4311	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4312	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4313	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4314	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4315	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4316	DOC4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4317	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4334	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR4335	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4336	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4337	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4338	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4339	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4340	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4341	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4342	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4343	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4344	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4345	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4346	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4347	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4348	DOC4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4349	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4358				HR4357 (Lo-16bits)
HR4359	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4366	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4367	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4368	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4369	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4370	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4371	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4372	NEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR4373	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4385	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	NEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4390	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4391	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4392	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4393	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4394	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4395	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4396	NEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4397	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4398	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4399	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4400	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4401	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4402	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4403	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4404	NEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4405	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4406	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4407	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4408	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4409	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4410	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

Holding Register	Parameter	Access	Length	
HR4411	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4412	NEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4413	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4414	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4415	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4416	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4417	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4418	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4419	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4420	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4421	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4436				HR4435 (Lo-16bits)
HR4437	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)
HR4445	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4486	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4487	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4488	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4489	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4490	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4491	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR4492	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4493	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)
HR4503	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4504				HR4503 (Lo-16bits)
HR4505	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4506				HR4505 (Lo-16bits)
HR4507	NEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4508				HR4507 (Lo-16bits)
HR4509	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4510	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4511	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4512	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4513	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4514	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4515	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4516	NEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4517	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4518				HR4517 (Lo-16bits)
HR4519	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4522				HR4521 (Lo-16bits)
HR4523	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4524				HR4523 (Lo-16bits)
HR4525	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4534	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4535	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4536	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4537	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4538	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4539	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4540	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4541	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4542	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4543	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4544	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4545	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4546	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4547	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4548	NEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4549	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4566	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4567	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4568	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4569	NEF2:Delay type	R(W)	1	0=DT;1=IDMT

Holding Register	Parameter	Access	Length	
HR4570	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4571	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4572	NEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR4573	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4590	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4591	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4592	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4593	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4594	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4595	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

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TN00011

Holding Register	Parameter	Access	Length	
HR4596	NEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4597	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4598	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4599	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4600	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4601	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4602	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4603	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4604	NEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4605	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4606	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4607	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4608	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4609	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4610	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4611	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4612	NEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4613	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4614	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4615	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4616	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4617	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4618	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

Holding Register	Parameter	Access	Length	
HR4619	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4620	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4621	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4638				HR4637 (Lo-16bits)
HR4639	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4640				HR4639 (Lo-16bits)
HR4641	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4642				HR4641 (Lo-16bits)
HR4643	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4644				HR4643 (Lo-16bits)
HR4645	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4646				HR4645 (Lo-16bits)
HR4647	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR4648				HR4647 (Lo-16bits)
HR4649	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4650				HR4649 (Lo-16bits)
HR4651	NEF2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4652				HR4651 (Lo-16bits)
HR4653	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4670				HR4669 (Lo-16bits)
HR4671	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4672				HR4671 (Lo-16bits)
HR4673	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4674				HR4673 (Lo-16bits)
HR4675	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4676				HR4675 (Lo-16bits)
HR4677	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4686	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4687	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4688	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4689	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4690	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4691	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4692	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4693	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4694				HR4693 (Lo-16bits)
HR4695	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4696				HR4695 (Lo-16bits)
HR4697	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4698				HR4697 (Lo-16bits)
HR4699	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4700				HR4699 (Lo-16bits)
HR4701	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4702				HR4701 (Lo-16bits)
HR4703	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4704				HR4703 (Lo-16bits)
HR4705	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4706				HR4705 (Lo-16bits)
HR4707	NEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4708				HR4707 (Lo-16bits)
HR4709	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4710	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4711	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4712	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4713	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4714	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4715	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4716	NEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4717	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4718				HR4717 (Lo-16bits)
HR4719	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4720				HR4719 (Lo-16bits)
HR4721	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4722				HR4721 (Lo-16bits)
HR4723	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4724				HR4723 (Lo-16bits)
HR4725	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)
HR4727	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4731	NEF2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4734	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4735	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4736	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4737	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4738	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4739	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4740	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4741	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4742	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4743	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4744	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4745	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4746	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4747	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4748	NEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4749	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR4753	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4766	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4767	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4768	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4769	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4770	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4771	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4772	NEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR4773	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4778				HR4777 (Lo-16bits)
HR4779	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR4781	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4790	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4791	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4792	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4793	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4794	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4795	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4796	NEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4797	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4798	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4799	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4800	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4801	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4802	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4803	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4804	NEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4805	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4806	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4807	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

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Holding Register	Parameter	Access	Length	
HR4808	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4809	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4810	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4811	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4812	NEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR4813	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4814	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4815	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4816	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4817	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4818	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4819	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4820	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR4821	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)
HR4827	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR4832				HR4831 (Lo-16bits)
HR4833	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4838				HR4837 (Lo-16bits)
HR4839	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4840				HR4839 (Lo-16bits)
HR4841	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4842				HR4841 (Lo-16bits)
HR4843	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4844				HR4843 (Lo-16bits)
HR4845	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4846				HR4845 (Lo-16bits)
HR4847	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4848				HR4847 (Lo-16bits)
HR4849	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4850				HR4849 (Lo-16bits)
HR4851	NEF3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4852				HR4851 (Lo-16bits)
HR4853	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)
HR4857	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4870				HR4869 (Lo-16bits)
HR4871	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4872				HR4871 (Lo-16bits)
HR4873	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4874				HR4873 (Lo-16bits)
HR4875	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4876				HR4875 (Lo-16bits)
HR4877	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4886	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4887	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4888	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4889	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4890	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4891	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4892	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR4893	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4894				HR4893 (Lo-16bits)
HR4895	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4896				HR4895 (Lo-16bits)
HR4897	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4898				HR4897 (Lo-16bits)
HR4899	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4900				HR4899 (Lo-16bits)
HR4901	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4902				HR4901 (Lo-16bits)
HR4903	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4904				HR4903 (Lo-16bits)
HR4905	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4906				HR4905 (Lo-16bits)
HR4907	NEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR4908				HR4907 (Lo-16bits)
HR4909	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4910	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4911	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4912	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4913	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4914	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR4915	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4916	NEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR4917	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4918				HR4917 (Lo-16bits)
HR4919	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4920				HR4919 (Lo-16bits)
HR4921	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4922				HR4921 (Lo-16bits)
HR4923	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4924				HR4923 (Lo-16bits)
HR4925	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4934	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4935	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4936	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4937	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4938	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4939	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR4940	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR4941	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4942	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4943	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4944	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4945	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4946	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4947	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4948	NEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR4949	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)
HR4961	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40.000] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4966	NEF4:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR4967	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4968	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4969	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4970	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4971	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4972	NEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR4973	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)
HR4979	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)
HR4987	NEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4990	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4991	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4992	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR4993	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4994	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4995	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4996	NEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR4997	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4998	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR4999	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5000	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5001	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5002	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5003	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5004	NEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5005	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5006	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5007	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5008	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5009	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5010	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5011	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5012	NEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR5013	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5014	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5015	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5016	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR5017	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5018	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5019	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5020	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5021	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5038				HR5037 (Lo-16bits)
HR5039	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5040				HR5039 (Lo-16bits)
HR5041	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5042				HR5041 (Lo-16bits)
HR5043	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5044				HR5043 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5045	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5046				HR5045 (Lo-16bits)
HR5047	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5048				HR5047 (Lo-16bits)
HR5049	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5050				HR5049 (Lo-16bits)
HR5051	NEF4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5052				HR5051 (Lo-16bits)
HR5053	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)
HR5069	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5070				HR5069 (Lo-16bits)
HR5071	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5072				HR5071 (Lo-16bits)
HR5073	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5074				HR5073 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5075	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5076				HR5075 (Lo-16bits)
HR5077	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5086	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5087	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5088	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5089	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5090	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5091	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5092	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5093	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5094				HR5093 (Lo-16bits)
HR5095	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5096				HR5095 (Lo-16bits)
HR5097	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5098				HR5097 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5099	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5100				HR5099 (Lo-16bits)
HR5101	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5102				HR5101 (Lo-16bits)
HR5103	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5104				HR5103 (Lo-16bits)
HR5105	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5106				HR5105 (Lo-16bits)
HR5107	NEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.10...50.00] (Hi-16bits)
HR5108				HR5107 (Lo-16bits)
HR5109	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5110	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5111	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5112	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5113	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5114	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5115	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5116	NEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5117	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5118				HR5117 (Lo-16bits)
HR5119	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5120				HR5119 (Lo-16bits)
HR5121	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5122				HR5121 (Lo-16bits)
HR5123	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5124				HR5123 (Lo-16bits)
HR5125	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5127	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5134	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5135	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5136	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5137	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5138	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5139	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5140	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5141	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5142	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5143	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5144	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5145	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5146	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5147	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5148	NEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR5149	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5150	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5151	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5152	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5153	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5154	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5155	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5156	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5157	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)
HR5163	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5170				HR5169 (Lo-16bits)
HR5171	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5179	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5190				HR5189 (Lo-16bits)
HR5191	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5192				HR5191 (Lo-16bits)
HR5193	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5194				HR5193 (Lo-16bits)
HR5195	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5196				HR5195 (Lo-16bits)
HR5197	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)
HR5203	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5205	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5214				HR5213 (Lo-16bits)
HR5215	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5216				HR5215 (Lo-16bits)
HR5217	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5218				HR5217 (Lo-16bits)
HR5219	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5220				HR5219 (Lo-16bits)
HR5221	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5229	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5238				HR5237 (Lo-16bits)
HR5239	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5240				HR5239 (Lo-16bits)
HR5241	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5242				HR5241 (Lo-16bits)
HR5243	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5244				HR5243 (Lo-16bits)
HR5245	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5246				HR5245 (Lo-16bits)
HR5247	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5248				HR5247 (Lo-16bits)
HR5249	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5250				HR5249 (Lo-16bits)
HR5251	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5252				HR5251 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5253	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5254	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5255	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5256	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5257	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5258	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5259	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5260	DEF1:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5261	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)
HR5265	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)
HR5267	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)
HR5289	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Unearth. pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)
HR5293	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Unearth. pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)
HR5313	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)
HR5319	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)
HR5335	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5342	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5343	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5344	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5345	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5346	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5347	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5348	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5349	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5350	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5351	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5352	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5353	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5354	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5355	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5356	DEF1:Delay type	R(W)	1	0=DT;1=IDMT
HR5357	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5358				HR5357 (Lo-16bits)
HR5359	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5360				HR5359 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5361	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5362				HR5361 (Lo-16bits)
HR5363	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5364				HR5363 (Lo-16bits)
HR5365	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)
HR5371	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5384				HR5383 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5385	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)
HR5395	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5406	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5407	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5408	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5409	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5410	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5411	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5412	DEF1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5413	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5414	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5415	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5416	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5417	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5418	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5419	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5420	DEF1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5421	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5422	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5423	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5424	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5425	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5426	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5427	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5428	DEF1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5429	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5430	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5431	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5432	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5433	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR5434	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5435	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5436	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5437	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)
HR5441	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5446				HR5445 (Lo-16bits)
HR5447	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5448				HR5447 (Lo-16bits)
HR5449	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5450				HR5449 (Lo-16bits)
HR5451	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5452				HR5451 (Lo-16bits)
HR5453	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5454				HR5453 (Lo-16bits)
HR5455	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5456				HR5455 (Lo-16bits)
HR5457	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5458				HR5457 (Lo-16bits)
HR5459	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5460				HR5459 (Lo-16bits)
HR5461	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5463	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)
HR5471	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)
HR5487	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5493	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)
HR5501	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5502	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5503	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5504	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5505	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5506	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5507	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5508	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5509	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5510				HR5509 (Lo-16bits)
HR5511	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5512				HR5511 (Lo-16bits)
HR5513	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5514				HR5513 (Lo-16bits)
HR5515	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5516				HR5515 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5517	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5518				HR5517 (Lo-16bits)
HR5519	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5520				HR5519 (Lo-16bits)
HR5521	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5522				HR5521 (Lo-16bits)
HR5523	DEF1:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5524				HR5523 (Lo-16bits)
HR5525	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5526	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5527	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5528	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5529	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5530	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5531	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5532	DEF1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5533	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5534				HR5533 (Lo-16bits)
HR5535	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5536				HR5535 (Lo-16bits)
HR5537	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5538				HR5537 (Lo-16bits)
HR5539	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5540				HR5539 (Lo-16bits)
HR5541	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5545	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5550	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5551	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5552	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5553	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5554	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5555	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5556	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5557	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5558	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5559	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5560	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5561	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5562	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5563	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5564	DEF1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5565	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5566	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat

Holding Register	Parameter	Access	Length	
HR5567	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5568	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5569	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5570	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5571	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5572	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5573	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)
HR5591	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR5597	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5598				HR5597 (Lo-16bits)
HR5599	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5606				HR5605 (Lo-16bits)
HR5607	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5608				HR5607 (Lo-16bits)
HR5609	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5610				HR5609 (Lo-16bits)
HR5611	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5612				HR5611 (Lo-16bits)
HR5613	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5616				HR5615 (Lo-16bits)
HR5617	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5618				HR5617 (Lo-16bits)
HR5619	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5622				HR5621 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5623	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5630				HR5629 (Lo-16bits)
HR5631	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5632				HR5631 (Lo-16bits)
HR5633	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5634				HR5633 (Lo-16bits)
HR5635	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5636				HR5635 (Lo-16bits)
HR5637	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)
HR5639	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5642				HR5641 (Lo-16bits)
HR5643	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5647	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5654				HR5653 (Lo-16bits)
HR5655	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5656				HR5655 (Lo-16bits)
HR5657	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5658				HR5657 (Lo-16bits)
HR5659	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5660				HR5659 (Lo-16bits)
HR5661	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5662				HR5661 (Lo-16bits)
HR5663	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5664				HR5663 (Lo-16bits)
HR5665	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5666				HR5665 (Lo-16bits)
HR5667	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5668				HR5667 (Lo-16bits)
HR5669	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5670	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5671	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR5672	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5673	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5674	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5675	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5676	DEF2:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR5677	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)
HR5685	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)
HR5691	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)
HR5695	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Unearth. pick-up setting I0set (I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)
HR5709	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)
HR5715	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)
HR5719	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Unearth. pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)
HR5733	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)
HR5757	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5758	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5759	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5760	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5761	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5762	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5763	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5764	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR5765	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5766	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5767	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5768	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5769	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5770	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5771	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5772	DEF2:Delay type	R(W)	1	0=DT;1=IDMT
HR5773	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5774				HR5773 (Lo-16bits)
HR5775	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5776				HR5775 (Lo-16bits)
HR5777	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5778				HR5777 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5779	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5780				HR5779 (Lo-16bits)
HR5781	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5803	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)
HR5807	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5822	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5823	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5824	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5825	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5826	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5827	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR5828	DEF2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR5829	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5830	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5831	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5832	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5833	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5834	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5835	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5836	DEF2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR5837	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5838	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5839	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5840	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5841	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5842	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5843	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5844	DEF2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR5845	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5846	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5847	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5848	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5849	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5850	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5851	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

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Holding Register	Parameter	Access	Length	
HR5852	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR5853	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5862				HR5861 (Lo-16bits)
HR5863	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5864				HR5863 (Lo-16bits)
HR5865	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5866				HR5865 (Lo-16bits)
HR5867	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5868				HR5867 (Lo-16bits)
HR5869	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5870				HR5869 (Lo-16bits)
HR5871	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5872				HR5871 (Lo-16bits)
HR5873	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5874				HR5873 (Lo-16bits)
HR5875	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5876				HR5875 (Lo-16bits)
HR5877	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)
HR5901	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5918	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5919	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5920	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5921	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5922	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5923	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5924	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR5925	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5926				HR5925 (Lo-16bits)
HR5927	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5928				HR5927 (Lo-16bits)
HR5929	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5930				HR5929 (Lo-16bits)
HR5931	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5932				HR5931 (Lo-16bits)
HR5933	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5934				HR5933 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR5935	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5936				HR5935 (Lo-16bits)
HR5937	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5938				HR5937 (Lo-16bits)
HR5939	DEF2:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5940				HR5939 (Lo-16bits)
HR5941	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5942	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5943	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5944	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5945	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5946	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5947	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5948	DEF2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR5949	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5950				HR5949 (Lo-16bits)
HR5951	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5952				HR5951 (Lo-16bits)
HR5953	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5954				HR5953 (Lo-16bits)
HR5955	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5956				HR5955 (Lo-16bits)
HR5957	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5966	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5967	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5968	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5969	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5970	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5971	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5972	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR5973	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5974	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5975	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5976	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5977	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5978	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5979	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5980	DEF2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR5981	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5982	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5983	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5984	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5985	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat

Holding Register	Parameter	Access	Length	
HR5986	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5987	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5988	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR5989	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5990				HR5989 (Lo-16bits)
HR5991	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)
HR5995	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6014				HR6013 (Lo-16bits)
HR6015	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6016				HR6015 (Lo-16bits)
HR6017	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6022				HR6021 (Lo-16bits)
HR6023	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6024				HR6023 (Lo-16bits)
HR6025	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6026				HR6025 (Lo-16bits)
HR6027	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6028				HR6027 (Lo-16bits)
HR6029	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6038				HR6037 (Lo-16bits)
HR6039	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6041	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6046				HR6045 (Lo-16bits)
HR6047	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6048				HR6047 (Lo-16bits)
HR6049	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6050				HR6049 (Lo-16bits)
HR6051	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6052				HR6051 (Lo-16bits)
HR6053	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)
HR6063	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6065	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6070				HR6069 (Lo-16bits)
HR6071	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6072				HR6071 (Lo-16bits)
HR6073	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6074				HR6073 (Lo-16bits)
HR6075	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6076				HR6075 (Lo-16bits)
HR6077	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6078				HR6077 (Lo-16bits)
HR6079	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6080				HR6079 (Lo-16bits)
HR6081	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6082				HR6081 (Lo-16bits)
HR6083	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6084				HR6083 (Lo-16bits)
HR6085	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6086	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6087	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6088	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6089	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR6090	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6091	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6092	DEF3:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6093	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)
HR6099	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)
HR6107	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Unearth. pick-up setting I0set (I0Sinfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)
HR6125	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)
HR6131	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Unearth. pick-up setting U0set(I0Sinfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6143	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)
HR6151	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)
HR6155	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6173	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6174	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6175	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6176	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6177	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6178	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6179	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6180	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6181	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6182	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6183	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6184	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6185	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6186	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6187	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6188	DEF3:Delay type	R(W)	1	0=DT;1=IDMT
HR6189	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6190				HR6189 (Lo-16bits)
HR6191	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6192				HR6191 (Lo-16bits)
HR6193	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6194				HR6193 (Lo-16bits)
HR6195	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6196				HR6195 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6197	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)
HR6203	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6221	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)
HR6227	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)
HR6229	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6238	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6239	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6240	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6241	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6242	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6243	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6244	DEF3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6245	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6246	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR6247	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6248	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6249	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6250	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6251	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6252	DEF3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6253	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6254	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6255	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6256	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6257	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6258	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6259	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6260	DEF3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6261	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6262	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6263	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6264	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6265	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6266	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6267	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6268	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type

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TN00011

Holding Register	Parameter	Access	Length	
HR6269	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)
HR6273	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6278				HR6277 (Lo-16bits)
HR6279	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6280				HR6279 (Lo-16bits)
HR6281	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6282				HR6281 (Lo-16bits)
HR6283	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6284				HR6283 (Lo-16bits)
HR6285	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6286				HR6285 (Lo-16bits)
HR6287	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6288				HR6287 (Lo-16bits)
HR6289	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6290				HR6289 (Lo-16bits)
HR6291	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6292				HR6291 (Lo-16bits)
HR6293	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6299	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)
HR6303	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)
HR6307	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6329	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)
HR6331	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)
HR6333	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6334	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6335	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6336	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6337	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6338	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6339	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6340	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6341	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6342				HR6341 (Lo-16bits)
HR6343	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6344				HR6343 (Lo-16bits)
HR6345	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6346				HR6345 (Lo-16bits)
HR6347	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6348				HR6347 (Lo-16bits)
HR6349	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6350				HR6349 (Lo-16bits)
HR6351	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6352				HR6351 (Lo-16bits)
HR6353	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6354				HR6353 (Lo-16bits)
HR6355	DEF3:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6356				HR6355 (Lo-16bits)
HR6357	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6358	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6359	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6360	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6361	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6362	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6363	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6364	DEF3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6365	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6366				HR6365 (Lo-16bits)
HR6367	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6368				HR6367 (Lo-16bits)
HR6369	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6370				HR6369 (Lo-16bits)
HR6371	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6372				HR6371 (Lo-16bits)
HR6373	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6381	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6382	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6383	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6384	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6385	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6386	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6387	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6388	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6389	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6390	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6391	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6392	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6393	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6394	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6395	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6396	DEF3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6397	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6398	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6399	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6400	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6401	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6402	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat

Holding Register	Parameter	Access	Length	
HR6403	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6404	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var];1=Petersen coil GND [32N Wat
HR6405	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6406				HR6405 (Lo-16bits)
HR6407	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6414				HR6413 (Lo-16bits)
HR6415	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)
HR6423	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6433	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6436				HR6435 (Lo-16bits)
HR6437	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6438				HR6437 (Lo-16bits)
HR6439	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6440				HR6439 (Lo-16bits)
HR6441	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6442				HR6441 (Lo-16bits)
HR6443	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6444				HR6443 (Lo-16bits)
HR6445	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6450				HR6449 (Lo-16bits)
HR6451	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6458				HR6457 (Lo-16bits)
HR6459	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6462				HR6461 (Lo-16bits)
HR6463	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6464				HR6463 (Lo-16bits)
HR6465	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6466				HR6465 (Lo-16bits)
HR6467	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6468				HR6467 (Lo-16bits)
HR6469	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6474				HR6473 (Lo-16bits)
HR6475	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR6482				HR6481 (Lo-16bits)
HR6483	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)
HR6485	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6486				HR6485 (Lo-16bits)
HR6487	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6488				HR6487 (Lo-16bits)
HR6489	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6490				HR6489 (Lo-16bits)
HR6491	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6492				HR6491 (Lo-16bits)
HR6493	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6494				HR6493 (Lo-16bits)
HR6495	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6496				HR6495 (Lo-16bits)
HR6497	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6498				HR6497 (Lo-16bits)
HR6499	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6500				HR6499 (Lo-16bits)
HR6501	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6502	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6503	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6504	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6505	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6506	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6507	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used

Holding Register	Parameter	Access	Length	
HR6508	DEF4:Multi-criteria detection	R(W)	1	0=Not Used;1=Is Used
HR6509	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)
HR6527	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6535	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Unearth. pick-up setting I0set (I0Sinfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)
HR6551	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Unearth. pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin

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Holding Register	Parameter	Access	Length	
HR6590	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6591	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6592	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6593	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6594	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6595	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6596	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time;1=Separate times for I0Cosfi and I0Sin
HR6597	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6598	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6599	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6600	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6601	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6602	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6603	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6604	DEF4:Delay type	R(W)	1	0=DT;1=IDMT
HR6605	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6606				HR6605 (Lo-16bits)
HR6607	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6608				HR6607 (Lo-16bits)
HR6609	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6610				HR6609 (Lo-16bits)
HR6611	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6612				HR6611 (Lo-16bits)
HR6613	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)
HR6625	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Definite (min) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6654	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6655	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6656	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6657	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6658	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6659	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6660	DEF4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6661	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6662	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6663	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6664	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR6665	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6666	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6667	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6668	DEF4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6669	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6670	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6671	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6672	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6673	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6674	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6675	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6676	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LTI;4=IEEE MI
HR6677	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6678	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6679	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6680	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6681	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6682	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6683	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6684	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type
HR6685	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6687	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6694				HR6693 (Lo-16bits)
HR6695	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6696				HR6695 (Lo-16bits)
HR6697	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6698				HR6697 (Lo-16bits)
HR6699	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6700				HR6699 (Lo-16bits)
HR6701	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6702				HR6701 (Lo-16bits)
HR6703	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6704				HR6703 (Lo-16bits)
HR6705	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6706				HR6705 (Lo-16bits)
HR6707	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6708				HR6707 (Lo-16bits)
HR6709	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6717	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)
HR6729	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)
HR6733	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6747	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6750	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6751	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6752	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6753	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6754	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6755	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6756	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No;1=Yes
HR6757	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6758				HR6757 (Lo-16bits)
HR6759	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6760				HR6759 (Lo-16bits)
HR6761	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6762				HR6761 (Lo-16bits)
HR6763	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6764				HR6763 (Lo-16bits)
HR6765	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6766				HR6765 (Lo-16bits)
HR6767	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6768				HR6767 (Lo-16bits)
HR6769	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6770				HR6769 (Lo-16bits)
HR6771	DEF4:2nd harmonic blocking limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6772				HR6771 (Lo-16bits)
HR6773	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6774	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6775	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6776	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6777	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6778	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6779	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6780	DEF4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6781	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6782				HR6781 (Lo-16bits)
HR6783	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6784				HR6783 (Lo-16bits)
HR6785	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6786				HR6785 (Lo-16bits)
HR6787	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6788				HR6787 (Lo-16bits)
HR6789	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6798	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR6799	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6800	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6801	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6802	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6803	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6804	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR6805	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6806	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6807	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6808	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6809	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6810	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6811	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6812	DEF4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR6813	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR6822				HR6821 (Lo-16bits)
HR6823	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6830				HR6829 (Lo-16bits)
HR6831	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)
HR6839	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR6852				HR6851 (Lo-16bits)
HR6853	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6854				HR6853 (Lo-16bits)
HR6855	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6856				HR6855 (Lo-16bits)
HR6857	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6858				HR6857 (Lo-16bits)
HR6859	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR6860				HR6859 (Lo-16bits)
HR6861	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6862	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6863	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6864	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6865	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6866	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6867	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6868	CUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR6869	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6878				HR6877 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6879	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6880				HR6879 (Lo-16bits)
HR6881	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6882				HR6881 (Lo-16bits)
HR6883	CUB1:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR6884				HR6883 (Lo-16bits)
HR6885	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6886	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6887	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6888	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6889	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6890	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6891	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6892	CUB1:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR6893	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6894	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6895	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6896	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6897	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6898	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6899	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6900	CUB1:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR6901	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6902	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6903	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6904	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

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TN00011

Holding Register	Parameter	Access	Length	
HR6905	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6906	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6907	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6908	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR6909	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6910	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6911	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6912	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6913	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6914	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6915	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6916	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR6917	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)
HR6925	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR6929	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)
HR6947	CUB1:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR6959	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6966				HR6965 (Lo-16bits)
HR6967	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6968				HR6967 (Lo-16bits)
HR6969	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6970				HR6969 (Lo-16bits)
HR6971	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6972				HR6971 (Lo-16bits)
HR6973	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)
HR6977	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6982	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6983	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6984	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6985	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6986	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6987	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR6988	CUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR6989	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR6990				HR6989 (Lo-16bits)
HR6991	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR6992				HR6991 (Lo-16bits)
HR6993	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR6994				HR6993 (Lo-16bits)
HR6995	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR6996				HR6995 (Lo-16bits)
HR6997	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR6998				HR6997 (Lo-16bits)
HR6999	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7000				HR6999 (Lo-16bits)
HR7001	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7002				HR7001 (Lo-16bits)
HR7003	CUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7004				HR7003 (Lo-16bits)
HR7005	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7006	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7007	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7008	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7009	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7010	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7011	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7012	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7013	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7014	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7015	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7016	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7017	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7018	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7019	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7020	CUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7021	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7024				HR7023 (Lo-16bits)
HR7025	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)
HR7029	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7041	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7054				HR7053 (Lo-16bits)
HR7055	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)
HR7059	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7062				HR7061 (Lo-16bits)
HR7063	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7070	CUB2:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7071	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7072	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7073	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7074	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7075	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7076	CUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR7077	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7084				HR7083 (Lo-16bits)
HR7085	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)
HR7089	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	CUB2:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7094	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7095	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7096	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard

Holding Register	Parameter	Access	Length	
HR7097	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7098	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7099	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7100	CUB2:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7101	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7102	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7103	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7104	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7105	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7106	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7107	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7108	CUB2:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7109	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7110	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7111	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7112	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7113	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7114	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7115	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7116	CUB2:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7117	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7118	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7119	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7120	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2

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Holding Register	Parameter	Access	Length	
HR7121	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7122	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7123	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7124	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7125	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7149	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)
HR7155	CUB2:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)
HR7167	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7174				HR7173 (Lo-16bits)
HR7175	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7176				HR7175 (Lo-16bits)
HR7177	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7178				HR7177 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7179	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7180				HR7179 (Lo-16bits)
HR7181	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7190	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7191	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7192	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7193	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7194	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7195	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7196	CUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7197	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7198				HR7197 (Lo-16bits)
HR7199	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7200				HR7199 (Lo-16bits)
HR7201	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7202				HR7201 (Lo-16bits)
HR7203	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7206				HR7205 (Lo-16bits)
HR7207	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7208				HR7207 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7209	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7210				HR7209 (Lo-16bits)
HR7211	CUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7212				HR7211 (Lo-16bits)
HR7213	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7214	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7215	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7216	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7217	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7218	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7219	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7220	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7221	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7222	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7223	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7224	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7225	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7226	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7227	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7228	CUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7229	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7231	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7232				HR7231 (Lo-16bits)
HR7233	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)
HR7255	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7261	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7278	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7279	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7280	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7281	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7282	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7283	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7284	CUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR7285	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7290				HR7289 (Lo-16bits)
HR7291	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	CUB3:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7302	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7303	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7304	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7305	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7306	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7307	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7308	CUB3:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7309	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7310	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7311	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7312	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7313	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7314	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7315	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7316	CUB3:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param

Holding Register	Parameter	Access	Length	
HR7317	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7318	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7319	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7320	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7321	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7322	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7323	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7324	CUB3:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7325	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7326	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7327	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7328	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7329	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7330	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7331	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7332	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7333	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7339	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)
HR7355	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB3:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7369	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)
HR7381	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)
HR7383	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)
HR7387	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7398	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR7399	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7400	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7401	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7402	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7403	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7404	CUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7405	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	CUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7422	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7423	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7424	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7425	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7426	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7427	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7428	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7429	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7430	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7431	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7432	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7433	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7434	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7435	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7436	CUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7437	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7451	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40.00] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)
HR7455	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1.00...200.00] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7481	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2.00] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7486	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7487	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7488	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7489	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7490	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7491	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7492	CUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR7493	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7502				HR7501 (Lo-16bits)
HR7503	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7507	CUB4:Definite (min) operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7510	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7511	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7512	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7513	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7514	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7515	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7516	CUB4:Delay curve series	R(W)	1	0=IEC;1=IEEE;2=Non standard
HR7517	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7518	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7519	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7520	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7521	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7522	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7523	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7524	CUB4:Delay characteristics IEC	R(W)	1	0=NI;1=EI;2=VI;3=LTI;4=Param
HR7525	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7526	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7527	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7528	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7529	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7530	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7531	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5
HR7532	CUB4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI;1=ANSI VI;2=ANSI EI;3=ANSI LI;4=IEEE MI;5

Holding Register	Parameter	Access	Length	
HR7533	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7534	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7535	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7536	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7537	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7538	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7539	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7540	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type;1=RD-type;2=Curve2
HR7541	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7559	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:IDMT constant A	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:IDMT constant B	R(W)	2	FLOAT [0.0000...5.0] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7589	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7590				HR7589 (Lo-16bits)
HR7591	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7592				HR7591 (Lo-16bits)
HR7593	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7594				HR7593 (Lo-16bits)
HR7595	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7596				HR7595 (Lo-16bits)
HR7597	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:IDMT constant C	R(W)	2	FLOAT [0.0000...250.0] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7606	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7607	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7608	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7609	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7610	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7611	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7612	CUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7613	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7618				HR7617 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7619	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7620				HR7619 (Lo-16bits)
HR7621	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7622				HR7621 (Lo-16bits)
HR7623	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7624				HR7623 (Lo-16bits)
HR7625	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	CUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7630	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7631	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7632	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7633	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7634	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7635	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7636	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7637	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7638	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7639	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7640	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7641	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7642	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7643	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7644	CUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7645	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7646	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7647	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7648	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7649	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7650	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7651	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7652	OV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7653	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7670	OV1:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR7671	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7672	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7673	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7674	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7675	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7676	OV1:Delay type	R(W)	1	0=DT;1=IDMT
HR7677	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	OV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7701	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7710				HR7709 (Lo-16bits)
HR7711	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7712				HR7711 (Lo-16bits)
HR7713	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7714				HR7713 (Lo-16bits)
HR7715	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7716				HR7715 (Lo-16bits)
HR7717	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)
HR7719	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7731	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	OV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7742	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7743	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7744	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7745	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7746	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7747	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7748	OV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7749	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7750	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7751	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7752	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7753	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7754	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7755	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7756	OV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7757	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7758	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7759	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7760	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7761	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7762	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7763	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7764	OV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7765	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7766	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7767	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7768	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7769	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7770	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7771	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7772	OV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7773	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR7783	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7790	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7791	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7792	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7793	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7794	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7795	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7796	OV2:Delay type	R(W)	1	0=DT; 1=IDMT
HR7797	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR7813	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR7843	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7846				HR7845 (Lo-16bits)
HR7847	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7848				HR7847 (Lo-16bits)
HR7849	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7850				HR7849 (Lo-16bits)
HR7851	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7852				HR7851 (Lo-16bits)
HR7853	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7854				HR7853 (Lo-16bits)
HR7855	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7856				HR7855 (Lo-16bits)
HR7857	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7858				HR7857 (Lo-16bits)
HR7859	OV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7860				HR7859 (Lo-16bits)
HR7861	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7862	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7863	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7864	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7865	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7866	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7867	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7868	OV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7869	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7870	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7871	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7872	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7873	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7874	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7875	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7876	OV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7877	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7878	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7879	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7880	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7881	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7882	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7883	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7884	OV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7885	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7886	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7887	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7888	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7889	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7890	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7891	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7892	OV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR7893	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7894				HR7893 (Lo-16bits)
HR7895	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7896				HR7895 (Lo-16bits)
HR7897	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7898				HR7897 (Lo-16bits)
HR7899	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7900				HR7899 (Lo-16bits)
HR7901	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7910	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7911	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7912	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7913	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7914	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7915	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7916	OV3:Delay type	R(W)	1	0=DT; 1=IDMT
HR7917	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR7926				HR7925 (Lo-16bits)
HR7927	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR7956				HR7955 (Lo-16bits)
HR7957	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7966				HR7965 (Lo-16bits)
HR7967	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7968				HR7967 (Lo-16bits)
HR7969	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7970				HR7969 (Lo-16bits)
HR7971	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7972				HR7971 (Lo-16bits)
HR7973	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7974				HR7973 (Lo-16bits)
HR7975	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7976				HR7975 (Lo-16bits)
HR7977	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7978				HR7977 (Lo-16bits)
HR7979	OV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR7980				HR7979 (Lo-16bits)
HR7981	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7982	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7983	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7984	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7985	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR7986	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7987	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7988	OV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR7989	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7990	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7991	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7992	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7993	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7994	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7995	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7996	OV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR7997	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7998	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR7999	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8000	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8001	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8002	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8003	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8004	OV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8005	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8006	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8007	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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Holding Register	Parameter	Access	Length	
HR8008	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8009	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8010	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8011	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8012	OV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8013	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...150.00] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8030	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8031	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8032	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8033	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8034	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8035	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8036	OV4:Delay type	R(W)	1	0=DT; 1=IDMT
HR8037	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8038				HR8037 (Lo-16bits)
HR8039	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR8068				HR8067 (Lo-16bits)
HR8069	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR8098				HR8097 (Lo-16bits)
HR8099	OV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8102	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8103	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8104	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8105	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8106	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8107	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8108	OV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8109	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8110	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8111	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8112	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8113	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8114	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8115	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8116	OV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8117	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8118	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8119	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8120	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8121	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8122	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8123	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8124	OV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8125	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8126	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8127	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8128	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8129	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8130	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8131	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8132	UV1:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8133	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8134				HR8133 (Lo-16bits)
HR8135	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8136				HR8135 (Lo-16bits)
HR8137	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8151	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV1:Block setting Uset<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8166	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8167	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8168	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8169	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8170	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8171	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8172	UV1:Delay type	R(W)	1	0=DT;1=IDMT
HR8173	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8181	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8211	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)
HR8229	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8238	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8239	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8240	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8241	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8242	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8243	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8244	UV1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8245	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8246	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8247	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8248	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8249	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8250	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8251	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8252	UV1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8253	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8254	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8255	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8256	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8257	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8258	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8259	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8260	UV1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8261	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8262	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

Holding Register	Parameter	Access	Length	
HR8263	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8264	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8265	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8266	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8267	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8268	UV2:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8269	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8270				HR8269 (Lo-16bits)
HR8271	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8272				HR8271 (Lo-16bits)
HR8273	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8274				HR8273 (Lo-16bits)
HR8275	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8276				HR8275 (Lo-16bits)
HR8277	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8282				HR8281 (Lo-16bits)
HR8283	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8293	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV2:Block setting Uset<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8302	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8303	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8304	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8305	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8306	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8307	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8308	UV2:Delay type	R(W)	1	0=DT;1=IDMT
HR8309	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8323	UV2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8353	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	UV2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8374	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8375	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8376	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8377	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8378	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8379	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8380	UV2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8381	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8382	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8383	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8384	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8385	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8386	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8387	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8388	UV2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8389	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8390	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8391	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8392	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8393	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8394	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8395	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8396	UV2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8397	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8398	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8399	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8400	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8401	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8402	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8403	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8404	UV3:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages

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TN00011

Holding Register	Parameter	Access	Length	
HR8405	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8406				HR8405 (Lo-16bits)
HR8407	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8408				HR8407 (Lo-16bits)
HR8409	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8410				HR8409 (Lo-16bits)
HR8411	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8412				HR8411 (Lo-16bits)
HR8413	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8435	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8438	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8439	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8440	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8441	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8442	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8443	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8444	UV3:Delay type	R(W)	1	0=DT;1=IDMT
HR8445	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8465	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8495	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	UV3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8510	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8511	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8512	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8513	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8514	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8515	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8516	UV3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8517	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8518	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8519	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8520	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8521	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8522	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8523	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8524	UV3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8525	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8526	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8527	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8528	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8529	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8530	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8531	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8532	UV3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8533	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8534	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8535	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8536	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8537	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8538	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8539	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8540	UV4:Pick-up terms	R(W)	1	0=1 voltage;1=2 voltages;2=3 voltages
HR8541	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8542				HR8541 (Lo-16bits)
HR8543	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8544				HR8543 (Lo-16bits)
HR8545	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8546				HR8545 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8547	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8548				HR8547 (Lo-16bits)
HR8549	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0.00...120.00] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8574	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8575	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8576	UV4:Delay type	R(W)	1	0=DT;1=IDMT

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Holding Register	Parameter	Access	Length	
HR8577	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8578	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8579	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8580	UV4:Delay type	R(W)	1	0=DT;1=IDMT
HR8581	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)
HR8595	UV4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8596				HR8595 (Lo-16bits)
HR8597	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)
HR8599	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8607	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8637	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	UV4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8646	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8647	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8648	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8649	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8650	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8651	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8652	UV4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8653	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8654	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8655	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8656	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8657	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8658	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8659	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8660	UV4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8661	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR8662	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8663	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8664	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8665	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8666	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8667	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8668	UV4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8669	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8670	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8671	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8672	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8673	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8674	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8675	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8676	VUB1:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8677	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8678				HR8677 (Lo-16bits)
HR8679	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8680				HR8679 (Lo-16bits)
HR8681	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8682				HR8681 (Lo-16bits)
HR8683	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8684				HR8683 (Lo-16bits)
HR8685	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8689	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8692				HR8691 (Lo-16bits)
HR8693	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8710	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8711	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8712	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8713	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8714	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8715	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8716	VUB1:Delay type	R(W)	1	0=DT;1=IDMT
HR8717	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8719	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	VUB1:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)
HR8737	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8749	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8754				HR8753 (Lo-16bits)
HR8755	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8779	VUB1:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8782	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8783	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8784	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8785	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8786	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8787	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8788	VUB1:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8789	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8790	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8791	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8792	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8793	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8794	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8795	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8796	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8797	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8798	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8799	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8800	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8801	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes

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Holding Register	Parameter	Access	Length	
HR8802	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8803	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8804	VUB1:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8805	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8806	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8807	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8808	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8809	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8810	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8811	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8812	VUB2:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8813	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8814				HR8813 (Lo-16bits)
HR8815	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8816				HR8815 (Lo-16bits)
HR8817	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8818				HR8817 (Lo-16bits)
HR8819	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8820				HR8819 (Lo-16bits)
HR8821	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8831	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8846	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8847	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8848	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8849	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8850	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8851	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8852	VUB2:Delay type	R(W)	1	0=DT;1=IDMT
HR8853	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR8861	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB2:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)
HR8877	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)
HR8879	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8886				HR8885 (Lo-16bits)
HR8887	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8888				HR8887 (Lo-16bits)
HR8889	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8890				HR8889 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR8891	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8892				HR8891 (Lo-16bits)
HR8893	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8894				HR8893 (Lo-16bits)
HR8895	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8896				HR8895 (Lo-16bits)
HR8897	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8898				HR8897 (Lo-16bits)
HR8899	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR8900				HR8899 (Lo-16bits)
HR8901	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8902				HR8901 (Lo-16bits)
HR8903	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8904				HR8903 (Lo-16bits)
HR8905	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8906				HR8905 (Lo-16bits)
HR8907	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8908				HR8907 (Lo-16bits)
HR8909	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8910				HR8909 (Lo-16bits)
HR8911	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8912				HR8911 (Lo-16bits)
HR8913	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8914				HR8913 (Lo-16bits)
HR8915	VUB2:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR8916				HR8915 (Lo-16bits)
HR8917	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8918	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8919	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8920	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR8921	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8922	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8923	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8924	VUB2:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR8925	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8926	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8927	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8928	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8929	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8930	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8931	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8932	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR8933	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8934	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8935	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8936	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8937	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8938	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8939	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8940	VUB2:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR8941	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8942	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <

Holding Register	Parameter	Access	Length	
HR8943	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8944	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8945	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8946	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8947	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8948	VUB3:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR8949	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8950				HR8949 (Lo-16bits)
HR8951	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8952				HR8951 (Lo-16bits)
HR8953	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8954				HR8953 (Lo-16bits)
HR8955	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8956				HR8955 (Lo-16bits)
HR8957	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)
HR8961	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR8973	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8974				HR8973 (Lo-16bits)
HR8975	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8982	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8983	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8984	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8985	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8986	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8987	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8988	VUB3:Delay type	R(W)	1	0=DT;1=IDMT
HR8989	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)
HR8991	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9003	VUB3:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9004				HR9003 (Lo-16bits)
HR9005	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)
HR9021	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9022				HR9021 (Lo-16bits)
HR9023	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9024				HR9023 (Lo-16bits)
HR9025	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9026				HR9025 (Lo-16bits)
HR9027	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9028				HR9027 (Lo-16bits)
HR9029	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9030				HR9029 (Lo-16bits)
HR9031	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9032				HR9031 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9033	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9034				HR9033 (Lo-16bits)
HR9035	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9036				HR9035 (Lo-16bits)
HR9037	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9038				HR9037 (Lo-16bits)
HR9039	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9040				HR9039 (Lo-16bits)
HR9041	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9042				HR9041 (Lo-16bits)
HR9043	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9044				HR9043 (Lo-16bits)
HR9045	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9046				HR9045 (Lo-16bits)
HR9047	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9048				HR9047 (Lo-16bits)
HR9049	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9050				HR9049 (Lo-16bits)
HR9051	VUB3:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9052				HR9051 (Lo-16bits)
HR9053	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9054	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9055	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9056	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9057	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9058	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9059	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9060	VUB3:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9061	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9062	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9063	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9064	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9065	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9066	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9067	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9068	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9069	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9070	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9071	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9072	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9073	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9074	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9075	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9076	VUB3:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9077	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9078	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9079	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9080	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9081	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9082	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9083	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <
HR9084	VUB4:Pick-up terms	R(W)	1	0=Over >;1=Under <

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Holding Register	Parameter	Access	Length	
HR9085	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9086				HR9085 (Lo-16bits)
HR9087	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9088				HR9087 (Lo-16bits)
HR9089	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9090				HR9089 (Lo-16bits)
HR9091	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9092				HR9091 (Lo-16bits)
HR9093	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5.00...150.00] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)
HR9103	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9115	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0.00...80.00] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9118	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9119	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9120	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9121	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9122	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9123	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9124	VUB4:Delay type	R(W)	1	0=DT;1=IDMT
HR9125	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9132				HR9131 (Lo-16bits)
HR9133	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB4:Definite operating time delay	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9145	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60.00] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9158				HR9157 (Lo-16bits)
HR9159	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9160				HR9159 (Lo-16bits)
HR9161	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9162				HR9161 (Lo-16bits)
HR9163	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9164				HR9163 (Lo-16bits)
HR9165	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9166				HR9165 (Lo-16bits)
HR9167	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9168				HR9167 (Lo-16bits)
HR9169	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9170				HR9169 (Lo-16bits)
HR9171	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25.00] (Hi-16bits)
HR9172				HR9171 (Lo-16bits)
HR9173	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9174				HR9173 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9175	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9176				HR9175 (Lo-16bits)
HR9177	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9178				HR9177 (Lo-16bits)
HR9179	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9180				HR9179 (Lo-16bits)
HR9181	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9182				HR9181 (Lo-16bits)
HR9183	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9184				HR9183 (Lo-16bits)
HR9185	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9186				HR9185 (Lo-16bits)
HR9187	VUB4:Release time delay	R(W)	2	FLOAT [0...150.0] (Hi-16bits)
HR9188				HR9187 (Lo-16bits)
HR9189	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9190	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9191	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9192	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9193	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9194	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9195	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9196	VUB4:Delayed pick-up release	R(W)	1	0=No;1=Yes
HR9197	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9198	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9199	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9200	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9201	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes

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TN00011

Holding Register	Parameter	Access	Length	
HR9202	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9203	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9204	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No;1=Yes
HR9205	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9206	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9207	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9208	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9209	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9210	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9211	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9212	VUB4:Continue time calculation during release time	R(W)	1	0=No;1=Yes
HR9213	FRQV1:f> Enable	R(W)	1	0=No;1=Yes
HR9214	FRQV1:f>> Enable	R(W)	1	0=No;1=Yes
HR9215	FRQV1:f>>> Enable	R(W)	1	0=No;1=Yes
HR9216	FRQV1:f>>>> Enable	R(W)	1	0=No;1=Yes
HR9217	FRQV1:f< Enable	R(W)	1	0=No;1=Yes
HR9218	FRQV1:f<< Enable	R(W)	1	0=No;1=Yes
HR9219	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9220	FRQV1:f<<< Enable	R(W)	1	0=No;1=Yes
HR9221	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9222	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9223	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9224	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9225	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9226	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9227	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9228	FRQV1:f> used in setting group	R(W)	1	0=No;1=Yes
HR9229	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)
HR9231	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	FRQV1:f> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)
HR9245	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR9257	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	FRQV1:f> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9262	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9263	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9264	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9265	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9266	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9267	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9268	FRQV1:f>> used in setting group	R(W)	1	0=No;1=Yes
HR9269	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	FRQV1:f>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9287	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9288				HR9287 (Lo-16bits)
HR9289	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9294				HR9293 (Lo-16bits)
HR9295	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9296				HR9295 (Lo-16bits)
HR9297	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	FRQV1:f>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9302	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9303	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9304	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9305	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9306	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9307	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9308	FRQV1:f>>> used in setting group	R(W)	1	0=No;1=Yes
HR9309	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9310				HR9309 (Lo-16bits)
HR9311	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9317	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9320				HR9319 (Lo-16bits)
HR9321	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9322				HR9321 (Lo-16bits)
HR9323	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9324				HR9323 (Lo-16bits)
HR9325	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9326				HR9325 (Lo-16bits)
HR9327	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9328				HR9327 (Lo-16bits)
HR9329	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9342	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9343	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9344	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9345	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9346	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9347	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9348	FRQV1:f>>>> used in setting group	R(W)	1	0=No;1=Yes
HR9349	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)
HR9361	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10.00...70.00] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9366				HR9365 (Lo-16bits)
HR9367	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9377	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9382	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9383	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9384	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9385	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9386	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9387	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9388	FRQV1:f< used in setting group	R(W)	1	0=No;1=Yes
HR9389	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)
HR9391	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9407	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9422	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9423	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9424	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9425	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9426	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9427	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9428	FRQV1:f<< used in setting group	R(W)	1	0=No;1=Yes
HR9429	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9437	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9462	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9463	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9464	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9465	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9466	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes

Holding Register	Parameter	Access	Length	
HR9467	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9468	FRQV1:f<<< used in setting group	R(W)	1	0=No;1=Yes
HR9469	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9497	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9502	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9503	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9504	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9505	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9506	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9507	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9508	FRQV1:f<<<< used in setting group	R(W)	1	0=No;1=Yes
HR9509	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7.00...65.00] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9526				HR9525 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9527	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9528				HR9527 (Lo-16bits)
HR9529	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9530				HR9529 (Lo-16bits)
HR9531	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9557	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9566				HR9565 (Lo-16bits)
HR9567	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9568				HR9567 (Lo-16bits)
HR9569	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9570				HR9569 (Lo-16bits)
HR9571	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9572				HR9571 (Lo-16bits)
HR9573	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)
HR9575	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled;1=Enabled
HR9580	M1CT1:Phase CT primary	R(W)	2	FLOAT [1.000...25000.000] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.200...10.000] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000.0] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9587				HR9586 (Lo-16bits)
HR9588	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000.0] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10.000] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	M2CT1:Phase CT primary	R(W)	2	FLOAT [1.0...25000.0] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)
HR9594	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...10.0] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000.0] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10.0] (Hi-16bits)
HR9599				HR9598 (Lo-16bits)
HR9600	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000.0] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10.000] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	M1VT1:VT Primary	R(W)	2	FLOAT [1.0...1000000.0] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	M1VT1:VT Secondary	R(W)	2	FLOAT [0.2...400.0] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	M1VT1:U3 Res/SS VT Primary	R(W)	2	FLOAT [1.0...1000000.0] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	M1VT1:U3 Res/SS VT Secondary	R(W)	2	FLOAT [0.2...400.0] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	M1VT1:U4 Res/SS VT Primary	R(W)	2	FLOAT [1.0...1000000.0] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	M1VT1:U4 Res/SS VT Secondary	R(W)	2	FLOAT [0.2...400.0] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	NOC1:I> [50/51] mode	R(W)	1	0=Disabled;1=Activated

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Holding Register	Parameter	Access	Length	
HR9617	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9618	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9619	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled;1=Activated
HR9620	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9621	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9622	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9623	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled;1=Activated
HR9624	CUB1:I2> [46 46R 46L] mode	R(W)	1	0=Disabled;1=Activated
HR9625	CUB2:I2>> [46 46R 46L] mode	R(W)	1	0=Disabled;1=Activated
HR9626	CUB3:I2>>> [46 46R 46L] mode	R(W)	1	0=Disabled;1=Activated
HR9627	CUB4:I2>>>> [46 46R 46L] mode	R(W)	1	0=Disabled;1=Activated
HR9628	OV1:U> [59] mode	R(W)	1	0=Disabled; 1=Activated
HR9629	OV2:U>> [59] mode	R(W)	1	0=Disabled; 1=Activated
HR9630	OV3:U>>> [59] mode	R(W)	1	0=Disabled; 1=Activated
HR9631	OV4:U>>>> [59] mode	R(W)	1	0=Disabled; 1=Activated
HR9632	UV1:U< [27] mode	R(W)	1	0=Disabled; 1=Activated
HR9633	UV2:U<< [27] mode	R(W)	1	0=Disabled; 1=Activated
HR9634	UV3:U<<< [27] mode	R(W)	1	0=Disabled; 1=Activated
HR9635	UV4:U<<<< [27] mode	R(W)	1	0=Disabled; 1=Activated
HR9636	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled; 1=Activated
HR9637	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled; 1=Activated
HR9638	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled; 1=Activated
HR9639	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled; 1=Activated
HR9640	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled; 1=Activated
HR9641	DOC1:Idir> [67] mode	R(W)	1	0=Disabled;1=Activated
HR9642	DOC2:Idir>> [67] mode	R(W)	1	0=Disabled;1=Activated
HR9643	DOC3:Idir>>> [67] mode	R(W)	1	0=Disabled;1=Activated
HR9644	DOC4:Idir>>>> [67] mode	R(W)	1	0=Disabled;1=Activated

Holding Register	Parameter	Access	Length	
HR9645	DEF1:I0dir> [67N/32N] mode	R(W)	1	0=Disabled;1=Activated
HR9646	DEF2:I0dir>> [67N/32N] mode	R(W)	1	0=Disabled;1=Activated
HR9647	DEF3:I0dir>>> [67N/32N] mode	R(W)	1	0=Disabled;1=Activated
HR9648	DEF4:I0dir>>>> [67N/32N] mode	R(W)	1	0=Disabled;1=Activated
HR9649	LOGIC:Logical double bit Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	LOGIC:State00 for Logical double bit Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	LOGIC:State01 for Logical double bit Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	LOGIC:State10 for Logical double bit Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	LOGIC:State11 for Logical double bit Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	LOGIC:Logical double bit Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	LOGIC:State00 for Logical double bit Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	LOGIC:State01 for Logical double bit Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	LOGIC:State10 for Logical double bit Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	LOGIC:State11 for Logical double bit Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	LOGIC:Logical double bit Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9671	LOGIC:State00 for Logical double bit Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	LOGIC:State01 for Logical double bit Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	LOGIC:State10 for Logical double bit Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	LOGIC:State11 for Logical double bit Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	LOGIC:Logical double bit Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	LOGIC:State00 for Logical double bit Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	LOGIC:State01 for Logical double bit Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	LOGIC:State10 for Logical double bit Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	LOGIC:State11 for Logical double bit Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	LOGIC:Logical double bit Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9690				HR9689 (Lo-16bits)
HR9691	LOGIC:State00 for Logical double bit Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	LOGIC:State01 for Logical double bit Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	LOGIC:State10 for Logical double bit Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9696				HR9695 (Lo-16bits)
HR9697	LOGIC:State11 for Logical double bit Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	LOGIC:Logical double bit Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	LOGIC:State00 for Logical double bit Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	LOGIC:State01 for Logical double bit Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	LOGIC:State10 for Logical double bit Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	LOGIC:State11 for Logical double bit Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	LOGIC:Logical double bit Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	LOGIC:State00 for Logical double bit Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	LOGIC:State01 for Logical double bit Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	LOGIC:State10 for Logical double bit Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	LOGIC:State11 for Logical double bit Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	LOGIC:Logical double bit Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9721	LOGIC:State00 for Logical double bit Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	LOGIC:State01 for Logical double bit Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	LOGIC:State10 for Logical double bit Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	LOGIC:State11 for Logical double bit Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	LOGIC:Logical double bit Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	LOGIC:State00 for Logical double bit Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	LOGIC:State01 for Logical double bit Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	LOGIC:State10 for Logical double bit Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	LOGIC:State11 for Logical double bit Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	LOGIC:Logical double bit Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	LOGIC:State00 for Logical double bit Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)
HR9743	LOGIC:State01 for Logical double bit Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	LOGIC:State10 for Logical double bit Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9746				HR9745 (Lo-16bits)
HR9747	LOGIC:State11 for Logical double bit Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	NOCS1:IPW> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=StartL1;5=Startl
HR9750	NOCS2:IPW>> condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked;4=StartL1;5=Startl
HR9751	OBJS1:Phase-wise object Breaker Status A(L1)	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR9752	OBJS1:Phase-wise object Breaker Status B(L2)	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR9753	OBJS1:Phase-wise object Breaker Status C(L3)	R	1	0=Intermediate;1=Open;2=Closed;3=Bad
HR9754	OBJS1:Phase-wise object Withdraw Status	R	1	0=WDIntermediate;1=WDCartOut;2=WDCart In;3=WDE
HR9755	LOGIC:Logical Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	LOGIC:Logical Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	LOGIC:Logical Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	LOGIC:Logical Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	LOGIC:Logical Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	LOGIC:Logical Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	LOGIC:Logical Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)
HR9769	LOGIC:Logical Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	LOGIC:Logical Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	LOGIC:Logical Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR9774				HR9773 (Lo-16bits)
HR9775	LOGIC:Logical Input 11	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9776				HR9775 (Lo-16bits)
HR9777	LOGIC:Logical Input 12	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9778				HR9777 (Lo-16bits)
HR9779	LOGIC:Logical Input 13	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9780				HR9779 (Lo-16bits)
HR9781	LOGIC:Logical Input 14	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9782				HR9781 (Lo-16bits)
HR9783	LOGIC:Logical Input 15	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9784				HR9783 (Lo-16bits)
HR9785	LOGIC:Logical Input 16	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	LOGIC:Logical Input 17	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	LOGIC:Logical Input 18	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	LOGIC:Logical Input 19	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	LOGIC:Logical Input 20	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	LOGIC:Logical Input 21	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)
HR9797	LOGIC:Logical Input 22	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)
HR9799	LOGIC:Logical Input 23	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	LOGIC:Logical Input 24	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9802				HR9801 (Lo-16bits)
HR9803	LOGIC:Logical Input 25	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9804				HR9803 (Lo-16bits)
HR9805	LOGIC:Logical Input 26	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9806				HR9805 (Lo-16bits)
HR9807	LOGIC:Logical Input 27	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9808				HR9807 (Lo-16bits)
HR9809	LOGIC:Logical Input 28	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9810				HR9809 (Lo-16bits)
HR9811	LOGIC:Logical Input 29	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9812				HR9811 (Lo-16bits)
HR9813	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9814				HR9813 (Lo-16bits)
HR9815	MEAS1:IL1 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9816				HR9815 (Lo-16bits)
HR9817	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9818				HR9817 (Lo-16bits)
HR9819	MEAS1:IL2 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9820				HR9819 (Lo-16bits)
HR9821	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9822				HR9821 (Lo-16bits)
HR9823	MEAS1:IL3 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9824				HR9823 (Lo-16bits)
HR9825	MEAS1:I01 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9826				HR9825 (Lo-16bits)
HR9827	MEAS1:I01 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9828				HR9827 (Lo-16bits)
HR9829	MEAS1:I02 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9830				HR9829 (Lo-16bits)
HR9831	MEAS1:I02 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9832				HR9831 (Lo-16bits)
HR9833	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR9834				HR9833 (Lo-16bits)
HR9835	MEAS2:I"L1 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9836				HR9835 (Lo-16bits)
HR9837	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9838				HR9837 (Lo-16bits)
HR9839	MEAS2:I"L2 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9840				HR9839 (Lo-16bits)
HR9841	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9842				HR9841 (Lo-16bits)
HR9843	MEAS2:I"L3 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9844				HR9843 (Lo-16bits)
HR9845	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9846				HR9845 (Lo-16bits)
HR9847	MEAS2:I"01 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9848				HR9847 (Lo-16bits)
HR9849	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9850				HR9849 (Lo-16bits)
HR9851	MEAS2:I"02 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9852				HR9851 (Lo-16bits)
HR9853	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9854				HR9853 (Lo-16bits)
HR9855	MEAS3:UL1 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9856				HR9855 (Lo-16bits)
HR9857	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9858				HR9857 (Lo-16bits)
HR9859	MEAS3:UL2 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9860				HR9859 (Lo-16bits)
HR9861	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)
HR9862				HR9861 (Lo-16bits)
HR9863	MEAS3:UL3 Power THD	R	2	FLOAT [0.00...100000.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR9864				HR9863 (Lo-16bits)
HR9865	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9866				HR9865 (Lo-16bits)
HR9867	POW1:Apparent Energy (S) while Export (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9868				HR9867 (Lo-16bits)
HR9869	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9870				HR9869 (Lo-16bits)
HR9871	POW1:Apparent Energy (S) while Import (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9872				HR9871 (Lo-16bits)
HR9873	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9874				HR9873 (Lo-16bits)
HR9875	POW1:Real Energy (P) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9876				HR9875 (Lo-16bits)
HR9877	POW1:Reactive Energy (Q) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9878				HR9877 (Lo-16bits)
HR9879	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9880				HR9879 (Lo-16bits)
HR9881	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9882				HR9881 (Lo-16bits)
HR9883	POW1:Real Energy (P) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9884				HR9883 (Lo-16bits)
HR9885	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9886				HR9885 (Lo-16bits)
HR9887	POW2:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR9888				HR9887 (Lo-16bits)
HR9889	POW2:Apparent Energy (S) while Export (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9890				HR9889 (Lo-16bits)
HR9891	POW2:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9892				HR9891 (Lo-16bits)
HR9893	POW2:Apparent Energy (S) while Import (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9894				HR9893 (Lo-16bits)
HR9895	POW2:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9896				HR9895 (Lo-16bits)
HR9897	POW2:Real Energy (P) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9898				HR9897 (Lo-16bits)
HR9899	POW2:Reactive Energy (Q) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9900				HR9899 (Lo-16bits)
HR9901	POW2:Real Energy (P) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9902				HR9901 (Lo-16bits)
HR9903	POW2:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9904				HR9903 (Lo-16bits)
HR9905	POW2:Real Energy (P) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9906				HR9905 (Lo-16bits)
HR9907	POW2:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9908				HR9907 (Lo-16bits)
HR9909	GNSIG:Common signals condition	R	1	0=Normal;1=Start; 2=Trip
HR9910	UCP1:IUC> condition	R	1	0=Normal;1=Blocked;2=StartOn(Alarm);3=StartOn(Alar
HR9911	CRF1:Cd> (87C) condition	R	1	0=Normal;1=Trip;2=Blocked

Holding Register	Parameter	Access	Length	
HR9912	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9913				HR9912 (Lo-16bits)
HR9914	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9915				HR9914 (Lo-16bits)
HR9916	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9917				HR9916 (Lo-16bits)
HR9918	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9919				HR9918 (Lo-16bits)
HR9920	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9921				HR9920 (Lo-16bits)
HR9922	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9923				HR9922 (Lo-16bits)
HR9924	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9925				HR9924 (Lo-16bits)
HR9926	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9927				HR9926 (Lo-16bits)
HR9928	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9929				HR9928 (Lo-16bits)
HR9930	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9931				HR9930 (Lo-16bits)
HR9932	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9933				HR9932 (Lo-16bits)
HR9934	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9935				HR9934 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR9936	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9937				HR9936 (Lo-16bits)
HR9938	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9939				HR9938 (Lo-16bits)
HR9940	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9941				HR9940 (Lo-16bits)
HR9942	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9943				HR9942 (Lo-16bits)
HR9944	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9945				HR9944 (Lo-16bits)
HR9946	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9947				HR9946 (Lo-16bits)
HR9948	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9949				HR9948 (Lo-16bits)
HR9950	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9951				HR9950 (Lo-16bits)
HR9952	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9953				HR9952 (Lo-16bits)
HR9954	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9955				HR9954 (Lo-16bits)
HR9956	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9957				HR9956 (Lo-16bits)
HR9958	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9959				HR9958 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR9960	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9961				HR9960 (Lo-16bits)
HR9962	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9963				HR9962 (Lo-16bits)
HR9964	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9965				HR9964 (Lo-16bits)
HR9966	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9967				HR9966 (Lo-16bits)
HR9968	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9969				HR9968 (Lo-16bits)
HR9970	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9971				HR9970 (Lo-16bits)
HR9972	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9973				HR9972 (Lo-16bits)
HR9974	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9975				HR9974 (Lo-16bits)
HR9976	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9977				HR9976 (Lo-16bits)
HR9978	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9979	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9980	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9981	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9982	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9983	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9984	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9985	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit

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Holding Register	Parameter	Access	Length	
HR9986	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9987	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9988	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9989	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9990	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9991	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9992	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9993	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit;1=OK;2=Invalid Shorted Circuit
HR9994	OOS1:Pole slip condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR9995	OOS1:Configuration status	R	1	0=Ok;1=Incorrect VT set;2=Incorrect char. Set
HR9996	OOS1:PoleSlip detection START	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9997				HR9996 (Lo-16bits)
HR9998	OOS1:PoleSlip TRIP	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9999				HR9998 (Lo-16bits)
HR10000	OOS1:PoleSlip detection BLOCKED	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10001				HR10000 (Lo-16bits)
HR10002	GSYN:Enable on-screen synchronizer view	RW	1	0=Disabled; 1=Activated
HR10003	GSYN:Synchronizing by	RW	1	0=Autosynchronizer;1=Manual commands
HR10004	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch;1=I know what I am doing
HR10005	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR10006				HR10005 (Lo-16bits)
HR10007	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR10008				HR10007 (Lo-16bits)
HR10009	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR10010				HR10009 (Lo-16bits)
HR10011	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR10012				HR10011 (Lo-16bits)
HR10013	GSYN:Open synchronizer HMI	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10014				HR10013 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10015	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10016				HR10015 (Lo-16bits)
HR10017	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10018				HR10017 (Lo-16bits)
HR10019	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10020				HR10019 (Lo-16bits)
HR10021	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10022				HR10021 (Lo-16bits)
HR10023	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10024				HR10023 (Lo-16bits)
HR10025	FREQS:SS1.meas.frqs	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR10026				HR10025 (Lo-16bits)
HR10027	FREQS:SS2.meas.frqs	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR10028				HR10027 (Lo-16bits)
HR10029	HSHC:HSIs	R	2	0b=HSI1;1b=HSI2;2b=HSI3;3b=HSI4;4b=HSI5;5b=HSI6
HR10030				HR10029 (Lo-16bits)
HR10031	SGS2:Transformer active setting group	R	1	0=SG1;1=SG2;2=SG3;3=SG4;4=SG5;5=SG6;6=SG7;7
HR10032	SGS2:Force setting group	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7
HR10033	SGS2:Force setting group change	RW	1	0=Disabled;1=Enabled
HR10034	SGS2:Used setting groups	RW	1	0=SG1;1=SG1...2;2=SG1...3;3=SG1...4;4=SG1...5;5=S
HR10035	SGS2:Remote setting group change	RW	1	0=none;1=SG1;2=SG2;3=SG3;4=SG4;5=SG5;6=SG6;7
HR10036	LOGIC:Logical Input 30	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10037				HR10036 (Lo-16bits)
HR10038	LOGIC:Logical Input 31	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10039				HR10038 (Lo-16bits)
HR10040	LOGIC:Logical Input 32	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10041				HR10040 (Lo-16bits)
HR10042	LDIF1:IdL> condition	R	2	0b=Idb> Trip;1b=Idb> Blocked;2b=Idi> Trip;3b=Idi> Bloc
HR10043				HR10042 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10044	TOLT1:TT> setting alarm	R	1	0=SF setting ok;1=Service factor set fault.Override to1.
HR10045	TOLT1:TT> setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault.Override to
HR10046	TOLT1:TT> setting alarm	R	1	0=Nominalcurrent calc ok;1=Nominalcurrent set fault.O
HR10047	TOLT1:TT> setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient
HR10048	TOLF1:TF> setting alarm	R	1	0=SF setting ok;1=Service factor set fault.Override to1.
HR10049	TOLF1:TF> setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault.Override to
HR10050	TOLF1:TF> setting alarm	R	1	0=Nominalcurrent calc ok;1=Nominalcurrent set fault.O
HR10051	TOLF1:TF> setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient
HR10052	TOLM1:TM>Setting alarm	R	1	0=SF setting ok;1=Service factor set fault.Override to1.
HR10053	TOLM1:TM>Setting alarm	R	1	0=Ambient t setting ok;1=Ambient t set fault.Override to
HR10054	TOLM1:TM>Setting alarm	R	1	0=Nominalcurrent calc ok;1=Nominalcurrent set fault.O
HR10055	TOLM1:TM>Setting alarm	R	1	0=Ambient setting ok;1=Inconsistent setting of ambient
HR10056	URX1:X< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR10057	URX2:X<< condition	R	1	0=Normal;1=Start; 2=Trip; 3=Blocked
HR10058	SYN1:SYN1 Mag diff	R	2	FLOAT [-120.00...120.00] (Hi-16bits)
HR10059				HR10058 (Lo-16bits)
HR10060	SYN1:SYN1 Ang diff	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR10061				HR10060 (Lo-16bits)
HR10062	SYN1:SYN1 freq diff	R	2	FLOAT [-75.000...75.000] (Hi-16bits)
HR10063				HR10062 (Lo-16bits)
HR10064	COL1:ICOL> [49OL] mode	R(W)	1	0=Disabled;1=Activated
HR10065	COL2:ICOL>> [49OL] mode	R(W)	1	0=Disabled;1=Activated
HR10066	CNU1:CNU> [50UB] mode	R(W)	1	0=Disabled;1=Activated
HR10067	UCP1:IUC> [46C] mode	R(W)	1	0=Disabled;1=Activated
HR10068	CRF1:Cd> (87C) mode	R(W)	1	0=Disabled; 1=Activated
HR10069	NOCS1:IPW> [50/51] mode	R(W)	1	0=Disabled; 1=Activated
HR10070	NOCS2:IPW>> mode	R(W)	1	0=Disabled; 1=Activated
HR10071	EFX1:I0x> [67NX] mode	R(W)	1	0=Disabled;1=Activated
HR10072	EFX1:Used U0> reference	R	1	0=VT meas.mode error;1=U3 Input;2=U4 Input
HR10073	EFX1:I0x> condition	R	1	0=Normal;1=Start forward;2=Trip forward;3=Blocked;4=

Holding Register	Parameter	Access	Length	
HR10074	EFX1:I0x> Last fault active residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10075				HR10074 (Lo-16bits)
HR10076	EFX1:I0x> Last fault apparent residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10077				HR10076 (Lo-16bits)
HR10078	EFX1:I0x> Last fault apparent/ active residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10079				HR10078 (Lo-16bits)
HR10080	EFX1:I0x> Last fault residual current	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10081				HR10080 (Lo-16bits)
HR10082	EFX1:I0x> Last fault zero sequence voltage	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10083				HR10082 (Lo-16bits)
HR10084	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10085				HR10084 (Lo-16bits)
HR10086	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10087				HR10086 (Lo-16bits)
HR10088	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10089				HR10088 (Lo-16bits)
HR10090	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10091				HR10090 (Lo-16bits)
HR10092	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10093				HR10092 (Lo-16bits)
HR10094	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10095				HR10094 (Lo-16bits)
HR10096	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10097				HR10096 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10098	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10099				HR10098 (Lo-16bits)
HR10100	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10101				HR10100 (Lo-16bits)
HR10102	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10103				HR10102 (Lo-16bits)
HR10104	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10105				HR10104 (Lo-16bits)
HR10106	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10107				HR10106 (Lo-16bits)
HR10108	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10109				HR10108 (Lo-16bits)
HR10110	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10111				HR10110 (Lo-16bits)
HR10112	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10113				HR10112 (Lo-16bits)
HR10114	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10115				HR10114 (Lo-16bits)
HR10116	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10117				HR10116 (Lo-16bits)
HR10118	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10119				HR10118 (Lo-16bits)
HR10120	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10121				HR10120 (Lo-16bits)
HR10122	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10123				HR10122 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10124	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10125				HR10124 (Lo-16bits)
HR10126	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10127				HR10126 (Lo-16bits)
HR10128	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10129				HR10128 (Lo-16bits)
HR10130	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10131				HR10130 (Lo-16bits)
HR10132	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10133				HR10132 (Lo-16bits)
HR10134	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10135				HR10134 (Lo-16bits)
HR10136	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10137				HR10136 (Lo-16bits)
HR10138	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10139				HR10138 (Lo-16bits)
HR10140	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10141				HR10140 (Lo-16bits)
HR10142	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10143				HR10142 (Lo-16bits)
HR10144	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10145				HR10144 (Lo-16bits)
HR10146	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0.000...20.000] (Hi-16bits)
HR10147				HR10146 (Lo-16bits)
HR10148	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10149				HR10148 (Lo-16bits)
HR10150	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10151				HR10150 (Lo-16bits)
HR10152	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10153				HR10152 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10154	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10155				HR10154 (Lo-16bits)
HR10156	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10157				HR10156 (Lo-16bits)
HR10158	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10159				HR10158 (Lo-16bits)
HR10160	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10161				HR10160 (Lo-16bits)
HR10162	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10163				HR10162 (Lo-16bits)
HR10164	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10165				HR10164 (Lo-16bits)
HR10166	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10167				HR10166 (Lo-16bits)
HR10168	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10169				HR10168 (Lo-16bits)
HR10170	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10171				HR10170 (Lo-16bits)
HR10172	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10173				HR10172 (Lo-16bits)
HR10174	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10175				HR10174 (Lo-16bits)
HR10176	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10177				HR10176 (Lo-16bits)
HR10178	EFX1:Operating time	R(W)	2	FLOAT [0.02...5.00] (Hi-16bits)
HR10179				HR10178 (Lo-16bits)
HR10180	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10181				HR10180 (Lo-16bits)
HR10182	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10183				HR10182 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10184	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10185				HR10184 (Lo-16bits)
HR10186	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10187				HR10186 (Lo-16bits)
HR10188	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10189				HR10188 (Lo-16bits)
HR10190	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10191				HR10190 (Lo-16bits)
HR10192	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10193				HR10192 (Lo-16bits)
HR10194	EFX1:Release delay	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10195				HR10194 (Lo-16bits)
HR10196	IEF1:I0Int> [67NT] mode	R(W)	1	0=Disabled; 1=Activated
HR10197	HOC1:Ih> [50H/51H/68H] mode	R(W)	1	0=Disabled; 1=Activated
HR10198	HOC2:Ih>> [50H/51H/68H] mode	R(W)	1	0=Disabled; 1=Activated
HR10199	HOC3:Ih>>> [50H/51H/68H] mode	R(W)	1	0=Disabled; 1=Activated
HR10200	HOC4:Ih>>>> [50H/51H/68H] mode	R(W)	1	0=Disabled; 1=Activated
HR10201	CLP1:CLP mode	R(W)	1	0=Disabled; 1=Activated
HR10202	CBF1:CBFP [50BF] mode	R(W)	1	0=Disabled; 1=Activated
HR10203	DIF1:Idx> [87T/87N] mode	R(W)	1	0=Disabled; 1=Activated
HR10204	LDIF1:IdL> mode	R(W)	1	0=Disabled; 1=Activated
HR10205	REF1:I0d> [87N] mode	R(W)	1	0=Disabled; 1=Activated
HR10206	TOLT1:TT> [49T] mode	R(W)	1	0=Disabled; 1=Activated
HR10207	TOLF1:TF> [49F] mode	R(W)	1	0=Disabled; 1=Activated
HR10208	TOLF1:IN thermal cap current	R(W)	2	FLOAT [0.00...40.00] (Hi-16bits)
HR10209				HR10208 (Lo-16bits)
HR10210	TOLF1:Obj.Max.temp (tmax=100%)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10211				HR10210 (Lo-16bits)
HR10212	TOLF1:Man.Amb.Temp.Set	R(W)	2	FLOAT [-60...500] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10213				HR10212 (Lo-16bits)
HR10214	TOLF1:RTD Module	R(W)	1	0=ModuleA;1=ModuleB;2=ModuleC;
HR10215	TOLF1:RTD Channel	R(W)	1	0=Ch0;1=Ch1;2=Ch2;3=Ch3;4=Ch4;5=Ch5;6=Ch6;7=C
HR10216	TOLF1:RTD Deg C / Deg F	R(W)	1	0=Deg C;1=Deg F
HR10217	TOLF1:RTD Amb.Temp.Read	R(W)	2	FLOAT [-101.0...2000.0] (Hi-16bits)
HR10218				HR10217 (Lo-16bits)
HR10219	TOLF1:tau (t const)	R(W)	2	FLOAT [0.0...500.0] (Hi-16bits)
HR10220				HR10219 (Lo-16bits)
HR10221	TOLF1:Max.Perm.OC.Current(norm Ik1s)	R(W)	2	FLOAT [1...9999999] (Hi-16bits)
HR10222				HR10221 (Lo-16bits)
HR10223	TOLF1:Max.OC.Time (norm 1s)	R(W)	2	FLOAT [0.1...5.0] (Hi-16bits)
HR10224				HR10223 (Lo-16bits)
HR10225	TOLF1:Nominal current	R(W)	2	FLOAT [1...9999999] (Hi-16bits)
HR10226				HR10225 (Lo-16bits)
HR10227	TOLF1:kSF (service factor)	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10228				HR10227 (Lo-16bits)
HR10229	TOLF1:Cold Reset default theta	R(W)	2	FLOAT [0.0...150.0] (Hi-16bits)
HR10230				HR10229 (Lo-16bits)
HR10231	TOLF1:Ambient lin or curve	R(W)	1	0=Linear est.;1=Set Curve
HR10232	TOLF1:Temp. reference (tref) kamb=1.00	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10233				HR10232 (Lo-16bits)
HR10234	TOLF1:Max.ambient temp	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10235				HR10234 (Lo-16bits)
HR10236	TOLF1:k at max.amb.temp.	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10237				HR10236 (Lo-16bits)
HR10238	TOLF1:Min.ambient temp	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10239				HR10238 (Lo-16bits)
HR10240	TOLF1:k at min.amb.temp.	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10241				HR10240 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10242	TOLF1:Amb.Temp.ref1	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10243				HR10242 (Lo-16bits)
HR10244	TOLF1:Amb.Temp.k1	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10245				HR10244 (Lo-16bits)
HR10246	TOLF1:Amb.Temp.ref2	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10247				HR10246 (Lo-16bits)
HR10248	TOLF1:Amb.Temp.k2	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10249				HR10248 (Lo-16bits)
HR10250	TOLF1:Add curvepoint 3	R(W)	1	0=Not used; 1=Used
HR10251	TOLF1:Amb.Temp.ref3	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10252				HR10251 (Lo-16bits)
HR10253	TOLF1:Amb.Temp.k3	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10254				HR10253 (Lo-16bits)
HR10255	TOLF1:Add curvepoint 4	R(W)	1	0=Not used; 1=Used
HR10256	TOLF1:Amb.Temp.ref4	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10257				HR10256 (Lo-16bits)
HR10258	TOLF1:Amb.Temp.k4	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10259				HR10258 (Lo-16bits)
HR10260	TOLF1:Add curvepoint 5	R(W)	1	0=Not used; 1=Used
HR10261	TOLF1:Amb.Temp.ref5	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10262				HR10261 (Lo-16bits)
HR10263	TOLF1:Amb.Temp.k5	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10264				HR10263 (Lo-16bits)
HR10265	TOLF1:Add curvepoint 6	R(W)	1	0=Not used; 1=Used
HR10266	TOLF1:Amb.Temp.ref6	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10267				HR10266 (Lo-16bits)
HR10268	TOLF1:Amb.Temp.k6	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10269				HR10268 (Lo-16bits)
HR10270	TOLF1:Add curvepoint 7	R(W)	1	0=Not used; 1=Used
HR10271	TOLF1:Amb.Temp.ref7	R(W)	2	FLOAT [-60...500] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR10272				HR10271 (Lo-16bits)
HR10273	TOLF1:Amb.Temp.k7	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10274				HR10273 (Lo-16bits)
HR10275	TOLF1:Add curvepoint 8	R(W)	1	0=Not used; 1=Used
HR10276	TOLF1:Amb.Temp.ref8	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10277				HR10276 (Lo-16bits)
HR10278	TOLF1:Amb.Temp.k8	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10279				HR10278 (Lo-16bits)
HR10280	TOLF1:Add curvepoint 9	R(W)	1	0=Not used; 1=Used
HR10281	TOLF1:Amb.Temp.ref9	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10282				HR10281 (Lo-16bits)
HR10283	TOLF1:Amb.Temp.k9	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10284				HR10283 (Lo-16bits)
HR10285	TOLF1:Add curvepoint 10	R(W)	1	0=Not used; 1=Used
HR10286	TOLF1:Amb.Temp.ref10	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10287				HR10286 (Lo-16bits)
HR10288	TOLF1:Amb.Temp.k10	R(W)	2	FLOAT [0.01...5.00] (Hi-16bits)
HR10289				HR10288 (Lo-16bits)
HR10290	TOLM1:TM> [49M] mode	R(W)	1	0=Disabled; 1=Activated
HR10291	TOLM1:Curve AI	RW	1	0=Disabled;1=Enabled
HR10292	LCR1:ISt> [48/14/50LR] mode	R(W)	1	0=Disabled; 1=Activated
HR10293	FSP1:N> [66/86] mode	R(W)	1	0=Disabled; 1=Activated
HR10294	NUC1:I< [37] mode	R(W)	1	0=Disabled; 1=Activated
HR10295	MJP1:Im> [50M/51M] mode	R(W)	1	0=Disabled; 1=Activated
HR10296	NOV1:U0> [59N] mode	R(W)	1	0=Disabled; 1=Activated
HR10297	NOV2:U0>> [59N] mode	R(W)	1	0=Disabled; 1=Activated
HR10298	NOV3:U0>>> [59N] mode	R(W)	1	0=Disabled; 1=Activated
HR10299	NOV4:U0>>>> [59N] mode	R(W)	1	0=Disabled; 1=Activated
HR10300	PGS1:PSx >/< [99] mode	R(W)	1	0=Disabled; 1=Activated
HR10301	FRQC1:f</> [Curr] mode	R(W)	1	0=Disabled; 1=Activated

Holding Register	Parameter	Access	Length	
HR10302	DFT1:df/dt >/< [81] mode	R(W)	1	0=Disabled; 1=Activated
HR10303	RHC1:Running Hour Counter mode	R(W)	1	0=Disabled; 1=Activated
HR10304	PWR1:PQS>/< [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10305	PWR2:PQS>>/<< [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10306	PWR3:PQS>>>/<<< [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10307	PWR4:PQS>>>>/<<<< [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10308	OPW1:P> [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10309	UPW1:P< [37] mode	R(W)	1	0=Disabled; 1=Activated
HR10310	RPW1:Prev> [32] mode	R(W)	1	0=Disabled; 1=Activated
HR10311	SEF1:U03rd< [64S] mode	R(W)	1	0=Disabled; 1=Activated
HR10312	UEX1:Q< [40] mode	R(W)	1	0=Disabled; 1=Activated
HR10313	UIM1:Z< [21U] mode	R(W)	1	0=Disabled; 1=Activated
HR10314	UIM2:Z<< [21U] mode	R(W)	1	0=Disabled; 1=Activated
HR10315	URX1:X< [40] mode	R(W)	1	0=Disabled; 1=Activated
HR10316	URX2:X<< [40] mode	R(W)	1	0=Disabled; 1=Activated
HR10317	VOC1:Iv> [51V] mode	R(W)	1	0=Disabled; 1=Activated
HR10318	IAE1:I>U< I.A.E. [50/27] mode	R(W)	1	0=Disabled; 1=Activated
HR10319	VHZ1:V/Hz > [24] mode	R(W)	1	0=Disabled; 1=Activated
HR10320	UPF1:PF< [55] mode	R(W)	1	0=Disabled; 1=Activated
HR10321	RTD1:RTD mode	R(W)	1	0=Disabled;1=Activated
HR10322	OOS1:Pole slip [78] mode	R(W)	1	0=Disabled; 1=Activated
HR10323	ARC1:I/I0 Arc> [50ARC/50NARC] mode	R(W)	1	0=Disabled;1=Activated
HR10324	ARC1:I/I0 Arc> Self supervision test pulse on option card 1	R(W)	1	0=Disabled;1=On
HR10325	ARC1:I/I0 Arc> Number of connected arc sub-units on option card 1	R(W)	1	0=1 AQ-101;1=2 AQ-101;2=3 AQ-101;3=4 AQ-101;4=
HR10326	ARC1:I/I0 Arc> arc sub-unit status on option card 1	R(W)	1	0=AQ101 unit count match;1=AQ 101 unit count misma
HR10327	ARC1:Sensor status	R	2	0b=Ph Curr Blocked;1b=Ph Curr Start;2b=Res Curr Blo Fault;16b=I/I0 Arc> Sensor 4 Fault;17b=I/I0 Arc> I/O-un
HR10328				HR10327 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10329	ARC1:Channel 1 sensors	R(W)	1	0=No Sensors;1=1 Sensor;2=2 Sensors;3=3 Sensors
HR10330	ARC1:Channel 1 sensor status	R	1	0=Sensors Ok;1=Configuration fault
HR10331	ARC1:Channel 2 sensors	R(W)	1	0=No Sensors;1=1 Sensor;2=2 Sensors;3=3 Sensors
HR10332	ARC1:Channel 2 sensor status	R	1	0=Sensors Ok;1=Configuration fault
HR10333	ARC1:Channel 3 sensors	R(W)	1	0=No Sensors;1=1 Sensor;2=2 Sensors;3=3 Sensors
HR10334	ARC1:Channel 3 sensor status	R	1	0=Sensors Ok;1=Configuration fault
HR10335	ARC1:Channel 4 sensors	R(W)	1	0=No Sensors;1=1 Sensor;2=2 Sensors;3=3 Sensors
HR10336	ARC1:Channel 4 sensor status	R	1	0=Sensors Ok;1=Configuration fault
HR10337	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10338				HR10337 (Lo-16bits)
HR10339	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10340				HR10339 (Lo-16bits)
HR10341	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10342				HR10341 (Lo-16bits)
HR10343	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10344				HR10343 (Lo-16bits)
HR10345	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10346				HR10345 (Lo-16bits)
HR10347	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10348				HR10347 (Lo-16bits)
HR10349	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10350				HR10349 (Lo-16bits)
HR10351	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10352				HR10351 (Lo-16bits)
HR10353	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10354	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10355	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10356	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10357	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10358	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02

Holding Register	Parameter	Access	Length	
HR10359	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10360	ARC1:I0 input selection	R(W)	1	0=None;1=I01;2=I02
HR10361	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10362				HR10361 (Lo-16bits)
HR10363	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10364				HR10363 (Lo-16bits)
HR10365	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10366				HR10365 (Lo-16bits)
HR10367	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10368				HR10367 (Lo-16bits)
HR10369	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10370				HR10369 (Lo-16bits)
HR10371	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10372				HR10371 (Lo-16bits)
HR10373	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10374				HR10373 (Lo-16bits)
HR10375	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10376				HR10375 (Lo-16bits)
HR10377	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10378	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10379	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10380	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10381	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10382	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10383	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10384	ARC1:Zone1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10385	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10386	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10387	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10388	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled

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Holding Register	Parameter	Access	Length	
HR10389	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10390	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10391	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10392	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10393	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10394	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10395	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10396	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10397	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10398	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10399	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10400	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10401	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10402	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10403	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10404	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10405	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10406	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10407	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10408	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10409	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10410	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10411	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10412	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10413	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10414	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10415	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10416	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10417	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10418	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled

Holding Register	Parameter	Access	Length	
HR10419	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10420	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10421	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10422	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10423	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10424	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10425	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10426	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10427	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10428	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10429	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10430	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10431	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10432	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10433	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10434	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10435	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10436	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10437	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10438	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10439	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10440	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10441	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10442	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10443	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10444	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10445	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10446	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10447	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10448	ARC1:Zone2 Enabled	R(W)	1	0=Disabled;1=Enabled

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TN00011

Holding Register	Parameter	Access	Length	
HR10449	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10450	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10451	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10452	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10453	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10454	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10455	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10456	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10457	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10458	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10459	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10460	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10461	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10462	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10463	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10464	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10465	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10466	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10467	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10468	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10469	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10470	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10471	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10472	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10473	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10474	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10475	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10476	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10477	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10478	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled

Holding Register	Parameter	Access	Length	
HR10479	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10480	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10481	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10482	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10483	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10484	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10485	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10486	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10487	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10488	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10489	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10490	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10491	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10492	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10493	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10494	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10495	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10496	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10497	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10498	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10499	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10500	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10501	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10502	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10503	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10504	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10505	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10506	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10507	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10508	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled

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Holding Register	Parameter	Access	Length	
HR10509	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10510	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10511	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10512	ARC1:Zone3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10513	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10514	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10515	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10516	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10517	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10518	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10519	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10520	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10521	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10522	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10523	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10524	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10525	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10526	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10527	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10528	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10529	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10530	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10531	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10532	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10533	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10534	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10535	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10536	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10537	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10538	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled

Holding Register	Parameter	Access	Length	
HR10539	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10540	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10541	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10542	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10543	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10544	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10545	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10546	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10547	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10548	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10549	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10550	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10551	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10552	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10553	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10554	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10555	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10556	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10557	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10558	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10559	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10560	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10561	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10562	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10563	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10564	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10565	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10566	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10567	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10568	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In

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TN00011

Holding Register	Parameter	Access	Length	
HR10569	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10570	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10571	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10572	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10573	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10574	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10575	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10576	ARC1:Zone4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10577	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10578	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10579	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10580	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10581	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10582	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10583	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10584	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10585	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10586	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10587	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10588	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10589	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10590	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10591	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10592	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled;1=Enabled
HR10593	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10594	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10595	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10596	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10597	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10598	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled

Holding Register	Parameter	Access	Length	
HR10599	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10600	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10601	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10602	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10603	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10604	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10605	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10606	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10607	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10608	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10609	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10610	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10611	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10612	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10613	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10614	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10615	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10616	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10617	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10618	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10619	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10620	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10621	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10622	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10623	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10624	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled;1=Enabled
HR10625	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10626	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10627	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10628	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In

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TN00011

Holding Register	Parameter	Access	Length	
HR10629	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10630	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10631	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10632	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled;1=Light In;2=Current In
HR10633	CTS1:CTS1 mode	R(W)	1	0=Disabled; 1=Activated
HR10634	CTS2:CTS2 mode	R(W)	1	0=Disabled; 1=Activated
HR10635	VTS1:VTS [60] mode	R(W)	1	0=Disabled; 1=Activated
HR10636	FLX1:Fault locator [21FL] mode	R(W)	1	0=Disabled; 1=Activated
HR10637	THD1:THD mode	R(W)	1	0=Disabled; 1=Activated
HR10638	THDV1:THDV mode	R(W)	1	0=Disabled; 1=Activated
HR10639	THDV1:THDV condition	R	1	0=Normal;1=Start; 2=Alarm; 3=Blocked
HR10640	CBW1:CBW mode	R(W)	1	0=Disabled; 1=Activated
HR10641	NJK1:U0> RECL [79] mode	R(W)	1	0=Disabled; 1=Activated
HR10642	SOF1:SOTF mode	R(W)	1	0=Disabled; 1=Activated
HR10643	DOS1:Energy dose counter mode	R(W)	1	0=Disabled; 1=Activated
HR10644	DOS2:Energy dose counter mode	R(W)	1	0=Disabled; 1=Activated
HR10645	SYN1:SYN(1,2,3) [25] mode	R(W)	1	0=Disabled;1=Activated
HR10646	VJP1:<DELTA><alpha> > [78] mode	R(W)	1	0=Disabled; 1=Activated
HR10647	OBJS1:Phase-wise object mode	R(W)	1	0=Disabled; 1=Activated
HR10648	OBJ1:Object1 mode	R(W)	1	0=Disabled; 1=Activated
HR10649	OBJ2:Object2 mode	R(W)	1	0=Disabled; 1=Activated
HR10650	OBJ3:Object3 mode	R(W)	1	0=Disabled; 1=Activated
HR10651	OBJ4:Object4 mode	R(W)	1	0=Disabled; 1=Activated
HR10652	OBJ5:Object5 mode	R(W)	1	0=Disabled; 1=Activated
HR10653	OBJ6:Object6 mode	R(W)	1	0=Disabled; 1=Activated
HR10654	OBJ7:Object7 mode	R(W)	1	0=Disabled; 1=Activated
HR10655	OBJ8:Object8 mode	R(W)	1	0=Disabled; 1=Activated
HR10656	OBJ9:Object9 mode	R(W)	1	0=Disabled; 1=Activated
HR10657	OBJ10:Object10 mode	R(W)	1	0=Disabled; 1=Activated
HR10658	CIN1:Indicator 1 mode	R(W)	1	0=Disabled; 1=Activated

Holding Register	Parameter	Access	Length	
HR10659	CIN2:Indicator 2 mode	R(W)	1	0=Disabled; 1=Activated
HR10660	CIN3:Indicator 3 mode	R(W)	1	0=Disabled; 1=Activated
HR10661	CIN4:Indicator 4 mode	R(W)	1	0=Disabled; 1=Activated
HR10662	CIN5:Indicator 5 mode	R(W)	1	0=Disabled; 1=Activated
HR10663	CIN6:Indicator 6 mode	R(W)	1	0=Disabled; 1=Activated
HR10664	CIN7:Indicator 7 mode	R(W)	1	0=Disabled; 1=Activated
HR10665	CIN8:Indicator 8 mode	R(W)	1	0=Disabled; 1=Activated
HR10666	CIN9:Indicator 9 mode	R(W)	1	0=Disabled; 1=Activated
HR10667	CIN10:Indicator 10 mode	R(W)	1	0=Disabled; 1=Activated
HR10668	CIN11:Indicator 11 mode	R(W)	1	0=Disabled; 1=Activated
HR10669	CIN12:Indicator 12 mode	R(W)	1	0=Disabled; 1=Activated
HR10670	CIN13:Indicator 13 mode	R(W)	1	0=Disabled; 1=Activated
HR10671	CIN14:Indicator 14 mode	R(W)	1	0=Disabled; 1=Activated
HR10672	CIN15:Indicator 15 mode	R(W)	1	0=Disabled; 1=Activated
HR10673	CIN16:Indicator 16 mode	R(W)	1	0=Disabled; 1=Activated
HR10674	CIN17:Indicator 17 mode	R(W)	1	0=Disabled; 1=Activated
HR10675	CIN18:Indicator 18 mode	R(W)	1	0=Disabled; 1=Activated
HR10676	CIN19:Indicator 19 mode	R(W)	1	0=Disabled; 1=Activated
HR10677	CIN20:Indicator 20 mode	R(W)	1	0=Disabled; 1=Activated
HR10678	AR1:Auto-recloser [79] mode	R(W)	1	0=Disabled; 1=Activated
HR10679	VRG1:Vreg [AVR/90] mode	R(W)	1	0=Disabled; 1=Activated
HR10680	EMON1:Monitor mode	R(W)	1	0=Disabled;1=Activated
HR10681	GMAG:Excitation control mode	R(W)	1	0=Disabled; 1=Activated
HR10682	GSYN:DeadBus synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10683				HR10682 (Lo-16bits)
HR10684	GSYN:BlackStart synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10685				HR10684 (Lo-16bits)
HR10686	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10687				HR10686 (Lo-16bits)
HR10688	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR10689				HR10688 (Lo-16bits)
HR10690	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10691				HR10690 (Lo-16bits)
HR10692	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10693				HR10692 (Lo-16bits)
HR10694	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10695				HR10694 (Lo-16bits)
HR10696	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10697				HR10696 (Lo-16bits)
HR10698	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10699				HR10698 (Lo-16bits)
HR10700	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10701				HR10700 (Lo-16bits)
HR10702	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10703				HR10702 (Lo-16bits)
HR10704	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10705				HR10704 (Lo-16bits)
HR10706	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10707				HR10706 (Lo-16bits)
HR10708	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10709				HR10708 (Lo-16bits)
HR10710	VREC:ms	R	1	UINT32 [0...999]
HR10711	VREC:ms	R	1	UINT32 [0...999]
HR10712	VREC:ms	R	1	UINT32 [0...999]
HR10713	VREC:ms	R	1	UINT32 [0...999]
HR10714	VREC:ms	R	1	UINT32 [0...999]
HR10715	VREC:ms	R	1	UINT32 [0...999]
HR10716	VREC:ms	R	1	UINT32 [0...999]
HR10717	VREC:ms	R	1	UINT32 [0...999]
HR10718	VREC:ms	R	1	UINT32 [0...999]

Holding Register	Parameter	Access	Length	
HR10719	VREC:ms	R	1	UINT32 [0...999]
HR10720	VREC:ms	R	1	UINT32 [0...999]
HR10721	VREC:ms	R	1	UINT32 [0...999]
HR10722	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10723				HR10722 (Lo-16bits)
HR10724	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10725				HR10724 (Lo-16bits)
HR10726	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10727				HR10726 (Lo-16bits)
HR10728	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10729				HR10728 (Lo-16bits)
HR10730	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10731				HR10730 (Lo-16bits)
HR10732	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10733				HR10732 (Lo-16bits)
HR10734	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10735				HR10734 (Lo-16bits)
HR10736	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10737				HR10736 (Lo-16bits)
HR10738	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10739				HR10738 (Lo-16bits)
HR10740	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10741				HR10740 (Lo-16bits)
HR10742	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10743				HR10742 (Lo-16bits)
HR10744	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10745				HR10744 (Lo-16bits)
HR10746	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10747				HR10746 (Lo-16bits)
HR10748	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR10749				HR10748 (Lo-16bits)
HR10750	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10751				HR10750 (Lo-16bits)
HR10752	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10753				HR10752 (Lo-16bits)
HR10754	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10755				HR10754 (Lo-16bits)
HR10756	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10757				HR10756 (Lo-16bits)
HR10758	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10759				HR10758 (Lo-16bits)
HR10760	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10761				HR10760 (Lo-16bits)
HR10762	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10763				HR10762 (Lo-16bits)
HR10764	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10765				HR10764 (Lo-16bits)
HR10766	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10767				HR10766 (Lo-16bits)
HR10768	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10769				HR10768 (Lo-16bits)
HR10770	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1.00] (Hi-16bits)
HR10771				HR10770 (Lo-16bits)
HR10772	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10773				HR10772 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10774	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10775				HR10774 (Lo-16bits)
HR10776	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10777				HR10776 (Lo-16bits)
HR10778	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10779				HR10778 (Lo-16bits)
HR10780	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10781				HR10780 (Lo-16bits)
HR10782	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10783				HR10782 (Lo-16bits)
HR10784	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10785				HR10784 (Lo-16bits)
HR10786	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10787				HR10786 (Lo-16bits)
HR10788	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10789				HR10788 (Lo-16bits)
HR10790	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10791				HR10790 (Lo-16bits)
HR10792	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10793				HR10792 (Lo-16bits)
HR10794	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10795				HR10794 (Lo-16bits)
HR10796	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10797				HR10796 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR10798	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10799				HR10798 (Lo-16bits)
HR10800	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10801				HR10800 (Lo-16bits)
HR10802	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10803				HR10802 (Lo-16bits)
HR10804	CIN1:Indicator 1 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10805	CIN1:Indicator 1 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10806	CIN2:Indicator 2 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10807	CIN2:Indicator 2 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10808	CIN3:Indicator 3 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10809	CIN3:Indicator 4 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10810	CIN4:Indicator 4 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10811	CIN4:Indicator 4 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10812	CIN5:Indicator 5 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10813	CIN5:Indicator 5 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10814	CIN6:Indicator 6 status input	R(W)	1	0=Signals;1=GooseDBPOS

Holding Register	Parameter	Access	Length	
HR10815	CIN6:Indicator 6 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10816	CIN7:Indicator 7 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10817	CIN7:Indicator 7 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10818	CIN8:Indicator 8 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10819	CIN8:Indicator 8 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10820	CIN9:Indicator 9 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10821	CIN9:Indicator 9 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10822	CIN10:Indicator 10 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10823	CIN10:Indicator 10 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10824	CIN11:Indicator 11 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10825	CIN11:Indicator 11 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10826	CIN12:Indicator 12 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10827	CIN12:Indicator 12 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10828	CIN13:Indicator 13 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10829	CIN13:Indicator 13 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;

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TN00011

Holding Register	Parameter	Access	Length	
HR10830	CIN14:Indicator 14 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10831	CIN14:Indicator 14 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10832	CIN15:Indicator 15 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10833	CIN15:Indicator 15 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10834	CIN16:Indicator 16 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10835	CIN16:Indicator 16 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10836	CIN17:Indicator 17 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10837	CIN17:Indicator 17 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10838	CIN18:Indicator 18 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10839	CIN18:Indicator 18 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10840	CIN19:Indicator 19 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10841	CIN19:Indicator 19 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10842	CIN20:Indicator 20 status input	R(W)	1	0=Signals;1=GooseDBPOS
HR10843	CIN20:Indicator 20 status to display	R(W)	1	0=GOOSE IN Switch 1;1=GOOSE IN Switch 2;2=GOOS 15;15=GOOSE IN Switch 16;16=GOOSE IN Switch 17; 29;29=GOOSE IN Switch 30;30=GOOSE IN Switch 31; 43;43=GOOSE IN Switch 44;44=GOOSE IN Switch 45; 57;57=GOOSE IN Switch 58;58=GOOSE IN Switch 59;
HR10844	PFC1:PFC> [55C] mode	R(W)	1	0=Disabled;1=Activated
HR10845	PFC1:PFC condition	R	1	0=Normal;1=Discharging;2=Blocked;3=Switching interv

Holding Register	Parameter	Access	Length	
HR10846	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10847				HR10846 (Lo-16bits)
HR10848	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10849				HR10848 (Lo-16bits)
HR10850	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10851				HR10850 (Lo-16bits)
HR10852	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10853				HR10852 (Lo-16bits)
HR10854	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10855				HR10854 (Lo-16bits)
HR10856	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10857				HR10856 (Lo-16bits)
HR10858	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10859				HR10858 (Lo-16bits)
HR10860	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10861				HR10860 (Lo-16bits)
HR10862	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10863				HR10862 (Lo-16bits)
HR10864	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10865				HR10864 (Lo-16bits)
HR10866	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10867				HR10866 (Lo-16bits)
HR10868	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10869				HR10868 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR10870	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10871				HR10870 (Lo-16bits)
HR10872	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10873				HR10872 (Lo-16bits)
HR10874	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10875				HR10874 (Lo-16bits)
HR10876	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10877				HR10876 (Lo-16bits)
HR10878	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10879				HR10878 (Lo-16bits)
HR10880	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10881				HR10880 (Lo-16bits)
HR10882	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10883				HR10882 (Lo-16bits)
HR10884	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10885				HR10884 (Lo-16bits)
HR10886	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10887				HR10886 (Lo-16bits)
HR10888	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10889				HR10888 (Lo-16bits)
HR10890	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10891				HR10890 (Lo-16bits)
HR10892	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10893				HR10892 (Lo-16bits)
HR10894	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10895				HR10894 (Lo-16bits)
HR10896	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10897				HR10896 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10898	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10899				HR10898 (Lo-16bits)
HR10900	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10901				HR10900 (Lo-16bits)
HR10902	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10903				HR10902 (Lo-16bits)
HR10904	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10905				HR10904 (Lo-16bits)
HR10906	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10907				HR10906 (Lo-16bits)
HR10908	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1.0...1.00] (Hi-16bits)
HR10909				HR10908 (Lo-16bits)
HR10910	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10911				HR10910 (Lo-16bits)
HR10912	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10913				HR10912 (Lo-16bits)
HR10914	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10915				HR10914 (Lo-16bits)
HR10916	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10917				HR10916 (Lo-16bits)
HR10918	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10919				HR10918 (Lo-16bits)
HR10920	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10921				HR10920 (Lo-16bits)
HR10922	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10923				HR10922 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR10924	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10925				HR10924 (Lo-16bits)
HR10926	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10927				HR10926 (Lo-16bits)
HR10928	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10929				HR10928 (Lo-16bits)
HR10930	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10931				HR10930 (Lo-16bits)
HR10932	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10933				HR10932 (Lo-16bits)
HR10934	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10935				HR10934 (Lo-16bits)
HR10936	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10937				HR10936 (Lo-16bits)
HR10938	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10939				HR10938 (Lo-16bits)
HR10940	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000.0...50000.0] (Hi-16bits)
HR10941				HR10940 (Lo-16bits)
HR10942	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10943				HR10942 (Lo-16bits)
HR10944	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10945				HR10944 (Lo-16bits)
HR10946	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10947				HR10946 (Lo-16bits)
HR10948	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10949				HR10948 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR10950	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10951				HR10950 (Lo-16bits)
HR10952	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10953				HR10952 (Lo-16bits)
HR10954	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10955				HR10954 (Lo-16bits)
HR10956	PFC1:Minimum current block limit	R(W)	2	FLOAT [0.00...1.00] (Hi-16bits)
HR10957				HR10956 (Lo-16bits)
HR10958	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10959				HR10958 (Lo-16bits)
HR10960	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10961				HR10960 (Lo-16bits)
HR10962	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10963				HR10962 (Lo-16bits)
HR10964	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10965				HR10964 (Lo-16bits)
HR10966	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10967				HR10966 (Lo-16bits)
HR10968	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10969				HR10968 (Lo-16bits)
HR10970	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10971				HR10970 (Lo-16bits)
HR10972	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0.0...50.0] (Hi-16bits)
HR10973				HR10972 (Lo-16bits)
HR10974	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10975				HR10974 (Lo-16bits)
HR10976	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10977				HR10976 (Lo-16bits)
HR10978	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10979				HR10978 (Lo-16bits)

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Holding Register	Parameter	Access	Length	
HR10980	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10981				HR10980 (Lo-16bits)
HR10982	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10983				HR10982 (Lo-16bits)
HR10984	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10985				HR10984 (Lo-16bits)
HR10986	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10987				HR10986 (Lo-16bits)
HR10988	PFC1:Bank control starting delay	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10989				HR10988 (Lo-16bits)
HR10990	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10991	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10992	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10993	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10994	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10995	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10996	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10997	PFC1:Fast bank recontrol in use	R(W)	1	0=No;1=Yes
HR10998	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR10999				HR10998 (Lo-16bits)
HR11000	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11001				HR11000 (Lo-16bits)
HR11002	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11003				HR11002 (Lo-16bits)
HR11004	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11005				HR11004 (Lo-16bits)
HR11006	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11007				HR11006 (Lo-16bits)
HR11008	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11009				HR11008 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR11010	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11011				HR11010 (Lo-16bits)
HR11012	PFC1:Switch interval time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11013				HR11012 (Lo-16bits)
HR11014	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11015				HR11014 (Lo-16bits)
HR11016	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11017				HR11016 (Lo-16bits)
HR11018	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11019				HR11018 (Lo-16bits)
HR11020	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11021				HR11020 (Lo-16bits)
HR11022	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11023				HR11022 (Lo-16bits)
HR11024	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11025				HR11024 (Lo-16bits)
HR11026	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11027				HR11026 (Lo-16bits)
HR11028	PFC1:Discharge time	R(W)	2	FLOAT [1.0...1800.0] (Hi-16bits)
HR11029				HR11028 (Lo-16bits)
HR11030	VRG1:Circulating current (A)	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11031				HR11030 (Lo-16bits)
HR11032	VRG1:Circulating current deviation (%Un)	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11033				HR11032 (Lo-16bits)
HR11034	VRG1:Voltage deviation	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11035				HR11034 (Lo-16bits)
HR11036	VRG1:Total deviation	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11037				HR11036 (Lo-16bits)
HR11038	VRG1:Tap voltage (kV)	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11039				HR11038 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR11040	VRG1:Voltage difference (kV)	R	2	FLOAT [-50000.00...50000.00] (Hi-16bits)
HR11041				HR11040 (Lo-16bits)
HR11042	VRG1:Absolute tap location (parallel engine)	R	2	FLOAT [0...50] (Hi-16bits)
HR11043				HR11042 (Lo-16bits)
HR11044	VRG1:T1 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11045				HR11044 (Lo-16bits)
HR11046	VRG1:T2 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11047				HR11046 (Lo-16bits)
HR11048	VRG1:T3 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11049				HR11048 (Lo-16bits)
HR11050	VRG1:T4 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11051				HR11050 (Lo-16bits)
HR11052	VRG1:T1 voltage deviation	R	2	FLOAT [-100.00...140.00] (Hi-16bits)
HR11053				HR11052 (Lo-16bits)
HR11054	VRG1:T2 voltage deviation	R	2	FLOAT [-100.00...140.00] (Hi-16bits)
HR11055				HR11054 (Lo-16bits)
HR11056	VRG1:T3 voltage deviation	R	2	FLOAT [-100.00...140.00] (Hi-16bits)
HR11057				HR11056 (Lo-16bits)
HR11058	VRG1:T4 voltage deviation	R	2	FLOAT [-100.00...140.00] (Hi-16bits)
HR11059				HR11058 (Lo-16bits)
HR11060	VRG1:T1 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11061				HR11060 (Lo-16bits)
HR11062	VRG1:T2 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11063				HR11062 (Lo-16bits)
HR11064	VRG1:T3 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11065				HR11064 (Lo-16bits)
HR11066	VRG1:T4 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11067				HR11066 (Lo-16bits)

Holding Register	Parameter	Access	Length	
HR11068	VRG1:T1 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11069				HR11068 (Lo-16bits)
HR11070	VRG1:T2 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11071				HR11070 (Lo-16bits)
HR11072	VRG1:T3 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11073				HR11072 (Lo-16bits)
HR11074	VRG1:T4 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11075				HR11074 (Lo-16bits)
HR11076	VRG1:T1 Auto/Manual	R	1	0=Automatic;1=Manual
HR11077	VRG1:T2 Auto/Manual	R	1	0=Automatic;1=Manual
HR11078	VRG1:T3 Auto/Manual	R	1	0=Automatic;1=Manual
HR11079	VRG1:T4 Auto/Manual	R	1	0=Automatic;1=Manual
HR11080	VRG1:T1 Independent/Parallel	R	1	0=Single;1=Parallel
HR11081	VRG1:T2 Independent/Parallel	R	1	0=Single;1=Parallel
HR11082	VRG1:T3 Independent/Parallel	R	1	0=Single;1=Parallel
HR11083	VRG1:T4 Independent/Parallel	R	1	0=Single;1=Parallel
HR11084	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11085				HR11084 (Lo-16bits)
HR11086	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11087				HR11086 (Lo-16bits)
HR11088	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11089				HR11088 (Lo-16bits)
HR11090	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11091				HR11090 (Lo-16bits)
HR11092	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11093				HR11092 (Lo-16bits)
HR11094	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11095				HR11094 (Lo-16bits)
HR11096	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11097				HR11096 (Lo-16bits)

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TN00011

Holding Register	Parameter	Access	Length	
HR11098	VRG1:U>/< T2 time delay (DT)	RW	2	FLOAT [0.000...1800.000] (Hi-16bits)
HR11099				HR11098 (Lo-16bits)
HR11100	AOUT2:mA In (T card) 1 mA In 1	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11101				HR11100 (Lo-16bits)
HR11102	AOUT2:mA In (T card) 1 mA In 2	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11103				HR11102 (Lo-16bits)
HR11104	AOUT2:mA In (T card) 1 mA In 3	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11105				HR11104 (Lo-16bits)
HR11106	AOUT2:mA In (T card) 1 mA In 4	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11107				HR11106 (Lo-16bits)
HR11108	AOUT2:mA In (T card) 2 mA In 1	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11109				HR11108 (Lo-16bits)
HR11110	AOUT2:mA In (T card) 2 mA In 2	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11111				HR11110 (Lo-16bits)
HR11112	AOUT2:mA In (T card) 2 mA In 3	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11113				HR11112 (Lo-16bits)
HR11114	AOUT2:mA In (T card) 2 mA In 4	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11115				HR11114 (Lo-16bits)
HR11116	AOUT2:mA Out Channel 1 Input Magnitude now (T option 1)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR11117				HR11116 (Lo-16bits)
HR11118	AOUT2:mA Out Channel 1 Outputs now (T option 1)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11119				HR11118 (Lo-16bits)
HR11120	AOUT2:mA Out Channel 2 Input Magnitude now (T option 2)	R	2	FLOAT [-10000000.000...10000000.000] (Hi-16bits)
HR11121				HR11120 (Lo-16bits)
HR11122	AOUT2:mA Out Channel 2 Outputs now (T option 2)	R	2	FLOAT [0...24.0000] (Hi-16bits)
HR11123				HR11122 (Lo-16bits)
HR11124	VRG1:AVR tap control status	R	1	0=OK;1=Tap control failure
HR11125	VRG1:AVR Parallel/Independent status to mimic	R	1	0=Parallel;1=Independent

Holding Register	Parameter	Access	Length	
HR11126	VRG1:AVR Follower&Master status to mimic	R	1	0=Follower;1=Master
HR11127	VRG1:AVR no parallel communication	R	1	0=Communication OK;1=Communication failure
HR11128	VRG1:AVR tap differential failure	R	1	0=Tap OK;1=Tap difference failure
HR11129	VRG1:Hierarchy status to mimic	R	1	0=Master & follower;1=Circulating reactive current
HR11130	VRG1:Auto/Manual status to mimic	R	1	0=Auto;1=Manual
HR11131	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11132	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11133	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11134	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11135	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11136	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11137	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11138	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11139	VRG1:Relay in test mode	R	1	1=On;2=Blocked;3=Test;4=Test/Blocked;5=Off
HR11140	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11141	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11142	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11143	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11144	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11145	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11146	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11147	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11148	VRG1:Maximum tap raise position	R	1	0=OK;1=Max. position
HR11149	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11150	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11151	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11152	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11153	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11154	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position

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Holding Register	Parameter	Access	Length	
HR11155	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11156	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11157	VRG1:Minimum tap lower position	R	1	0=OK;1=Min. position
HR11158	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11159				HR11158 (Lo-16bits)
HR11160	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11161				HR11160 (Lo-16bits)
HR11162	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11163				HR11162 (Lo-16bits)
HR11164	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11165				HR11164 (Lo-16bits)
HR11166	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11167				HR11166 (Lo-16bits)
HR11168	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11169				HR11168 (Lo-16bits)
HR11170	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11171				HR11170 (Lo-16bits)
HR11172	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11173				HR11172 (Lo-16bits)
HR11174	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11175				HR11174 (Lo-16bits)
HR11176	VRG1:Health	R	1	0=OK;1=FAIL
HR11177	VRG1:Health	R	1	0=OK;1=FAIL
HR11178	VRG1:Health	R	1	0=OK;1=FAIL
HR11179	VRG1:Health	R	1	0=OK;1=FAIL
HR11180	VRG1:Health	R	1	0=OK;1=FAIL

Holding Register	Parameter	Access	Length	
HR11181	VRG1:Health	R	1	0=OK;1=FAIL
HR11182	VRG1:Health	R	1	0=OK;1=FAIL
HR11183	VRG1:Health	R	1	0=OK;1=FAIL
HR11184	VRG1:Health	R	1	0=OK;1=FAIL
HR11185	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11186	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11187	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11188	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11189	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11190	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11191	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11192	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11193	VRG1:Automatic local block	R	1	0=Normal;1=Blocked
HR11194	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11195	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11196	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11197	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11198	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11199	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11200	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11201	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11202	VRG1:Operating mode (Auto)	R	1	0=Automatic;1=Manual
HR11203	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11204	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11205	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11206	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11207	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11208	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11209	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11210	VRG1:Parallel operation	R	1	0=Single;1=Parallel

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Holding Register	Parameter	Access	Length	
HR11211	VRG1:Parallel operation	R	1	0=Single;1=Parallel
HR11212	VRG1:Status	R	1	0=Follower;1=Leader
HR11213	VRG1:Status	R	1	0=Follower;1=Leader
HR11214	VRG1:Status	R	1	0=Follower;1=Leader
HR11215	VRG1:Status	R	1	0=Follower;1=Leader
HR11216	VRG1:Status	R	1	0=Follower;1=Leader
HR11217	VRG1:Status	R	1	0=Follower;1=Leader
HR11218	VRG1:Status	R	1	0=Follower;1=Leader
HR11219	VRG1:Status	R	1	0=Follower;1=Leader
HR11220	VRG1:Status	R	1	0=Follower;1=Leader
HR11221	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11222	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11223	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11224	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11225	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11226	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11227	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11228	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11229	VRG1:Operation hierarchy	R	1	0=Leader & follower;1=Circulating reactive current
HR11230	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11231	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11232	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11233	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11234	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11235	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11236	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11237	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11238	VRG1:Parallel group	R	1	0=NA;1=1;2=2;3=3;4=4
HR11239	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11240	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9

Holding Register	Parameter	Access	Length	
HR11241	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11242	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11243	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11244	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11245	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11246	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11247	VRG1:Local parallel transformer ID	R	1	0=NA;1=1;2=2;3=3;4=4;5=5;6=6;7=7;8=8;9=9
HR11248	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11249				HR11248 (Lo-16bits)
HR11250	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11251				HR11250 (Lo-16bits)
HR11252	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11253				HR11252 (Lo-16bits)
HR11254	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11255				HR11254 (Lo-16bits)
HR11256	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11257				HR11256 (Lo-16bits)
HR11258	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11259				HR11258 (Lo-16bits)
HR11260	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11261				HR11260 (Lo-16bits)
HR11262	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11263				HR11262 (Lo-16bits)
HR11264	VRG1:Tap voltage	R	2	FLOAT [0.00...250.00] (Hi-16bits)
HR11265				HR11264 (Lo-16bits)
HR11266	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11267				HR11266 (Lo-16bits)
HR11268	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11269				HR11268 (Lo-16bits)
HR11270	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)

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Holding Register	Parameter	Access	Length	
HR11271				HR11270 (Lo-16bits)
HR11272	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11273				HR11272 (Lo-16bits)
HR11274	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11275				HR11274 (Lo-16bits)
HR11276	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11277				HR11276 (Lo-16bits)
HR11278	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11279				HR11278 (Lo-16bits)
HR11280	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11281				HR11280 (Lo-16bits)
HR11282	VRG1:Local reactance X1	R	2	FLOAT [0.00...500.00] (Hi-16bits)
HR11283				HR11282 (Lo-16bits)
HR11284	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11285				HR11284 (Lo-16bits)
HR11286	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11287				HR11286 (Lo-16bits)
HR11288	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11289				HR11288 (Lo-16bits)
HR11290	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11291				HR11290 (Lo-16bits)
HR11292	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11293				HR11292 (Lo-16bits)
HR11294	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11295				HR11294 (Lo-16bits)
HR11296	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11297				HR11296 (Lo-16bits)
HR11298	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)
HR11299				HR11298 (Lo-16bits)
HR11300	VRG1:Local load current I1	R	2	FLOAT [0.00...1250.00] (Hi-16bits)

Holding Register	Parameter	Access	Length	
HR11301				HR11300 (Lo-16bits)
HR11302	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11303				HR11302 (Lo-16bits)
HR11304	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11305				HR11304 (Lo-16bits)
HR11306	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11307				HR11306 (Lo-16bits)
HR11308	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11309				HR11308 (Lo-16bits)
HR11310	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11311				HR11310 (Lo-16bits)
HR11312	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11313				HR11312 (Lo-16bits)
HR11314	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11315				HR11314 (Lo-16bits)
HR11316	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11317				HR11316 (Lo-16bits)
HR11318	VRG1:Local load current I1 angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR11319				HR11318 (Lo-16bits)
HR11320	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11321	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11322	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11323	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11324	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11325	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11326	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11327	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11328	VRG1:Leader command	R	1	0=Normal;1=Lower;2=Raise
HR11329	VRG1:ParallelTransfModeKind	R	1	1=No Mode predifined;2=Master;3=Follower;4=Power F
HR11330	COL1:ICOL> [49OL] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked

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TN00011

Holding Register	Parameter	Access	Length	
HR11331	COL2:ICOL>> [49OL] condition	R	1	0=Normal;1=Start;2=Trip;3=Blocked
HR11332	CNU1:CNU> [50UB] condition	R	1	0=Normal;1=Alarm Start;2=Alarm On;3=Trip Start;4=Tri
HR11333	ASC1:Curve5 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11334				HR11333 (Lo-16bits)
HR11335	ASC1:Curve5 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11336				HR11335 (Lo-16bits)
HR11337	ASC1:Curve6 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11338				HR11337 (Lo-16bits)
HR11339	ASC1:Curve6 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11340				HR11339 (Lo-16bits)
HR11341	ASC1:Curve7 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11342				HR11341 (Lo-16bits)
HR11343	ASC1:Curve7 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11344				HR11343 (Lo-16bits)
HR11345	ASC1:Curve8 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11346				HR11345 (Lo-16bits)
HR11347	ASC1:Curve8 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11348				HR11347 (Lo-16bits)
HR11349	ASC1:Curve9 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11350				HR11349 (Lo-16bits)
HR11351	ASC1:Curve9 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11352				HR11351 (Lo-16bits)
HR11353	ASC1:Curve10 Input	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11354				HR11353 (Lo-16bits)
HR11355	ASC1:Curve10 Output	R	2	FLOAT [-1000000.00...1000000.00] (Hi-16bits)
HR11356				HR11355 (Lo-16bits)
HR19906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR19907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR19908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR19909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC

Holding Register	Parameter	Access	Length	
HR19910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC
HR19911	Object S1 control register	R=0/W	1	SELECTOPEN=0xA001;SELECTCLOSE=0xA002,CANC

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