

AQ 200 Modbus map

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1 AQ-21x frame Modbus map

1.1 FVSPE Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware FVSPE_v2.1.1.3-247. The firmware is used by the following device types:

- AQ-F201
- AQ-F205
- AQ-F210
- AQ-F215
- AQ-V211
- AQ-S214
- AQ-S215
- AQ-P215
- AQ-E215



NOTICE!

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 1.1 - 1. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

Holding Register	Parameter	Access	Length	Info
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1: Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1: Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1: Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1: Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1: Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1: Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1: Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1: Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR429				HR428 (Lo-16bits)
HR430	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I ^L L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I ^L L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I ^L L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I ^L L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I ^L L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I ^L L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I ^L L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I ^L L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I ^L L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I ^L L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I ^L L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I ^L L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I ^L L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I ^L L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I ^L L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I ² L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I ² L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I ² L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I ² L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I ² L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I ² L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I ² L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I ² L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I ² L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I ² L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I ² L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I ² L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I ² L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I ² L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I ² L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I ¹ L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I ¹ L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I ¹ L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I ¹ L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I ¹ L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I ¹ L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I ¹ L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I ¹ L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I ¹ L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I ¹ L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I ¹ L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I ¹ L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I ¹ L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I ¹ L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I ¹ L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR813				HR812 (Lo-16bits)
HR814	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR873				HR872 (Lo-16bits)
HR874	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR927				HR926 (Lo-16bits)
HR928	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR951				HR950 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR952	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR977				HR976 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR978	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1008	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1038	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1068	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1098	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1128	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1158	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1188	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1217				HR1216 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1218	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)
HR1228	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1229				HR1228 (Lo-16bits)
HR1230	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)
HR1232	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)
HR1234	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1244	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)
HR1252	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)
HR1256	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)
HR1258	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1259				HR1258 (Lo-16bits)
HR1260	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1268	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)
HR1276	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1279				HR1278 (Lo-16bits)
HR1280	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1281				HR1280 (Lo-16bits)
HR1282	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)
HR1288	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1291				HR1290 (Lo-16bits)
HR1292	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1314	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	POW1:Reset 3ph Energies	RW	1	0=- 1=Reset
HR1317	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1338				HR1337 (Lo-16bits)
HR1339	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1340				HR1339 (Lo-16bits)
HR1341	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1342				HR1341 (Lo-16bits)
HR1343	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1344				HR1343 (Lo-16bits)
HR1345	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1346				HR1345 (Lo-16bits)
HR1347	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1348				HR1347 (Lo-16bits)
HR1349	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1350				HR1349 (Lo-16bits)
HR1351	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1352				HR1351 (Lo-16bits)
HR1353	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1354				HR1353 (Lo-16bits)
HR1355	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1356				HR1355 (Lo-16bits)
HR1357	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1358				HR1357 (Lo-16bits)
HR1359	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1366				HR1365 (Lo-16bits)
HR1367	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)
HR1381	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1382				HR1381 (Lo-16bits)
HR1383	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	M2CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1393	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1402				HR1401 (Lo-16bits)
HR1403	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)
HR1405	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)
HR1409	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1410				HR1409 (Lo-16bits)
HR1411	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)
HR1417	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1419	DIGI:DIs (cpu)	R	2	0b=DI1 1b=DI2 2b=DI3 (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	DIGI:DIs (input option 1)	R	2	0b=DI4 1b=DI5 2b=DI6 3b=DI7 4b=DI8 5b=DI9 6b=DI10 7b=DI11 (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	DIGI:DIs (input option 2)	R	2	0b=DI12 1b=DI13 2b=DI14 3b=DI15 4b=DI16 5b=DI17 6b=DI18 7b=DI19 (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	DIGI:DIs (input option 3)	R	2	0b=DI20 1b=DI21 2b=DI22 3b=DI23 4b=DI24 5b=DI25 6b=DI26 7b=DI27 (Hi-16bits)
HR1426				HR1425 (Lo-16bits)
HR1427	DIGI:DIs (input option 4)	R	2	0b=DI28 1b=DI29 2b=DI30 3b=DI31 4b=DI32 5b=DI33 6b=DI34 7b=DI35 (Hi-16bits)
HR1428				HR1427 (Lo-16bits)
HR1429	DIGI:DIs (input option 5)	R	2	0b=DI36 1b=DI37 2b=DI38 3b=DI39 4b=DI40 5b=DI41 6b=DI42 7b=DI43 (Hi-16bits)
HR1430				HR1429 (Lo-16bits)
HR1431	DIGI:DIs (input option 6)	R	2	0b=DI44 1b=DI45 2b=DI46 3b=DI47 4b=DI48 5b=DI49 6b=DI50 7b=DI51 (Hi-16bits)
HR1432				HR1431 (Lo-16bits)
HR1433	DIGI:Dis 1-32	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1434				HR1433 (Lo-16bits)
HR1435	DIGI:Dis 33-64	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 (Hi-16bits)
HR1436				HR1435 (Lo-16bits)
HR1437	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1438				HR1437 (Lo-16bits)
HR1439	DIGI:DI1 Counter Clear	RW	1	0=- 1=Clear
HR1440	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1441				HR1440 (Lo-16bits)
HR1442	DIGI:DI2 Counter Clear	RW	1	0=- 1=Clear
HR1443	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1444				HR1443 (Lo-16bits)
HR1445	DIGI:DI3 Counter Clear	RW	1	0=- 1=Clear
HR1446	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1447				HR1446 (Lo-16bits)
HR1448	DIGI:DI4 Counter Clear	RW	1	0=- 1=Clear
HR1449	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1450				HR1449 (Lo-16bits)
HR1451	DIGI:DI5 Counter Clear	RW	1	0=- 1=Clear
HR1452	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1453				HR1452 (Lo-16bits)
HR1454	DIGI:DI6 Counter Clear	RW	1	0=- 1=Clear
HR1455	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1456				HR1455 (Lo-16bits)
HR1457	DIGI:DI7 Counter Clear	RW	1	0=- 1=Clear
HR1458	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1459				HR1458 (Lo-16bits)
HR1460	DIGI:DI8 Counter Clear	RW	1	0=- 1=Clear
HR1461	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1462				HR1461 (Lo-16bits)
HR1463	DIGI:DI9 Counter Clear	RW	1	0=- 1=Clear
HR1464	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1465				HR1464 (Lo-16bits)
HR1466	DIGI:DI10 Counter Clear	RW	1	0=- 1=Clear
HR1467	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)
HR1469	DIGI:DI11 Counter Clear	RW	1	0=- 1=Clear
HR1470	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1471				HR1470 (Lo-16bits)
HR1472	DIGI:DI12 Counter Clear	RW	1	0=- 1=Clear
HR1473	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1474				HR1473 (Lo-16bits)
HR1475	DIGI:DI13 Counter Clear	RW	1	0=- 1=Clear
HR1476	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1477				HR1476 (Lo-16bits)
HR1478	DIGI:DI14 Counter Clear	RW	1	0=- 1=Clear
HR1479	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1480				HR1479 (Lo-16bits)
HR1481	DIGI:DI15 Counter Clear	RW	1	0=- 1=Clear
HR1482	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1483				HR1482 (Lo-16bits)
HR1484	DIGI:DI16 Counter Clear	RW	1	0=- 1=Clear
HR1485	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1486				HR1485 (Lo-16bits)
HR1487	DIGI:DI17 Counter Clear	RW	1	0=- 1=Clear
HR1488	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1489				HR1488 (Lo-16bits)
HR1490	DIGI:DI18 Counter Clear	RW	1	0=- 1=Clear
HR1491	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1492				HR1491 (Lo-16bits)
HR1493	DIGI:DI19 Counter Clear	RW	1	0=- 1=Clear
HR1494	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1495				HR1494 (Lo-16bits)
HR1496	DIGI:DI20 Counter Clear	RW	1	0=- 1=Clear
HR1497	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1498				HR1497 (Lo-16bits)
HR1499	DIGI:DI21 Counter Clear	RW	1	0=- 1=Clear
HR1500	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1501				HR1500 (Lo-16bits)
HR1502	DIGI:DI22 Counter Clear	RW	1	0=- 1=Clear
HR1503	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1504				HR1503 (Lo-16bits)
HR1505	DIGI:DI23 Counter Clear	RW	1	0=- 1=Clear
HR1506	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1507				HR1506 (Lo-16bits)
HR1508	DIGI:DI24 Counter Clear	RW	1	0=- 1=Clear
HR1509	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1510				HR1509 (Lo-16bits)
HR1511	DIGI:DI25 Counter Clear	RW	1	0=- 1=Clear
HR1512	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1513				HR1512 (Lo-16bits)
HR1514	DIGI:DI26 Counter Clear	RW	1	0=- 1=Clear
HR1515	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIGI:DI27 Counter Clear	RW	1	0=- 1=Clear
HR1518	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	DIGI:DI28 Counter Clear	RW	1	0=- 1=Clear
HR1521	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIGI:DI29 Counter Clear	RW	1	0=- 1=Clear
HR1524	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	DIGI:DI30 Counter Clear	RW	1	0=- 1=Clear
HR1527	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	DIGI:DI31 Counter Clear	RW	1	0=- 1=Clear
HR1530	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1531				HR1530 (Lo-16bits)
HR1532	DIGI:DI32 Counter Clear	RW	1	0=- 1=Clear
HR1533	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	DIGI:DI33 Counter Clear	RW	1	0=- 1=Clear
HR1536	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	DIGI:DI34 Counter Clear	RW	1	0=- 1=Clear
HR1539	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	DIGI:DI35 Counter Clear	RW	1	0=- 1=Clear
HR1542	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1543				HR1542 (Lo-16bits)
HR1544	DIGI:DI36 Counter Clear	RW	1	0=- 1=Clear
HR1545	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	DIGI:DI37 Counter Clear	RW	1	0=- 1=Clear
HR1548	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	DIGI:DI38 Counter Clear	RW	1	0=- 1=Clear
HR1551	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	DIGI:DI39 Counter Clear	RW	1	0=- 1=Clear
HR1554	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	DIGI:DI40 Counter Clear	RW	1	0=- 1=Clear
HR1557	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	DIGI:DI41 Counter Clear	RW	1	0=- 1=Clear
HR1560	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1561				HR1560 (Lo-16bits)
HR1562	DIGI:DI42 Counter Clear	RW	1	0=- 1=Clear
HR1563	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	DIGI:DI43 Counter Clear	RW	1	0=- 1=Clear
HR1566	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	DIGI:DI44 Counter Clear	RW	1	0=- 1=Clear
HR1569	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	DIGI:DI45 Counter Clear	RW	1	0=- 1=Clear
HR1572	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	DIGI:DI46 Counter Clear	RW	1	0=- 1=Clear
HR1575	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	DIGI:DI47 Counter Clear	RW	1	0=- 1=Clear
HR1578	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	DIGI:DI48 Counter Clear	RW	1	0=- 1=Clear
HR1581	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	DIGI:DI49 Counter Clear	RW	1	0=- 1=Clear
HR1584	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	DIGI:DI50 Counter Clear	RW	1	0=- 1=Clear
HR1587	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	DIGI:DI51 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1590	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1593	SGS:Force SG	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1594	SGS:Force SG change	RW	1	0=Disabled 1=Enabled
HR1595	SGS:Used setting groups	RW	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1596	SGS:Remote SG change	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1597	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR1598	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)
HR1600	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1605				HR1604 (Lo-16bits)
HR1606	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1617				HR1616 (Lo-16bits)
HR1618	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1627				HR1626 (Lo-16bits)
HR1628	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)
HR1630	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1635				HR1634 (Lo-16bits)
HR1636	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR1639	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1644				HR1643 (Lo-16bits)
HR1645	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1688	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	NOC1: > [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1691	NOC1: > condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR1692	NOC1: > Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1693	NOC1: > Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1694	NOC1: > Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1695	NOC1: > Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	NOC1: > Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	NOC1: > Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)
HR1713	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1716				HR1715 (Lo-16bits)
HR1717	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1718	NOC1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR1719	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1720	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1721	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1722	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1723	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1724	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1726				HR1725 (Lo-16bits)
HR1727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1732				HR1731 (Lo-16bits)
HR1733	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1736				HR1735 (Lo-16bits)
HR1737	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1738				HR1737 (Lo-16bits)
HR1739	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1742	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1743	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1744	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR1745	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1746	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1747	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1748	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1749	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1750	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1751	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1752	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1753	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1754	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1755	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1756	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1757	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1758	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1759	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1760	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1761	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1762	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1763	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1764	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1765	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR1766	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1767	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1768	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1769	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1770	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1771	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1772	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1776				HR1775 (Lo-16bits)
HR1777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1778				HR1777 (Lo-16bits)
HR1779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1780				HR1779 (Lo-16bits)
HR1781	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1784				HR1783 (Lo-16bits)
HR1785	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1790				HR1789 (Lo-16bits)
HR1791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1794				HR1793 (Lo-16bits)
HR1795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1796				HR1795 (Lo-16bits)
HR1797	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1798				HR1797 (Lo-16bits)
HR1799	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1800				HR1799 (Lo-16bits)
HR1801	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)
HR1803	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1838	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1839	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1840	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1841	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1842	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1843	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1844	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1845	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1848				HR1847 (Lo-16bits)
HR1849	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1852				HR1851 (Lo-16bits)
HR1853	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1854				HR1853 (Lo-16bits)
HR1855	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1856				HR1855 (Lo-16bits)
HR1857	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1858				HR1857 (Lo-16bits)
HR1859	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1860				HR1859 (Lo-16bits)
HR1861	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1862	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1863	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1864	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1865	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1866	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1867	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1868	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1870				HR1869 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1872				HR1871 (Lo-16bits)
HR1873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1874				HR1873 (Lo-16bits)
HR1875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1876				HR1875 (Lo-16bits)
HR1877	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1878				HR1877 (Lo-16bits)
HR1879	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1880				HR1879 (Lo-16bits)
HR1881	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1882				HR1881 (Lo-16bits)
HR1883	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1884				HR1883 (Lo-16bits)
HR1885	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1886	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1887	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1888	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1889	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1890	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1891	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1892	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1893	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1894	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1895	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1896	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1897	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1898	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1899	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1900	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1901	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1902				HR1901 (Lo-16bits)
HR1903	NOC1:Delay type	RW	1	0=DT 1=IDMT
HR1904	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	NOC1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR1907	NOC1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1908	NOC1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1909	NOC1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR1910	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1917				HR1916 (Lo-16bits)
HR1918	NOC1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR1919	NOC1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR1920	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1921	NOC2:I>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1922	NOC2:I>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1923	NOC2:I>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1924	NOC2:I>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1925	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1926				HR1925 (Lo-16bits)
HR1927	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1942				HR1941 (Lo-16bits)
HR1943	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1948	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1949	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1950	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1951	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1952	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1953	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1954	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1955	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1958				HR1957 (Lo-16bits)
HR1959	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1962				HR1961 (Lo-16bits)
HR1963	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1969	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1972	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1973	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1974	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1975	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1976	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1977	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1978	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1979	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1980	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1981	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1982	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1983	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1984	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1985	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1986	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1987	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1988	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1989	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1990	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1991	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR1992	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1993	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1994	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1995	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1996	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1997	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1998	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1999	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2000	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2001	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2002	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2003	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2017	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2044				HR2043 (Lo-16bits)
HR2045	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2046				HR2045 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2047	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2048				HR2047 (Lo-16bits)
HR2049	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2050				HR2049 (Lo-16bits)
HR2051	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2052				HR2051 (Lo-16bits)
HR2053	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2054				HR2053 (Lo-16bits)
HR2055	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2056				HR2055 (Lo-16bits)
HR2057	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)
HR2059	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)
HR2061	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2068	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2069	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2070	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2071	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2072	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2073	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2074	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2075	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)
HR2081	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)
HR2091	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2092	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2093	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2094	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2095	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2096	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2097	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2098	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2099	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2116	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2117	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2118	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2119	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2120	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2121	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2122	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2123	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2124	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2125	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2126	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2127	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2128	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2129	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2130	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2131	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	NOC2:Delay type	RW	1	0=DT 1=IDMT
HR2134	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2135				HR2134 (Lo-16bits)
HR2136	NOC2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2137	NOC2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2138	NOC2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2139	NOC2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2140	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2142	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	NOC2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2149	NOC2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2150	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2151	NOC3:I>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2152	NOC3:I>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2153	NOC3:I>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2154	NOC3:I>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2155	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2158				HR2157 (Lo-16bits)
HR2159	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)
HR2165	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2167	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2178	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2179	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2180	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2181	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2182	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2183	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2184	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2185	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2186				HR2185 (Lo-16bits)
HR2187	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2195	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)
HR2201	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2202	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2203	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2204	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2205	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2206	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2207	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2208	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2209	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2210	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2211	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2212	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2213	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2214	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2215	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2216	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2217	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2218	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2219	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2220	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2221	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2222	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2223	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2224	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2225	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2226	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2227	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2228	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2229	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2230	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2231	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2232	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2233	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2240				HR2239 (Lo-16bits)
HR2241	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2242				HR2241 (Lo-16bits)
HR2243	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2246				HR2245 (Lo-16bits)
HR2247	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)
HR2251	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)
HR2253	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2254				HR2253 (Lo-16bits)
HR2255	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2256				HR2255 (Lo-16bits)
HR2257	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)
HR2267	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2272				HR2271 (Lo-16bits)
HR2273	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2286				HR2285 (Lo-16bits)
HR2287	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)
HR2291	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2298	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2299	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2300	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2301	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2302	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2303	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2304	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2305	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2308				HR2307 (Lo-16bits)
HR2309	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)
HR2311	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2314				HR2313 (Lo-16bits)
HR2315	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2322	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2323	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2324	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2325	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2326	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2327	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2328	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2329	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2330				HR2329 (Lo-16bits)
HR2331	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2332				HR2331 (Lo-16bits)
HR2333	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2334				HR2333 (Lo-16bits)
HR2335	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2336				HR2335 (Lo-16bits)
HR2337	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2338				HR2337 (Lo-16bits)
HR2339	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2340				HR2339 (Lo-16bits)
HR2341	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2342				HR2341 (Lo-16bits)
HR2343	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2344				HR2343 (Lo-16bits)
HR2345	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2346	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2347	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2348	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2349	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2350	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2351	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2352	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2353	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2354	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2355	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2356	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2357	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2358	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2359	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2360	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2361	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2362				HR2361 (Lo-16bits)
HR2363	NOC3:Delay type	RW	1	0=DT 1=IDMT
HR2364	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	NOC3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2367	NOC3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2368	NOC3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2369	NOC3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2370	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	NOC3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2379	NOC3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2380	NOC4: >>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2381	NOC4: >>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2382	NOC4: >>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2383	NOC4: >>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2384	NOC4: >>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2385	NOC4: >>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2386				HR2385 (Lo-16bits)
HR2387	NOC4: >>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	NOC4: >>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2391	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)
HR2393	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2402				HR2401 (Lo-16bits)
HR2403	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2404				HR2403 (Lo-16bits)
HR2405	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2406				HR2405 (Lo-16bits)
HR2407	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2408	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2409	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2410	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2411	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2412	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2413	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2414	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2415	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2416				HR2415 (Lo-16bits)
HR2417	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2418				HR2417 (Lo-16bits)
HR2419	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2420				HR2419 (Lo-16bits)
HR2421	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2422				HR2421 (Lo-16bits)
HR2423	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2424				HR2423 (Lo-16bits)
HR2425	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2426				HR2425 (Lo-16bits)
HR2427	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2428				HR2427 (Lo-16bits)
HR2429	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2430				HR2429 (Lo-16bits)
HR2431	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2432	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2433	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2434	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2435	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2436	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2437	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2438	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2439	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2440	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2441	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2442	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2443	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2444	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2445	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2446	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2447	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2448	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2449	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2450	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2451	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2452	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2453	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2454	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2455	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2456	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2457	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2458	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2459	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2460	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2461	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2462	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2463	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2464				HR2463 (Lo-16bits)
HR2465	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2466				HR2465 (Lo-16bits)
HR2467	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2468				HR2467 (Lo-16bits)
HR2469	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2470				HR2469 (Lo-16bits)
HR2471	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2472				HR2471 (Lo-16bits)
HR2473	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2474				HR2473 (Lo-16bits)
HR2475	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2476				HR2475 (Lo-16bits)
HR2477	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2478				HR2477 (Lo-16bits)
HR2479	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2480				HR2479 (Lo-16bits)
HR2481	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2482				HR2481 (Lo-16bits)
HR2483	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2484				HR2483 (Lo-16bits)
HR2485	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2486				HR2485 (Lo-16bits)
HR2487	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2488				HR2487 (Lo-16bits)
HR2489	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2490				HR2489 (Lo-16bits)
HR2491	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2492				HR2491 (Lo-16bits)
HR2493	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2494				HR2493 (Lo-16bits)
HR2495	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2496				HR2495 (Lo-16bits)
HR2497	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2498				HR2497 (Lo-16bits)
HR2499	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2500				HR2499 (Lo-16bits)
HR2501	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2502				HR2501 (Lo-16bits)
HR2503	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2504				HR2503 (Lo-16bits)
HR2505	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2506				HR2505 (Lo-16bits)
HR2507	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2508				HR2507 (Lo-16bits)
HR2509	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2510				HR2509 (Lo-16bits)
HR2511	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2512				HR2511 (Lo-16bits)
HR2513	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2514				HR2513 (Lo-16bits)
HR2515	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2516				HR2515 (Lo-16bits)
HR2517	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2526				HR2525 (Lo-16bits)
HR2527	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2528	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2529	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2530	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2531	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2532	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2533	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2534	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2535	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)
HR2537	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2549	NOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2552	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2553	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2554	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2555	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2556	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2557	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2558	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2559	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)
HR2565	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2575	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2576	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2577	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2578	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2579	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2580	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2581	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2582	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2583	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2584	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2585	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2586	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2587	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2588	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2589	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2590	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2591	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2592				HR2591 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2593	NOC4:Delay type	RW	1	0=DT 1=IDMT
HR2594	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2595				HR2594 (Lo-16bits)
HR2596	NOC4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2597	NOC4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2598	NOC4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2599	NOC4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2600	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2601				HR2600 (Lo-16bits)
HR2602	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2603				HR2602 (Lo-16bits)
HR2604	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2607				HR2606 (Lo-16bits)
HR2608	NOC4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2609	NOC4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2610	ROC1:ldrw> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2611	ROC1:ldrw> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2612	ROC2:ldrw>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2613	ROC2:ldrw>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2614	ROC3:ldrw>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2615	ROC3:ldrw>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2616	ROC4:ldrw>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2617	ROC4:ldrw>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse

Holding Register	Parameter	Access	Length	Info
HR2618	ROC5:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2619	ROC5:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2620	ROC6:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2621	ROC6:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2622	ROC7:ldrw>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2623	ROC7:ldrw>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2624	ROC8:ldrw>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2625	ROC8:ldrw>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2626	ROV1:Urw>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2627	ROV2:Urw>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2628	ROV3:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2629	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2630	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2631	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2632	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled 1=Activated
HR2633	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=Start On (Alarm) 3=Start On (Alarm & Trip) 4=Start On (Trip) 5=Alarm On 6=Start On 7=Start On Alarm On 8=Trip On 9=Alarm On Trip On 10=Multistatus On
HR2634	DOC1:ldir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2635	DOC1:ldir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2636	DOC1:ldir> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2637	DOC1:ldir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DOC1:ldir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2640				HR2639 (Lo-16bits)
HR2641	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2642				HR2641 (Lo-16bits)
HR2643	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2646	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2647	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2648	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2649	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2650	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2651	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2652	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2653	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2654				HR2653 (Lo-16bits)
HR2655	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2658				HR2657 (Lo-16bits)
HR2659	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2660				HR2659 (Lo-16bits)
HR2661	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2663	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2670				HR2669 (Lo-16bits)
HR2671	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)
HR2673	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2688				HR2687 (Lo-16bits)
HR2689	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2702	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2703	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2704	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2705	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2706	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2707	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2708	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2709	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2716				HR2715 (Lo-16bits)
HR2717	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2726	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2727	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2728	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2729	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2730	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2731	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2732	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2733	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2734	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2735	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2736	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2737	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2738	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2739	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2740	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2741	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2742	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2743	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2744	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2745	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2746	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2747	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2748	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2749	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2750	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2751	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2752	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2753	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2754	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2755	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2756	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2757	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2762				HR2761 (Lo-16bits)
HR2763	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2792				HR2791 (Lo-16bits)
HR2793	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2822	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2823	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2824	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2825	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2826	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2827	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2828	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2829	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2830				HR2829 (Lo-16bits)
HR2831	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2832				HR2831 (Lo-16bits)
HR2833	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)
HR2837	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2845	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2846	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2847	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2848	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2849	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2850	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2851	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2852	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2853	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)
HR2861	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2870	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2871	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2872	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2873	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2874	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2875	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2876	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2877	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2878	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2879	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2880	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2881	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2882	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2883	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2884	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2885	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2886	DOC2:ldir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2887	DOC2:ldir>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C

Holding Register	Parameter	Access	Length	Info
HR2888	DOC2:Idir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2889				HR2888 (Lo-16bits)
HR2890	DOC2:Idir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2891				HR2890 (Lo-16bits)
HR2892	DOC2:Idir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2893				HR2892 (Lo-16bits)
HR2894	DOC2:Idir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2895				HR2894 (Lo-16bits)
HR2896	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2897	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2898	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2899	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2900	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2901	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2902	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2903	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2904	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2905				HR2904 (Lo-16bits)
HR2906	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2907				HR2906 (Lo-16bits)
HR2908	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2909				HR2908 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2910	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2911				HR2910 (Lo-16bits)
HR2912	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2913				HR2912 (Lo-16bits)
HR2914	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2915				HR2914 (Lo-16bits)
HR2916	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2917				HR2916 (Lo-16bits)
HR2918	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2919				HR2918 (Lo-16bits)
HR2920	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2921				HR2920 (Lo-16bits)
HR2922	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2923				HR2922 (Lo-16bits)
HR2924	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2925				HR2924 (Lo-16bits)
HR2926	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2927				HR2926 (Lo-16bits)
HR2928	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2929				HR2928 (Lo-16bits)
HR2930	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2931				HR2930 (Lo-16bits)
HR2932	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2933				HR2932 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2934	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2935				HR2934 (Lo-16bits)
HR2936	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2937				HR2936 (Lo-16bits)
HR2938	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2939				HR2938 (Lo-16bits)
HR2940	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2941				HR2940 (Lo-16bits)
HR2942	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2943				HR2942 (Lo-16bits)
HR2944	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2945				HR2944 (Lo-16bits)
HR2946	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2947				HR2946 (Lo-16bits)
HR2948	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2949				HR2948 (Lo-16bits)
HR2950	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2951				HR2950 (Lo-16bits)
HR2952	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2953	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2954	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2955	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2956	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2957	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2958	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2959	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2960	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2961				HR2960 (Lo-16bits)
HR2962	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2963				HR2962 (Lo-16bits)
HR2964	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2965				HR2964 (Lo-16bits)
HR2966	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2967				HR2966 (Lo-16bits)
HR2968	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2969				HR2968 (Lo-16bits)
HR2970	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2971				HR2970 (Lo-16bits)
HR2972	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2973				HR2972 (Lo-16bits)
HR2974	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2975				HR2974 (Lo-16bits)
HR2976	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2977	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2978	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2979	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2980	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2981	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2982	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2983	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2984	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2985	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2986	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2987	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2988	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2989	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2990	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2991	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2992	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2993	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2994	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2995	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2996	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2997	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2998	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2999	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3000	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3001	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3002	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3003	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3004	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3005	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3006	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3007	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3008	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3009				HR3008 (Lo-16bits)
HR3010	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3011				HR3010 (Lo-16bits)
HR3012	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3013				HR3012 (Lo-16bits)
HR3014	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3015				HR3014 (Lo-16bits)
HR3016	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3017				HR3016 (Lo-16bits)
HR3018	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3019				HR3018 (Lo-16bits)
HR3020	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3021				HR3020 (Lo-16bits)
HR3022	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3023				HR3022 (Lo-16bits)
HR3024	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3025				HR3024 (Lo-16bits)
HR3026	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3027				HR3026 (Lo-16bits)
HR3028	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3029				HR3028 (Lo-16bits)
HR3030	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3031				HR3030 (Lo-16bits)
HR3032	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3033				HR3032 (Lo-16bits)
HR3034	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3035				HR3034 (Lo-16bits)
HR3036	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3037				HR3036 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3038	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3039				HR3038 (Lo-16bits)
HR3040	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3041				HR3040 (Lo-16bits)
HR3042	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3043				HR3042 (Lo-16bits)
HR3044	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3045				HR3044 (Lo-16bits)
HR3046	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3047				HR3046 (Lo-16bits)
HR3048	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3049				HR3048 (Lo-16bits)
HR3050	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3051				HR3050 (Lo-16bits)
HR3052	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3053				HR3052 (Lo-16bits)
HR3054	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3055				HR3054 (Lo-16bits)
HR3056	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3057				HR3056 (Lo-16bits)
HR3058	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3059				HR3058 (Lo-16bits)
HR3060	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3061				HR3060 (Lo-16bits)
HR3062	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3063				HR3062 (Lo-16bits)
HR3064	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3065				HR3064 (Lo-16bits)
HR3066	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3067				HR3066 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3068	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3069				HR3068 (Lo-16bits)
HR3070	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3071				HR3070 (Lo-16bits)
HR3072	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3073	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3074	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3075	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3076	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3077	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3078	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3079	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3080	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3081				HR3080 (Lo-16bits)
HR3082	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3083				HR3082 (Lo-16bits)
HR3084	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3085				HR3084 (Lo-16bits)
HR3086	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3087				HR3086 (Lo-16bits)
HR3088	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3089				HR3088 (Lo-16bits)
HR3090	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3091				HR3090 (Lo-16bits)
HR3092	DOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3093				HR3092 (Lo-16bits)
HR3094	DOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3095				HR3094 (Lo-16bits)
HR3096	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3097	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3098	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3099	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3100	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3101	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3102	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3103	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3104	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3105				HR3104 (Lo-16bits)
HR3106	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3107				HR3106 (Lo-16bits)
HR3108	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3109				HR3108 (Lo-16bits)
HR3110	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3111				HR3110 (Lo-16bits)
HR3112	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3113				HR3112 (Lo-16bits)
HR3114	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3115				HR3114 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3116	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3117				HR3116 (Lo-16bits)
HR3118	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3119				HR3118 (Lo-16bits)
HR3120	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3121	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3122	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3123	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3124	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3125	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3126	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3127	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3128	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3129	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3130	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3131	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3132	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3133	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3134	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3135	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3136	DOC3:ldir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3137	DOC3:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3138	DOC3:ldir>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3139	DOC3:ldir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	DOC3:ldir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)
HR3143	DOC3:ldir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	DOC3:ldir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)
HR3147	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3148	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3149	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3150	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3151	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3152	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3153	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3154	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3155	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3156				HR3155 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3157	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)
HR3165	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3181	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3204	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3205	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3206	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3207	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3208	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3209	DOC3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3210	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3211	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3228	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3229	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3230	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3231	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3232	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3233	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3234	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3235	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3236	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3237	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3238	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3239	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3240	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3241	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3242	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3243	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3244	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3245	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3246	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3247	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3248	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3249	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3250	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3251	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3252	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3253	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3254	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3255	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3256	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3257	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3258	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3259	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3285	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3315	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3324	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3325	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3326	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3327	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3328	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3329	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3330	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3331	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3339	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)
HR3347	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3348	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3349	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3350	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3351	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3352	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3353	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3354	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3355	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3363	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3372	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3373	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3374	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3375	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3376	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3377	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3378	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3379	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3380	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3381	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3382	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3383	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3384	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3385	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3386	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3387	DOC4:ldir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3388	DOC4:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3389	DOC4:ldir>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3390	DOC4:ldir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3391				HR3390 (Lo-16bits)
HR3392	DOC4:ldir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3393				HR3392 (Lo-16bits)
HR3394	DOC4:ldir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3395				HR3394 (Lo-16bits)
HR3396	DOC4:ldir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3397				HR3396 (Lo-16bits)
HR3398	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3399	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3400	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3401	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3402	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3403	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3404	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR3405	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3406	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3407				HR3406 (Lo-16bits)
HR3408	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3409				HR3408 (Lo-16bits)
HR3410	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3411				HR3410 (Lo-16bits)
HR3412	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3413				HR3412 (Lo-16bits)
HR3414	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3415				HR3414 (Lo-16bits)
HR3416	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3417				HR3416 (Lo-16bits)
HR3418	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3419				HR3418 (Lo-16bits)
HR3420	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3421				HR3420 (Lo-16bits)
HR3422	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3423				HR3422 (Lo-16bits)
HR3424	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3425				HR3424 (Lo-16bits)
HR3426	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3427				HR3426 (Lo-16bits)
HR3428	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3429				HR3428 (Lo-16bits)
HR3430	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3431				HR3430 (Lo-16bits)
HR3432	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3433				HR3432 (Lo-16bits)
HR3434	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3435				HR3434 (Lo-16bits)
HR3436	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3437				HR3436 (Lo-16bits)
HR3438	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3439				HR3438 (Lo-16bits)
HR3440	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3441				HR3440 (Lo-16bits)
HR3442	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3443				HR3442 (Lo-16bits)
HR3444	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3445				HR3444 (Lo-16bits)
HR3446	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3447				HR3446 (Lo-16bits)
HR3448	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3449				HR3448 (Lo-16bits)
HR3450	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3451				HR3450 (Lo-16bits)
HR3452	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3453				HR3452 (Lo-16bits)
HR3454	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3455	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3456	DOC4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3457	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3458	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3459	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3460	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3461	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3462	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3463				HR3462 (Lo-16bits)
HR3464	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3465				HR3464 (Lo-16bits)
HR3466	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3467				HR3466 (Lo-16bits)
HR3468	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3469				HR3468 (Lo-16bits)
HR3470	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3471				HR3470 (Lo-16bits)
HR3472	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3473				HR3472 (Lo-16bits)
HR3474	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3475				HR3474 (Lo-16bits)
HR3476	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3477				HR3476 (Lo-16bits)
HR3478	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3479	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3480	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3481	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3482	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3483	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3484	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3485	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3486	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3487	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3488	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3489	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3490	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3491	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3492	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3493	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3494	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3495	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3496	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3497	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3498	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3499	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3500	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3501	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3502	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3503	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3504	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3505	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3506	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3507	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3508	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3509	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3510	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3511				HR3510 (Lo-16bits)
HR3512	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3513				HR3512 (Lo-16bits)
HR3514	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3515				HR3514 (Lo-16bits)
HR3516	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3517				HR3516 (Lo-16bits)
HR3518	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3519				HR3518 (Lo-16bits)
HR3520	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3521				HR3520 (Lo-16bits)
HR3522	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3523				HR3522 (Lo-16bits)
HR3524	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3525				HR3524 (Lo-16bits)
HR3526	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3527				HR3526 (Lo-16bits)
HR3528	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3529				HR3528 (Lo-16bits)
HR3530	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3531				HR3530 (Lo-16bits)
HR3532	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3533				HR3532 (Lo-16bits)
HR3534	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3535				HR3534 (Lo-16bits)
HR3536	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3537				HR3536 (Lo-16bits)
HR3538	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3539				HR3538 (Lo-16bits)
HR3540	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3541				HR3540 (Lo-16bits)
HR3542	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3543				HR3542 (Lo-16bits)
HR3544	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3545				HR3544 (Lo-16bits)
HR3546	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3547				HR3546 (Lo-16bits)
HR3548	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3549				HR3548 (Lo-16bits)
HR3550	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3551				HR3550 (Lo-16bits)
HR3552	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3553				HR3552 (Lo-16bits)
HR3554	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3555				HR3554 (Lo-16bits)
HR3556	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3557				HR3556 (Lo-16bits)
HR3558	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3559				HR3558 (Lo-16bits)
HR3560	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3561				HR3560 (Lo-16bits)
HR3562	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3563				HR3562 (Lo-16bits)
HR3564	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3565				HR3564 (Lo-16bits)
HR3566	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3567				HR3566 (Lo-16bits)
HR3568	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3569				HR3568 (Lo-16bits)
HR3570	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3571				HR3570 (Lo-16bits)
HR3572	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3573				HR3572 (Lo-16bits)
HR3574	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3575	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3576	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3577	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3578	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3579	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3580	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3581	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3582	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3583				HR3582 (Lo-16bits)
HR3584	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3585				HR3584 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3586	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3587				HR3586 (Lo-16bits)
HR3588	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3589				HR3588 (Lo-16bits)
HR3590	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3591				HR3590 (Lo-16bits)
HR3592	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3593				HR3592 (Lo-16bits)
HR3594	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3595				HR3594 (Lo-16bits)
HR3596	DOC4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3597				HR3596 (Lo-16bits)
HR3598	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3599	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3600	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3601	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3602	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3603	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3604	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3605	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3606	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3607				HR3606 (Lo-16bits)
HR3608	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3609				HR3608 (Lo-16bits)
HR3610	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3611				HR3610 (Lo-16bits)
HR3612	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3613				HR3612 (Lo-16bits)
HR3614	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3615				HR3614 (Lo-16bits)
HR3616	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3617				HR3616 (Lo-16bits)
HR3618	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3619				HR3618 (Lo-16bits)
HR3620	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3621				HR3620 (Lo-16bits)
HR3622	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3623	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3624	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3625	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3626	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3627	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3628	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3629	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3630	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3631	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3632	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3633	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3634	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3635	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3636	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3637	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3638	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3639	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3640	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3641	NEF1:I0> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3642	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3643				HR3642 (Lo-16bits)
HR3644	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3645				HR3644 (Lo-16bits)
HR3646	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3647				HR3646 (Lo-16bits)
HR3648	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3649				HR3648 (Lo-16bits)
HR3650	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3651				HR3650 (Lo-16bits)
HR3652	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3653				HR3652 (Lo-16bits)
HR3654	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3655				HR3654 (Lo-16bits)
HR3656	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3657				HR3656 (Lo-16bits)
HR3658	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3659				HR3658 (Lo-16bits)
HR3660	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3661	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3662	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3663	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3664	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3665	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3666	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3667	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3668	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3669				HR3668 (Lo-16bits)
HR3670	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3671				HR3670 (Lo-16bits)
HR3672	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3673				HR3672 (Lo-16bits)
HR3674	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3675				HR3674 (Lo-16bits)
HR3676	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3677				HR3676 (Lo-16bits)
HR3678	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3679				HR3678 (Lo-16bits)
HR3680	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3681				HR3680 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3682	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3683				HR3682 (Lo-16bits)
HR3684	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3685	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3686	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3687	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3688	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3689	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3690	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3691	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3692	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3693	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3694	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3695	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3696	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3697	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3698	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3699	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3700	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3701	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3702	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3703	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3704	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3705	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3706	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3707	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3708	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3709	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3710	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3711	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3712	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3713	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3714	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3715	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3716	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3717				HR3716 (Lo-16bits)
HR3718	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3719				HR3718 (Lo-16bits)
HR3720	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3721				HR3720 (Lo-16bits)
HR3722	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3723				HR3722 (Lo-16bits)
HR3724	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3725				HR3724 (Lo-16bits)
HR3726	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3727				HR3726 (Lo-16bits)
HR3728	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3729				HR3728 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3730	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3731				HR3730 (Lo-16bits)
HR3732	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3733				HR3732 (Lo-16bits)
HR3734	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3735				HR3734 (Lo-16bits)
HR3736	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3737				HR3736 (Lo-16bits)
HR3738	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3739				HR3738 (Lo-16bits)
HR3740	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3741				HR3740 (Lo-16bits)
HR3742	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3743				HR3742 (Lo-16bits)
HR3744	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3745				HR3744 (Lo-16bits)
HR3746	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3747				HR3746 (Lo-16bits)
HR3748	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3749				HR3748 (Lo-16bits)
HR3750	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3751				HR3750 (Lo-16bits)
HR3752	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3753				HR3752 (Lo-16bits)
HR3754	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3755				HR3754 (Lo-16bits)
HR3756	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3757				HR3756 (Lo-16bits)
HR3758	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3759				HR3758 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3760	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3761				HR3760 (Lo-16bits)
HR3762	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3763				HR3762 (Lo-16bits)
HR3764	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3765				HR3764 (Lo-16bits)
HR3766	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3767				HR3766 (Lo-16bits)
HR3768	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3769				HR3768 (Lo-16bits)
HR3770	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3771				HR3770 (Lo-16bits)
HR3772	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3773				HR3772 (Lo-16bits)
HR3774	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3775				HR3774 (Lo-16bits)
HR3776	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3777				HR3776 (Lo-16bits)
HR3778	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3779				HR3778 (Lo-16bits)
HR3780	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3781	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3782	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3783	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3784	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3785	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3786	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3787	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3788	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3789				HR3788 (Lo-16bits)
HR3790	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3791				HR3790 (Lo-16bits)
HR3792	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3793				HR3792 (Lo-16bits)
HR3794	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3795				HR3794 (Lo-16bits)
HR3796	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3797				HR3796 (Lo-16bits)
HR3798	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3799				HR3798 (Lo-16bits)
HR3800	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3801				HR3800 (Lo-16bits)
HR3802	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3803				HR3802 (Lo-16bits)
HR3804	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3805	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3806	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3807	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3808	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3809	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3810	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3811	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3812	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3813				HR3812 (Lo-16bits)
HR3814	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3815				HR3814 (Lo-16bits)
HR3816	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3817				HR3816 (Lo-16bits)
HR3818	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3819				HR3818 (Lo-16bits)
HR3820	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3821				HR3820 (Lo-16bits)
HR3822	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3823				HR3822 (Lo-16bits)
HR3824	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3825				HR3824 (Lo-16bits)
HR3826	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3827				HR3826 (Lo-16bits)
HR3828	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3829	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3830	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3831	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3832	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3833	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3834	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3835	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3836	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3837	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3838	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3839	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3840	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3841	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3842	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3843	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3844	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR3845				HR3844 (Lo-16bits)
HR3846	NEF1:Delay type	RW	1	0=DT 1=IDMT
HR3847	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	NEF1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR3850	NEF1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3851	NEF1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3852	NEF1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR3853	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3855	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	NEF1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR3862	NEF1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR3863	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3864	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3865	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3866	NEF2:I0>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3867	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3882				HR3881 (Lo-16bits)
HR3883	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3886	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3887	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3888	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3889	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3890	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3891	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3892	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3893	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3908				HR3907 (Lo-16bits)
HR3909	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3910	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3911	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3912	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3913	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3914	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3915	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3916	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3917	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3918	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3919	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3920	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3921	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3922	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3923	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3924	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3925	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3926	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3927	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3928	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3929	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3930	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3931	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3932	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3933	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3934	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3935	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3936	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3937	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3938	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3939	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3940	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3941	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3956				HR3955 (Lo-16bits)
HR3957	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)
HR3965	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3966				HR3965 (Lo-16bits)
HR3967	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3968				HR3967 (Lo-16bits)
HR3969	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3970				HR3969 (Lo-16bits)
HR3971	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3972				HR3971 (Lo-16bits)
HR3973	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3974				HR3973 (Lo-16bits)
HR3975	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3976				HR3975 (Lo-16bits)
HR3977	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3978				HR3977 (Lo-16bits)
HR3979	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3980				HR3979 (Lo-16bits)
HR3981	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3986				HR3985 (Lo-16bits)
HR3987	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4006	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4007	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4008	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4009	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4010	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4011	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4012	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4013	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4030	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4031	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4032	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4033	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4034	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4035	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4036	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4037	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4054	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4055	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4056	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4057	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4058	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4059	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4060	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4061	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4062	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4063	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4064	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4065	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4066	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4067	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4068	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4069	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4070				HR4069 (Lo-16bits)
HR4071	NEF2:Delay type	RW	1	0=DT 1=IDMT
HR4072	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4073				HR4072 (Lo-16bits)
HR4074	NEF2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4075	NEF2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4076	NEF2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4077	NEF2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4078	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4079				HR4078 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4080	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4081				HR4080 (Lo-16bits)
HR4082	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4083				HR4082 (Lo-16bits)
HR4084	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4085				HR4084 (Lo-16bits)
HR4086	NEF2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4087	NEF2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4088	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4089	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4090	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4091	NEF3:I0>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4092	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4093				HR4092 (Lo-16bits)
HR4094	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4095				HR4094 (Lo-16bits)
HR4096	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4097				HR4096 (Lo-16bits)
HR4098	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4099				HR4098 (Lo-16bits)
HR4100	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4101				HR4100 (Lo-16bits)
HR4102	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4103				HR4102 (Lo-16bits)
HR4104	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4105				HR4104 (Lo-16bits)
HR4106	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4107				HR4106 (Lo-16bits)
HR4108	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4109				HR4108 (Lo-16bits)
HR4110	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4111	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4112	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4113	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4114	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4115	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4116	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4117	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4118	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4119				HR4118 (Lo-16bits)
HR4120	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4121				HR4120 (Lo-16bits)
HR4122	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4123				HR4122 (Lo-16bits)
HR4124	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4125				HR4124 (Lo-16bits)
HR4126	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4127				HR4126 (Lo-16bits)
HR4128	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4129				HR4128 (Lo-16bits)
HR4130	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4131				HR4130 (Lo-16bits)
HR4132	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4133				HR4132 (Lo-16bits)
HR4134	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4135	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4136	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4137	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4138	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4139	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4140	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4141	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4142	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4143	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4144	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4145	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4146	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4147	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4148	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4149	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4150	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4151	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4152	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4153	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4154	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4155	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4156	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4157	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4158	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4159	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4160	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4161	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4162	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4163	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4164	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4165	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4166	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4167				HR4166 (Lo-16bits)
HR4168	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4169				HR4168 (Lo-16bits)
HR4170	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4171				HR4170 (Lo-16bits)
HR4172	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4173				HR4172 (Lo-16bits)
HR4174	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4175				HR4174 (Lo-16bits)
HR4176	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4177				HR4176 (Lo-16bits)
HR4178	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4179				HR4178 (Lo-16bits)
HR4180	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4181				HR4180 (Lo-16bits)
HR4182	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4183				HR4182 (Lo-16bits)
HR4184	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4185				HR4184 (Lo-16bits)
HR4186	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4187				HR4186 (Lo-16bits)
HR4188	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4189				HR4188 (Lo-16bits)
HR4190	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4191				HR4190 (Lo-16bits)
HR4192	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4193				HR4192 (Lo-16bits)
HR4194	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4195				HR4194 (Lo-16bits)
HR4196	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4197				HR4196 (Lo-16bits)
HR4198	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4199				HR4198 (Lo-16bits)
HR4200	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4201				HR4200 (Lo-16bits)
HR4202	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4203				HR4202 (Lo-16bits)
HR4204	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4205				HR4204 (Lo-16bits)
HR4206	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4207				HR4206 (Lo-16bits)
HR4208	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4209				HR4208 (Lo-16bits)
HR4210	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4211				HR4210 (Lo-16bits)
HR4212	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4213				HR4212 (Lo-16bits)
HR4214	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4215				HR4214 (Lo-16bits)
HR4216	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4217				HR4216 (Lo-16bits)
HR4218	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4219				HR4218 (Lo-16bits)
HR4220	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4221				HR4220 (Lo-16bits)
HR4222	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4223				HR4222 (Lo-16bits)
HR4224	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4225				HR4224 (Lo-16bits)
HR4226	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4227				HR4226 (Lo-16bits)
HR4228	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4229				HR4228 (Lo-16bits)
HR4230	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4231	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4232	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4233	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4234	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4235	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4236	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4237	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4238	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4239				HR4238 (Lo-16bits)
HR4240	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4241				HR4240 (Lo-16bits)
HR4242	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4243				HR4242 (Lo-16bits)
HR4244	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4245				HR4244 (Lo-16bits)
HR4246	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4247				HR4246 (Lo-16bits)
HR4248	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4249				HR4248 (Lo-16bits)
HR4250	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4251				HR4250 (Lo-16bits)
HR4252	NEF3:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4253				HR4252 (Lo-16bits)
HR4254	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4255	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4256	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4257	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4258	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4259	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4260	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4261	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4262	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4263				HR4262 (Lo-16bits)
HR4264	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4265				HR4264 (Lo-16bits)
HR4266	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4267				HR4266 (Lo-16bits)
HR4268	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4269				HR4268 (Lo-16bits)
HR4270	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4271				HR4270 (Lo-16bits)
HR4272	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4273				HR4272 (Lo-16bits)
HR4274	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4275				HR4274 (Lo-16bits)
HR4276	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4277				HR4276 (Lo-16bits)
HR4278	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4279	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4280	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4281	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4282	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4283	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4284	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4285	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4286	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4287	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4288	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4289	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4290	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4291	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4292	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4293	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4294	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4295				HR4294 (Lo-16bits)
HR4296	NEF3:Delay type	RW	1	0=DT 1=IDMT
HR4297	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	NEF3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4300	NEF3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4301	NEF3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4302	NEF3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4303	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4305	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4310				HR4309 (Lo-16bits)
HR4311	NEF3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4312	NEF3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4313	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4314	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4315	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4316	NEF4:I0>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4317	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4332				HR4331 (Lo-16bits)
HR4333	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4336	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4337	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4338	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4339	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4340	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4341	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4342	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4343	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4358				HR4357 (Lo-16bits)
HR4359	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4360	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4361	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4362	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4363	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4364	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4365	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4366	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4367	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4368	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4369	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4370	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4371	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4372	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4373	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4374	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4375	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4376	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4377	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4378	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4379	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4380	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4381	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4382	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4383	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4384	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4385	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4386	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4387	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4388	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4389	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4390	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4391	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4394				HR4393 (Lo-16bits)
HR4395	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4396				HR4395 (Lo-16bits)
HR4397	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4406				HR4405 (Lo-16bits)
HR4407	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)
HR4415	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4418				HR4417 (Lo-16bits)
HR4419	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4420				HR4419 (Lo-16bits)
HR4421	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4436				HR4435 (Lo-16bits)
HR4437	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)
HR4445	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4456	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4457	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4458	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4459	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4460	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4461	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4462	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4463	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4480	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4481	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4482	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4483	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4484	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4485	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4486	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4487	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)
HR4493	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)
HR4503	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4504	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4505	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4506	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4507	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4508	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4509	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4510	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4511	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4512	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4513	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4514	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4515	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4516	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4517	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4518	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4519	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF4:Delay type	RW	1	0=DT 1=IDMT
HR4522	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4523				HR4522 (Lo-16bits)
HR4524	NEF4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4525	NEF4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4526	NEF4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4527	NEF4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4528	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4529				HR4528 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4530	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4531				HR4530 (Lo-16bits)
HR4532	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4533				HR4532 (Lo-16bits)
HR4534	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4535				HR4534 (Lo-16bits)
HR4536	NEF4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4537	NEF4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4538	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR4539	DEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4540	DEF1:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4541	DEF1:I0dir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4542	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4543				HR4542 (Lo-16bits)
HR4544	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4545				HR4544 (Lo-16bits)
HR4546	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4547				HR4546 (Lo-16bits)
HR4548	DEF1:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4549	DEF1:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4550	DEF1:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4551	DEF1:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR4552	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4553	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4554	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4555	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4556	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4557				HR4556 (Lo-16bits)
HR4558	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4559				HR4558 (Lo-16bits)
HR4560	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4561				HR4560 (Lo-16bits)
HR4562	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4563				HR4562 (Lo-16bits)
HR4564	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4565				HR4564 (Lo-16bits)
HR4566	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4567				HR4566 (Lo-16bits)
HR4568	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4569				HR4568 (Lo-16bits)
HR4570	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4571				HR4570 (Lo-16bits)
HR4572	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4573				HR4572 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4574	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4575				HR4574 (Lo-16bits)
HR4576	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4577				HR4576 (Lo-16bits)
HR4578	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4579				HR4578 (Lo-16bits)
HR4580	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4581				HR4580 (Lo-16bits)
HR4582	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4583				HR4582 (Lo-16bits)
HR4584	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4585				HR4584 (Lo-16bits)
HR4586	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4587				HR4586 (Lo-16bits)
HR4588	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4589				HR4588 (Lo-16bits)
HR4590	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4591				HR4590 (Lo-16bits)
HR4592	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4593				HR4592 (Lo-16bits)
HR4594	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4595				HR4594 (Lo-16bits)
HR4596	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4597				HR4596 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4598	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4599				HR4598 (Lo-16bits)
HR4600	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4601				HR4600 (Lo-16bits)
HR4602	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4603				HR4602 (Lo-16bits)
HR4604	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4605				HR4604 (Lo-16bits)
HR4606	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4607				HR4606 (Lo-16bits)
HR4608	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4609				HR4608 (Lo-16bits)
HR4610	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4611				HR4610 (Lo-16bits)
HR4612	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4613				HR4612 (Lo-16bits)
HR4614	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4615				HR4614 (Lo-16bits)
HR4616	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4617				HR4616 (Lo-16bits)
HR4618	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4619				HR4618 (Lo-16bits)
HR4620	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4621				HR4620 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4622	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4623				HR4622 (Lo-16bits)
HR4624	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4625				HR4624 (Lo-16bits)
HR4626	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4627				HR4626 (Lo-16bits)
HR4628	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4629				HR4628 (Lo-16bits)
HR4630	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4631				HR4630 (Lo-16bits)
HR4632	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4633				HR4632 (Lo-16bits)
HR4634	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4635				HR4634 (Lo-16bits)
HR4636	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4637				HR4636 (Lo-16bits)
HR4638	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4639				HR4638 (Lo-16bits)
HR4640	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4641				HR4640 (Lo-16bits)
HR4642	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4643				HR4642 (Lo-16bits)
HR4644	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4645				HR4644 (Lo-16bits)
HR4646	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4647				HR4646 (Lo-16bits)
HR4648	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4649				HR4648 (Lo-16bits)
HR4650	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4651				HR4650 (Lo-16bits)
HR4652	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4653	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4654	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4655	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4656	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4657	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4658	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4659	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4660	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4661				HR4660 (Lo-16bits)
HR4662	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4663				HR4662 (Lo-16bits)
HR4664	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4665				HR4664 (Lo-16bits)
HR4666	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4667				HR4666 (Lo-16bits)
HR4668	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4669				HR4668 (Lo-16bits)
HR4670	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4671				HR4670 (Lo-16bits)
HR4672	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4673				HR4672 (Lo-16bits)
HR4674	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4675				HR4674 (Lo-16bits)
HR4676	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4677				HR4676 (Lo-16bits)
HR4678	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4679				HR4678 (Lo-16bits)
HR4680	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4681				HR4680 (Lo-16bits)
HR4682	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4683				HR4682 (Lo-16bits)
HR4684	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4685				HR4684 (Lo-16bits)
HR4686	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4687				HR4686 (Lo-16bits)
HR4688	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4689				HR4688 (Lo-16bits)
HR4690	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4691				HR4690 (Lo-16bits)
HR4692	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4693				HR4692 (Lo-16bits)
HR4694	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4695				HR4694 (Lo-16bits)
HR4696	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4697				HR4696 (Lo-16bits)
HR4698	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4699				HR4698 (Lo-16bits)
HR4700	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4701				HR4700 (Lo-16bits)
HR4702	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4703				HR4702 (Lo-16bits)
HR4704	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4705				HR4704 (Lo-16bits)
HR4706	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4707				HR4706 (Lo-16bits)
HR4708	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4709				HR4708 (Lo-16bits)
HR4710	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4711				HR4710 (Lo-16bits)
HR4712	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4713				HR4712 (Lo-16bits)
HR4714	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4715				HR4714 (Lo-16bits)
HR4716	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4717				HR4716 (Lo-16bits)
HR4718	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4719				HR4718 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4720	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4721				HR4720 (Lo-16bits)
HR4722	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4723				HR4722 (Lo-16bits)
HR4724	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4725				HR4724 (Lo-16bits)
HR4726	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4727				HR4726 (Lo-16bits)
HR4728	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4729				HR4728 (Lo-16bits)
HR4730	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4731				HR4730 (Lo-16bits)
HR4732	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4733				HR4732 (Lo-16bits)
HR4734	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4735				HR4734 (Lo-16bits)
HR4736	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4737				HR4736 (Lo-16bits)
HR4738	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4739				HR4738 (Lo-16bits)
HR4740	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4741	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4742	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4743	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4744	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4745	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4746	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	Info
HR4747	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4748	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4749	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4750	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4751	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4752	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4753	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4754	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4755	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4756	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4757				HR4756 (Lo-16bits)
HR4758	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4759				HR4758 (Lo-16bits)
HR4760	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4761				HR4760 (Lo-16bits)
HR4762	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4763				HR4762 (Lo-16bits)
HR4764	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4765				HR4764 (Lo-16bits)
HR4766	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4767				HR4766 (Lo-16bits)
HR4768	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4769				HR4768 (Lo-16bits)
HR4770	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4771				HR4770 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4772	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4773				HR4772 (Lo-16bits)
HR4774	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4775				HR4774 (Lo-16bits)
HR4776	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4777				HR4776 (Lo-16bits)
HR4778	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4779				HR4778 (Lo-16bits)
HR4780	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4781				HR4780 (Lo-16bits)
HR4782	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4783				HR4782 (Lo-16bits)
HR4784	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4785				HR4784 (Lo-16bits)
HR4786	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4787				HR4786 (Lo-16bits)
HR4788	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4789				HR4788 (Lo-16bits)
HR4790	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4791				HR4790 (Lo-16bits)
HR4792	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4793				HR4792 (Lo-16bits)
HR4794	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4795				HR4794 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4796	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4797				HR4796 (Lo-16bits)
HR4798	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4799				HR4798 (Lo-16bits)
HR4800	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4801				HR4800 (Lo-16bits)
HR4802	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4803				HR4802 (Lo-16bits)
HR4804	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4805	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4806	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4807	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4808	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4809	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4810	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4811	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4812	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4813	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4814	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4815	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4816	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4817	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4818	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4819	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR4820	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4821	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4822	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4823	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4824	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4825	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4826	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4827	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4828	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4829	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4830	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4831	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4832	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4833	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4834	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4835	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4836	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4837				HR4836 (Lo-16bits)
HR4838	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4839				HR4838 (Lo-16bits)
HR4840	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4841				HR4840 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4842	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4843				HR4842 (Lo-16bits)
HR4844	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4845				HR4844 (Lo-16bits)
HR4846	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4847				HR4846 (Lo-16bits)
HR4848	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4849				HR4848 (Lo-16bits)
HR4850	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4851				HR4850 (Lo-16bits)
HR4852	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4853				HR4852 (Lo-16bits)
HR4854	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4855				HR4854 (Lo-16bits)
HR4856	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4857				HR4856 (Lo-16bits)
HR4858	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4859				HR4858 (Lo-16bits)
HR4860	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4861				HR4860 (Lo-16bits)
HR4862	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4863				HR4862 (Lo-16bits)
HR4864	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4865				HR4864 (Lo-16bits)
HR4866	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4867				HR4866 (Lo-16bits)
HR4868	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4869				HR4868 (Lo-16bits)
HR4870	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4871				HR4870 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4872	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4873				HR4872 (Lo-16bits)
HR4874	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4875				HR4874 (Lo-16bits)
HR4876	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4877				HR4876 (Lo-16bits)
HR4878	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4879				HR4878 (Lo-16bits)
HR4880	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4881				HR4880 (Lo-16bits)
HR4882	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4883				HR4882 (Lo-16bits)
HR4884	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4885				HR4884 (Lo-16bits)
HR4886	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4887				HR4886 (Lo-16bits)
HR4888	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4889				HR4888 (Lo-16bits)
HR4890	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4891				HR4890 (Lo-16bits)
HR4892	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4893				HR4892 (Lo-16bits)
HR4894	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4895				HR4894 (Lo-16bits)
HR4896	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4897				HR4896 (Lo-16bits)
HR4898	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4899				HR4898 (Lo-16bits)
HR4900	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4901	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4902	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4903	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4904	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4905	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4906	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4907	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4908	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4909				HR4908 (Lo-16bits)
HR4910	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4911				HR4910 (Lo-16bits)
HR4912	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4913				HR4912 (Lo-16bits)
HR4914	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4915				HR4914 (Lo-16bits)
HR4916	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4917				HR4916 (Lo-16bits)
HR4918	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4919				HR4918 (Lo-16bits)
HR4920	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4921				HR4920 (Lo-16bits)
HR4922	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4923				HR4922 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4924	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4925	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4926	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4927	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4928	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4929	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4930	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4931	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4932	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4933				HR4932 (Lo-16bits)
HR4934	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4935				HR4934 (Lo-16bits)
HR4936	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4937				HR4936 (Lo-16bits)
HR4938	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4939				HR4938 (Lo-16bits)
HR4940	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4941				HR4940 (Lo-16bits)
HR4942	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4943				HR4942 (Lo-16bits)
HR4944	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4945				HR4944 (Lo-16bits)
HR4946	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4947				HR4946 (Lo-16bits)
HR4948	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4949	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4950	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4951	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4952	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4953	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4954	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4955	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4956	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4957	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4958	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4959	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4960	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4961	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4962	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4963	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4964	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4965	DEF2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4966	DEF2:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined

Holding Register	Parameter	Access	Length	Info
HR4967	DEF2:I0dir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4968	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4969				HR4968 (Lo-16bits)
HR4970	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4971				HR4970 (Lo-16bits)
HR4972	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4973				HR4972 (Lo-16bits)
HR4974	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4975	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4976	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4977	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4978	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4979	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4980	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4981	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4982	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4983				HR4982 (Lo-16bits)
HR4984	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4985				HR4984 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4986	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4987				HR4986 (Lo-16bits)
HR4988	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4989				HR4988 (Lo-16bits)
HR4990	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4991				HR4990 (Lo-16bits)
HR4992	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4993				HR4992 (Lo-16bits)
HR4994	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4995				HR4994 (Lo-16bits)
HR4996	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4997				HR4996 (Lo-16bits)
HR4998	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4999				HR4998 (Lo-16bits)
HR5000	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5001				HR5000 (Lo-16bits)
HR5002	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5003				HR5002 (Lo-16bits)
HR5004	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5005				HR5004 (Lo-16bits)
HR5006	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5007				HR5006 (Lo-16bits)
HR5008	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5009				HR5008 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5010	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5011				HR5010 (Lo-16bits)
HR5012	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5013				HR5012 (Lo-16bits)
HR5014	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5015				HR5014 (Lo-16bits)
HR5016	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5017				HR5016 (Lo-16bits)
HR5018	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5019				HR5018 (Lo-16bits)
HR5020	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5021				HR5020 (Lo-16bits)
HR5022	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5023				HR5022 (Lo-16bits)
HR5024	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5025				HR5024 (Lo-16bits)
HR5026	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5027				HR5026 (Lo-16bits)
HR5028	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5029				HR5028 (Lo-16bits)
HR5030	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5031				HR5030 (Lo-16bits)
HR5032	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5033				HR5032 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5034	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5035				HR5034 (Lo-16bits)
HR5036	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5037				HR5036 (Lo-16bits)
HR5038	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5039				HR5038 (Lo-16bits)
HR5040	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5041				HR5040 (Lo-16bits)
HR5042	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5043				HR5042 (Lo-16bits)
HR5044	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5045				HR5044 (Lo-16bits)
HR5046	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5047				HR5046 (Lo-16bits)
HR5048	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5049				HR5048 (Lo-16bits)
HR5050	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5051				HR5050 (Lo-16bits)
HR5052	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5053				HR5052 (Lo-16bits)
HR5054	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5055				HR5054 (Lo-16bits)
HR5056	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5057				HR5056 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5058	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5059				HR5058 (Lo-16bits)
HR5060	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5061				HR5060 (Lo-16bits)
HR5062	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5063				HR5062 (Lo-16bits)
HR5064	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5065				HR5064 (Lo-16bits)
HR5066	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5067				HR5066 (Lo-16bits)
HR5068	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5069				HR5068 (Lo-16bits)
HR5070	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5071				HR5070 (Lo-16bits)
HR5072	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5073				HR5072 (Lo-16bits)
HR5074	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5075				HR5074 (Lo-16bits)
HR5076	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5077				HR5076 (Lo-16bits)
HR5078	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5079	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5080	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used

Holding Register	Parameter	Access	Length	Info
HR5081	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5082	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5083	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5084	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5085	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5086	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5087				HR5086 (Lo-16bits)
HR5088	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5089				HR5088 (Lo-16bits)
HR5090	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5091				HR5090 (Lo-16bits)
HR5092	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5093				HR5092 (Lo-16bits)
HR5094	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5095				HR5094 (Lo-16bits)
HR5096	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5097				HR5096 (Lo-16bits)
HR5098	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5099				HR5098 (Lo-16bits)
HR5100	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5101				HR5100 (Lo-16bits)
HR5102	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5103				HR5102 (Lo-16bits)
HR5104	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5105				HR5104 (Lo-16bits)
HR5106	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5107				HR5106 (Lo-16bits)
HR5108	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5109				HR5108 (Lo-16bits)
HR5110	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5111				HR5110 (Lo-16bits)
HR5112	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5113				HR5112 (Lo-16bits)
HR5114	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5115				HR5114 (Lo-16bits)
HR5116	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5117				HR5116 (Lo-16bits)
HR5118	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5119				HR5118 (Lo-16bits)
HR5120	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5121				HR5120 (Lo-16bits)
HR5122	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5123				HR5122 (Lo-16bits)
HR5124	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5125				HR5124 (Lo-16bits)
HR5126	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5127				HR5126 (Lo-16bits)
HR5128	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5129				HR5128 (Lo-16bits)
HR5130	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5131				HR5130 (Lo-16bits)
HR5132	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5133				HR5132 (Lo-16bits)
HR5134	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5135				HR5134 (Lo-16bits)
HR5136	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5137				HR5136 (Lo-16bits)
HR5138	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5139				HR5138 (Lo-16bits)
HR5140	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5141				HR5140 (Lo-16bits)
HR5142	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5143				HR5142 (Lo-16bits)
HR5144	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5145				HR5144 (Lo-16bits)
HR5146	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5147				HR5146 (Lo-16bits)
HR5148	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5149				HR5148 (Lo-16bits)
HR5150	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5151				HR5150 (Lo-16bits)
HR5152	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5153				HR5152 (Lo-16bits)
HR5154	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5155				HR5154 (Lo-16bits)
HR5156	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5157				HR5156 (Lo-16bits)
HR5158	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5159				HR5158 (Lo-16bits)
HR5160	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5161				HR5160 (Lo-16bits)
HR5162	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5163				HR5162 (Lo-16bits)
HR5164	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5165				HR5164 (Lo-16bits)
HR5166	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5167	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5168	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5169	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5170	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5171	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5172	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5173	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5174	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5175	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5176	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5177	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5178	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5179	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5180	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5181	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5182	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5183				HR5182 (Lo-16bits)
HR5184	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5185				HR5184 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5186	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5187				HR5186 (Lo-16bits)
HR5188	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5189				HR5188 (Lo-16bits)
HR5190	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5191				HR5190 (Lo-16bits)
HR5192	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5193				HR5192 (Lo-16bits)
HR5194	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5195				HR5194 (Lo-16bits)
HR5196	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5197				HR5196 (Lo-16bits)
HR5198	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5199				HR5198 (Lo-16bits)
HR5200	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5201				HR5200 (Lo-16bits)
HR5202	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5203				HR5202 (Lo-16bits)
HR5204	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5205				HR5204 (Lo-16bits)
HR5206	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5207				HR5206 (Lo-16bits)
HR5208	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5209				HR5208 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5210	DEF2:Definite operating time delay(Uneearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5211				HR5210 (Lo-16bits)
HR5212	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5213				HR5212 (Lo-16bits)
HR5214	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5215				HR5214 (Lo-16bits)
HR5216	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5217				HR5216 (Lo-16bits)
HR5218	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5219				HR5218 (Lo-16bits)
HR5220	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5221				HR5220 (Lo-16bits)
HR5222	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5223				HR5222 (Lo-16bits)
HR5224	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5225				HR5224 (Lo-16bits)
HR5226	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5227				HR5226 (Lo-16bits)
HR5228	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5229				HR5228 (Lo-16bits)
HR5230	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5231	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5232	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5233	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5234	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5235	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5236	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5237	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5238	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5239	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5240	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5241	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5242	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5243	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5244	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5245	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5246	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5247	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5248	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5249	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5250	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5251	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5252	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5253	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5254	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5255	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR5256	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5257	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5258	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5259	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5260	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5261	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5262	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5263				HR5262 (Lo-16bits)
HR5264	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5265				HR5264 (Lo-16bits)
HR5266	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5267				HR5266 (Lo-16bits)
HR5268	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5269				HR5268 (Lo-16bits)
HR5270	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5271				HR5270 (Lo-16bits)
HR5272	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5273				HR5272 (Lo-16bits)
HR5274	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5275				HR5274 (Lo-16bits)
HR5276	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5277				HR5276 (Lo-16bits)
HR5278	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5279				HR5278 (Lo-16bits)
HR5280	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5281				HR5280 (Lo-16bits)
HR5282	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5283				HR5282 (Lo-16bits)
HR5284	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5285				HR5284 (Lo-16bits)
HR5286	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5287				HR5286 (Lo-16bits)
HR5288	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5289				HR5288 (Lo-16bits)
HR5290	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5291				HR5290 (Lo-16bits)
HR5292	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5293				HR5292 (Lo-16bits)
HR5294	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5295				HR5294 (Lo-16bits)
HR5296	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5297				HR5296 (Lo-16bits)
HR5298	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5299				HR5298 (Lo-16bits)
HR5300	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5301				HR5300 (Lo-16bits)
HR5302	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5303				HR5302 (Lo-16bits)
HR5304	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5305				HR5304 (Lo-16bits)
HR5306	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5307				HR5306 (Lo-16bits)
HR5308	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5309				HR5308 (Lo-16bits)
HR5310	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5311				HR5310 (Lo-16bits)
HR5312	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5313				HR5312 (Lo-16bits)
HR5314	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5315				HR5314 (Lo-16bits)
HR5316	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5317				HR5316 (Lo-16bits)
HR5318	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5319				HR5318 (Lo-16bits)
HR5320	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5321				HR5320 (Lo-16bits)
HR5322	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5323				HR5322 (Lo-16bits)
HR5324	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5325				HR5324 (Lo-16bits)
HR5326	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5327	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5328	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5329	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5330	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5331	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5332	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5333	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5334	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5335				HR5334 (Lo-16bits)
HR5336	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5337				HR5336 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5338	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5339				HR5338 (Lo-16bits)
HR5340	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5341				HR5340 (Lo-16bits)
HR5342	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5343				HR5342 (Lo-16bits)
HR5344	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5345				HR5344 (Lo-16bits)
HR5346	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5347				HR5346 (Lo-16bits)
HR5348	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5349				HR5348 (Lo-16bits)
HR5350	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5351	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5352	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5353	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5354	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5355	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5356	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5357	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5358	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5359				HR5358 (Lo-16bits)
HR5360	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5361				HR5360 (Lo-16bits)
HR5362	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5363				HR5362 (Lo-16bits)
HR5364	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5365				HR5364 (Lo-16bits)
HR5366	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5367				HR5366 (Lo-16bits)
HR5368	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5369				HR5368 (Lo-16bits)
HR5370	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5371				HR5370 (Lo-16bits)
HR5372	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5373				HR5372 (Lo-16bits)
HR5374	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5375	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5376	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5377	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5378	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5379	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5380	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5381	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5382	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5383	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5384	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5385	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5386	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5387	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5388	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5389	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5390	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5391	DEF3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5392	DEF3:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5393	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5394	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5395				HR5394 (Lo-16bits)
HR5396	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5397				HR5396 (Lo-16bits)
HR5398	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5399				HR5398 (Lo-16bits)
HR5400	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5401	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5402	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5403	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5404	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5405	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5406	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5407	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5408	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5409				HR5408 (Lo-16bits)
HR5410	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5411				HR5410 (Lo-16bits)
HR5412	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5413				HR5412 (Lo-16bits)
HR5414	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5415				HR5414 (Lo-16bits)
HR5416	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5417				HR5416 (Lo-16bits)
HR5418	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5419				HR5418 (Lo-16bits)
HR5420	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5421				HR5420 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5422	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5423				HR5422 (Lo-16bits)
HR5424	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5425				HR5424 (Lo-16bits)
HR5426	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5427				HR5426 (Lo-16bits)
HR5428	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5429				HR5428 (Lo-16bits)
HR5430	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5431				HR5430 (Lo-16bits)
HR5432	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5433				HR5432 (Lo-16bits)
HR5434	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5435				HR5434 (Lo-16bits)
HR5436	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5437				HR5436 (Lo-16bits)
HR5438	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5439				HR5438 (Lo-16bits)
HR5440	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5441				HR5440 (Lo-16bits)
HR5442	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5443				HR5442 (Lo-16bits)
HR5444	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5445				HR5444 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5446	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5447				HR5446 (Lo-16bits)
HR5448	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5449				HR5448 (Lo-16bits)
HR5450	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5451				HR5450 (Lo-16bits)
HR5452	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5453				HR5452 (Lo-16bits)
HR5454	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5455				HR5454 (Lo-16bits)
HR5456	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5457				HR5456 (Lo-16bits)
HR5458	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5459				HR5458 (Lo-16bits)
HR5460	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5461				HR5460 (Lo-16bits)
HR5462	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5463				HR5462 (Lo-16bits)
HR5464	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5465				HR5464 (Lo-16bits)
HR5466	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5467				HR5466 (Lo-16bits)
HR5468	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5469				HR5468 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5470	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5471				HR5470 (Lo-16bits)
HR5472	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5473				HR5472 (Lo-16bits)
HR5474	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5475				HR5474 (Lo-16bits)
HR5476	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5477				HR5476 (Lo-16bits)
HR5478	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5479				HR5478 (Lo-16bits)
HR5480	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5481				HR5480 (Lo-16bits)
HR5482	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5483				HR5482 (Lo-16bits)
HR5484	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5485				HR5484 (Lo-16bits)
HR5486	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5487				HR5486 (Lo-16bits)
HR5488	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5489				HR5488 (Lo-16bits)
HR5490	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5491				HR5490 (Lo-16bits)
HR5492	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5493				HR5492 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5494	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5495				HR5494 (Lo-16bits)
HR5496	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5497				HR5496 (Lo-16bits)
HR5498	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5499				HR5498 (Lo-16bits)
HR5500	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5501				HR5500 (Lo-16bits)
HR5502	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5503				HR5502 (Lo-16bits)
HR5504	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5505	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5506	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5507	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5508	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5509	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5510	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5511	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5512	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5513				HR5512 (Lo-16bits)
HR5514	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5515				HR5514 (Lo-16bits)
HR5516	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5517				HR5516 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5518	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5519				HR5518 (Lo-16bits)
HR5520	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5521				HR5520 (Lo-16bits)
HR5522	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5523				HR5522 (Lo-16bits)
HR5524	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5525				HR5524 (Lo-16bits)
HR5526	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5527				HR5526 (Lo-16bits)
HR5528	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5529				HR5528 (Lo-16bits)
HR5530	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5531				HR5530 (Lo-16bits)
HR5532	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5533				HR5532 (Lo-16bits)
HR5534	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5535				HR5534 (Lo-16bits)
HR5536	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5537				HR5536 (Lo-16bits)
HR5538	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5539				HR5538 (Lo-16bits)
HR5540	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5541				HR5540 (Lo-16bits)
HR5542	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5543				HR5542 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5544	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5545				HR5544 (Lo-16bits)
HR5546	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5547				HR5546 (Lo-16bits)
HR5548	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5549				HR5548 (Lo-16bits)
HR5550	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5551				HR5550 (Lo-16bits)
HR5552	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5553				HR5552 (Lo-16bits)
HR5554	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5555				HR5554 (Lo-16bits)
HR5556	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5557				HR5556 (Lo-16bits)
HR5558	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5559				HR5558 (Lo-16bits)
HR5560	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5561				HR5560 (Lo-16bits)
HR5562	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5563				HR5562 (Lo-16bits)
HR5564	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5565				HR5564 (Lo-16bits)
HR5566	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5567				HR5566 (Lo-16bits)
HR5568	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5569				HR5568 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5570	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5571				HR5570 (Lo-16bits)
HR5572	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5573				HR5572 (Lo-16bits)
HR5574	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5575				HR5574 (Lo-16bits)
HR5576	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5577				HR5576 (Lo-16bits)
HR5578	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5579				HR5578 (Lo-16bits)
HR5580	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5581				HR5580 (Lo-16bits)
HR5582	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5583				HR5582 (Lo-16bits)
HR5584	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5585				HR5584 (Lo-16bits)
HR5586	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5587				HR5586 (Lo-16bits)
HR5588	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5589				HR5588 (Lo-16bits)
HR5590	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5591				HR5590 (Lo-16bits)
HR5592	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5593	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5594	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5595	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5596	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR5597	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinf
HR5598	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinf
HR5599	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinf
HR5600	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5601	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5602	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5603	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5604	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5605	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5606	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5607	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5608	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5609				HR5608 (Lo-16bits)
HR5610	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5611				HR5610 (Lo-16bits)
HR5612	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5613				HR5612 (Lo-16bits)
HR5614	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5615				HR5614 (Lo-16bits)
HR5616	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5617				HR5616 (Lo-16bits)
HR5618	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5619				HR5618 (Lo-16bits)
HR5620	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5621				HR5620 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5622	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5623				HR5622 (Lo-16bits)
HR5624	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5625				HR5624 (Lo-16bits)
HR5626	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5627				HR5626 (Lo-16bits)
HR5628	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5629				HR5628 (Lo-16bits)
HR5630	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5631				HR5630 (Lo-16bits)
HR5632	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5633				HR5632 (Lo-16bits)
HR5634	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5635				HR5634 (Lo-16bits)
HR5636	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5637				HR5636 (Lo-16bits)
HR5638	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5639				HR5638 (Lo-16bits)
HR5640	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5641				HR5640 (Lo-16bits)
HR5642	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5643				HR5642 (Lo-16bits)
HR5644	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5645				HR5644 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5646	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5647				HR5646 (Lo-16bits)
HR5648	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5649				HR5648 (Lo-16bits)
HR5650	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5651				HR5650 (Lo-16bits)
HR5652	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5653				HR5652 (Lo-16bits)
HR5654	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5655				HR5654 (Lo-16bits)
HR5656	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5657	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5658	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5659	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5660	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5661	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5662	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5663	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5664	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5665	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5666	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5667	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5668	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5669	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR5670	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5671	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5672	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5673	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5674	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5675	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5676	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5677	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5678	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5679	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5680	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5681	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5682	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5683	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5684	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5685	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5686	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5687	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5688	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5689				HR5688 (Lo-16bits)
HR5690	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5691				HR5690 (Lo-16bits)
HR5692	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5693				HR5692 (Lo-16bits)
HR5694	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5695				HR5694 (Lo-16bits)
HR5696	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5697				HR5696 (Lo-16bits)
HR5698	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5699				HR5698 (Lo-16bits)
HR5700	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5701				HR5700 (Lo-16bits)
HR5702	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5703				HR5702 (Lo-16bits)
HR5704	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5705				HR5704 (Lo-16bits)
HR5706	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5707				HR5706 (Lo-16bits)
HR5708	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5709				HR5708 (Lo-16bits)
HR5710	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5711				HR5710 (Lo-16bits)
HR5712	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5713				HR5712 (Lo-16bits)
HR5714	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5715				HR5714 (Lo-16bits)
HR5716	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5717				HR5716 (Lo-16bits)
HR5718	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5719				HR5718 (Lo-16bits)
HR5720	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5721				HR5720 (Lo-16bits)
HR5722	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5723				HR5722 (Lo-16bits)
HR5724	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5725				HR5724 (Lo-16bits)
HR5726	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5727				HR5726 (Lo-16bits)
HR5728	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5729				HR5728 (Lo-16bits)
HR5730	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5731				HR5730 (Lo-16bits)
HR5732	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5733				HR5732 (Lo-16bits)
HR5734	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5735				HR5734 (Lo-16bits)
HR5736	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5737				HR5736 (Lo-16bits)
HR5738	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5739				HR5738 (Lo-16bits)
HR5740	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5741				HR5740 (Lo-16bits)
HR5742	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5743				HR5742 (Lo-16bits)
HR5744	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5745				HR5744 (Lo-16bits)
HR5746	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5747				HR5746 (Lo-16bits)
HR5748	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5749				HR5748 (Lo-16bits)
HR5750	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5751				HR5750 (Lo-16bits)
HR5752	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5753	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5754	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5755	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5756	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5757	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5758	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5759	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5760	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5761				HR5760 (Lo-16bits)
HR5762	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5763				HR5762 (Lo-16bits)
HR5764	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5765				HR5764 (Lo-16bits)
HR5766	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5767				HR5766 (Lo-16bits)
HR5768	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5769				HR5768 (Lo-16bits)
HR5770	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5771				HR5770 (Lo-16bits)
HR5772	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5773				HR5772 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5774	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5775				HR5774 (Lo-16bits)
HR5776	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5777	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5778	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5779	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5780	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5781	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5782	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5783	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5784	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5785				HR5784 (Lo-16bits)
HR5786	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5787				HR5786 (Lo-16bits)
HR5788	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5789				HR5788 (Lo-16bits)
HR5790	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5791				HR5790 (Lo-16bits)
HR5792	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5793				HR5792 (Lo-16bits)
HR5794	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5795				HR5794 (Lo-16bits)
HR5796	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5797				HR5796 (Lo-16bits)
HR5798	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5799				HR5798 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5800	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5801	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5802	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5803	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5804	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5805	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5806	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5807	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5808	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5809	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5810	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5811	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5812	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5813	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5814	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5815	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5816	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip

Holding Register	Parameter	Access	Length	Info
HR5817	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5818	DEF4:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5819	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5820	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5821				HR5820 (Lo-16bits)
HR5822	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5823				HR5822 (Lo-16bits)
HR5824	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5825				HR5824 (Lo-16bits)
HR5826	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5827	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5828	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5829	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5830	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5831	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5832	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5833	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5834	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5835				HR5834 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5836	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5837				HR5836 (Lo-16bits)
HR5838	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5839				HR5838 (Lo-16bits)
HR5840	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5841				HR5840 (Lo-16bits)
HR5842	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5843				HR5842 (Lo-16bits)
HR5844	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5845				HR5844 (Lo-16bits)
HR5846	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5847				HR5846 (Lo-16bits)
HR5848	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5849				HR5848 (Lo-16bits)
HR5850	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5851				HR5850 (Lo-16bits)
HR5852	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5853				HR5852 (Lo-16bits)
HR5854	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5855				HR5854 (Lo-16bits)
HR5856	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5857				HR5856 (Lo-16bits)
HR5858	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5859				HR5858 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5860	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5861				HR5860 (Lo-16bits)
HR5862	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5863				HR5862 (Lo-16bits)
HR5864	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5865				HR5864 (Lo-16bits)
HR5866	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5867				HR5866 (Lo-16bits)
HR5868	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5869				HR5868 (Lo-16bits)
HR5870	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5871				HR5870 (Lo-16bits)
HR5872	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5873				HR5872 (Lo-16bits)
HR5874	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5875				HR5874 (Lo-16bits)
HR5876	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5877				HR5876 (Lo-16bits)
HR5878	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5879				HR5878 (Lo-16bits)
HR5880	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5881				HR5880 (Lo-16bits)
HR5882	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5883				HR5882 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5884	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5885				HR5884 (Lo-16bits)
HR5886	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5887				HR5886 (Lo-16bits)
HR5888	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5889				HR5888 (Lo-16bits)
HR5890	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5891				HR5890 (Lo-16bits)
HR5892	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5893				HR5892 (Lo-16bits)
HR5894	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5895				HR5894 (Lo-16bits)
HR5896	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5897				HR5896 (Lo-16bits)
HR5898	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5899				HR5898 (Lo-16bits)
HR5900	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5901				HR5900 (Lo-16bits)
HR5902	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5903				HR5902 (Lo-16bits)
HR5904	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5905				HR5904 (Lo-16bits)
HR5906	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5907				HR5906 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5908	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5909				HR5908 (Lo-16bits)
HR5910	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5911				HR5910 (Lo-16bits)
HR5912	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5913				HR5912 (Lo-16bits)
HR5914	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5915				HR5914 (Lo-16bits)
HR5916	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5917				HR5916 (Lo-16bits)
HR5918	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5919				HR5918 (Lo-16bits)
HR5920	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5921				HR5920 (Lo-16bits)
HR5922	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5923				HR5922 (Lo-16bits)
HR5924	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5925				HR5924 (Lo-16bits)
HR5926	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5927				HR5926 (Lo-16bits)
HR5928	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5929				HR5928 (Lo-16bits)
HR5930	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5931	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used

Holding Register	Parameter	Access	Length	Info
HR5932	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5933	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5934	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5935	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5936	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5937	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5938	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5939				HR5938 (Lo-16bits)
HR5940	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5941				HR5940 (Lo-16bits)
HR5942	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5943				HR5942 (Lo-16bits)
HR5944	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5945				HR5944 (Lo-16bits)
HR5946	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5947				HR5946 (Lo-16bits)
HR5948	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5949				HR5948 (Lo-16bits)
HR5950	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5951				HR5950 (Lo-16bits)
HR5952	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5953				HR5952 (Lo-16bits)
HR5954	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5955				HR5954 (Lo-16bits)
HR5956	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5957				HR5956 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5958	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5959				HR5958 (Lo-16bits)
HR5960	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5961				HR5960 (Lo-16bits)
HR5962	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5963				HR5962 (Lo-16bits)
HR5964	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5965				HR5964 (Lo-16bits)
HR5966	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5967				HR5966 (Lo-16bits)
HR5968	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5969				HR5968 (Lo-16bits)
HR5970	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5971				HR5970 (Lo-16bits)
HR5972	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5973				HR5972 (Lo-16bits)
HR5974	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5975				HR5974 (Lo-16bits)
HR5976	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5977				HR5976 (Lo-16bits)
HR5978	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5979				HR5978 (Lo-16bits)
HR5980	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5981				HR5980 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5982	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5983				HR5982 (Lo-16bits)
HR5984	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5985				HR5984 (Lo-16bits)
HR5986	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5987				HR5986 (Lo-16bits)
HR5988	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5989				HR5988 (Lo-16bits)
HR5990	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5991				HR5990 (Lo-16bits)
HR5992	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5993				HR5992 (Lo-16bits)
HR5994	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5995				HR5994 (Lo-16bits)
HR5996	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5997				HR5996 (Lo-16bits)
HR5998	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5999				HR5998 (Lo-16bits)
HR6000	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6001				HR6000 (Lo-16bits)
HR6002	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6003				HR6002 (Lo-16bits)
HR6004	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6005				HR6004 (Lo-16bits)
HR6006	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6007				HR6006 (Lo-16bits)
HR6008	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6009				HR6008 (Lo-16bits)
HR6010	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6011				HR6010 (Lo-16bits)
HR6012	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6013				HR6012 (Lo-16bits)
HR6014	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6015				HR6014 (Lo-16bits)
HR6016	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6017				HR6016 (Lo-16bits)
HR6018	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6019	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6020	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6021	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6022	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6023	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6024	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6025	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6026	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6027	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6028	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6029	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6030	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6031	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6032	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6033	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6034	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6035				HR6034 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6036	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6037				HR6036 (Lo-16bits)
HR6038	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6039				HR6038 (Lo-16bits)
HR6040	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6041				HR6040 (Lo-16bits)
HR6042	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6043				HR6042 (Lo-16bits)
HR6044	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6045				HR6044 (Lo-16bits)
HR6046	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6047				HR6046 (Lo-16bits)
HR6048	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6049				HR6048 (Lo-16bits)
HR6050	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6051				HR6050 (Lo-16bits)
HR6052	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6053				HR6052 (Lo-16bits)
HR6054	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6055				HR6054 (Lo-16bits)
HR6056	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6057				HR6056 (Lo-16bits)
HR6058	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6059				HR6058 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6060	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6061				HR6060 (Lo-16bits)
HR6062	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6063				HR6062 (Lo-16bits)
HR6064	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6065				HR6064 (Lo-16bits)
HR6066	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6067				HR6066 (Lo-16bits)
HR6068	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6069				HR6068 (Lo-16bits)
HR6070	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6071				HR6070 (Lo-16bits)
HR6072	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6073				HR6072 (Lo-16bits)
HR6074	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6075				HR6074 (Lo-16bits)
HR6076	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6077				HR6076 (Lo-16bits)
HR6078	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6079				HR6078 (Lo-16bits)
HR6080	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6081				HR6080 (Lo-16bits)
HR6082	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6083	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6084	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6085	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6086	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6087	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6088	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6089	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6090	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6091	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6092	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6093	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6094	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6095	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6096	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6097	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6098	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6099	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6100	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6101	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6102	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6103	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6104	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6105	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6106	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR6107	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6108	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6109	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6110	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6111	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6112	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6113	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6114	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6115				HR6114 (Lo-16bits)
HR6116	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6117				HR6116 (Lo-16bits)
HR6118	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6119				HR6118 (Lo-16bits)
HR6120	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6121				HR6120 (Lo-16bits)
HR6122	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6123				HR6122 (Lo-16bits)
HR6124	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6125				HR6124 (Lo-16bits)
HR6126	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6127				HR6126 (Lo-16bits)
HR6128	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6129				HR6128 (Lo-16bits)
HR6130	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6131				HR6130 (Lo-16bits)
HR6132	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6133				HR6132 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6134	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6135				HR6134 (Lo-16bits)
HR6136	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6137				HR6136 (Lo-16bits)
HR6138	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6139				HR6138 (Lo-16bits)
HR6140	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6141				HR6140 (Lo-16bits)
HR6142	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6143				HR6142 (Lo-16bits)
HR6144	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6145				HR6144 (Lo-16bits)
HR6146	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6147				HR6146 (Lo-16bits)
HR6148	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6149				HR6148 (Lo-16bits)
HR6150	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6151				HR6150 (Lo-16bits)
HR6152	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6153				HR6152 (Lo-16bits)
HR6154	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6155				HR6154 (Lo-16bits)
HR6156	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6157				HR6156 (Lo-16bits)
HR6158	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6159				HR6158 (Lo-16bits)
HR6160	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6161				HR6160 (Lo-16bits)
HR6162	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6163				HR6162 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6164	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6165				HR6164 (Lo-16bits)
HR6166	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6167				HR6166 (Lo-16bits)
HR6168	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6169				HR6168 (Lo-16bits)
HR6170	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6171				HR6170 (Lo-16bits)
HR6172	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6173				HR6172 (Lo-16bits)
HR6174	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6175				HR6174 (Lo-16bits)
HR6176	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6177				HR6176 (Lo-16bits)
HR6178	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6179	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6180	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6181	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6182	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6183	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6184	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6185	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6186	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6187				HR6186 (Lo-16bits)
HR6188	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6189				HR6188 (Lo-16bits)
HR6190	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6191				HR6190 (Lo-16bits)
HR6192	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6193				HR6192 (Lo-16bits)
HR6194	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6195				HR6194 (Lo-16bits)
HR6196	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6197				HR6196 (Lo-16bits)
HR6198	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6199				HR6198 (Lo-16bits)
HR6200	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6201				HR6200 (Lo-16bits)
HR6202	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6203	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6204	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6205	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6206	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6207	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6208	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6209	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6210	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6211				HR6210 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6212	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6213				HR6212 (Lo-16bits)
HR6214	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6215				HR6214 (Lo-16bits)
HR6216	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6217				HR6216 (Lo-16bits)
HR6218	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6219				HR6218 (Lo-16bits)
HR6220	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6221				HR6220 (Lo-16bits)
HR6222	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6223				HR6222 (Lo-16bits)
HR6224	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6225				HR6224 (Lo-16bits)
HR6226	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6227	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6228	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6229	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6230	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6231	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6232	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6233	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6234	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6235	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6236	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6237	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6238	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6239	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6240	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6241	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6242	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR6243	IEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR6244	CUB1:I2> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6245	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6246	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6247				HR6246 (Lo-16bits)
HR6248	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6249				HR6248 (Lo-16bits)
HR6250	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6251				HR6250 (Lo-16bits)
HR6252	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6253				HR6252 (Lo-16bits)
HR6254	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6255				HR6254 (Lo-16bits)
HR6256	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6257				HR6256 (Lo-16bits)
HR6258	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6259				HR6258 (Lo-16bits)
HR6260	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6261				HR6260 (Lo-16bits)
HR6262	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6263				HR6262 (Lo-16bits)
HR6264	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6265				HR6264 (Lo-16bits)
HR6266	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6267				HR6266 (Lo-16bits)
HR6268	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6269				HR6268 (Lo-16bits)
HR6270	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6271				HR6270 (Lo-16bits)
HR6272	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6273				HR6272 (Lo-16bits)
HR6274	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6275				HR6274 (Lo-16bits)
HR6276	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6277				HR6276 (Lo-16bits)
HR6278	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6279				HR6278 (Lo-16bits)
HR6280	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6281				HR6280 (Lo-16bits)
HR6282	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6283				HR6282 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6284	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6285				HR6284 (Lo-16bits)
HR6286	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6287				HR6286 (Lo-16bits)
HR6288	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6289				HR6288 (Lo-16bits)
HR6290	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6291				HR6290 (Lo-16bits)
HR6292	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6293				HR6292 (Lo-16bits)
HR6294	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6295	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6296	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6297	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6298	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6299	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6300	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6301	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6302	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6303				HR6302 (Lo-16bits)
HR6304	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6305				HR6304 (Lo-16bits)
HR6306	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6307				HR6306 (Lo-16bits)
HR6308	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6309				HR6308 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6310	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6311				HR6310 (Lo-16bits)
HR6312	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6313				HR6312 (Lo-16bits)
HR6314	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6315				HR6314 (Lo-16bits)
HR6316	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6317				HR6316 (Lo-16bits)
HR6318	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6319	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6320	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6321	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6322	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6323	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6324	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6325	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6326	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6327	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6328	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6329	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6330	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6331	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6332	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6333	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6334	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6335	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6336	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6337	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6338	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6339	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6340	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6341	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6342	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6343	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6344	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6345	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6346	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6347	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6348	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6349	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6350	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6351				HR6350 (Lo-16bits)
HR6352	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6353				HR6352 (Lo-16bits)
HR6354	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6355				HR6354 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6356	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6357				HR6356 (Lo-16bits)
HR6358	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6359				HR6358 (Lo-16bits)
HR6360	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6361				HR6360 (Lo-16bits)
HR6362	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6363				HR6362 (Lo-16bits)
HR6364	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6365				HR6364 (Lo-16bits)
HR6366	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6367				HR6366 (Lo-16bits)
HR6368	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6369				HR6368 (Lo-16bits)
HR6370	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6371				HR6370 (Lo-16bits)
HR6372	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6373				HR6372 (Lo-16bits)
HR6374	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6375				HR6374 (Lo-16bits)
HR6376	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6377				HR6376 (Lo-16bits)
HR6378	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6379				HR6378 (Lo-16bits)
HR6380	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6381				HR6380 (Lo-16bits)
HR6382	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6383				HR6382 (Lo-16bits)
HR6384	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6385				HR6384 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6386	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6387				HR6386 (Lo-16bits)
HR6388	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6389				HR6388 (Lo-16bits)
HR6390	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6391				HR6390 (Lo-16bits)
HR6392	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6393				HR6392 (Lo-16bits)
HR6394	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6395				HR6394 (Lo-16bits)
HR6396	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6397				HR6396 (Lo-16bits)
HR6398	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6399				HR6398 (Lo-16bits)
HR6400	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6401				HR6400 (Lo-16bits)
HR6402	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6403				HR6402 (Lo-16bits)
HR6404	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6405				HR6404 (Lo-16bits)
HR6406	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6407				HR6406 (Lo-16bits)
HR6408	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6409				HR6408 (Lo-16bits)
HR6410	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6411				HR6410 (Lo-16bits)
HR6412	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6413				HR6412 (Lo-16bits)
HR6414	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6415	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6416	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6417	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6418	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6419	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6420	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6421	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6422	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6423				HR6422 (Lo-16bits)
HR6424	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6425				HR6424 (Lo-16bits)
HR6426	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6427				HR6426 (Lo-16bits)
HR6428	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6429				HR6428 (Lo-16bits)
HR6430	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6431				HR6430 (Lo-16bits)
HR6432	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6433				HR6432 (Lo-16bits)
HR6434	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6435				HR6434 (Lo-16bits)
HR6436	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6437				HR6436 (Lo-16bits)
HR6438	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6439	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6440	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6441	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6442	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6443	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6444	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6445	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6446	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6447	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6448	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6449	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6450	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6451	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6452	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6453	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6454	CUB2:I2>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6455	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6456	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6457				HR6456 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6458	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6459				HR6458 (Lo-16bits)
HR6460	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6461				HR6460 (Lo-16bits)
HR6462	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6463				HR6462 (Lo-16bits)
HR6464	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6465				HR6464 (Lo-16bits)
HR6466	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6467				HR6466 (Lo-16bits)
HR6468	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6469				HR6468 (Lo-16bits)
HR6470	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6471				HR6470 (Lo-16bits)
HR6472	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6473				HR6472 (Lo-16bits)
HR6474	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6475				HR6474 (Lo-16bits)
HR6476	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6477				HR6476 (Lo-16bits)
HR6478	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6479				HR6478 (Lo-16bits)
HR6480	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6481				HR6480 (Lo-16bits)
HR6482	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6483				HR6482 (Lo-16bits)
HR6484	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6485				HR6484 (Lo-16bits)
HR6486	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6487				HR6486 (Lo-16bits)
HR6488	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6489				HR6488 (Lo-16bits)
HR6490	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6491				HR6490 (Lo-16bits)
HR6492	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6493				HR6492 (Lo-16bits)
HR6494	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6495				HR6494 (Lo-16bits)
HR6496	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6497				HR6496 (Lo-16bits)
HR6498	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6499				HR6498 (Lo-16bits)
HR6500	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6501				HR6500 (Lo-16bits)
HR6502	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6503				HR6502 (Lo-16bits)
HR6504	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6505	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6506	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6507	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6508	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6509	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6510	CUB2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6511	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6512	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6513				HR6512 (Lo-16bits)
HR6514	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6515				HR6514 (Lo-16bits)
HR6516	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6517				HR6516 (Lo-16bits)
HR6518	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6519				HR6518 (Lo-16bits)
HR6520	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6521				HR6520 (Lo-16bits)
HR6522	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6523				HR6522 (Lo-16bits)
HR6524	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6525				HR6524 (Lo-16bits)
HR6526	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6527				HR6526 (Lo-16bits)
HR6528	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6529	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6530	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6531	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6532	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6533	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6534	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6535	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6536	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6537	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6538	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6539	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6540	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6541	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6542	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6543	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6544	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6545	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6546	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6547	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6548	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6549	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6550	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6551	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6552	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6553	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6554	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6555	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6556	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6557	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6558	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6559	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6560	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6561				HR6560 (Lo-16bits)
HR6562	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6563				HR6562 (Lo-16bits)
HR6564	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6565				HR6564 (Lo-16bits)
HR6566	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6567				HR6566 (Lo-16bits)
HR6568	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6569				HR6568 (Lo-16bits)
HR6570	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6571				HR6570 (Lo-16bits)
HR6572	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6573				HR6572 (Lo-16bits)
HR6574	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6575				HR6574 (Lo-16bits)
HR6576	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6577				HR6576 (Lo-16bits)
HR6578	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6579				HR6578 (Lo-16bits)
HR6580	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6581				HR6580 (Lo-16bits)
HR6582	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6583				HR6582 (Lo-16bits)
HR6584	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6585				HR6584 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6586	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6587				HR6586 (Lo-16bits)
HR6588	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6589				HR6588 (Lo-16bits)
HR6590	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6591				HR6590 (Lo-16bits)
HR6592	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6593				HR6592 (Lo-16bits)
HR6594	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6595				HR6594 (Lo-16bits)
HR6596	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6597				HR6596 (Lo-16bits)
HR6598	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6599				HR6598 (Lo-16bits)
HR6600	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6601				HR6600 (Lo-16bits)
HR6602	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6603				HR6602 (Lo-16bits)
HR6604	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6605				HR6604 (Lo-16bits)
HR6606	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6607				HR6606 (Lo-16bits)
HR6608	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6609				HR6608 (Lo-16bits)
HR6610	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6611				HR6610 (Lo-16bits)
HR6612	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6613				HR6612 (Lo-16bits)
HR6614	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6615				HR6614 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6616	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6617				HR6616 (Lo-16bits)
HR6618	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6619				HR6618 (Lo-16bits)
HR6620	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6621				HR6620 (Lo-16bits)
HR6622	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6623				HR6622 (Lo-16bits)
HR6624	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6625	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6626	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6627	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6628	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6629	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6630	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6631	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6632	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6633				HR6632 (Lo-16bits)
HR6634	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6635				HR6634 (Lo-16bits)
HR6636	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6637				HR6636 (Lo-16bits)
HR6638	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6639				HR6638 (Lo-16bits)
HR6640	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6641				HR6640 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6642	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6643				HR6642 (Lo-16bits)
HR6644	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6645				HR6644 (Lo-16bits)
HR6646	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6647				HR6646 (Lo-16bits)
HR6648	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6649	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6650	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6651	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6652	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6653	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6654	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6655	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6656	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6657	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6658	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6659	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6660	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6661	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6662	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6663	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6664	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6665	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6666	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6667				HR6666 (Lo-16bits)
HR6668	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6669				HR6668 (Lo-16bits)
HR6670	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6671				HR6670 (Lo-16bits)
HR6672	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6673				HR6672 (Lo-16bits)
HR6674	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6675				HR6674 (Lo-16bits)
HR6676	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6677				HR6676 (Lo-16bits)
HR6678	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6679				HR6678 (Lo-16bits)
HR6680	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6681				HR6680 (Lo-16bits)
HR6682	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6683				HR6682 (Lo-16bits)
HR6684	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6685				HR6684 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6686	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6687				HR6686 (Lo-16bits)
HR6688	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6689				HR6688 (Lo-16bits)
HR6690	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6691				HR6690 (Lo-16bits)
HR6692	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6693				HR6692 (Lo-16bits)
HR6694	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6695				HR6694 (Lo-16bits)
HR6696	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6697				HR6696 (Lo-16bits)
HR6698	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6699				HR6698 (Lo-16bits)
HR6700	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6701				HR6700 (Lo-16bits)
HR6702	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6703				HR6702 (Lo-16bits)
HR6704	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6705				HR6704 (Lo-16bits)
HR6706	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6707				HR6706 (Lo-16bits)
HR6708	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6709				HR6708 (Lo-16bits)
HR6710	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6711				HR6710 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6712	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6713				HR6712 (Lo-16bits)
HR6714	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6715	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6716	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6717	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6718	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6719	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6720	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6721	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6722	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6723				HR6722 (Lo-16bits)
HR6724	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6725				HR6724 (Lo-16bits)
HR6726	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6727				HR6726 (Lo-16bits)
HR6728	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6729				HR6728 (Lo-16bits)
HR6730	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6731				HR6730 (Lo-16bits)
HR6732	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6733				HR6732 (Lo-16bits)
HR6734	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6735				HR6734 (Lo-16bits)
HR6736	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6737				HR6736 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6738	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6739	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6740	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6741	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6742	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6743	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6744	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6745	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6746	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6747	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6748	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6749	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6750	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6751	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6752	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6753	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6754	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6755	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6756	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6757	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6758	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6759	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6760	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6761	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6762	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6763	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6764	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6765	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6766	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6767	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6768	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6769	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6770	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6771				HR6770 (Lo-16bits)
HR6772	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6773				HR6772 (Lo-16bits)
HR6774	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6775				HR6774 (Lo-16bits)
HR6776	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6777				HR6776 (Lo-16bits)
HR6778	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6779				HR6778 (Lo-16bits)
HR6780	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6781				HR6780 (Lo-16bits)
HR6782	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6783				HR6782 (Lo-16bits)
HR6784	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6785				HR6784 (Lo-16bits)
HR6786	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6787				HR6786 (Lo-16bits)
HR6788	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6789				HR6788 (Lo-16bits)
HR6790	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6791				HR6790 (Lo-16bits)
HR6792	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6793				HR6792 (Lo-16bits)
HR6794	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6795				HR6794 (Lo-16bits)
HR6796	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6797				HR6796 (Lo-16bits)
HR6798	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6799				HR6798 (Lo-16bits)
HR6800	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6801				HR6800 (Lo-16bits)
HR6802	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6803				HR6802 (Lo-16bits)
HR6804	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6805				HR6804 (Lo-16bits)
HR6806	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6807				HR6806 (Lo-16bits)
HR6808	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6809				HR6808 (Lo-16bits)
HR6810	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6811				HR6810 (Lo-16bits)
HR6812	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6813				HR6812 (Lo-16bits)
HR6814	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6815				HR6814 (Lo-16bits)
HR6816	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6817				HR6816 (Lo-16bits)
HR6818	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6819				HR6818 (Lo-16bits)
HR6820	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6821				HR6820 (Lo-16bits)
HR6822	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6823				HR6822 (Lo-16bits)
HR6824	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6825				HR6824 (Lo-16bits)
HR6826	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6827				HR6826 (Lo-16bits)
HR6828	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6829				HR6828 (Lo-16bits)
HR6830	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6831				HR6830 (Lo-16bits)
HR6832	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6833				HR6832 (Lo-16bits)
HR6834	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6835	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6836	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6837	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6838	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6839	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6840	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6841	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6842	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6843				HR6842 (Lo-16bits)
HR6844	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6845				HR6844 (Lo-16bits)
HR6846	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6847				HR6846 (Lo-16bits)
HR6848	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6849				HR6848 (Lo-16bits)
HR6850	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6851				HR6850 (Lo-16bits)
HR6852	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6853				HR6852 (Lo-16bits)
HR6854	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6855				HR6854 (Lo-16bits)
HR6856	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6857				HR6856 (Lo-16bits)
HR6858	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6859	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6860	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6861	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6862	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6863	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6864	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6865	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6866	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6867	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6868	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6869	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6870	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6871	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6872	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6873	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6874	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6875	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6876	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6877				HR6876 (Lo-16bits)
HR6878	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6879				HR6878 (Lo-16bits)
HR6880	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6881				HR6880 (Lo-16bits)
HR6882	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6883				HR6882 (Lo-16bits)
HR6884	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6885				HR6884 (Lo-16bits)
HR6886	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6887				HR6886 (Lo-16bits)
HR6888	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6889				HR6888 (Lo-16bits)
HR6890	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6891				HR6890 (Lo-16bits)
HR6892	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6893				HR6892 (Lo-16bits)
HR6894	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6895				HR6894 (Lo-16bits)
HR6896	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6897				HR6896 (Lo-16bits)
HR6898	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6899				HR6898 (Lo-16bits)
HR6900	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6901				HR6900 (Lo-16bits)
HR6902	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6903				HR6902 (Lo-16bits)
HR6904	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6905				HR6904 (Lo-16bits)
HR6906	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6907				HR6906 (Lo-16bits)
HR6908	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6909				HR6908 (Lo-16bits)
HR6910	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6911				HR6910 (Lo-16bits)
HR6912	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6913				HR6912 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6914	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6915				HR6914 (Lo-16bits)
HR6916	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6917				HR6916 (Lo-16bits)
HR6918	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6919				HR6918 (Lo-16bits)
HR6920	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6921				HR6920 (Lo-16bits)
HR6922	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6923				HR6922 (Lo-16bits)
HR6924	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6925	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6926	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6927	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6928	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6929	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6930	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6931	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6932	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6933				HR6932 (Lo-16bits)
HR6934	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6935				HR6934 (Lo-16bits)
HR6936	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6937				HR6936 (Lo-16bits)
HR6938	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6939				HR6938 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6940	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6941				HR6940 (Lo-16bits)
HR6942	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6943				HR6942 (Lo-16bits)
HR6944	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6945				HR6944 (Lo-16bits)
HR6946	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6947				HR6946 (Lo-16bits)
HR6948	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6949	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6950	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6951	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6952	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6953	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6954	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6955	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6956	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6957	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6958	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6959	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6960	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6961	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6962	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6963	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6964	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6965	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6966	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6967	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6968	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6969	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6970	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6971	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6972	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6973	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6974	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6975	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6976	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6977	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6978	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6979	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6980	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6981				HR6980 (Lo-16bits)
HR6982	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6983				HR6982 (Lo-16bits)
HR6984	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6985				HR6984 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6986	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6987				HR6986 (Lo-16bits)
HR6988	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6989				HR6988 (Lo-16bits)
HR6990	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6991				HR6990 (Lo-16bits)
HR6992	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6993				HR6992 (Lo-16bits)
HR6994	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6995				HR6994 (Lo-16bits)
HR6996	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6997				HR6996 (Lo-16bits)
HR6998	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6999				HR6998 (Lo-16bits)
HR7000	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7001				HR7000 (Lo-16bits)
HR7002	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7003				HR7002 (Lo-16bits)
HR7004	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7005				HR7004 (Lo-16bits)
HR7006	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7007				HR7006 (Lo-16bits)
HR7008	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7009				HR7008 (Lo-16bits)
HR7010	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7011				HR7010 (Lo-16bits)
HR7012	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7013				HR7012 (Lo-16bits)
HR7014	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7015				HR7014 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7016	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7017				HR7016 (Lo-16bits)
HR7018	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7019				HR7018 (Lo-16bits)
HR7020	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7021				HR7020 (Lo-16bits)
HR7022	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7023				HR7022 (Lo-16bits)
HR7024	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7025				HR7024 (Lo-16bits)
HR7026	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7027				HR7026 (Lo-16bits)
HR7028	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7029				HR7028 (Lo-16bits)
HR7030	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7031				HR7030 (Lo-16bits)
HR7032	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7033				HR7032 (Lo-16bits)
HR7034	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7035				HR7034 (Lo-16bits)
HR7036	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7037				HR7036 (Lo-16bits)
HR7038	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7039				HR7038 (Lo-16bits)
HR7040	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7041				HR7040 (Lo-16bits)
HR7042	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7043				HR7042 (Lo-16bits)
HR7044	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7045	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7046	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7047	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7048	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7049	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7050	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7051	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7052	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7053				HR7052 (Lo-16bits)
HR7054	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7055				HR7054 (Lo-16bits)
HR7056	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7057				HR7056 (Lo-16bits)
HR7058	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7059				HR7058 (Lo-16bits)
HR7060	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7061				HR7060 (Lo-16bits)
HR7062	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7063				HR7062 (Lo-16bits)
HR7064	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7065				HR7064 (Lo-16bits)
HR7066	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7067				HR7066 (Lo-16bits)
HR7068	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7069	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7070	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7071	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7072	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7073	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7074	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7075	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7076	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7077	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7078	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7079	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7080	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7081	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7082	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7083	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7084	HOC1:lh> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7085	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7089	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7106	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7107				HR7106 (Lo-16bits)
HR7108	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7109				HR7108 (Lo-16bits)
HR7110	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7111				HR7110 (Lo-16bits)
HR7112	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7113				HR7112 (Lo-16bits)
HR7114	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7115				HR7114 (Lo-16bits)
HR7116	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7117				HR7116 (Lo-16bits)
HR7118	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7119				HR7118 (Lo-16bits)
HR7120	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7121				HR7120 (Lo-16bits)
HR7122	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7123				HR7122 (Lo-16bits)
HR7124	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7125				HR7124 (Lo-16bits)
HR7126	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7127	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7148	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7149				HR7148 (Lo-16bits)
HR7150	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7151				HR7150 (Lo-16bits)
HR7152	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7153				HR7152 (Lo-16bits)
HR7154	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7155				HR7154 (Lo-16bits)
HR7156	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7157				HR7156 (Lo-16bits)
HR7158	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7159				HR7158 (Lo-16bits)
HR7160	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7161				HR7160 (Lo-16bits)
HR7162	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7163				HR7162 (Lo-16bits)
HR7164	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7165				HR7164 (Lo-16bits)
HR7166	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7167				HR7166 (Lo-16bits)
HR7168	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR7169	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked
HR7170	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR7171	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR7172	DIF1:Idb> condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR7173				HR7172 (Lo-16bits)
HR7174	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR7175				HR7174 (Lo-16bits)
HR7176	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7177				HR7176 (Lo-16bits)
HR7178	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7179	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7180	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7181	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR7182	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7183	TOLT1:TT> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7184	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7185	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7186	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7187	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7188	TOLT1:Temp C or F deg	RW	1	0=C 1=F
HR7189	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7190	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR7191	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7194	TOLF1:TF> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7195	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7196	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7197	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7198	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal

Holding Register	Parameter	Access	Length	Info
HR7199	TOLF1:Temp C or F deg	RW	1	0=C 1=F
HR7200	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7201	TOLF1:RTD Sensor	R	1	0=Invalid 1=OK
HR7202	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set 1=Estimate
HR7203	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7206	TOLM1:TM> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7207	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7208	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7209	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7210	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR7211	TOLM1:Temp C or F deg	RW	1	0=C 1=F
HR7212	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing 1=NPS Biasing in use
HR7213	TOLM1:Time constants	RW	1	0=Single 1=Multiple
HR7214	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually 1=Estimate (on-line)
HR7215	TOLM1:T const dyn. balancing	RW	1	0=Fixed 1=Dynamic
HR7216	TOLM1:Dev.Temp (tmax)	RW	1	0=A 1=B 2=F 3=H 4=Manual set
HR7217	TOLM1:Amb. T Selection	RW	1	0=Manual set 1=RTD
HR7218	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR7219	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7220				HR7219 (Lo-16bits)
HR7221	LCR1:ISr> condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR7222	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7223	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7224	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7225	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7226	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7227	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7228	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7229	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7230	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7231	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7232	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7233	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7234	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7235	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7252	OV1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7253	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7254	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7255	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7256	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7257	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7258	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7259	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7279	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)
HR7285	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7309	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7324	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7325	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7326	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7327	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7328	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7329	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7330	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7331	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7332	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7333	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7334	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7335	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7336	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7337	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7338	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7339	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7340	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7341	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7342	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7343	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7344	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7345	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7346	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7347	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7348	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7349	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7350	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7351	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7352	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7353	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7354	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7355	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7356	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7357	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7374	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7375	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7376	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7377	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7378	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7379	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7380	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7381	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7383	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)
HR7387	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7410				HR7409 (Lo-16bits)
HR7411	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)
HR7427	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)
HR7429	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)
HR7431	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7432				HR7431 (Lo-16bits)
HR7433	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7434				HR7433 (Lo-16bits)
HR7435	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7436				HR7435 (Lo-16bits)
HR7437	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7440				HR7439 (Lo-16bits)
HR7441	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7446	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7447	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7448	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7449	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7450	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7451	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7452	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7453	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7454	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7455	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7456	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7457	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7458	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7459	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7460	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7461	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7462	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7463	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7464	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7465	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7466	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7467	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7468	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7469	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7470	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7471	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7472	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7473	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7474	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7475	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7476	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7477	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7478	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7479	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7483	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7496	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7497	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7498	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7499	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7500	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7501	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7502	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7503	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7511	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7526				HR7525 (Lo-16bits)
HR7527	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7528				HR7527 (Lo-16bits)
HR7529	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7530				HR7529 (Lo-16bits)
HR7531	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7532				HR7531 (Lo-16bits)
HR7533	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7534				HR7533 (Lo-16bits)
HR7535	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7536				HR7535 (Lo-16bits)
HR7537	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7539	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7568	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7569	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7570	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7571	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7572	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7573	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7574	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7575	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7576	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7577	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7578	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7579	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7580	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7581	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7582	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7583	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7584	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7585	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7586	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7587	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7588	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7589	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7590	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7591	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7592	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7593	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7594	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7595	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7596	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7597	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7598	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7599	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7600	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7601	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7613	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7618	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7619	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7620	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7621	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7622	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7623	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7624	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7625	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7630				HR7629 (Lo-16bits)
HR7631	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7632				HR7631 (Lo-16bits)
HR7633	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7634				HR7633 (Lo-16bits)
HR7635	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7636				HR7635 (Lo-16bits)
HR7637	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7638				HR7637 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7639	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7640				HR7639 (Lo-16bits)
HR7641	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7642				HR7641 (Lo-16bits)
HR7643	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7644				HR7643 (Lo-16bits)
HR7645	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7669	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7690	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7691	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7692	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7693	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7694	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7695	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7696	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7697	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7698	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7699	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7700	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7701	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7702	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7703	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7704	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7705	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7706	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7707	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7708	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7709	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7710	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7711	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7712	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7713	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR7714	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7715	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7716	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7717	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7718	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7719	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7720	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7721	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7722	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7723	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7744				HR7743 (Lo-16bits)
HR7745	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7746				HR7745 (Lo-16bits)
HR7747	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7748				HR7747 (Lo-16bits)
HR7749	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7750				HR7749 (Lo-16bits)
HR7751	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7752				HR7751 (Lo-16bits)
HR7753	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7754				HR7753 (Lo-16bits)
HR7755	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7756	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7757	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7758	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7759	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7760	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7761	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7762	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7763	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7772				HR7771 (Lo-16bits)
HR7773	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7801	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7828	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7829	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7830	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7831	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7832	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7833	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7834	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7835	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7836	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7837	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7838	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7839	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7840	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7841	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7842	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7843	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7844	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7845	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7846	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7847	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7848	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7849	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7850	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7851	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7852	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7853	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7854	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7855	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7856	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7857	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7858	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7859	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7860	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7861	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)
HR7865	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7875	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)
HR7891	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7894	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7895	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7896	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7897	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7898	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7899	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7900	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7901	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7904				HR7903 (Lo-16bits)
HR7905	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)
HR7917	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7931	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7961	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7966	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7967	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7968	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7969	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7970	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7971	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7972	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7973	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7974	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7975	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7976	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7977	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7978	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7979	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7980	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7981	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7982	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7983	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7984	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7985	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7986	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7987	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7988	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7989	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7990	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7991	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7992	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7993	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7994	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7995	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7996	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7997	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7998	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7999	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8003	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8030				HR8029 (Lo-16bits)
HR8031	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8032	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8033	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8034	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8035	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8036	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8037	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8038	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8039	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8056				HR8055 (Lo-16bits)
HR8057	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8086				HR8085 (Lo-16bits)
HR8087	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)
HR8103	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8104	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8105	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8106	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8107	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8108	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8109	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8110	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8111	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8112	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8113	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8114	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8115	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8116	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8117	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8118	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8119	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8120	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8121	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8122	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8123	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8124	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8125	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8126	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8127	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR8128	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8129	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR8130	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8131	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8132	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8133	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8134	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8135	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8136	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8137	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8155	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8170	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8171	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8172	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8173	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8174	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8175	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8176	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8177	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8181	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8208				HR8207 (Lo-16bits)
HR8209	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)
HR8229	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8238				HR8237 (Lo-16bits)
HR8239	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8242	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8243	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8244	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8245	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8246	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8247	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8248	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8249	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8250	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8251	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8252	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8253	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8254	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8255	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8256	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8257	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8258	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8259	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8260	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8261	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8262	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8263	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8264	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8265	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8266	NOV1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8267	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8268	NOV2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8269	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8270	NOV3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8271	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8272	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8273	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8274	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8275	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8276	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8277	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8278	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <

Holding Register	Parameter	Access	Length	Info
HR8279	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8280	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8281	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8282	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8283	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8307	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8316	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8317	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8318	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8319	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8320	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8321	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8322	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8323	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8333	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8362				HR8361 (Lo-16bits)
HR8363	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8388	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8389	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8390	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8391	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8392	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8393	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8394	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8395	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8396	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8397	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8398	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8399	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8400	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8401	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8402	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8403	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8404	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8405	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8406	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8407	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8408	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8409	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8410	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8411	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8412	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8413	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8414	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8415	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8416	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8417	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8418	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8419	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8420	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8421	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8435	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8454	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8455	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8456	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8457	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8458	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8459	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8460	VUB2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8461	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8487	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8517	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8526	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8527	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8528	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8529	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8530	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8531	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8532	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8533	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8534	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8535	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8536	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8537	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8538	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8539	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8540	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8541	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8542	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8543	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8544	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8545	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8546	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8547	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8548	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8549	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8550	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8551	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8552	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8553	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8554	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8555	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8556	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8557	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8558	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8559	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8561	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8588				HR8587 (Lo-16bits)
HR8589	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8592	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8593	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8594	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8595	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8596	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8597	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8598	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8599	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8614				HR8613 (Lo-16bits)
HR8615	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8644				HR8643 (Lo-16bits)
HR8645	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8664	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8665	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8666	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8667	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8668	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8669	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8670	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8671	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8672	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8673	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8674	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8675	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8676	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8677	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8678	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8679	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8680	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8681	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8682	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8683	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8684	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8685	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8686	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8687	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8688	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8689	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8690	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8691	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8692	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8693	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8694	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8695	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8696	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8697	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8715	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8730	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8731	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8732	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8733	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8734	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8735	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8736	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8737	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8741	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8754				HR8753 (Lo-16bits)
HR8755	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8768				HR8767 (Lo-16bits)
HR8769	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8798				HR8797 (Lo-16bits)
HR8799	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8802	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8803	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8804	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8805	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8806	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8807	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8808	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8809	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8810	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8811	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8812	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8813	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8814	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8815	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8816	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8817	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8818	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8819	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8820	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8821	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8822	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8823	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8824	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8825	PGS1:PS1 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8826	PGS1:PS2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8827	PGS1:PS3 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8828	PGS1:PS4 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8829	PGS1:PS5 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8830	PGS1:PS6 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8831	PGS1:PS7 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8832	PGS1:PS8 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8833	PGS1:PS9 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8834	PGS1:PS10 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8835	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8836	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8837	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8838	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8839	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8840	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8841	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8842	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8843	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR8844	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR8845	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8846	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes
HR8847	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8848	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR8849	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8850	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR8851	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8852	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR8853	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8854	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR8855	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8856	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8857	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8858	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8859	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8860	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8861	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8862	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8863	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8864	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8865	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8866	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8867	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8868	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8869				HR8868 (Lo-16bits)
HR8870	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8871				HR8870 (Lo-16bits)
HR8872	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8873				HR8872 (Lo-16bits)
HR8874	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8875				HR8874 (Lo-16bits)
HR8876	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8877				HR8876 (Lo-16bits)
HR8878	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8879				HR8878 (Lo-16bits)
HR8880	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8881				HR8880 (Lo-16bits)
HR8882	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8883				HR8882 (Lo-16bits)
HR8884	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8885				HR8884 (Lo-16bits)
HR8886	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8887				HR8886 (Lo-16bits)
HR8888	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8889				HR8888 (Lo-16bits)
HR8890	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8891				HR8890 (Lo-16bits)
HR8892	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8893				HR8892 (Lo-16bits)
HR8894	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8895				HR8894 (Lo-16bits)
HR8896	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8897				HR8896 (Lo-16bits)
HR8898	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8899				HR8898 (Lo-16bits)
HR8900	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8901	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8902	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8903	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8904	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8905	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8906	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8907	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8908	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8909				HR8908 (Lo-16bits)
HR8910	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8911				HR8910 (Lo-16bits)
HR8912	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8913				HR8912 (Lo-16bits)
HR8914	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8915				HR8914 (Lo-16bits)
HR8916	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8917				HR8916 (Lo-16bits)
HR8918	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8919				HR8918 (Lo-16bits)
HR8920	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8921				HR8920 (Lo-16bits)
HR8922	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8923				HR8922 (Lo-16bits)
HR8924	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8925				HR8924 (Lo-16bits)
HR8926	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8927				HR8926 (Lo-16bits)
HR8928	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8929				HR8928 (Lo-16bits)
HR8930	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8931				HR8930 (Lo-16bits)
HR8932	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8933				HR8932 (Lo-16bits)
HR8934	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8935				HR8934 (Lo-16bits)
HR8936	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8937				HR8936 (Lo-16bits)
HR8938	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8939				HR8938 (Lo-16bits)
HR8940	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8941	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8942	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8943	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8944	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8945	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8946	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8947	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8948	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8949				HR8948 (Lo-16bits)
HR8950	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8951				HR8950 (Lo-16bits)
HR8952	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8953				HR8952 (Lo-16bits)
HR8954	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8955				HR8954 (Lo-16bits)
HR8956	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8957				HR8956 (Lo-16bits)
HR8958	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8959				HR8958 (Lo-16bits)
HR8960	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8961				HR8960 (Lo-16bits)
HR8962	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8963				HR8962 (Lo-16bits)
HR8964	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8965				HR8964 (Lo-16bits)
HR8966	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8967				HR8966 (Lo-16bits)
HR8968	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8969				HR8968 (Lo-16bits)
HR8970	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8971				HR8970 (Lo-16bits)
HR8972	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8973				HR8972 (Lo-16bits)
HR8974	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8975				HR8974 (Lo-16bits)
HR8976	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8977				HR8976 (Lo-16bits)
HR8978	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8979				HR8978 (Lo-16bits)
HR8980	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8981	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8982	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8983	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8984	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8985	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8986	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8987	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8988	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8989				HR8988 (Lo-16bits)
HR8990	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8991				HR8990 (Lo-16bits)
HR8992	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8993				HR8992 (Lo-16bits)
HR8994	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8995				HR8994 (Lo-16bits)
HR8996	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8997				HR8996 (Lo-16bits)
HR8998	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8999				HR8998 (Lo-16bits)
HR9000	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9001				HR9000 (Lo-16bits)
HR9002	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9003				HR9002 (Lo-16bits)
HR9004	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9005				HR9004 (Lo-16bits)
HR9006	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9007				HR9006 (Lo-16bits)
HR9008	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9009				HR9008 (Lo-16bits)
HR9010	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9011				HR9010 (Lo-16bits)
HR9012	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9013				HR9012 (Lo-16bits)
HR9014	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9015				HR9014 (Lo-16bits)
HR9016	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9017				HR9016 (Lo-16bits)
HR9018	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9019				HR9018 (Lo-16bits)
HR9020	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9021	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9022	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9023	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9024	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9025	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9026	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9027	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9028	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9029				HR9028 (Lo-16bits)
HR9030	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9031				HR9030 (Lo-16bits)
HR9032	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9033				HR9032 (Lo-16bits)
HR9034	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9035				HR9034 (Lo-16bits)
HR9036	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9037				HR9036 (Lo-16bits)
HR9038	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9039				HR9038 (Lo-16bits)
HR9040	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9041				HR9040 (Lo-16bits)
HR9042	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9043				HR9042 (Lo-16bits)
HR9044	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9045				HR9044 (Lo-16bits)
HR9046	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9047				HR9046 (Lo-16bits)
HR9048	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9049				HR9048 (Lo-16bits)
HR9050	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9051				HR9050 (Lo-16bits)
HR9052	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9053				HR9052 (Lo-16bits)
HR9054	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9055				HR9054 (Lo-16bits)
HR9056	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9057				HR9056 (Lo-16bits)
HR9058	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9059				HR9058 (Lo-16bits)
HR9060	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9061	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9062	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9063	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9064	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9065	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9066	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9067	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9068	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9069				HR9068 (Lo-16bits)
HR9070	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9071				HR9070 (Lo-16bits)
HR9072	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9073				HR9072 (Lo-16bits)
HR9074	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9075				HR9074 (Lo-16bits)
HR9076	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9077				HR9076 (Lo-16bits)
HR9078	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9079				HR9078 (Lo-16bits)
HR9080	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9081				HR9080 (Lo-16bits)
HR9082	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9083				HR9082 (Lo-16bits)
HR9084	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9085				HR9084 (Lo-16bits)
HR9086	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9087				HR9086 (Lo-16bits)
HR9088	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9089				HR9088 (Lo-16bits)
HR9090	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9091				HR9090 (Lo-16bits)
HR9092	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9093				HR9092 (Lo-16bits)
HR9094	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9095				HR9094 (Lo-16bits)
HR9096	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9097				HR9096 (Lo-16bits)
HR9098	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9099				HR9098 (Lo-16bits)
HR9100	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9101	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9102	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9103	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9104	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9105	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9106	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9107	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9108	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9109				HR9108 (Lo-16bits)
HR9110	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9111				HR9110 (Lo-16bits)
HR9112	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9113				HR9112 (Lo-16bits)
HR9114	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9115				HR9114 (Lo-16bits)
HR9116	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9117				HR9116 (Lo-16bits)
HR9118	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9119				HR9118 (Lo-16bits)
HR9120	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9121				HR9120 (Lo-16bits)
HR9122	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9123				HR9122 (Lo-16bits)
HR9124	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9125				HR9124 (Lo-16bits)
HR9126	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9127				HR9126 (Lo-16bits)
HR9128	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9129				HR9128 (Lo-16bits)
HR9130	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9131				HR9130 (Lo-16bits)
HR9132	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9133				HR9132 (Lo-16bits)
HR9134	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9135				HR9134 (Lo-16bits)
HR9136	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9137				HR9136 (Lo-16bits)
HR9138	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9139				HR9138 (Lo-16bits)
HR9140	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9141	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9142	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9143	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9144	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9145	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9146	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9147	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9148	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9149				HR9148 (Lo-16bits)
HR9150	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9151				HR9150 (Lo-16bits)
HR9152	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9153				HR9152 (Lo-16bits)
HR9154	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9155				HR9154 (Lo-16bits)
HR9156	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9157				HR9156 (Lo-16bits)
HR9158	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9159				HR9158 (Lo-16bits)
HR9160	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9161				HR9160 (Lo-16bits)
HR9162	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9163				HR9162 (Lo-16bits)
HR9164	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9165				HR9164 (Lo-16bits)
HR9166	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9167				HR9166 (Lo-16bits)
HR9168	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9169				HR9168 (Lo-16bits)
HR9170	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9171				HR9170 (Lo-16bits)
HR9172	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9173				HR9172 (Lo-16bits)
HR9174	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9175				HR9174 (Lo-16bits)
HR9176	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9177				HR9176 (Lo-16bits)
HR9178	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9179				HR9178 (Lo-16bits)
HR9180	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9181	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9182	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9183	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9184	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9185	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9186	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9187	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9188	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR9189	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR9194	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9195	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9196	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9197	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9198	OPW1:P> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9199	UPW1:P< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9200	RPW1:Prev> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9201	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9202	SEF1:U03rd< Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR9203	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9204	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9205	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9206	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9207	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR9208	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=VHz(1) Blocked 2=VHz(1) Start 3=VHz(1) Alarm 4=VHz(1) Trip
HR9209	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=VHz(2) Start 2=VHz(2) Alarm 3=VHz(2) Trip 4=VHz(2) Blocked
HR9210	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR9211	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9216	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9217				HR9216 (Lo-16bits)
HR9218	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9219				HR9218 (Lo-16bits)
HR9220	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9221				HR9220 (Lo-16bits)
HR9222	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit

Holding Register	Parameter	Access	Length	Info
HR9223	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9224				HR9223 (Lo-16bits)
HR9225	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9226				HR9225 (Lo-16bits)
HR9227	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9228				HR9227 (Lo-16bits)
HR9229	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9230	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9231				HR9230 (Lo-16bits)
HR9232	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9233				HR9232 (Lo-16bits)
HR9234	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9235				HR9234 (Lo-16bits)
HR9236	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9237	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9244	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9245				HR9244 (Lo-16bits)
HR9246	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9247				HR9246 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9248	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9249				HR9248 (Lo-16bits)
HR9250	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9251	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9260	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9261				HR9260 (Lo-16bits)
HR9262	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9263				HR9262 (Lo-16bits)
HR9264	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9265				HR9264 (Lo-16bits)
HR9266	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9267				HR9266 (Lo-16bits)
HR9268	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9269	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9273	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9276	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9277				HR9276 (Lo-16bits)
HR9278	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9279				HR9278 (Lo-16bits)
HR9280	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9281				HR9280 (Lo-16bits)
HR9282	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9283	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9288				HR9287 (Lo-16bits)
HR9289	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9290	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9291				HR9290 (Lo-16bits)
HR9292	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9293				HR9292 (Lo-16bits)
HR9294	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9295				HR9294 (Lo-16bits)
HR9296	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9297	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9298				HR9297 (Lo-16bits)
HR9299	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9302				HR9301 (Lo-16bits)
HR9303	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9304	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9305				HR9304 (Lo-16bits)
HR9306	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9307				HR9306 (Lo-16bits)
HR9308	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9309				HR9308 (Lo-16bits)
HR9310	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9311	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)
HR9317	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9320	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9321				HR9320 (Lo-16bits)
HR9322	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9323				HR9322 (Lo-16bits)
HR9324	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9325				HR9324 (Lo-16bits)
HR9326	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9327				HR9326 (Lo-16bits)
HR9328	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9329	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9347	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)
HR9361	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9366				HR9365 (Lo-16bits)
HR9367	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9373	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)
HR9391	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9403	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9433	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	ARC1:I/O Arc> condition	R	1	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked
HR9460	ARC1:I/O Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9461				HR9460 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9462	ARC1:I/O Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9463				HR9462 (Lo-16bits)
HR9464	ARC1:I/O Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9465				HR9464 (Lo-16bits)
HR9466	ARC1:I/O Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9467				HR9466 (Lo-16bits)
HR9468	ARC1:I/O Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9469				HR9468 (Lo-16bits)
HR9470	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9471	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9472	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTBusfail 3=VTLinefail 4=Blocked
HR9473	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR9474	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR9475	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked
HR9476	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR9477	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR9478	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR9479	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9488				HR9487 (Lo-16bits)
HR9489	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9511	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	VJP1:<DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR9514	VJP1:Voltage meas selected	R	1	0=Selection Ok 1=Selection not available
HR9515	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9516	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9517	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9518	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9519	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9520	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9521	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9522	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9523	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9524	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9525	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9526	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9527	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9528	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9529	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9530	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR9531	AR1:AR Status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR9534	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR9535				HR9534 (Lo-16bits)
HR9536	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR9537				HR9536 (Lo-16bits)
HR9538	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR9539	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	VRG1:Voltage meas condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok
HR9544	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR9545				HR9544 (Lo-16bits)
HR9546	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR9547				HR9546 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9548	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR9549	VRG1:Remote tap control input	RW	1	0=Stop 1=Lower 2=Higher
HR9550	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9551				HR9550 (Lo-16bits)
HR9552	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9553				HR9552 (Lo-16bits)
HR9554	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9555				HR9554 (Lo-16bits)
HR9556	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9557				HR9556 (Lo-16bits)
HR9558	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9559				HR9558 (Lo-16bits)
HR9560	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9561				HR9560 (Lo-16bits)
HR9562	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9563				HR9562 (Lo-16bits)
HR9564	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9565				HR9564 (Lo-16bits)
HR9566	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9567				HR9566 (Lo-16bits)
HR9568	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9569				HR9568 (Lo-16bits)
HR9570	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9571				HR9570 (Lo-16bits)
HR9572	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9573				HR9572 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9574	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9575				HR9574 (Lo-16bits)
HR9576	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9577				HR9576 (Lo-16bits)
HR9578	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9579				HR9578 (Lo-16bits)
HR9580	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9587				HR9586 (Lo-16bits)
HR9588	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)
HR9594	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9599				HR9598 (Lo-16bits)
HR9600	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9603				HR9602 (Lo-16bits)
HR9604	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9617				HR9616 (Lo-16bits)
HR9618	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9619				HR9618 (Lo-16bits)
HR9620	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9621				HR9620 (Lo-16bits)
HR9622	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9623				HR9622 (Lo-16bits)
HR9624	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9625				HR9624 (Lo-16bits)
HR9626	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9627				HR9626 (Lo-16bits)
HR9628	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9629				HR9628 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9630	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLT detected
HR9631	EMON1:IO> (50N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9632	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLT detected
HR9633	EMON1:IOdir> (67N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9634	EMON1:IOInt> (67NT) Monitor condition	R	1	0=Normal 1=FLT FW 2=FLT REV
HR9635	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=No voltage
HR9636	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9637	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLT detected
HR9638	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR9639				HR9638 (Lo-16bits)
HR9640	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR9641				HR9640 (Lo-16bits)
HR9642	GSYN:Synchronizer mode	RW	1	0=Disabled 1=Activated
HR9643	GSYN:Synchronizing by	RW	1	0=Autosynchronizer 1=Manual commands
HR9644	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch 1=I know what I am doing
HR9645	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9650				HR9649 (Lo-16bits)
HR9651	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9675	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9690				HR9689 (Lo-16bits)
HR9691	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)
HR9697	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9700				HR9699 (Lo-16bits)
HR9701	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9725	VREC:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9726	VREC:Scale current values to primary	RW	1	0=No 1=Yes
HR9727	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	RW	1	0=No 1=Yes
HR9728	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9729				HR9728 (Lo-16bits)
HR9730	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR9731				HR9730 (Lo-16bits)
HR9732	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR9733				HR9732 (Lo-16bits)
HR9734	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR9735				HR9734 (Lo-16bits)
HR9736	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR9737				HR9736 (Lo-16bits)
HR9738	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR9739				HR9738 (Lo-16bits)
HR9740	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR9741				HR9740 (Lo-16bits)
HR9742	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR9743				HR9742 (Lo-16bits)
HR9744	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR9745				HR9744 (Lo-16bits)
HR9746	RTB:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9747	RTB:Scale current values to primary	RW	1	0=No 1=Yes
HR9748	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9749				HR9748 (Lo-16bits)
HR9750	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9751				HR9750 (Lo-16bits)
HR9752	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9753				HR9752 (Lo-16bits)
HR9754	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9755				HR9754 (Lo-16bits)
HR9756	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9757				HR9756 (Lo-16bits)
HR9758	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9759				HR9758 (Lo-16bits)
HR9760	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9761				HR9760 (Lo-16bits)
HR9762	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9763				HR9762 (Lo-16bits)
HR9764	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9765				HR9764 (Lo-16bits)
HR9766	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9767				HR9766 (Lo-16bits)
HR9768	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR9769				HR9768 (Lo-16bits)
HR9770	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR9771				HR9770 (Lo-16bits)
HR9772	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9773				HR9772 (Lo-16bits)
HR9774	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9775				HR9774 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9776	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9777				HR9776 (Lo-16bits)
HR9778	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9779				HR9778 (Lo-16bits)
HR9780	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9781				HR9780 (Lo-16bits)
HR9782	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9783				HR9782 (Lo-16bits)
HR9784	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9785				HR9784 (Lo-16bits)
HR9786	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9787				HR9786 (Lo-16bits)
HR9788	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9789				HR9788 (Lo-16bits)
HR9790	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9791				HR9790 (Lo-16bits)
HR9792	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9793				HR9792 (Lo-16bits)
HR9794	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9795				HR9794 (Lo-16bits)
HR9796	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9797				HR9796 (Lo-16bits)
HR9798	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9799				HR9798 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9800	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9801				HR9800 (Lo-16bits)
HR9802	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9803				HR9802 (Lo-16bits)
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

1.2 T Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware T_v2.1.1.3-247. The firmware is used by the following device types:

- AQ-T216
- AQ-T215



NOTICE!

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools* → *Communication* → *Modbus map*.

Table. 1.2 - 2. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter

Holding Register	Parameter	Access	Length	Info
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1: Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1: Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR30	MEAS1: Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1: Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1: Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1: Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1: Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1: Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1: Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1: Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1: Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1: calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1: Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1: Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1: Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1: Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1: Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR59				HR58 (Lo-16bits)
HR60	MEAS1: Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1: Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1: Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1: Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1: Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1: Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1: Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1: Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1: Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1: Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1: Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1: Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1: Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1: Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR118	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR148	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR178	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR208	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR237				HR236 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR238	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR268	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR298	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR328	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR358	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR388	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR418	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR448	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR501				HR500 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR502	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR532	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR562	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR592	MEAS2:I ² L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I ² L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I ² L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I ² L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I ² L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I ² L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I ² L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I ² L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I ² L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I ² L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I ² L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I ² L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I ² L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I ² L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I ² L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR622	MEAS2:I ^L L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I ^L L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I ^L L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I ^L L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I ^L L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I ^L L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I ^L L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I ^L L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I ^L L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I ^L L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I ^L L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I ^L L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I ^L L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I ^L L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I ^L L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR652	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR682	MEAS2:I ³ L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I ³ L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I ³ L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I ³ L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I ³ L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I ³ L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I ³ L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I ³ L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I ³ L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I ³ L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I ³ L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I ³ L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I ³ L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I ⁰¹ Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I ⁰¹ Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR711				HR710 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR712	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR742	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR772	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR802	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR831				HR830 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR832	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR861				HR860 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR862	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR891				HR890 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR892	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR917				HR916 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR918	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR942	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR967				HR966 (Lo-16bits)
HR968	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR996	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1026	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1056	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1086	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1116	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1146	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1176	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1206	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1217				HR1216 (Lo-16bits)
HR1218	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)
HR1228	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1229				HR1228 (Lo-16bits)
HR1230	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)
HR1232	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1234	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)
HR1244	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)
HR1252	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)
HR1256	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1258	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1259				HR1258 (Lo-16bits)
HR1260	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)
HR1268	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)
HR1276	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1279				HR1278 (Lo-16bits)
HR1280	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1281				HR1280 (Lo-16bits)
HR1282	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)
HR1288	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1291				HR1290 (Lo-16bits)
HR1292	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1305				HR1304 (Lo-16bits)
HR1306	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	POW1:Reset 3ph Energies	RW	1	0=- 1=Reset
HR1317	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1328				HR1327 (Lo-16bits)
HR1329	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1338				HR1337 (Lo-16bits)
HR1339	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1340				HR1339 (Lo-16bits)
HR1341	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1342				HR1341 (Lo-16bits)
HR1343	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1344				HR1343 (Lo-16bits)
HR1345	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1346				HR1345 (Lo-16bits)
HR1347	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1348				HR1347 (Lo-16bits)
HR1349	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1350				HR1349 (Lo-16bits)
HR1351	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1352				HR1351 (Lo-16bits)
HR1353	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1354				HR1353 (Lo-16bits)
HR1355	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1356				HR1355 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1357	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1358				HR1357 (Lo-16bits)
HR1359	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)
HR1381	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1382				HR1381 (Lo-16bits)
HR1383	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)
HR1393	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1402				HR1401 (Lo-16bits)
HR1403	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)
HR1405	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)
HR1409	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1410				HR1409 (Lo-16bits)
HR1411	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)
HR1417	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)
HR1419	DIGI:DIs (cpu)	R	2	0b=DI1 1b=DI2 2b=DI3 (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	DIGI:DIs (input option 1)	R	2	0b=DI4 1b=DI5 2b=DI6 3b=DI7 4b=DI8 5b=DI9 6b=DI10 7b=DI11 (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	DIGI:DIs (input option 2)	R	2	0b=DI12 1b=DI13 2b=DI14 3b=DI15 4b=DI16 5b=DI17 6b=DI18 7b=DI19 (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	DIGI:DIs (input option 3)	R	2	0b=DI20 1b=DI21 2b=DI22 3b=DI23 4b=DI24 5b=DI25 6b=DI26 7b=DI27 (Hi-16bits)
HR1426				HR1425 (Lo-16bits)
HR1427	DIGI:DIs (input option 4)	R	2	0b=D28 1b=DI29 2b=DI30 3b=DI31 4b=DI32 5b=DI33 6b=DI34 7b=DI35 (Hi-16bits)
HR1428				HR1427 (Lo-16bits)
HR1429	DIGI:DIs (input option 5)	R	2	0b=DI36 1b=DI37 2b=DI38 3b=DI39 4b=DI40 5b=DI41 6b=DI42 7b=DI43 (Hi-16bits)
HR1430				HR1429 (Lo-16bits)
HR1431	DIGI:DIs (input option 6)	R	2	0b=DI44 1b=DI45 2b=DI46 3b=DI47 4b=DI48 5b=DI49 6b=DI50 7b=DI51 (Hi-16bits)
HR1432				HR1431 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1433	DIGI:Dis 1-32	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1434				HR1433 (Lo-16bits)
HR1435	DIGI:Dis 33-64	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 (Hi-16bits)
HR1436				HR1435 (Lo-16bits)
HR1437	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1438				HR1437 (Lo-16bits)
HR1439	DIGI:DI1 Counter Clear	RW	1	0=- 1=Clear
HR1440	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1441				HR1440 (Lo-16bits)
HR1442	DIGI:DI2 Counter Clear	RW	1	0=- 1=Clear
HR1443	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1444				HR1443 (Lo-16bits)
HR1445	DIGI:DI3 Counter Clear	RW	1	0=- 1=Clear
HR1446	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1447				HR1446 (Lo-16bits)
HR1448	DIGI:DI4 Counter Clear	RW	1	0=- 1=Clear
HR1449	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1450				HR1449 (Lo-16bits)
HR1451	DIGI:DI5 Counter Clear	RW	1	0=- 1=Clear
HR1452	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1453				HR1452 (Lo-16bits)
HR1454	DIGI:DI6 Counter Clear	RW	1	0=- 1=Clear
HR1455	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1456				HR1455 (Lo-16bits)
HR1457	DIGI:DI7 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1458	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1459				HR1458 (Lo-16bits)
HR1460	DIGI:DI8 Counter Clear	RW	1	0=- 1=Clear
HR1461	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1462				HR1461 (Lo-16bits)
HR1463	DIGI:DI9 Counter Clear	RW	1	0=- 1=Clear
HR1464	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1465				HR1464 (Lo-16bits)
HR1466	DIGI:DI10 Counter Clear	RW	1	0=- 1=Clear
HR1467	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)
HR1469	DIGI:DI11 Counter Clear	RW	1	0=- 1=Clear
HR1470	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1471				HR1470 (Lo-16bits)
HR1472	DIGI:DI12 Counter Clear	RW	1	0=- 1=Clear
HR1473	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1474				HR1473 (Lo-16bits)
HR1475	DIGI:DI13 Counter Clear	RW	1	0=- 1=Clear
HR1476	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1477				HR1476 (Lo-16bits)
HR1478	DIGI:DI14 Counter Clear	RW	1	0=- 1=Clear
HR1479	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1480				HR1479 (Lo-16bits)
HR1481	DIGI:DI15 Counter Clear	RW	1	0=- 1=Clear
HR1482	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1483				HR1482 (Lo-16bits)
HR1484	DIGI:DI16 Counter Clear	RW	1	0=- 1=Clear
HR1485	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1486				HR1485 (Lo-16bits)
HR1487	DIGI:DI17 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1488	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1489				HR1488 (Lo-16bits)
HR1490	DIGI:DI18 Counter Clear	RW	1	0=- 1=Clear
HR1491	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1492				HR1491 (Lo-16bits)
HR1493	DIGI:DI19 Counter Clear	RW	1	0=- 1=Clear
HR1494	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1495				HR1494 (Lo-16bits)
HR1496	DIGI:DI20 Counter Clear	RW	1	0=- 1=Clear
HR1497	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1498				HR1497 (Lo-16bits)
HR1499	DIGI:DI21 Counter Clear	RW	1	0=- 1=Clear
HR1500	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1501				HR1500 (Lo-16bits)
HR1502	DIGI:DI22 Counter Clear	RW	1	0=- 1=Clear
HR1503	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1504				HR1503 (Lo-16bits)
HR1505	DIGI:DI23 Counter Clear	RW	1	0=- 1=Clear
HR1506	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1507				HR1506 (Lo-16bits)
HR1508	DIGI:DI24 Counter Clear	RW	1	0=- 1=Clear
HR1509	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1510				HR1509 (Lo-16bits)
HR1511	DIGI:DI25 Counter Clear	RW	1	0=- 1=Clear
HR1512	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1513				HR1512 (Lo-16bits)
HR1514	DIGI:DI26 Counter Clear	RW	1	0=- 1=Clear
HR1515	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIGI:DI27 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1518	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	DIGI:DI28 Counter Clear	RW	1	0=- 1=Clear
HR1521	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIGI:DI29 Counter Clear	RW	1	0=- 1=Clear
HR1524	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	DIGI:DI30 Counter Clear	RW	1	0=- 1=Clear
HR1527	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	DIGI:DI31 Counter Clear	RW	1	0=- 1=Clear
HR1530	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1531				HR1530 (Lo-16bits)
HR1532	DIGI:DI32 Counter Clear	RW	1	0=- 1=Clear
HR1533	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	DIGI:DI33 Counter Clear	RW	1	0=- 1=Clear
HR1536	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	DIGI:DI34 Counter Clear	RW	1	0=- 1=Clear
HR1539	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	DIGI:DI35 Counter Clear	RW	1	0=- 1=Clear
HR1542	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1543				HR1542 (Lo-16bits)
HR1544	DIGI:DI36 Counter Clear	RW	1	0=- 1=Clear
HR1545	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	DIGI:DI37 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1548	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	DIGI:DI38 Counter Clear	RW	1	0=- 1=Clear
HR1551	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	DIGI:DI39 Counter Clear	RW	1	0=- 1=Clear
HR1554	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	DIGI:DI40 Counter Clear	RW	1	0=- 1=Clear
HR1557	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	DIGI:DI41 Counter Clear	RW	1	0=- 1=Clear
HR1560	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1561				HR1560 (Lo-16bits)
HR1562	DIGI:DI42 Counter Clear	RW	1	0=- 1=Clear
HR1563	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	DIGI:DI43 Counter Clear	RW	1	0=- 1=Clear
HR1566	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	DIGI:DI44 Counter Clear	RW	1	0=- 1=Clear
HR1569	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	DIGI:DI45 Counter Clear	RW	1	0=- 1=Clear
HR1572	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	DIGI:DI46 Counter Clear	RW	1	0=- 1=Clear
HR1575	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	DIGI:DI47 Counter Clear	RW	1	0=- 1=Clear

Holding Register	Parameter	Access	Length	Info
HR1578	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	DIGI:DI48 Counter Clear	RW	1	0=- 1=Clear
HR1581	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	DIGI:DI49 Counter Clear	RW	1	0=- 1=Clear
HR1584	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	DIGI:DI50 Counter Clear	RW	1	0=- 1=Clear
HR1587	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	DIGI:DI51 Counter Clear	RW	1	0=- 1=Clear
HR1590	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1593	SGS:Force SG	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1594	SGS:Force SG change	RW	1	0=Disabled 1=Enabled
HR1595	SGS:Used setting groups	RW	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1596	SGS:Remote SG change	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1597	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR1598	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)
HR1600	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	DEV:SW version	R	2	STRING [...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1605				HR1604 (Lo-16bits)
HR1606	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1617				HR1616 (Lo-16bits)
HR1618	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1627				HR1626 (Lo-16bits)
HR1628	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)
HR1630	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1635				HR1634 (Lo-16bits)
HR1636	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR1639	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1659	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1683	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1688	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	NOC1: > [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1691	NOC1: > condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1692	NOC1: > Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1693	NOC1: > Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1694	NOC1: > Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1695	NOC1: > Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	NOC1: > Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	NOC1: > Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1709	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)
HR1713	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1716				HR1715 (Lo-16bits)
HR1717	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1718	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1719	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1720	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1721	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1722	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1723	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1724	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1726				HR1725 (Lo-16bits)
HR1727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1732				HR1731 (Lo-16bits)
HR1733	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1736				HR1735 (Lo-16bits)
HR1737	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1738				HR1737 (Lo-16bits)
HR1739	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1742	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1743	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1744	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1745	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1746	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1747	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1748	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1749	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1750	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1751	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1752	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1753	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1754	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1755	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1756	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1757	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1758	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1759	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR1760	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1761	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1762	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1763	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1764	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1765	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1766	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1767	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1768	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1769	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1770	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1771	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1772	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1776				HR1775 (Lo-16bits)
HR1777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1778				HR1777 (Lo-16bits)
HR1779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1780				HR1779 (Lo-16bits)
HR1781	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1784				HR1783 (Lo-16bits)
HR1785	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1790				HR1789 (Lo-16bits)
HR1791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)
HR1793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1794				HR1793 (Lo-16bits)
HR1795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1796				HR1795 (Lo-16bits)
HR1797	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1798				HR1797 (Lo-16bits)
HR1799	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1800				HR1799 (Lo-16bits)
HR1801	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)
HR1803	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1814				HR1813 (Lo-16bits)
HR1815	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1838	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1839	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1840	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1841	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1842	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1843	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1844	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1845	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1852				HR1851 (Lo-16bits)
HR1853	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1854				HR1853 (Lo-16bits)
HR1855	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1856				HR1855 (Lo-16bits)
HR1857	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1858				HR1857 (Lo-16bits)
HR1859	NOC1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1860				HR1859 (Lo-16bits)
HR1861	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1862	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1863	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1864	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1865	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1866	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1867	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1868	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1870				HR1869 (Lo-16bits)
HR1871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1872				HR1871 (Lo-16bits)
HR1873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1874				HR1873 (Lo-16bits)
HR1875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1876				HR1875 (Lo-16bits)
HR1877	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1878				HR1877 (Lo-16bits)
HR1879	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1880				HR1879 (Lo-16bits)
HR1881	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1882				HR1881 (Lo-16bits)
HR1883	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1884				HR1883 (Lo-16bits)
HR1885	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1886	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1887	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1888	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1889	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1890	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1891	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1892	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1893	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1894	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1895	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1896	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1897	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1898	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1899	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1900	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1901	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1902				HR1901 (Lo-16bits)
HR1903	NOC1:Delay type	RW	1	0=DT 1=IDMT
HR1904	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	NOC1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR1907	NOC1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1908	NOC1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR1909	NOC1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR1910	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	NOC1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR1919	NOC1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR1920	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1921	NOC2:I>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1922	NOC2:I>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1923	NOC2:I>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1924	NOC2:I>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1925	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1926				HR1925 (Lo-16bits)
HR1927	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1934				HR1933 (Lo-16bits)
HR1935	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1948	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1949	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1950	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1951	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1952	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1953	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1954	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1955	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1958				HR1957 (Lo-16bits)
HR1959	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1962				HR1961 (Lo-16bits)
HR1963	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1972	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1973	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1974	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1975	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1976	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1977	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1978	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1979	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1980	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1981	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1982	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1983	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1984	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1985	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1986	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1987	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1988	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1989	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1990	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1991	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1992	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1993	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1994	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1995	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1996	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1997	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1998	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1999	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2000	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2001	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2002	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2003	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2008				HR2007 (Lo-16bits)
HR2009	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2038				HR2037 (Lo-16bits)
HR2039	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2044				HR2043 (Lo-16bits)
HR2045	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2046				HR2045 (Lo-16bits)
HR2047	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2048				HR2047 (Lo-16bits)
HR2049	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2050				HR2049 (Lo-16bits)
HR2051	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2052				HR2051 (Lo-16bits)
HR2053	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2054				HR2053 (Lo-16bits)
HR2055	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2056				HR2055 (Lo-16bits)
HR2057	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)
HR2059	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)
HR2061	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2068	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2069	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2070	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2071	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2072	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2073	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2074	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2075	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)
HR2081	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2091	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2092	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2093	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2094	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2095	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2096	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2097	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2098	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2099	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2116	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2117	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2118	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2119	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2120	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2121	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2122	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2123	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2124	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2125	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2126	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2127	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2128	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2129	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2130	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2131	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	NOC2:Delay type	RW	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2134	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2135				HR2134 (Lo-16bits)
HR2136	NOC2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2137	NOC2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2138	NOC2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2139	NOC2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2140	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	NOC2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2149	NOC2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2150	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2151	NOC3:I>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2152	NOC3:I>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2153	NOC3:I>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2154	NOC3:I>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2155	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2158				HR2157 (Lo-16bits)
HR2159	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)
HR2165	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2178	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2179	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2180	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2181	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2182	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2183	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2184	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2185	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2186				HR2185 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2187	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)
HR2195	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)
HR2201	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2202	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2203	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2204	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2205	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2206	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2207	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2208	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2209	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2210	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2211	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2212	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2213	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2214	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2215	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2216	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2217	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2218	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2219	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2220	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2221	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2222	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2223	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2224	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2225	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2226	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2227	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2228	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2229	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2230	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2231	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2232	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2233	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2240				HR2239 (Lo-16bits)
HR2241	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2242				HR2241 (Lo-16bits)
HR2243	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2246				HR2245 (Lo-16bits)
HR2247	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)
HR2251	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)
HR2253	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2254				HR2253 (Lo-16bits)
HR2255	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2256				HR2255 (Lo-16bits)
HR2257	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2262				HR2261 (Lo-16bits)
HR2263	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)
HR2267	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2272				HR2271 (Lo-16bits)
HR2273	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2286				HR2285 (Lo-16bits)
HR2287	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)
HR2291	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2292				HR2291 (Lo-16bits)
HR2293	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2298	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2299	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2300	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2301	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2302	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2303	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2304	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2305	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2308				HR2307 (Lo-16bits)
HR2309	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)
HR2311	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2314				HR2313 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2315	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2322	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2323	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2324	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2325	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2326	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2327	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2328	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2329	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2330				HR2329 (Lo-16bits)
HR2331	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2332				HR2331 (Lo-16bits)
HR2333	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2334				HR2333 (Lo-16bits)
HR2335	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2336				HR2335 (Lo-16bits)
HR2337	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2338				HR2337 (Lo-16bits)
HR2339	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2340				HR2339 (Lo-16bits)
HR2341	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2342				HR2341 (Lo-16bits)
HR2343	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2344				HR2343 (Lo-16bits)
HR2345	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2346	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2347	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2348	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2349	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2350	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2351	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2352	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2353	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2354	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2355	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2356	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2357	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2358	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2359	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2360	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2361	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2362				HR2361 (Lo-16bits)
HR2363	NOC3:Delay type	RW	1	0=DT 1=IDMT
HR2364	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	NOC3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2367	NOC3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2368	NOC3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2369	NOC3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2370	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	NOC3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2379	NOC3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2380	NOC4: >>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2381	NOC4: >>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2382	NOC4: >>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC

Holding Register	Parameter	Access	Length	Info
HR2383	NOC4: >>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2384	NOC4: >>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2385	NOC4: >>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2386				HR2385 (Lo-16bits)
HR2387	NOC4: >>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	NOC4: >>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)
HR2391	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)
HR2393	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2402				HR2401 (Lo-16bits)
HR2403	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2404				HR2403 (Lo-16bits)
HR2405	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2406				HR2405 (Lo-16bits)
HR2407	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2408	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2409	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2410	NOC4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2411	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2412	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2413	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2414	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2415	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2416				HR2415 (Lo-16bits)
HR2417	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2418				HR2417 (Lo-16bits)
HR2419	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2420				HR2419 (Lo-16bits)
HR2421	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2422				HR2421 (Lo-16bits)
HR2423	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2424				HR2423 (Lo-16bits)
HR2425	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2426				HR2425 (Lo-16bits)
HR2427	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2428				HR2427 (Lo-16bits)
HR2429	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2430				HR2429 (Lo-16bits)
HR2431	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2432	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2433	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2434	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2435	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2436	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2437	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2438	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2439	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2440	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2441	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2442	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2443	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2444	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2445	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2446	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2447	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2448	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2449	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2450	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2451	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2452	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2453	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2454	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2455	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2456	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2457	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2458	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2459	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2460	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2461	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2462	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2463	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2464				HR2463 (Lo-16bits)
HR2465	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2466				HR2465 (Lo-16bits)
HR2467	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2468				HR2467 (Lo-16bits)
HR2469	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2470				HR2469 (Lo-16bits)
HR2471	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2472				HR2471 (Lo-16bits)
HR2473	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2474				HR2473 (Lo-16bits)
HR2475	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2476				HR2475 (Lo-16bits)
HR2477	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2478				HR2477 (Lo-16bits)
HR2479	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2480				HR2479 (Lo-16bits)
HR2481	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2482				HR2481 (Lo-16bits)
HR2483	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2484				HR2483 (Lo-16bits)
HR2485	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2486				HR2485 (Lo-16bits)
HR2487	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2488				HR2487 (Lo-16bits)
HR2489	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2490				HR2489 (Lo-16bits)
HR2491	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2492				HR2491 (Lo-16bits)
HR2493	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2494				HR2493 (Lo-16bits)
HR2495	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2496				HR2495 (Lo-16bits)
HR2497	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2498				HR2497 (Lo-16bits)
HR2499	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2500				HR2499 (Lo-16bits)
HR2501	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2502				HR2501 (Lo-16bits)
HR2503	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2504				HR2503 (Lo-16bits)
HR2505	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2506				HR2505 (Lo-16bits)
HR2507	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2508				HR2507 (Lo-16bits)
HR2509	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2510				HR2509 (Lo-16bits)
HR2511	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2512				HR2511 (Lo-16bits)
HR2513	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2514				HR2513 (Lo-16bits)
HR2515	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2516				HR2515 (Lo-16bits)
HR2517	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2528	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2529	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2530	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2531	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2532	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2533	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2534	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2535	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)
HR2537	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2541	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2552	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2553	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2554	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2555	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2556	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2557	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2558	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2559	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2565	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2576	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2577	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2578	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2579	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2580	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2581	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2582	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2583	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2584	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2585	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2586	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2587	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2588	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2589	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2590	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2591	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2592				HR2591 (Lo-16bits)
HR2593	NOC4:Delay type	RW	1	0=DT 1=IDMT
HR2594	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2595				HR2594 (Lo-16bits)
HR2596	NOC4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2597	NOC4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2598	NOC4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2599	NOC4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2600	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2601				HR2600 (Lo-16bits)
HR2602	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2603				HR2602 (Lo-16bits)
HR2604	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2607				HR2606 (Lo-16bits)
HR2608	NOC4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2609	NOC4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR2610	ROC1:ldrw> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2611	ROC1:ldrw> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2612	ROC2:ldrw>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2613	ROC2:ldrw>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2614	ROC3:ldrw>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2615	ROC3:ldrw>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2616	ROC4:ldrw>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2617	ROC4:ldrw>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2618	ROC5:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2619	ROC5:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2620	ROC6:ldrw>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2621	ROC6:ldrw>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2622	ROC7:ldrw>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2623	ROC7:ldrw>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2624	ROC8:ldrw>>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2625	ROC8:ldrw>>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2626	ROV1:Urw>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2627	ROV2:Urw>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2628	ROV3:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2629	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2630	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2631	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2632	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR2633	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=Start On (Alarm) 3=Start On (Alarm & Trip) 4=Start On (Trip) 5=Alarm On 6=Start On 7=Start On Alarm On 8=Trip On 9=Alarm On Trip On 10=Multistatus On
HR2634	DOC1:Idir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2635	DOC1:Idir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2636	DOC1:Idir> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2637	DOC1:Idir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DOC1:Idir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2640				HR2639 (Lo-16bits)
HR2641	DOC1:Idir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2642				HR2641 (Lo-16bits)
HR2643	DOC1:Idir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2646	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2647	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2648	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2649	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2650	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2651	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2652	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2653	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2654				HR2653 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2655	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2658				HR2657 (Lo-16bits)
HR2659	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2660				HR2659 (Lo-16bits)
HR2661	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2670				HR2669 (Lo-16bits)
HR2671	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)
HR2673	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2679	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2688				HR2687 (Lo-16bits)
HR2689	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2702	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2703	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2704	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2705	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2706	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2707	DOC1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2708	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2709	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2726	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2727	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2728	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2729	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2730	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2731	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2732	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2733	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2734	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2735	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2736	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2737	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2738	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2739	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2740	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2741	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2742	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2743	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2744	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2745	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2746	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2747	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2748	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2749	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2750	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2751	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2752	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2753	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2754	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2755	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2756	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2757	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)
HR2763	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2783	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2804				HR2803 (Lo-16bits)
HR2805	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2813	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2822	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2823	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2824	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2825	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2826	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2827	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2828	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2829	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2830				HR2829 (Lo-16bits)
HR2831	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2832				HR2831 (Lo-16bits)
HR2833	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2837	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2846	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2847	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2848	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2849	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2850	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2851	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2852	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2853	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2861	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2870	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2871	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2872	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2873	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2874	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2875	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2876	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2877	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2878	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2879	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2880	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2881	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2882	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2883	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2884	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2885	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2886	DOC2:ldir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2887	DOC2:ldir>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2888	DOC2:ldir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2889				HR2888 (Lo-16bits)
HR2890	DOC2:ldir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2891				HR2890 (Lo-16bits)
HR2892	DOC2:ldir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2893				HR2892 (Lo-16bits)
HR2894	DOC2:ldir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2895				HR2894 (Lo-16bits)
HR2896	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2897	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2898	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2899	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2900	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2901	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2902	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2903	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2904	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2905				HR2904 (Lo-16bits)
HR2906	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2907				HR2906 (Lo-16bits)
HR2908	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2909				HR2908 (Lo-16bits)
HR2910	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2911				HR2910 (Lo-16bits)
HR2912	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2913				HR2912 (Lo-16bits)
HR2914	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2915				HR2914 (Lo-16bits)
HR2916	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2917				HR2916 (Lo-16bits)
HR2918	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2919				HR2918 (Lo-16bits)
HR2920	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2921				HR2920 (Lo-16bits)
HR2922	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2923				HR2922 (Lo-16bits)
HR2924	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2925				HR2924 (Lo-16bits)
HR2926	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2927				HR2926 (Lo-16bits)
HR2928	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2929				HR2928 (Lo-16bits)
HR2930	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2931				HR2930 (Lo-16bits)
HR2932	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2933				HR2932 (Lo-16bits)
HR2934	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2935				HR2934 (Lo-16bits)
HR2936	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2937				HR2936 (Lo-16bits)
HR2938	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2939				HR2938 (Lo-16bits)
HR2940	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2941				HR2940 (Lo-16bits)
HR2942	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2943				HR2942 (Lo-16bits)
HR2944	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2945				HR2944 (Lo-16bits)
HR2946	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2947				HR2946 (Lo-16bits)
HR2948	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2949				HR2948 (Lo-16bits)
HR2950	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2951				HR2950 (Lo-16bits)
HR2952	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2953	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2954	DOC2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2955	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2956	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2957	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2958	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2959	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2960	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2961				HR2960 (Lo-16bits)
HR2962	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2963				HR2962 (Lo-16bits)
HR2964	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2965				HR2964 (Lo-16bits)
HR2966	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2967				HR2966 (Lo-16bits)
HR2968	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2969				HR2968 (Lo-16bits)
HR2970	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2971				HR2970 (Lo-16bits)
HR2972	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2973				HR2972 (Lo-16bits)
HR2974	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2975				HR2974 (Lo-16bits)
HR2976	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2977	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2978	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2979	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2980	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2981	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2982	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2983	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2984	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2985	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2986	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2987	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2988	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2989	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2990	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2991	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2992	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2993	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2994	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2995	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2996	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2997	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2998	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2999	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3000	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3001	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3002	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3003	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3004	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3005	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3006	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3007	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3008	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3009				HR3008 (Lo-16bits)
HR3010	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3011				HR3010 (Lo-16bits)
HR3012	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3013				HR3012 (Lo-16bits)
HR3014	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3015				HR3014 (Lo-16bits)
HR3016	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3017				HR3016 (Lo-16bits)
HR3018	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3019				HR3018 (Lo-16bits)
HR3020	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3021				HR3020 (Lo-16bits)
HR3022	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3023				HR3022 (Lo-16bits)
HR3024	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3025				HR3024 (Lo-16bits)
HR3026	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3027				HR3026 (Lo-16bits)
HR3028	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3029				HR3028 (Lo-16bits)
HR3030	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3031				HR3030 (Lo-16bits)
HR3032	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3033				HR3032 (Lo-16bits)
HR3034	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3035				HR3034 (Lo-16bits)
HR3036	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3037				HR3036 (Lo-16bits)
HR3038	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3039				HR3038 (Lo-16bits)
HR3040	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3041				HR3040 (Lo-16bits)
HR3042	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3043				HR3042 (Lo-16bits)
HR3044	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3045				HR3044 (Lo-16bits)
HR3046	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3047				HR3046 (Lo-16bits)
HR3048	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3049				HR3048 (Lo-16bits)
HR3050	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3051				HR3050 (Lo-16bits)
HR3052	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3053				HR3052 (Lo-16bits)
HR3054	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3055				HR3054 (Lo-16bits)
HR3056	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3057				HR3056 (Lo-16bits)
HR3058	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3059				HR3058 (Lo-16bits)
HR3060	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3061				HR3060 (Lo-16bits)
HR3062	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3063				HR3062 (Lo-16bits)
HR3064	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3065				HR3064 (Lo-16bits)
HR3066	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3067				HR3066 (Lo-16bits)
HR3068	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3069				HR3068 (Lo-16bits)
HR3070	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3071				HR3070 (Lo-16bits)
HR3072	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3073	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3074	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3075	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3076	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3077	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3078	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3079	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3080	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3081				HR3080 (Lo-16bits)
HR3082	DOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3083				HR3082 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3084	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3085				HR3084 (Lo-16bits)
HR3086	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3087				HR3086 (Lo-16bits)
HR3088	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3089				HR3088 (Lo-16bits)
HR3090	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3091				HR3090 (Lo-16bits)
HR3092	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3093				HR3092 (Lo-16bits)
HR3094	DOC2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3095				HR3094 (Lo-16bits)
HR3096	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3097	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3098	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3099	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3100	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3101	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3102	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3103	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3104	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3105				HR3104 (Lo-16bits)
HR3106	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3107				HR3106 (Lo-16bits)
HR3108	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3109				HR3108 (Lo-16bits)
HR3110	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3111				HR3110 (Lo-16bits)
HR3112	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3113				HR3112 (Lo-16bits)
HR3114	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3115				HR3114 (Lo-16bits)
HR3116	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3117				HR3116 (Lo-16bits)
HR3118	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3119				HR3118 (Lo-16bits)
HR3120	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3121	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3122	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3123	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3124	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3125	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3126	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3127	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3128	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3129	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3130	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3131	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3132	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3133	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3134	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3135	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3136	DOC3:ldir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3137	DOC3:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3138	DOC3:ldir>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3139	DOC3:ldir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	DOC3:ldir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)
HR3143	DOC3:ldir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	DOC3:ldir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)
HR3147	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3148	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3149	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR3150	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3151	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3152	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3153	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3154	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3155	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3156				HR3155 (Lo-16bits)
HR3157	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)
HR3165	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3173	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3200				HR3199 (Lo-16bits)
HR3201	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3204	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3205	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3206	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3207	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3208	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3209	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3210	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3211	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3226				HR3225 (Lo-16bits)
HR3227	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3228	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3229	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3230	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3231	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3232	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3233	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3234	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3235	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3236	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3237	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3238	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3239	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3240	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3241	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3242	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3243	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3244	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3245	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3246	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3247	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3248	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3249	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3250	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3251	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3252	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3253	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3254	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3255	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3256	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3257	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3258	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3259	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3274				HR3273 (Lo-16bits)
HR3275	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3304				HR3303 (Lo-16bits)
HR3305	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)
HR3315	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3324	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3325	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3326	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3327	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3328	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3329	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3330	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3331	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	DOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)
HR3347	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3348	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3349	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3350	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3351	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3352	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3353	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3354	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3355	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3372	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3373	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3374	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3375	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3376	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3377	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3378	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3379	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3380	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3381	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3382	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3383	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3384	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3385	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3386	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3387	DOC4:ldir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3388	DOC4:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3389	DOC4:ldir>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3390	DOC4:ldir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3391				HR3390 (Lo-16bits)
HR3392	DOC4:ldir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3393				HR3392 (Lo-16bits)
HR3394	DOC4:ldir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3395				HR3394 (Lo-16bits)
HR3396	DOC4:ldir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3397				HR3396 (Lo-16bits)
HR3398	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3399	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3400	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3401	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3402	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3403	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3404	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3405	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3406	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3407				HR3406 (Lo-16bits)
HR3408	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3409				HR3408 (Lo-16bits)
HR3410	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3411				HR3410 (Lo-16bits)
HR3412	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3413				HR3412 (Lo-16bits)
HR3414	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3415				HR3414 (Lo-16bits)
HR3416	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3417				HR3416 (Lo-16bits)
HR3418	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3419				HR3418 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3420	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3421				HR3420 (Lo-16bits)
HR3422	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3423				HR3422 (Lo-16bits)
HR3424	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3425				HR3424 (Lo-16bits)
HR3426	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3427				HR3426 (Lo-16bits)
HR3428	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3429				HR3428 (Lo-16bits)
HR3430	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3431				HR3430 (Lo-16bits)
HR3432	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3433				HR3432 (Lo-16bits)
HR3434	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3435				HR3434 (Lo-16bits)
HR3436	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3437				HR3436 (Lo-16bits)
HR3438	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3439				HR3438 (Lo-16bits)
HR3440	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3441				HR3440 (Lo-16bits)
HR3442	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3443				HR3442 (Lo-16bits)
HR3444	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3445				HR3444 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3446	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3447				HR3446 (Lo-16bits)
HR3448	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3449				HR3448 (Lo-16bits)
HR3450	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3451				HR3450 (Lo-16bits)
HR3452	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3453				HR3452 (Lo-16bits)
HR3454	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3455	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3456	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3457	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3458	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3459	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3460	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3461	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3462	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3463				HR3462 (Lo-16bits)
HR3464	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3465				HR3464 (Lo-16bits)
HR3466	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3467				HR3466 (Lo-16bits)
HR3468	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3469				HR3468 (Lo-16bits)
HR3470	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3471				HR3470 (Lo-16bits)
HR3472	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3473				HR3472 (Lo-16bits)
HR3474	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3475				HR3474 (Lo-16bits)
HR3476	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3477				HR3476 (Lo-16bits)
HR3478	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3479	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3480	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3481	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3482	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3483	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3484	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3485	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3486	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3487	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3488	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3489	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3490	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3491	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3492	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3493	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3494	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3495	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3496	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3497	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3498	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3499	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3500	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3501	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3502	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3503	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3504	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3505	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3506	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3507	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3508	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3509	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3510	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3511				HR3510 (Lo-16bits)
HR3512	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3513				HR3512 (Lo-16bits)
HR3514	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3515				HR3514 (Lo-16bits)
HR3516	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3517				HR3516 (Lo-16bits)
HR3518	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3519				HR3518 (Lo-16bits)
HR3520	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3521				HR3520 (Lo-16bits)
HR3522	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3523				HR3522 (Lo-16bits)
HR3524	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3525				HR3524 (Lo-16bits)
HR3526	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3527				HR3526 (Lo-16bits)
HR3528	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3529				HR3528 (Lo-16bits)
HR3530	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3531				HR3530 (Lo-16bits)
HR3532	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3533				HR3532 (Lo-16bits)
HR3534	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3535				HR3534 (Lo-16bits)
HR3536	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3537				HR3536 (Lo-16bits)
HR3538	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3539				HR3538 (Lo-16bits)
HR3540	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3541				HR3540 (Lo-16bits)
HR3542	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3543				HR3542 (Lo-16bits)
HR3544	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3545				HR3544 (Lo-16bits)
HR3546	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3547				HR3546 (Lo-16bits)
HR3548	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3549				HR3548 (Lo-16bits)
HR3550	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3551				HR3550 (Lo-16bits)
HR3552	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3553				HR3552 (Lo-16bits)
HR3554	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3555				HR3554 (Lo-16bits)
HR3556	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3557				HR3556 (Lo-16bits)
HR3558	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3559				HR3558 (Lo-16bits)
HR3560	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3561				HR3560 (Lo-16bits)
HR3562	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3563				HR3562 (Lo-16bits)
HR3564	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3565				HR3564 (Lo-16bits)
HR3566	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3567				HR3566 (Lo-16bits)
HR3568	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3569				HR3568 (Lo-16bits)
HR3570	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3571				HR3570 (Lo-16bits)
HR3572	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3573				HR3572 (Lo-16bits)
HR3574	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3575	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3576	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3577	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3578	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3579	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3580	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3581	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3582	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3583				HR3582 (Lo-16bits)
HR3584	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3585				HR3584 (Lo-16bits)
HR3586	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3587				HR3586 (Lo-16bits)
HR3588	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3589				HR3588 (Lo-16bits)
HR3590	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3591				HR3590 (Lo-16bits)
HR3592	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3593				HR3592 (Lo-16bits)
HR3594	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3595				HR3594 (Lo-16bits)
HR3596	DOC4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3597				HR3596 (Lo-16bits)
HR3598	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3599	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3600	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3601	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3602	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3603	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3604	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3605	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3606	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3607				HR3606 (Lo-16bits)
HR3608	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3609				HR3608 (Lo-16bits)
HR3610	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3611				HR3610 (Lo-16bits)
HR3612	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3613				HR3612 (Lo-16bits)
HR3614	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3615				HR3614 (Lo-16bits)
HR3616	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3617				HR3616 (Lo-16bits)
HR3618	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3619				HR3618 (Lo-16bits)
HR3620	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3621				HR3620 (Lo-16bits)
HR3622	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3623	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3624	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3625	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3626	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3627	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3628	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3629	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3630	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3631	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3632	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3633	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3634	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3635	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3636	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3637	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3638	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3639	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3640	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3641	NEF1:I0> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3642	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3643				HR3642 (Lo-16bits)
HR3644	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3645				HR3644 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3646	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3647				HR3646 (Lo-16bits)
HR3648	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3649				HR3648 (Lo-16bits)
HR3650	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3651				HR3650 (Lo-16bits)
HR3652	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3653				HR3652 (Lo-16bits)
HR3654	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3655				HR3654 (Lo-16bits)
HR3656	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3657				HR3656 (Lo-16bits)
HR3658	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3659				HR3658 (Lo-16bits)
HR3660	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3661	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3662	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3663	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3664	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3665	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3666	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3667	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3668	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3669				HR3668 (Lo-16bits)
HR3670	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3671				HR3670 (Lo-16bits)
HR3672	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3673				HR3672 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3674	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3675				HR3674 (Lo-16bits)
HR3676	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3677				HR3676 (Lo-16bits)
HR3678	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3679				HR3678 (Lo-16bits)
HR3680	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3681				HR3680 (Lo-16bits)
HR3682	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3683				HR3682 (Lo-16bits)
HR3684	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3685	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3686	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3687	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3688	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3689	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3690	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3691	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3692	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3693	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3694	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3695	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3696	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3697	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3698	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3699	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3700	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3701	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3702	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3703	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3704	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3705	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3706	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3707	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3708	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3709	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3710	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3711	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3712	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3713	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3714	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3715	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3716	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3717				HR3716 (Lo-16bits)
HR3718	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3719				HR3718 (Lo-16bits)
HR3720	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3721				HR3720 (Lo-16bits)
HR3722	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3723				HR3722 (Lo-16bits)
HR3724	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3725				HR3724 (Lo-16bits)
HR3726	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3727				HR3726 (Lo-16bits)
HR3728	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3729				HR3728 (Lo-16bits)
HR3730	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3731				HR3730 (Lo-16bits)
HR3732	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3733				HR3732 (Lo-16bits)
HR3734	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3735				HR3734 (Lo-16bits)
HR3736	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3737				HR3736 (Lo-16bits)
HR3738	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3739				HR3738 (Lo-16bits)
HR3740	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3741				HR3740 (Lo-16bits)
HR3742	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3743				HR3742 (Lo-16bits)
HR3744	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3745				HR3744 (Lo-16bits)
HR3746	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3747				HR3746 (Lo-16bits)
HR3748	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3749				HR3748 (Lo-16bits)
HR3750	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3751				HR3750 (Lo-16bits)
HR3752	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3753				HR3752 (Lo-16bits)
HR3754	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3755				HR3754 (Lo-16bits)
HR3756	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3757				HR3756 (Lo-16bits)
HR3758	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3759				HR3758 (Lo-16bits)
HR3760	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3761				HR3760 (Lo-16bits)
HR3762	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3763				HR3762 (Lo-16bits)
HR3764	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3765				HR3764 (Lo-16bits)
HR3766	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3767				HR3766 (Lo-16bits)
HR3768	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3769				HR3768 (Lo-16bits)
HR3770	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3771				HR3770 (Lo-16bits)
HR3772	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3773				HR3772 (Lo-16bits)
HR3774	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3775				HR3774 (Lo-16bits)
HR3776	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3777				HR3776 (Lo-16bits)
HR3778	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3779				HR3778 (Lo-16bits)
HR3780	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3781	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3782	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3783	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3784	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3785	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3786	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3787	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3788	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3789				HR3788 (Lo-16bits)
HR3790	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3791				HR3790 (Lo-16bits)
HR3792	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3793				HR3792 (Lo-16bits)
HR3794	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3795				HR3794 (Lo-16bits)
HR3796	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3797				HR3796 (Lo-16bits)
HR3798	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3799				HR3798 (Lo-16bits)
HR3800	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3801				HR3800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3802	NEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3803				HR3802 (Lo-16bits)
HR3804	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3805	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3806	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3807	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3808	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3809	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3810	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3811	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3812	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3813				HR3812 (Lo-16bits)
HR3814	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3815				HR3814 (Lo-16bits)
HR3816	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3817				HR3816 (Lo-16bits)
HR3818	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3819				HR3818 (Lo-16bits)
HR3820	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3821				HR3820 (Lo-16bits)
HR3822	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3823				HR3822 (Lo-16bits)
HR3824	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3825				HR3824 (Lo-16bits)
HR3826	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3827				HR3826 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3828	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3829	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3830	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3831	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3832	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3833	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3834	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3835	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3836	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3837	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3838	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3839	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3840	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3841	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3842	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3843	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3844	NEF1:Pick-up setting lset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR3845				HR3844 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3846	NEF1:Delay type	RW	1	0=DT 1=IDMT
HR3847	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	NEF1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR3850	NEF1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3851	NEF1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3852	NEF1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR3853	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	NEF1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR3862	NEF1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR3863	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3864	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3865	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3866	NEF2:I0>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3867	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3871	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3886	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3887	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3888	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3889	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3890	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3891	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3892	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3893	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3899	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3910	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3911	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3912	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3913	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3914	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3915	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3916	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3917	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3918	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3919	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3920	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3921	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3922	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3923	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3924	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3925	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3926	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3927	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3928	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3929	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3930	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3931	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3932	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3933	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3934	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3935	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3936	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3937	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3938	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3939	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3940	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3941	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3944				HR3943 (Lo-16bits)
HR3945	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)
HR3965	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3966				HR3965 (Lo-16bits)
HR3967	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3968				HR3967 (Lo-16bits)
HR3969	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3970				HR3969 (Lo-16bits)
HR3971	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3972				HR3971 (Lo-16bits)
HR3973	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3974				HR3973 (Lo-16bits)
HR3975	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3976				HR3975 (Lo-16bits)
HR3977	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3978				HR3977 (Lo-16bits)
HR3979	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3980				HR3979 (Lo-16bits)
HR3981	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4004				HR4003 (Lo-16bits)
HR4005	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4006	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4007	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4008	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4009	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4010	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4011	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4012	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4013	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4027	NEF2:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4030	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4031	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4032	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4033	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4034	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4035	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4036	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4037	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4053	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4054	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4055	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4056	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4057	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4058	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4059	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4060	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4061	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4062	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4063	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4064	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4065	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4066	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4067	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4068	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4069	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4070				HR4069 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4071	NEF2:Delay type	RW	1	0=DT 1=IDMT
HR4072	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4073				HR4072 (Lo-16bits)
HR4074	NEF2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4075	NEF2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4076	NEF2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4077	NEF2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4078	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4079				HR4078 (Lo-16bits)
HR4080	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4081				HR4080 (Lo-16bits)
HR4082	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4083				HR4082 (Lo-16bits)
HR4084	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4085				HR4084 (Lo-16bits)
HR4086	NEF2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4087	NEF2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4088	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4089	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4090	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4091	NEF3:I0>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4092	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4093				HR4092 (Lo-16bits)
HR4094	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4095				HR4094 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4096	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4097				HR4096 (Lo-16bits)
HR4098	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4099				HR4098 (Lo-16bits)
HR4100	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4101				HR4100 (Lo-16bits)
HR4102	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4103				HR4102 (Lo-16bits)
HR4104	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4105				HR4104 (Lo-16bits)
HR4106	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4107				HR4106 (Lo-16bits)
HR4108	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4109				HR4108 (Lo-16bits)
HR4110	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4111	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4112	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4113	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4114	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4115	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4116	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4117	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4118	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4119				HR4118 (Lo-16bits)
HR4120	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4121				HR4120 (Lo-16bits)
HR4122	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4123				HR4122 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4124	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4125				HR4124 (Lo-16bits)
HR4126	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4127				HR4126 (Lo-16bits)
HR4128	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4129				HR4128 (Lo-16bits)
HR4130	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4131				HR4130 (Lo-16bits)
HR4132	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4133				HR4132 (Lo-16bits)
HR4134	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4135	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4136	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4137	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4138	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4139	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4140	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4141	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4142	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4143	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4144	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4145	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4146	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4147	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR4148	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4149	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4150	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4151	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4152	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4153	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4154	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4155	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4156	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4157	NEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4158	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4159	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4160	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4161	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4162	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4163	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4164	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4165	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4166	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4167				HR4166 (Lo-16bits)
HR4168	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4169				HR4168 (Lo-16bits)
HR4170	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4171				HR4170 (Lo-16bits)
HR4172	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4173				HR4172 (Lo-16bits)
HR4174	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4175				HR4174 (Lo-16bits)
HR4176	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4177				HR4176 (Lo-16bits)
HR4178	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4179				HR4178 (Lo-16bits)
HR4180	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4181				HR4180 (Lo-16bits)
HR4182	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4183				HR4182 (Lo-16bits)
HR4184	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4185				HR4184 (Lo-16bits)
HR4186	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4187				HR4186 (Lo-16bits)
HR4188	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4189				HR4188 (Lo-16bits)
HR4190	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4191				HR4190 (Lo-16bits)
HR4192	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4193				HR4192 (Lo-16bits)
HR4194	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4195				HR4194 (Lo-16bits)
HR4196	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4197				HR4196 (Lo-16bits)
HR4198	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4199				HR4198 (Lo-16bits)
HR4200	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4201				HR4200 (Lo-16bits)
HR4202	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4203				HR4202 (Lo-16bits)
HR4204	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4205				HR4204 (Lo-16bits)
HR4206	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4207				HR4206 (Lo-16bits)
HR4208	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4209				HR4208 (Lo-16bits)
HR4210	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4211				HR4210 (Lo-16bits)
HR4212	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4213				HR4212 (Lo-16bits)
HR4214	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4215				HR4214 (Lo-16bits)
HR4216	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4217				HR4216 (Lo-16bits)
HR4218	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4219				HR4218 (Lo-16bits)
HR4220	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4221				HR4220 (Lo-16bits)
HR4222	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4223				HR4222 (Lo-16bits)
HR4224	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4225				HR4224 (Lo-16bits)
HR4226	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4227				HR4226 (Lo-16bits)
HR4228	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4229				HR4228 (Lo-16bits)
HR4230	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4231	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4232	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4233	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4234	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4235	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4236	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4237	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4238	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4239				HR4238 (Lo-16bits)
HR4240	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4241				HR4240 (Lo-16bits)
HR4242	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4243				HR4242 (Lo-16bits)
HR4244	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4245				HR4244 (Lo-16bits)
HR4246	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4247				HR4246 (Lo-16bits)
HR4248	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4249				HR4248 (Lo-16bits)
HR4250	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4251				HR4250 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4252	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4253				HR4252 (Lo-16bits)
HR4254	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4255	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4256	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4257	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4258	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4259	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4260	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4261	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4262	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4263				HR4262 (Lo-16bits)
HR4264	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4265				HR4264 (Lo-16bits)
HR4266	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4267				HR4266 (Lo-16bits)
HR4268	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4269				HR4268 (Lo-16bits)
HR4270	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4271				HR4270 (Lo-16bits)
HR4272	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4273				HR4272 (Lo-16bits)
HR4274	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4275				HR4274 (Lo-16bits)
HR4276	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4277				HR4276 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4278	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4279	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4280	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4281	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4282	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4283	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4284	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4285	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4286	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4287	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4288	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4289	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4290	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4291	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4292	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4293	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4294	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4295				HR4294 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4296	NEF3:Delay type	RW	1	0=DT 1=IDMT
HR4297	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	NEF3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4300	NEF3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4301	NEF3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4302	NEF3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4303	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4310				HR4309 (Lo-16bits)
HR4311	NEF3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4312	NEF3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4313	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4314	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4315	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4316	NEF4:I0>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4317	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4321	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4336	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4337	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4338	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4339	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4340	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4341	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4342	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4343	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4349	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4360	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4361	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4362	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4363	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4364	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4365	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4366	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4367	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4368	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4369	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4370	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4371	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4372	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR4373	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4374	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4375	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4376	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4377	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4378	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4379	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4380	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4381	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4382	NEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4383	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4384	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4385	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4386	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4387	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4388	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4389	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4390	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4391	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4394				HR4393 (Lo-16bits)
HR4395	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4396				HR4395 (Lo-16bits)
HR4397	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)
HR4415	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4418				HR4417 (Lo-16bits)
HR4419	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4420				HR4419 (Lo-16bits)
HR4421	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4424				HR4423 (Lo-16bits)
HR4425	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)
HR4445	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4454				HR4453 (Lo-16bits)
HR4455	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4456	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4457	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4458	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4459	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4460	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4461	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4462	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4463	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4477	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4480	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4481	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4482	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4483	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4484	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4485	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4486	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4487	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)
HR4493	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4503	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4504	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4505	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4506	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4507	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4508	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4509	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4510	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4511	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4512	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4513	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4514	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4515	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4516	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4517	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4518	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4519	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4521	NEF4:Delay type	RW	1	0=DT 1=IDMT
HR4522	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4523				HR4522 (Lo-16bits)
HR4524	NEF4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4525	NEF4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4526	NEF4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4527	NEF4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4528	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4529				HR4528 (Lo-16bits)
HR4530	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4531				HR4530 (Lo-16bits)
HR4532	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4533				HR4532 (Lo-16bits)
HR4534	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4535				HR4534 (Lo-16bits)
HR4536	NEF4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4537	NEF4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR4538	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4539	DEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4540	DEF1:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4541	DEF1:I0dir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4542	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4543				HR4542 (Lo-16bits)
HR4544	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4545				HR4544 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4546	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4547				HR4546 (Lo-16bits)
HR4548	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4549	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4550	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4551	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4552	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4553	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4554	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4555	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4556	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4557				HR4556 (Lo-16bits)
HR4558	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4559				HR4558 (Lo-16bits)
HR4560	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4561				HR4560 (Lo-16bits)
HR4562	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4563				HR4562 (Lo-16bits)
HR4564	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4565				HR4564 (Lo-16bits)
HR4566	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4567				HR4566 (Lo-16bits)
HR4568	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4569				HR4568 (Lo-16bits)
HR4570	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4571				HR4570 (Lo-16bits)
HR4572	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4573				HR4572 (Lo-16bits)
HR4574	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4575				HR4574 (Lo-16bits)
HR4576	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4577				HR4576 (Lo-16bits)
HR4578	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4579				HR4578 (Lo-16bits)
HR4580	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4581				HR4580 (Lo-16bits)
HR4582	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4583				HR4582 (Lo-16bits)
HR4584	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4585				HR4584 (Lo-16bits)
HR4586	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4587				HR4586 (Lo-16bits)
HR4588	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4589				HR4588 (Lo-16bits)
HR4590	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4591				HR4590 (Lo-16bits)
HR4592	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4593				HR4592 (Lo-16bits)
HR4594	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4595				HR4594 (Lo-16bits)
HR4596	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4597				HR4596 (Lo-16bits)
HR4598	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4599				HR4598 (Lo-16bits)
HR4600	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4601				HR4600 (Lo-16bits)
HR4602	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4603				HR4602 (Lo-16bits)
HR4604	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4605				HR4604 (Lo-16bits)
HR4606	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4607				HR4606 (Lo-16bits)
HR4608	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4609				HR4608 (Lo-16bits)
HR4610	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4611				HR4610 (Lo-16bits)
HR4612	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4613				HR4612 (Lo-16bits)
HR4614	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4615				HR4614 (Lo-16bits)
HR4616	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4617				HR4616 (Lo-16bits)
HR4618	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4619				HR4618 (Lo-16bits)
HR4620	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4621				HR4620 (Lo-16bits)
HR4622	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4623				HR4622 (Lo-16bits)
HR4624	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4625				HR4624 (Lo-16bits)
HR4626	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4627				HR4626 (Lo-16bits)
HR4628	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4629				HR4628 (Lo-16bits)
HR4630	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4631				HR4630 (Lo-16bits)
HR4632	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4633				HR4632 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4634	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4635				HR4634 (Lo-16bits)
HR4636	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4637				HR4636 (Lo-16bits)
HR4638	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4639				HR4638 (Lo-16bits)
HR4640	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4641				HR4640 (Lo-16bits)
HR4642	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4643				HR4642 (Lo-16bits)
HR4644	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4645				HR4644 (Lo-16bits)
HR4646	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4647				HR4646 (Lo-16bits)
HR4648	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4649				HR4648 (Lo-16bits)
HR4650	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4651				HR4650 (Lo-16bits)
HR4652	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4653	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4654	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4655	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4656	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used

Holding Register	Parameter	Access	Length	Info
HR4657	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4658	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4659	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4660	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4661				HR4660 (Lo-16bits)
HR4662	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4663				HR4662 (Lo-16bits)
HR4664	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4665				HR4664 (Lo-16bits)
HR4666	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4667				HR4666 (Lo-16bits)
HR4668	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4669				HR4668 (Lo-16bits)
HR4670	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4671				HR4670 (Lo-16bits)
HR4672	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4673				HR4672 (Lo-16bits)
HR4674	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4675				HR4674 (Lo-16bits)
HR4676	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4677				HR4676 (Lo-16bits)
HR4678	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4679				HR4678 (Lo-16bits)
HR4680	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4681				HR4680 (Lo-16bits)
HR4682	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4683				HR4682 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4684	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4685				HR4684 (Lo-16bits)
HR4686	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4687				HR4686 (Lo-16bits)
HR4688	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4689				HR4688 (Lo-16bits)
HR4690	DEF1:Unearth. Pick-up setting I0set (I0Sinfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4691				HR4690 (Lo-16bits)
HR4692	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4693				HR4692 (Lo-16bits)
HR4694	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4695				HR4694 (Lo-16bits)
HR4696	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4697				HR4696 (Lo-16bits)
HR4698	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4699				HR4698 (Lo-16bits)
HR4700	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4701				HR4700 (Lo-16bits)
HR4702	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4703				HR4702 (Lo-16bits)
HR4704	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4705				HR4704 (Lo-16bits)
HR4706	DEF1:Unearth. Pick-up setting U0set(I0Sinfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4707				HR4706 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4708	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4709				HR4708 (Lo-16bits)
HR4710	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4711				HR4710 (Lo-16bits)
HR4712	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4713				HR4712 (Lo-16bits)
HR4714	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4715				HR4714 (Lo-16bits)
HR4716	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4717				HR4716 (Lo-16bits)
HR4718	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4719				HR4718 (Lo-16bits)
HR4720	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4721				HR4720 (Lo-16bits)
HR4722	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4723				HR4722 (Lo-16bits)
HR4724	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4725				HR4724 (Lo-16bits)
HR4726	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4727				HR4726 (Lo-16bits)
HR4728	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4729				HR4728 (Lo-16bits)
HR4730	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4731				HR4730 (Lo-16bits)
HR4732	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4733				HR4732 (Lo-16bits)
HR4734	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4735				HR4734 (Lo-16bits)
HR4736	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4737				HR4736 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4738	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4739				HR4738 (Lo-16bits)
HR4740	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4741	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4742	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4743	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4744	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4745	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4746	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4747	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4748	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4749	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4750	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4751	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4752	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4753	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4754	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4755	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4756	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4757				HR4756 (Lo-16bits)
HR4758	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4759				HR4758 (Lo-16bits)
HR4760	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4761				HR4760 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4762	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4763				HR4762 (Lo-16bits)
HR4764	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4765				HR4764 (Lo-16bits)
HR4766	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4767				HR4766 (Lo-16bits)
HR4768	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4769				HR4768 (Lo-16bits)
HR4770	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4771				HR4770 (Lo-16bits)
HR4772	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4773				HR4772 (Lo-16bits)
HR4774	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4775				HR4774 (Lo-16bits)
HR4776	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4777				HR4776 (Lo-16bits)
HR4778	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4779				HR4778 (Lo-16bits)
HR4780	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4781				HR4780 (Lo-16bits)
HR4782	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4783				HR4782 (Lo-16bits)
HR4784	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4785				HR4784 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4786	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4787				HR4786 (Lo-16bits)
HR4788	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4789				HR4788 (Lo-16bits)
HR4790	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4791				HR4790 (Lo-16bits)
HR4792	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4793				HR4792 (Lo-16bits)
HR4794	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4795				HR4794 (Lo-16bits)
HR4796	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4797				HR4796 (Lo-16bits)
HR4798	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4799				HR4798 (Lo-16bits)
HR4800	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4801				HR4800 (Lo-16bits)
HR4802	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4803				HR4802 (Lo-16bits)
HR4804	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4805	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4806	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4807	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4808	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4809	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4810	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4811	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4812	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4813	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4814	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4815	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4816	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4817	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4818	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4819	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4820	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4821	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4822	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4823	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4824	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4825	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4826	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4827	DEF1:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4828	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4829	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4830	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4831	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4832	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4833	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4834	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4835	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4836	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4837				HR4836 (Lo-16bits)
HR4838	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4839				HR4838 (Lo-16bits)
HR4840	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4841				HR4840 (Lo-16bits)
HR4842	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4843				HR4842 (Lo-16bits)
HR4844	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4845				HR4844 (Lo-16bits)
HR4846	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4847				HR4846 (Lo-16bits)
HR4848	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4849				HR4848 (Lo-16bits)
HR4850	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4851				HR4850 (Lo-16bits)
HR4852	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4853				HR4852 (Lo-16bits)
HR4854	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4855				HR4854 (Lo-16bits)
HR4856	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4857				HR4856 (Lo-16bits)
HR4858	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4859				HR4858 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4860	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4861				HR4860 (Lo-16bits)
HR4862	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4863				HR4862 (Lo-16bits)
HR4864	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4865				HR4864 (Lo-16bits)
HR4866	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4867				HR4866 (Lo-16bits)
HR4868	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4869				HR4868 (Lo-16bits)
HR4870	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4871				HR4870 (Lo-16bits)
HR4872	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4873				HR4872 (Lo-16bits)
HR4874	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4875				HR4874 (Lo-16bits)
HR4876	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4877				HR4876 (Lo-16bits)
HR4878	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4879				HR4878 (Lo-16bits)
HR4880	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4881				HR4880 (Lo-16bits)
HR4882	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4883				HR4882 (Lo-16bits)
HR4884	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4885				HR4884 (Lo-16bits)
HR4886	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4887				HR4886 (Lo-16bits)
HR4888	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4889				HR4888 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4890	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4891				HR4890 (Lo-16bits)
HR4892	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4893				HR4892 (Lo-16bits)
HR4894	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4895				HR4894 (Lo-16bits)
HR4896	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4897				HR4896 (Lo-16bits)
HR4898	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4899				HR4898 (Lo-16bits)
HR4900	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4901	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4902	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4903	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4904	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4905	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4906	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4907	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4908	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4909				HR4908 (Lo-16bits)
HR4910	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4911				HR4910 (Lo-16bits)
HR4912	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4913				HR4912 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4914	DEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4915				HR4914 (Lo-16bits)
HR4916	DEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4917				HR4916 (Lo-16bits)
HR4918	DEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4919				HR4918 (Lo-16bits)
HR4920	DEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4921				HR4920 (Lo-16bits)
HR4922	DEF1:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4923				HR4922 (Lo-16bits)
HR4924	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4925	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4926	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4927	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4928	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4929	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4930	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4931	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4932	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4933				HR4932 (Lo-16bits)
HR4934	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4935				HR4934 (Lo-16bits)
HR4936	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4937				HR4936 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4938	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4939				HR4938 (Lo-16bits)
HR4940	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4941				HR4940 (Lo-16bits)
HR4942	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4943				HR4942 (Lo-16bits)
HR4944	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4945				HR4944 (Lo-16bits)
HR4946	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4947				HR4946 (Lo-16bits)
HR4948	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4949	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4950	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4951	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4952	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4953	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4954	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4955	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4956	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4957	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4958	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4959	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4960	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4961	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4962	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4963	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4964	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR4965	DEF2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4966	DEF2:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4967	DEF2:I0dir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4968	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4969				HR4968 (Lo-16bits)
HR4970	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4971				HR4970 (Lo-16bits)
HR4972	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4973				HR4972 (Lo-16bits)
HR4974	DEF2:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4975	DEF2:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4976	DEF2:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4977	DEF2:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR4978	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4979	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4980	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4981	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4982	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4983				HR4982 (Lo-16bits)
HR4984	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4985				HR4984 (Lo-16bits)
HR4986	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4987				HR4986 (Lo-16bits)
HR4988	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4989				HR4988 (Lo-16bits)
HR4990	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4991				HR4990 (Lo-16bits)
HR4992	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4993				HR4992 (Lo-16bits)
HR4994	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4995				HR4994 (Lo-16bits)
HR4996	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4997				HR4996 (Lo-16bits)
HR4998	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4999				HR4998 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5000	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5001				HR5000 (Lo-16bits)
HR5002	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5003				HR5002 (Lo-16bits)
HR5004	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5005				HR5004 (Lo-16bits)
HR5006	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5007				HR5006 (Lo-16bits)
HR5008	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5009				HR5008 (Lo-16bits)
HR5010	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5011				HR5010 (Lo-16bits)
HR5012	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5013				HR5012 (Lo-16bits)
HR5014	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5015				HR5014 (Lo-16bits)
HR5016	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5017				HR5016 (Lo-16bits)
HR5018	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5019				HR5018 (Lo-16bits)
HR5020	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5021				HR5020 (Lo-16bits)
HR5022	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5023				HR5022 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5024	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5025				HR5024 (Lo-16bits)
HR5026	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5027				HR5026 (Lo-16bits)
HR5028	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5029				HR5028 (Lo-16bits)
HR5030	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5031				HR5030 (Lo-16bits)
HR5032	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5033				HR5032 (Lo-16bits)
HR5034	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5035				HR5034 (Lo-16bits)
HR5036	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5037				HR5036 (Lo-16bits)
HR5038	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5039				HR5038 (Lo-16bits)
HR5040	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5041				HR5040 (Lo-16bits)
HR5042	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5043				HR5042 (Lo-16bits)
HR5044	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5045				HR5044 (Lo-16bits)
HR5046	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5047				HR5046 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5048	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5049				HR5048 (Lo-16bits)
HR5050	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5051				HR5050 (Lo-16bits)
HR5052	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5053				HR5052 (Lo-16bits)
HR5054	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5055				HR5054 (Lo-16bits)
HR5056	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5057				HR5056 (Lo-16bits)
HR5058	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5059				HR5058 (Lo-16bits)
HR5060	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5061				HR5060 (Lo-16bits)
HR5062	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5063				HR5062 (Lo-16bits)
HR5064	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5065				HR5064 (Lo-16bits)
HR5066	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5067				HR5066 (Lo-16bits)
HR5068	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5069				HR5068 (Lo-16bits)
HR5070	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5071				HR5070 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5072	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5073				HR5072 (Lo-16bits)
HR5074	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5075				HR5074 (Lo-16bits)
HR5076	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5077				HR5076 (Lo-16bits)
HR5078	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5079	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5080	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5081	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5082	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5083	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5084	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5085	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5086	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5087				HR5086 (Lo-16bits)
HR5088	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5089				HR5088 (Lo-16bits)
HR5090	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5091				HR5090 (Lo-16bits)
HR5092	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5093				HR5092 (Lo-16bits)
HR5094	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5095				HR5094 (Lo-16bits)
HR5096	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5097				HR5096 (Lo-16bits)
HR5098	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5099				HR5098 (Lo-16bits)
HR5100	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5101				HR5100 (Lo-16bits)
HR5102	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5103				HR5102 (Lo-16bits)
HR5104	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5105				HR5104 (Lo-16bits)
HR5106	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5107				HR5106 (Lo-16bits)
HR5108	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5109				HR5108 (Lo-16bits)
HR5110	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5111				HR5110 (Lo-16bits)
HR5112	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5113				HR5112 (Lo-16bits)
HR5114	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5115				HR5114 (Lo-16bits)
HR5116	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5117				HR5116 (Lo-16bits)
HR5118	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5119				HR5118 (Lo-16bits)
HR5120	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5121				HR5120 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5122	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5123				HR5122 (Lo-16bits)
HR5124	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5125				HR5124 (Lo-16bits)
HR5126	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5127				HR5126 (Lo-16bits)
HR5128	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5129				HR5128 (Lo-16bits)
HR5130	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5131				HR5130 (Lo-16bits)
HR5132	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5133				HR5132 (Lo-16bits)
HR5134	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5135				HR5134 (Lo-16bits)
HR5136	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5137				HR5136 (Lo-16bits)
HR5138	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5139				HR5138 (Lo-16bits)
HR5140	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5141				HR5140 (Lo-16bits)
HR5142	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5143				HR5142 (Lo-16bits)
HR5144	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5145				HR5144 (Lo-16bits)
HR5146	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5147				HR5146 (Lo-16bits)
HR5148	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5149				HR5148 (Lo-16bits)
HR5150	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5151				HR5150 (Lo-16bits)
HR5152	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5153				HR5152 (Lo-16bits)
HR5154	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5155				HR5154 (Lo-16bits)
HR5156	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5157				HR5156 (Lo-16bits)
HR5158	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5159				HR5158 (Lo-16bits)
HR5160	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5161				HR5160 (Lo-16bits)
HR5162	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5163				HR5162 (Lo-16bits)
HR5164	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5165				HR5164 (Lo-16bits)
HR5166	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5167	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5168	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5169	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5170	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5171	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5172	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5173	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5174	DEF2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR5175	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5176	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5177	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5178	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5179	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5180	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5181	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5182	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5183				HR5182 (Lo-16bits)
HR5184	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5185				HR5184 (Lo-16bits)
HR5186	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5187				HR5186 (Lo-16bits)
HR5188	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5189				HR5188 (Lo-16bits)
HR5190	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5191				HR5190 (Lo-16bits)
HR5192	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5193				HR5192 (Lo-16bits)
HR5194	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5195				HR5194 (Lo-16bits)
HR5196	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5197				HR5196 (Lo-16bits)
HR5198	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5199				HR5198 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5200	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5201				HR5200 (Lo-16bits)
HR5202	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5203				HR5202 (Lo-16bits)
HR5204	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5205				HR5204 (Lo-16bits)
HR5206	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5207				HR5206 (Lo-16bits)
HR5208	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5209				HR5208 (Lo-16bits)
HR5210	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5211				HR5210 (Lo-16bits)
HR5212	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5213				HR5212 (Lo-16bits)
HR5214	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5215				HR5214 (Lo-16bits)
HR5216	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5217				HR5216 (Lo-16bits)
HR5218	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5219				HR5218 (Lo-16bits)
HR5220	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5221				HR5220 (Lo-16bits)
HR5222	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5223				HR5222 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5224	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5225				HR5224 (Lo-16bits)
HR5226	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5227				HR5226 (Lo-16bits)
HR5228	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5229				HR5228 (Lo-16bits)
HR5230	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5231	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5232	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5233	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5234	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5235	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5236	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5237	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5238	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5239	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5240	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5241	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5242	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5243	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5244	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5245	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5246	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5247	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5248	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5249	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5250	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5251	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5252	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5253	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5254	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5255	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5256	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5257	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5258	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5259	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5260	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5261	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5262	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5263				HR5262 (Lo-16bits)
HR5264	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5265				HR5264 (Lo-16bits)
HR5266	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5267				HR5266 (Lo-16bits)
HR5268	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5269				HR5268 (Lo-16bits)
HR5270	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5271				HR5270 (Lo-16bits)
HR5272	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5273				HR5272 (Lo-16bits)
HR5274	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5275				HR5274 (Lo-16bits)
HR5276	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5277				HR5276 (Lo-16bits)
HR5278	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5279				HR5278 (Lo-16bits)
HR5280	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5281				HR5280 (Lo-16bits)
HR5282	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5283				HR5282 (Lo-16bits)
HR5284	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5285				HR5284 (Lo-16bits)
HR5286	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5287				HR5286 (Lo-16bits)
HR5288	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5289				HR5288 (Lo-16bits)
HR5290	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5291				HR5290 (Lo-16bits)
HR5292	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5293				HR5292 (Lo-16bits)
HR5294	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5295				HR5294 (Lo-16bits)
HR5296	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5297				HR5296 (Lo-16bits)
HR5298	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5299				HR5298 (Lo-16bits)
HR5300	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5301				HR5300 (Lo-16bits)
HR5302	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5303				HR5302 (Lo-16bits)
HR5304	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5305				HR5304 (Lo-16bits)
HR5306	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5307				HR5306 (Lo-16bits)
HR5308	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5309				HR5308 (Lo-16bits)
HR5310	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5311				HR5310 (Lo-16bits)
HR5312	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5313				HR5312 (Lo-16bits)
HR5314	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5315				HR5314 (Lo-16bits)
HR5316	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5317				HR5316 (Lo-16bits)
HR5318	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5319				HR5318 (Lo-16bits)
HR5320	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5321				HR5320 (Lo-16bits)
HR5322	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5323				HR5322 (Lo-16bits)
HR5324	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5325				HR5324 (Lo-16bits)
HR5326	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5327	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5328	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5329	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5330	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5331	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5332	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5333	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5334	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5335				HR5334 (Lo-16bits)
HR5336	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5337				HR5336 (Lo-16bits)
HR5338	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5339				HR5338 (Lo-16bits)
HR5340	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5341				HR5340 (Lo-16bits)
HR5342	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5343				HR5342 (Lo-16bits)
HR5344	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5345				HR5344 (Lo-16bits)
HR5346	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5347				HR5346 (Lo-16bits)
HR5348	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5349				HR5348 (Lo-16bits)
HR5350	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5351	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5352	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5353	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5354	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5355	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5356	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5357	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5358	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5359				HR5358 (Lo-16bits)
HR5360	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5361				HR5360 (Lo-16bits)
HR5362	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5363				HR5362 (Lo-16bits)
HR5364	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5365				HR5364 (Lo-16bits)
HR5366	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5367				HR5366 (Lo-16bits)
HR5368	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5369				HR5368 (Lo-16bits)
HR5370	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5371				HR5370 (Lo-16bits)
HR5372	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5373				HR5372 (Lo-16bits)
HR5374	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5375	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5376	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5377	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5378	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5379	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5380	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5381	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5382	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5383	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5384	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5385	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5386	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5387	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5388	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5389	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5390	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5391	DEF3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5392	DEF3:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5393	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip

Holding Register	Parameter	Access	Length	Info
HR5394	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5395				HR5394 (Lo-16bits)
HR5396	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5397				HR5396 (Lo-16bits)
HR5398	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5399				HR5398 (Lo-16bits)
HR5400	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5401	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5402	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5403	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5404	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5405	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5406	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5407	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5408	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5409				HR5408 (Lo-16bits)
HR5410	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5411				HR5410 (Lo-16bits)
HR5412	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5413				HR5412 (Lo-16bits)
HR5414	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5415				HR5414 (Lo-16bits)
HR5416	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5417				HR5416 (Lo-16bits)
HR5418	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5419				HR5418 (Lo-16bits)
HR5420	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5421				HR5420 (Lo-16bits)
HR5422	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5423				HR5422 (Lo-16bits)
HR5424	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5425				HR5424 (Lo-16bits)
HR5426	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5427				HR5426 (Lo-16bits)
HR5428	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5429				HR5428 (Lo-16bits)
HR5430	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5431				HR5430 (Lo-16bits)
HR5432	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5433				HR5432 (Lo-16bits)
HR5434	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5435				HR5434 (Lo-16bits)
HR5436	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5437				HR5436 (Lo-16bits)
HR5438	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5439				HR5438 (Lo-16bits)
HR5440	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5441				HR5440 (Lo-16bits)
HR5442	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5443				HR5442 (Lo-16bits)
HR5444	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5445				HR5444 (Lo-16bits)
HR5446	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5447				HR5446 (Lo-16bits)
HR5448	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5449				HR5448 (Lo-16bits)
HR5450	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5451				HR5450 (Lo-16bits)
HR5452	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5453				HR5452 (Lo-16bits)
HR5454	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5455				HR5454 (Lo-16bits)
HR5456	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5457				HR5456 (Lo-16bits)
HR5458	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5459				HR5458 (Lo-16bits)
HR5460	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5461				HR5460 (Lo-16bits)
HR5462	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5463				HR5462 (Lo-16bits)
HR5464	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5465				HR5464 (Lo-16bits)
HR5466	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5467				HR5466 (Lo-16bits)
HR5468	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5469				HR5468 (Lo-16bits)
HR5470	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5471				HR5470 (Lo-16bits)
HR5472	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5473				HR5472 (Lo-16bits)
HR5474	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5475				HR5474 (Lo-16bits)
HR5476	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5477				HR5476 (Lo-16bits)
HR5478	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5479				HR5478 (Lo-16bits)
HR5480	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5481				HR5480 (Lo-16bits)
HR5482	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5483				HR5482 (Lo-16bits)
HR5484	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5485				HR5484 (Lo-16bits)
HR5486	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5487				HR5486 (Lo-16bits)
HR5488	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5489				HR5488 (Lo-16bits)
HR5490	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5491				HR5490 (Lo-16bits)
HR5492	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5493				HR5492 (Lo-16bits)
HR5494	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5495				HR5494 (Lo-16bits)
HR5496	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5497				HR5496 (Lo-16bits)
HR5498	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5499				HR5498 (Lo-16bits)
HR5500	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5501				HR5500 (Lo-16bits)
HR5502	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5503				HR5502 (Lo-16bits)
HR5504	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5505	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5506	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5507	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used

Holding Register	Parameter	Access	Length	Info
HR5508	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5509	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5510	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5511	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5512	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5513				HR5512 (Lo-16bits)
HR5514	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5515				HR5514 (Lo-16bits)
HR5516	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5517				HR5516 (Lo-16bits)
HR5518	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5519				HR5518 (Lo-16bits)
HR5520	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5521				HR5520 (Lo-16bits)
HR5522	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5523				HR5522 (Lo-16bits)
HR5524	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5525				HR5524 (Lo-16bits)
HR5526	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5527				HR5526 (Lo-16bits)
HR5528	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5529				HR5528 (Lo-16bits)
HR5530	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5531				HR5530 (Lo-16bits)
HR5532	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5533				HR5532 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5534	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5535				HR5534 (Lo-16bits)
HR5536	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5537				HR5536 (Lo-16bits)
HR5538	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5539				HR5538 (Lo-16bits)
HR5540	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5541				HR5540 (Lo-16bits)
HR5542	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5543				HR5542 (Lo-16bits)
HR5544	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5545				HR5544 (Lo-16bits)
HR5546	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5547				HR5546 (Lo-16bits)
HR5548	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5549				HR5548 (Lo-16bits)
HR5550	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5551				HR5550 (Lo-16bits)
HR5552	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5553				HR5552 (Lo-16bits)
HR5554	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5555				HR5554 (Lo-16bits)
HR5556	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5557				HR5556 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5558	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5559				HR5558 (Lo-16bits)
HR5560	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5561				HR5560 (Lo-16bits)
HR5562	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5563				HR5562 (Lo-16bits)
HR5564	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5565				HR5564 (Lo-16bits)
HR5566	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5567				HR5566 (Lo-16bits)
HR5568	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5569				HR5568 (Lo-16bits)
HR5570	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5571				HR5570 (Lo-16bits)
HR5572	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5573				HR5572 (Lo-16bits)
HR5574	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5575				HR5574 (Lo-16bits)
HR5576	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5577				HR5576 (Lo-16bits)
HR5578	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5579				HR5578 (Lo-16bits)
HR5580	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5581				HR5580 (Lo-16bits)
HR5582	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5583				HR5582 (Lo-16bits)
HR5584	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5585				HR5584 (Lo-16bits)
HR5586	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5587				HR5586 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5588	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5589				HR5588 (Lo-16bits)
HR5590	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5591				HR5590 (Lo-16bits)
HR5592	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5593	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5594	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5595	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5596	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5597	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5598	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5599	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5600	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5601	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5602	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5603	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5604	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5605	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5606	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5607	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5608	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5609				HR5608 (Lo-16bits)
HR5610	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5611				HR5610 (Lo-16bits)
HR5612	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5613				HR5612 (Lo-16bits)
HR5614	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5615				HR5614 (Lo-16bits)
HR5616	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5617				HR5616 (Lo-16bits)
HR5618	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5619				HR5618 (Lo-16bits)
HR5620	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5621				HR5620 (Lo-16bits)
HR5622	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5623				HR5622 (Lo-16bits)
HR5624	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5625				HR5624 (Lo-16bits)
HR5626	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5627				HR5626 (Lo-16bits)
HR5628	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5629				HR5628 (Lo-16bits)
HR5630	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5631				HR5630 (Lo-16bits)
HR5632	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5633				HR5632 (Lo-16bits)
HR5634	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5635				HR5634 (Lo-16bits)
HR5636	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5637				HR5636 (Lo-16bits)
HR5638	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5639				HR5638 (Lo-16bits)
HR5640	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5641				HR5640 (Lo-16bits)
HR5642	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5643				HR5642 (Lo-16bits)
HR5644	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5645				HR5644 (Lo-16bits)
HR5646	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5647				HR5646 (Lo-16bits)
HR5648	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5649				HR5648 (Lo-16bits)
HR5650	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5651				HR5650 (Lo-16bits)
HR5652	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5653				HR5652 (Lo-16bits)
HR5654	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5655				HR5654 (Lo-16bits)
HR5656	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5657	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5658	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5659	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5660	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5661	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5662	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5663	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5664	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5665	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5666	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5667	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5668	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5669	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5670	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5671	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5672	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5673	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5674	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5675	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5676	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5677	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5678	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5679	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5680	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5681	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5682	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR5683	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5684	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5685	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5686	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5687	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5688	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5689				HR5688 (Lo-16bits)
HR5690	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5691				HR5690 (Lo-16bits)
HR5692	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5693				HR5692 (Lo-16bits)
HR5694	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5695				HR5694 (Lo-16bits)
HR5696	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5697				HR5696 (Lo-16bits)
HR5698	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5699				HR5698 (Lo-16bits)
HR5700	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5701				HR5700 (Lo-16bits)
HR5702	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5703				HR5702 (Lo-16bits)
HR5704	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5705				HR5704 (Lo-16bits)
HR5706	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5707				HR5706 (Lo-16bits)
HR5708	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5709				HR5708 (Lo-16bits)
HR5710	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5711				HR5710 (Lo-16bits)
HR5712	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5713				HR5712 (Lo-16bits)
HR5714	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5715				HR5714 (Lo-16bits)
HR5716	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5717				HR5716 (Lo-16bits)
HR5718	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5719				HR5718 (Lo-16bits)
HR5720	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5721				HR5720 (Lo-16bits)
HR5722	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5723				HR5722 (Lo-16bits)
HR5724	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5725				HR5724 (Lo-16bits)
HR5726	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5727				HR5726 (Lo-16bits)
HR5728	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5729				HR5728 (Lo-16bits)
HR5730	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5731				HR5730 (Lo-16bits)
HR5732	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5733				HR5732 (Lo-16bits)
HR5734	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5735				HR5734 (Lo-16bits)
HR5736	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5737				HR5736 (Lo-16bits)
HR5738	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5739				HR5738 (Lo-16bits)
HR5740	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5741				HR5740 (Lo-16bits)
HR5742	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5743				HR5742 (Lo-16bits)
HR5744	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5745				HR5744 (Lo-16bits)
HR5746	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5747				HR5746 (Lo-16bits)
HR5748	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5749				HR5748 (Lo-16bits)
HR5750	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5751				HR5750 (Lo-16bits)
HR5752	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5753	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5754	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5755	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5756	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5757	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5758	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5759	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5760	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5761				HR5760 (Lo-16bits)
HR5762	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5763				HR5762 (Lo-16bits)
HR5764	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5765				HR5764 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5766	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5767				HR5766 (Lo-16bits)
HR5768	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5769				HR5768 (Lo-16bits)
HR5770	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5771				HR5770 (Lo-16bits)
HR5772	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5773				HR5772 (Lo-16bits)
HR5774	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5775				HR5774 (Lo-16bits)
HR5776	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5777	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5778	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5779	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5780	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5781	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5782	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5783	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5784	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5785				HR5784 (Lo-16bits)
HR5786	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5787				HR5786 (Lo-16bits)
HR5788	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5789				HR5788 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5790	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5791				HR5790 (Lo-16bits)
HR5792	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5793				HR5792 (Lo-16bits)
HR5794	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5795				HR5794 (Lo-16bits)
HR5796	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5797				HR5796 (Lo-16bits)
HR5798	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5799				HR5798 (Lo-16bits)
HR5800	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5801	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5802	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5803	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5804	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5805	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5806	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5807	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5808	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5809	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5810	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5811	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5812	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5813	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5814	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5815	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5816	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR5817	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5818	DEF4:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5819	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5820	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5821				HR5820 (Lo-16bits)
HR5822	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5823				HR5822 (Lo-16bits)
HR5824	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5825				HR5824 (Lo-16bits)
HR5826	DEF4:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5827	DEF4:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5828	DEF4:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5829	DEF4:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5830	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5831	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5832	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5833	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5834	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5835				HR5834 (Lo-16bits)
HR5836	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5837				HR5836 (Lo-16bits)
HR5838	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5839				HR5838 (Lo-16bits)
HR5840	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5841				HR5840 (Lo-16bits)
HR5842	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5843				HR5842 (Lo-16bits)
HR5844	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5845				HR5844 (Lo-16bits)
HR5846	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5847				HR5846 (Lo-16bits)
HR5848	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5849				HR5848 (Lo-16bits)
HR5850	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5851				HR5850 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5852	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5853				HR5852 (Lo-16bits)
HR5854	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5855				HR5854 (Lo-16bits)
HR5856	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5857				HR5856 (Lo-16bits)
HR5858	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5859				HR5858 (Lo-16bits)
HR5860	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5861				HR5860 (Lo-16bits)
HR5862	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5863				HR5862 (Lo-16bits)
HR5864	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5865				HR5864 (Lo-16bits)
HR5866	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5867				HR5866 (Lo-16bits)
HR5868	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5869				HR5868 (Lo-16bits)
HR5870	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5871				HR5870 (Lo-16bits)
HR5872	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5873				HR5872 (Lo-16bits)
HR5874	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5875				HR5874 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5876	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5877				HR5876 (Lo-16bits)
HR5878	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5879				HR5878 (Lo-16bits)
HR5880	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5881				HR5880 (Lo-16bits)
HR5882	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5883				HR5882 (Lo-16bits)
HR5884	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5885				HR5884 (Lo-16bits)
HR5886	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5887				HR5886 (Lo-16bits)
HR5888	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5889				HR5888 (Lo-16bits)
HR5890	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5891				HR5890 (Lo-16bits)
HR5892	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5893				HR5892 (Lo-16bits)
HR5894	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5895				HR5894 (Lo-16bits)
HR5896	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5897				HR5896 (Lo-16bits)
HR5898	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5899				HR5898 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5900	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5901				HR5900 (Lo-16bits)
HR5902	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5903				HR5902 (Lo-16bits)
HR5904	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5905				HR5904 (Lo-16bits)
HR5906	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5907				HR5906 (Lo-16bits)
HR5908	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5909				HR5908 (Lo-16bits)
HR5910	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5911				HR5910 (Lo-16bits)
HR5912	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5913				HR5912 (Lo-16bits)
HR5914	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5915				HR5914 (Lo-16bits)
HR5916	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5917				HR5916 (Lo-16bits)
HR5918	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5919				HR5918 (Lo-16bits)
HR5920	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5921				HR5920 (Lo-16bits)
HR5922	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5923				HR5922 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5924	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5925				HR5924 (Lo-16bits)
HR5926	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5927				HR5926 (Lo-16bits)
HR5928	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5929				HR5928 (Lo-16bits)
HR5930	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5931	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5932	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5933	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5934	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5935	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5936	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5937	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5938	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5939				HR5938 (Lo-16bits)
HR5940	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5941				HR5940 (Lo-16bits)
HR5942	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5943				HR5942 (Lo-16bits)
HR5944	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5945				HR5944 (Lo-16bits)
HR5946	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5947				HR5946 (Lo-16bits)
HR5948	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5949				HR5948 (Lo-16bits)
HR5950	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5951				HR5950 (Lo-16bits)
HR5952	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5953				HR5952 (Lo-16bits)
HR5954	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5955				HR5954 (Lo-16bits)
HR5956	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5957				HR5956 (Lo-16bits)
HR5958	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5959				HR5958 (Lo-16bits)
HR5960	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5961				HR5960 (Lo-16bits)
HR5962	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5963				HR5962 (Lo-16bits)
HR5964	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5965				HR5964 (Lo-16bits)
HR5966	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5967				HR5966 (Lo-16bits)
HR5968	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5969				HR5968 (Lo-16bits)
HR5970	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5971				HR5970 (Lo-16bits)
HR5972	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5973				HR5972 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5974	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5975				HR5974 (Lo-16bits)
HR5976	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5977				HR5976 (Lo-16bits)
HR5978	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5979				HR5978 (Lo-16bits)
HR5980	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5981				HR5980 (Lo-16bits)
HR5982	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5983				HR5982 (Lo-16bits)
HR5984	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5985				HR5984 (Lo-16bits)
HR5986	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5987				HR5986 (Lo-16bits)
HR5988	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5989				HR5988 (Lo-16bits)
HR5990	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5991				HR5990 (Lo-16bits)
HR5992	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5993				HR5992 (Lo-16bits)
HR5994	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5995				HR5994 (Lo-16bits)
HR5996	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5997				HR5996 (Lo-16bits)
HR5998	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5999				HR5998 (Lo-16bits)
HR6000	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6001				HR6000 (Lo-16bits)
HR6002	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6003				HR6002 (Lo-16bits)
HR6004	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6005				HR6004 (Lo-16bits)
HR6006	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6007				HR6006 (Lo-16bits)
HR6008	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6009				HR6008 (Lo-16bits)
HR6010	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6011				HR6010 (Lo-16bits)
HR6012	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6013				HR6012 (Lo-16bits)
HR6014	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6015				HR6014 (Lo-16bits)
HR6016	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6017				HR6016 (Lo-16bits)
HR6018	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6019	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6020	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6021	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6022	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6023	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6024	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6025	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6026	DEF4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6027	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6028	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6029	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6030	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6031	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6032	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6033	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6034	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6035				HR6034 (Lo-16bits)
HR6036	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6037				HR6036 (Lo-16bits)
HR6038	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6039				HR6038 (Lo-16bits)
HR6040	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6041				HR6040 (Lo-16bits)
HR6042	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6043				HR6042 (Lo-16bits)
HR6044	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6045				HR6044 (Lo-16bits)
HR6046	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6047				HR6046 (Lo-16bits)
HR6048	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6049				HR6048 (Lo-16bits)
HR6050	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6051				HR6050 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6052	DEF4:Definite operating time delay(Uneearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6053				HR6052 (Lo-16bits)
HR6054	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6055				HR6054 (Lo-16bits)
HR6056	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6057				HR6056 (Lo-16bits)
HR6058	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6059				HR6058 (Lo-16bits)
HR6060	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6061				HR6060 (Lo-16bits)
HR6062	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6063				HR6062 (Lo-16bits)
HR6064	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6065				HR6064 (Lo-16bits)
HR6066	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6067				HR6066 (Lo-16bits)
HR6068	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6069				HR6068 (Lo-16bits)
HR6070	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6071				HR6070 (Lo-16bits)
HR6072	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6073				HR6072 (Lo-16bits)
HR6074	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6075				HR6074 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6076	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6077				HR6076 (Lo-16bits)
HR6078	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6079				HR6078 (Lo-16bits)
HR6080	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6081				HR6080 (Lo-16bits)
HR6082	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6083	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6084	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6085	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6086	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6087	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6088	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6089	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6090	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6091	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6092	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6093	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6094	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6095	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6096	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6097	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6098	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6099	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6100	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6101	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6102	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6103	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6104	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6105	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6106	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6107	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6108	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6109	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6110	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6111	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6112	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6113	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6114	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6115				HR6114 (Lo-16bits)
HR6116	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6117				HR6116 (Lo-16bits)
HR6118	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6119				HR6118 (Lo-16bits)
HR6120	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6121				HR6120 (Lo-16bits)
HR6122	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6123				HR6122 (Lo-16bits)
HR6124	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6125				HR6124 (Lo-16bits)
HR6126	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6127				HR6126 (Lo-16bits)
HR6128	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6129				HR6128 (Lo-16bits)
HR6130	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6131				HR6130 (Lo-16bits)
HR6132	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6133				HR6132 (Lo-16bits)
HR6134	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6135				HR6134 (Lo-16bits)
HR6136	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6137				HR6136 (Lo-16bits)
HR6138	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6139				HR6138 (Lo-16bits)
HR6140	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6141				HR6140 (Lo-16bits)
HR6142	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6143				HR6142 (Lo-16bits)
HR6144	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6145				HR6144 (Lo-16bits)
HR6146	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6147				HR6146 (Lo-16bits)
HR6148	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6149				HR6148 (Lo-16bits)
HR6150	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6151				HR6150 (Lo-16bits)
HR6152	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6153				HR6152 (Lo-16bits)
HR6154	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6155				HR6154 (Lo-16bits)
HR6156	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6157				HR6156 (Lo-16bits)
HR6158	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6159				HR6158 (Lo-16bits)
HR6160	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6161				HR6160 (Lo-16bits)
HR6162	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6163				HR6162 (Lo-16bits)
HR6164	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6165				HR6164 (Lo-16bits)
HR6166	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6167				HR6166 (Lo-16bits)
HR6168	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6169				HR6168 (Lo-16bits)
HR6170	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6171				HR6170 (Lo-16bits)
HR6172	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6173				HR6172 (Lo-16bits)
HR6174	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6175				HR6174 (Lo-16bits)
HR6176	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6177				HR6176 (Lo-16bits)
HR6178	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6179	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6180	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6181	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6182	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6183	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6184	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6185	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6186	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6187				HR6186 (Lo-16bits)
HR6188	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6189				HR6188 (Lo-16bits)
HR6190	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6191				HR6190 (Lo-16bits)
HR6192	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6193				HR6192 (Lo-16bits)
HR6194	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6195				HR6194 (Lo-16bits)
HR6196	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6197				HR6196 (Lo-16bits)
HR6198	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6199				HR6198 (Lo-16bits)
HR6200	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6201				HR6200 (Lo-16bits)
HR6202	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6203	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6204	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6205	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6206	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6207	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6208	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6209	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6210	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6211				HR6210 (Lo-16bits)
HR6212	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6213				HR6212 (Lo-16bits)
HR6214	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6215				HR6214 (Lo-16bits)
HR6216	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6217				HR6216 (Lo-16bits)
HR6218	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6219				HR6218 (Lo-16bits)
HR6220	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6221				HR6220 (Lo-16bits)
HR6222	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6223				HR6222 (Lo-16bits)
HR6224	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6225				HR6224 (Lo-16bits)
HR6226	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6227	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6228	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6229	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6230	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6231	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6232	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6233	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6234	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6235	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6236	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6237	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6238	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6239	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6240	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6241	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6242	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR6243	IEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR6244	CUB1:I2> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6245	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6246	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6247				HR6246 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6248	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6249				HR6248 (Lo-16bits)
HR6250	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6251				HR6250 (Lo-16bits)
HR6252	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6253				HR6252 (Lo-16bits)
HR6254	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6255				HR6254 (Lo-16bits)
HR6256	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6257				HR6256 (Lo-16bits)
HR6258	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6259				HR6258 (Lo-16bits)
HR6260	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6261				HR6260 (Lo-16bits)
HR6262	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6263				HR6262 (Lo-16bits)
HR6264	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6265				HR6264 (Lo-16bits)
HR6266	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6267				HR6266 (Lo-16bits)
HR6268	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6269				HR6268 (Lo-16bits)
HR6270	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6271				HR6270 (Lo-16bits)
HR6272	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6273				HR6272 (Lo-16bits)
HR6274	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6275				HR6274 (Lo-16bits)
HR6276	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6277				HR6276 (Lo-16bits)
HR6278	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6279				HR6278 (Lo-16bits)
HR6280	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6281				HR6280 (Lo-16bits)
HR6282	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6283				HR6282 (Lo-16bits)
HR6284	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6285				HR6284 (Lo-16bits)
HR6286	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6287				HR6286 (Lo-16bits)
HR6288	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6289				HR6288 (Lo-16bits)
HR6290	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6291				HR6290 (Lo-16bits)
HR6292	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6293				HR6292 (Lo-16bits)
HR6294	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6295	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6296	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6297	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6298	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6299	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6300	CUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6301	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6302	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6303				HR6302 (Lo-16bits)
HR6304	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6305				HR6304 (Lo-16bits)
HR6306	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6307				HR6306 (Lo-16bits)
HR6308	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6309				HR6308 (Lo-16bits)
HR6310	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6311				HR6310 (Lo-16bits)
HR6312	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6313				HR6312 (Lo-16bits)
HR6314	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6315				HR6314 (Lo-16bits)
HR6316	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6317				HR6316 (Lo-16bits)
HR6318	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6319	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6320	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6321	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6322	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6323	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6324	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6325	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6326	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6327	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6328	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6329	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6330	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6331	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6332	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6333	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6334	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6335	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6336	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6337	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6338	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6339	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6340	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6341	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6342	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6343	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6344	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6345	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6346	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6347	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6348	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6349	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6350	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6351				HR6350 (Lo-16bits)
HR6352	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6353				HR6352 (Lo-16bits)
HR6354	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6355				HR6354 (Lo-16bits)
HR6356	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6357				HR6356 (Lo-16bits)
HR6358	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6359				HR6358 (Lo-16bits)
HR6360	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6361				HR6360 (Lo-16bits)
HR6362	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6363				HR6362 (Lo-16bits)
HR6364	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6365				HR6364 (Lo-16bits)
HR6366	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6367				HR6366 (Lo-16bits)
HR6368	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6369				HR6368 (Lo-16bits)
HR6370	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6371				HR6370 (Lo-16bits)
HR6372	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6373				HR6372 (Lo-16bits)
HR6374	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6375				HR6374 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6376	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6377				HR6376 (Lo-16bits)
HR6378	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6379				HR6378 (Lo-16bits)
HR6380	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6381				HR6380 (Lo-16bits)
HR6382	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6383				HR6382 (Lo-16bits)
HR6384	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6385				HR6384 (Lo-16bits)
HR6386	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6387				HR6386 (Lo-16bits)
HR6388	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6389				HR6388 (Lo-16bits)
HR6390	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6391				HR6390 (Lo-16bits)
HR6392	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6393				HR6392 (Lo-16bits)
HR6394	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6395				HR6394 (Lo-16bits)
HR6396	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6397				HR6396 (Lo-16bits)
HR6398	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6399				HR6398 (Lo-16bits)
HR6400	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6401				HR6400 (Lo-16bits)
HR6402	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6403				HR6402 (Lo-16bits)
HR6404	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6405				HR6404 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6406	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6407				HR6406 (Lo-16bits)
HR6408	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6409				HR6408 (Lo-16bits)
HR6410	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6411				HR6410 (Lo-16bits)
HR6412	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6413				HR6412 (Lo-16bits)
HR6414	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6415	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6416	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6417	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6418	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6419	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6420	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6421	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6422	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6423				HR6422 (Lo-16bits)
HR6424	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6425				HR6424 (Lo-16bits)
HR6426	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6427				HR6426 (Lo-16bits)
HR6428	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6429				HR6428 (Lo-16bits)
HR6430	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6431				HR6430 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6432	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6433				HR6432 (Lo-16bits)
HR6434	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6435				HR6434 (Lo-16bits)
HR6436	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6437				HR6436 (Lo-16bits)
HR6438	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6439	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6440	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6441	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6442	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6443	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6444	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6445	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6446	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6447	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6448	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6449	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6450	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6451	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6452	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6453	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6454	CUB2:I2>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6455	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6456	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6457				HR6456 (Lo-16bits)
HR6458	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6459				HR6458 (Lo-16bits)
HR6460	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6461				HR6460 (Lo-16bits)
HR6462	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6463				HR6462 (Lo-16bits)
HR6464	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6465				HR6464 (Lo-16bits)
HR6466	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6467				HR6466 (Lo-16bits)
HR6468	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6469				HR6468 (Lo-16bits)
HR6470	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6471				HR6470 (Lo-16bits)
HR6472	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6473				HR6472 (Lo-16bits)
HR6474	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6475				HR6474 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6476	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6477				HR6476 (Lo-16bits)
HR6478	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6479				HR6478 (Lo-16bits)
HR6480	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6481				HR6480 (Lo-16bits)
HR6482	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6483				HR6482 (Lo-16bits)
HR6484	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6485				HR6484 (Lo-16bits)
HR6486	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6487				HR6486 (Lo-16bits)
HR6488	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6489				HR6488 (Lo-16bits)
HR6490	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6491				HR6490 (Lo-16bits)
HR6492	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6493				HR6492 (Lo-16bits)
HR6494	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6495				HR6494 (Lo-16bits)
HR6496	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6497				HR6496 (Lo-16bits)
HR6498	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6499				HR6498 (Lo-16bits)
HR6500	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6501				HR6500 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6502	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6503				HR6502 (Lo-16bits)
HR6504	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6505	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6506	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6507	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6508	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6509	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6510	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6511	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6512	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6513				HR6512 (Lo-16bits)
HR6514	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6515				HR6514 (Lo-16bits)
HR6516	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6517				HR6516 (Lo-16bits)
HR6518	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6519				HR6518 (Lo-16bits)
HR6520	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6521				HR6520 (Lo-16bits)
HR6522	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6523				HR6522 (Lo-16bits)
HR6524	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6525				HR6524 (Lo-16bits)
HR6526	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6527				HR6526 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6528	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6529	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6530	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6531	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6532	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6533	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6534	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6535	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6536	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6537	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6538	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6539	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6540	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6541	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6542	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6543	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6544	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6545	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6546	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6547	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6548	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6549	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6550	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6551	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6552	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6553	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6554	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6555	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6556	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6557	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6558	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6559	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6560	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6561				HR6560 (Lo-16bits)
HR6562	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6563				HR6562 (Lo-16bits)
HR6564	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6565				HR6564 (Lo-16bits)
HR6566	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6567				HR6566 (Lo-16bits)
HR6568	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6569				HR6568 (Lo-16bits)
HR6570	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6571				HR6570 (Lo-16bits)
HR6572	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6573				HR6572 (Lo-16bits)
HR6574	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6575				HR6574 (Lo-16bits)
HR6576	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6577				HR6576 (Lo-16bits)
HR6578	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6579				HR6578 (Lo-16bits)
HR6580	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6581				HR6580 (Lo-16bits)
HR6582	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6583				HR6582 (Lo-16bits)
HR6584	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6585				HR6584 (Lo-16bits)
HR6586	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6587				HR6586 (Lo-16bits)
HR6588	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6589				HR6588 (Lo-16bits)
HR6590	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6591				HR6590 (Lo-16bits)
HR6592	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6593				HR6592 (Lo-16bits)
HR6594	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6595				HR6594 (Lo-16bits)
HR6596	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6597				HR6596 (Lo-16bits)
HR6598	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6599				HR6598 (Lo-16bits)
HR6600	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6601				HR6600 (Lo-16bits)
HR6602	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6603				HR6602 (Lo-16bits)
HR6604	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6605				HR6604 (Lo-16bits)
HR6606	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6607				HR6606 (Lo-16bits)
HR6608	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6609				HR6608 (Lo-16bits)
HR6610	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6611				HR6610 (Lo-16bits)
HR6612	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6613				HR6612 (Lo-16bits)
HR6614	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6615				HR6614 (Lo-16bits)
HR6616	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6617				HR6616 (Lo-16bits)
HR6618	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6619				HR6618 (Lo-16bits)
HR6620	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6621				HR6620 (Lo-16bits)
HR6622	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6623				HR6622 (Lo-16bits)
HR6624	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6625	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6626	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6627	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6628	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6629	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6630	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6631	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6632	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6633				HR6632 (Lo-16bits)
HR6634	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6635				HR6634 (Lo-16bits)
HR6636	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6637				HR6636 (Lo-16bits)
HR6638	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6639				HR6638 (Lo-16bits)
HR6640	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6641				HR6640 (Lo-16bits)
HR6642	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6643				HR6642 (Lo-16bits)
HR6644	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6645				HR6644 (Lo-16bits)
HR6646	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6647				HR6646 (Lo-16bits)
HR6648	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6649	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6650	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6651	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6652	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6653	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6654	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6655	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6656	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6657	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6658	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6659	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6660	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6661	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6662	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6663	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6664	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6665	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6666	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6667				HR6666 (Lo-16bits)
HR6668	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6669				HR6668 (Lo-16bits)
HR6670	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6671				HR6670 (Lo-16bits)
HR6672	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6673				HR6672 (Lo-16bits)
HR6674	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6675				HR6674 (Lo-16bits)
HR6676	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6677				HR6676 (Lo-16bits)
HR6678	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6679				HR6678 (Lo-16bits)
HR6680	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6681				HR6680 (Lo-16bits)
HR6682	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6683				HR6682 (Lo-16bits)
HR6684	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6685				HR6684 (Lo-16bits)
HR6686	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6687				HR6686 (Lo-16bits)
HR6688	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6689				HR6688 (Lo-16bits)
HR6690	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6691				HR6690 (Lo-16bits)
HR6692	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6693				HR6692 (Lo-16bits)
HR6694	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6695				HR6694 (Lo-16bits)
HR6696	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6697				HR6696 (Lo-16bits)
HR6698	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6699				HR6698 (Lo-16bits)
HR6700	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6701				HR6700 (Lo-16bits)
HR6702	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6703				HR6702 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6704	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6705				HR6704 (Lo-16bits)
HR6706	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6707				HR6706 (Lo-16bits)
HR6708	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6709				HR6708 (Lo-16bits)
HR6710	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6711				HR6710 (Lo-16bits)
HR6712	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6713				HR6712 (Lo-16bits)
HR6714	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6715	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6716	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6717	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6718	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6719	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6720	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6721	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6722	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6723				HR6722 (Lo-16bits)
HR6724	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6725				HR6724 (Lo-16bits)
HR6726	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6727				HR6726 (Lo-16bits)
HR6728	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6729				HR6728 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6730	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6731				HR6730 (Lo-16bits)
HR6732	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6733				HR6732 (Lo-16bits)
HR6734	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6735				HR6734 (Lo-16bits)
HR6736	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6737				HR6736 (Lo-16bits)
HR6738	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6739	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6740	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6741	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6742	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6743	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6744	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6745	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6746	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6747	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6748	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6749	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6750	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6751	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6752	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6753	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6754	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6755	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6756	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6757	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6758	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6759	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6760	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6761	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6762	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6763	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6764	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6765	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6766	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6767	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6768	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6769	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6770	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6771				HR6770 (Lo-16bits)
HR6772	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6773				HR6772 (Lo-16bits)
HR6774	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6775				HR6774 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6776	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6777				HR6776 (Lo-16bits)
HR6778	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6779				HR6778 (Lo-16bits)
HR6780	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6781				HR6780 (Lo-16bits)
HR6782	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6783				HR6782 (Lo-16bits)
HR6784	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6785				HR6784 (Lo-16bits)
HR6786	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6787				HR6786 (Lo-16bits)
HR6788	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6789				HR6788 (Lo-16bits)
HR6790	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6791				HR6790 (Lo-16bits)
HR6792	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6793				HR6792 (Lo-16bits)
HR6794	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6795				HR6794 (Lo-16bits)
HR6796	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6797				HR6796 (Lo-16bits)
HR6798	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6799				HR6798 (Lo-16bits)
HR6800	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6801				HR6800 (Lo-16bits)
HR6802	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6803				HR6802 (Lo-16bits)
HR6804	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6805				HR6804 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6806	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6807				HR6806 (Lo-16bits)
HR6808	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6809				HR6808 (Lo-16bits)
HR6810	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6811				HR6810 (Lo-16bits)
HR6812	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6813				HR6812 (Lo-16bits)
HR6814	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6815				HR6814 (Lo-16bits)
HR6816	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6817				HR6816 (Lo-16bits)
HR6818	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6819				HR6818 (Lo-16bits)
HR6820	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6821				HR6820 (Lo-16bits)
HR6822	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6823				HR6822 (Lo-16bits)
HR6824	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6825				HR6824 (Lo-16bits)
HR6826	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6827				HR6826 (Lo-16bits)
HR6828	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6829				HR6828 (Lo-16bits)
HR6830	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6831				HR6830 (Lo-16bits)
HR6832	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6833				HR6832 (Lo-16bits)
HR6834	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6835	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6836	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6837	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6838	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6839	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6840	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6841	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6842	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6843				HR6842 (Lo-16bits)
HR6844	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6845				HR6844 (Lo-16bits)
HR6846	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6847				HR6846 (Lo-16bits)
HR6848	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6849				HR6848 (Lo-16bits)
HR6850	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6851				HR6850 (Lo-16bits)
HR6852	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6853				HR6852 (Lo-16bits)
HR6854	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6855				HR6854 (Lo-16bits)
HR6856	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6857				HR6856 (Lo-16bits)
HR6858	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6859	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6860	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6861	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6862	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6863	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6864	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6865	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6866	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6867	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6868	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6869	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6870	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6871	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6872	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6873	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6874	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6875	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6876	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6877				HR6876 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6878	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6879				HR6878 (Lo-16bits)
HR6880	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6881				HR6880 (Lo-16bits)
HR6882	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6883				HR6882 (Lo-16bits)
HR6884	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6885				HR6884 (Lo-16bits)
HR6886	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6887				HR6886 (Lo-16bits)
HR6888	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6889				HR6888 (Lo-16bits)
HR6890	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6891				HR6890 (Lo-16bits)
HR6892	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6893				HR6892 (Lo-16bits)
HR6894	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6895				HR6894 (Lo-16bits)
HR6896	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6897				HR6896 (Lo-16bits)
HR6898	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6899				HR6898 (Lo-16bits)
HR6900	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6901				HR6900 (Lo-16bits)
HR6902	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6903				HR6902 (Lo-16bits)
HR6904	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6905				HR6904 (Lo-16bits)
HR6906	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6907				HR6906 (Lo-16bits)
HR6908	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6909				HR6908 (Lo-16bits)
HR6910	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6911				HR6910 (Lo-16bits)
HR6912	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6913				HR6912 (Lo-16bits)
HR6914	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6915				HR6914 (Lo-16bits)
HR6916	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6917				HR6916 (Lo-16bits)
HR6918	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6919				HR6918 (Lo-16bits)
HR6920	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6921				HR6920 (Lo-16bits)
HR6922	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6923				HR6922 (Lo-16bits)
HR6924	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6925	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6926	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6927	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6928	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6929	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6930	CUB4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6931	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6932	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6933				HR6932 (Lo-16bits)
HR6934	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6935				HR6934 (Lo-16bits)
HR6936	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6937				HR6936 (Lo-16bits)
HR6938	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6939				HR6938 (Lo-16bits)
HR6940	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6941				HR6940 (Lo-16bits)
HR6942	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6943				HR6942 (Lo-16bits)
HR6944	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6945				HR6944 (Lo-16bits)
HR6946	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6947				HR6946 (Lo-16bits)
HR6948	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6949	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6950	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6951	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6952	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6953	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6954	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6955	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6956	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6957	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6958	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6959	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6960	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6961	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6962	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6963	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6964	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6965	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6966	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6967	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6968	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6969	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6970	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6971	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6972	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6973	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6974	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6975	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6976	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6977	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6978	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6979	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6980	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6981				HR6980 (Lo-16bits)
HR6982	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6983				HR6982 (Lo-16bits)
HR6984	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6985				HR6984 (Lo-16bits)
HR6986	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6987				HR6986 (Lo-16bits)
HR6988	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6989				HR6988 (Lo-16bits)
HR6990	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6991				HR6990 (Lo-16bits)
HR6992	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6993				HR6992 (Lo-16bits)
HR6994	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6995				HR6994 (Lo-16bits)
HR6996	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6997				HR6996 (Lo-16bits)
HR6998	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6999				HR6998 (Lo-16bits)
HR7000	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7001				HR7000 (Lo-16bits)
HR7002	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7003				HR7002 (Lo-16bits)
HR7004	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7005				HR7004 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7006	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7007				HR7006 (Lo-16bits)
HR7008	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7009				HR7008 (Lo-16bits)
HR7010	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7011				HR7010 (Lo-16bits)
HR7012	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7013				HR7012 (Lo-16bits)
HR7014	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7015				HR7014 (Lo-16bits)
HR7016	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7017				HR7016 (Lo-16bits)
HR7018	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7019				HR7018 (Lo-16bits)
HR7020	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7021				HR7020 (Lo-16bits)
HR7022	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7023				HR7022 (Lo-16bits)
HR7024	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7025				HR7024 (Lo-16bits)
HR7026	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7027				HR7026 (Lo-16bits)
HR7028	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7029				HR7028 (Lo-16bits)
HR7030	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7031				HR7030 (Lo-16bits)
HR7032	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7033				HR7032 (Lo-16bits)
HR7034	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7035				HR7034 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7036	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7037				HR7036 (Lo-16bits)
HR7038	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7039				HR7038 (Lo-16bits)
HR7040	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7041				HR7040 (Lo-16bits)
HR7042	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7043				HR7042 (Lo-16bits)
HR7044	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7045	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7046	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7047	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7048	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7049	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7050	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7051	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7052	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7053				HR7052 (Lo-16bits)
HR7054	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7055				HR7054 (Lo-16bits)
HR7056	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7057				HR7056 (Lo-16bits)
HR7058	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7059				HR7058 (Lo-16bits)
HR7060	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7061				HR7060 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7062	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7063				HR7062 (Lo-16bits)
HR7064	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7065				HR7064 (Lo-16bits)
HR7066	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7067				HR7066 (Lo-16bits)
HR7068	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7069	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7070	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7071	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7072	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7073	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7074	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7075	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7076	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7077	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7078	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7079	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7080	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7081	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7082	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7083	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7084	HOC1:Ih> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7085	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)
HR7089	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7106	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7107				HR7106 (Lo-16bits)
HR7108	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7109				HR7108 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7110	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7111				HR7110 (Lo-16bits)
HR7112	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7113				HR7112 (Lo-16bits)
HR7114	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7115				HR7114 (Lo-16bits)
HR7116	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7117				HR7116 (Lo-16bits)
HR7118	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7119				HR7118 (Lo-16bits)
HR7120	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7121				HR7120 (Lo-16bits)
HR7122	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7123				HR7122 (Lo-16bits)
HR7124	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7125				HR7124 (Lo-16bits)
HR7126	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7127	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)
HR7137	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7140				HR7139 (Lo-16bits)
HR7141	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7148	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7149				HR7148 (Lo-16bits)
HR7150	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7151				HR7150 (Lo-16bits)
HR7152	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7153				HR7152 (Lo-16bits)
HR7154	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7155				HR7154 (Lo-16bits)
HR7156	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7157				HR7156 (Lo-16bits)
HR7158	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7159				HR7158 (Lo-16bits)
HR7160	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7161				HR7160 (Lo-16bits)
HR7162	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7163				HR7162 (Lo-16bits)
HR7164	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7165				HR7164 (Lo-16bits)
HR7166	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7167				HR7166 (Lo-16bits)
HR7168	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR7169	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked

Holding Register	Parameter	Access	Length	Info
HR7170	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR7171	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR7172	DIF1:Idb> condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR7173				HR7172 (Lo-16bits)
HR7174	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR7175				HR7174 (Lo-16bits)
HR7176	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)
HR7177				HR7176 (Lo-16bits)
HR7178	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7179	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7180	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7181	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR7182	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7183	TOLT1:TT> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7184	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7185	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7186	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7187	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7188	TOLT1:Temp C or F deg	RW	1	0=C 1=F
HR7189	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7190	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR7191	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7193	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7194	TOLF1:TF> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7195	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7196	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7197	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7198	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7199	TOLF1:Temp C or F deg	RW	1	0=C 1=F
HR7200	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7201	TOLF1:RTD Sensor	R	1	0=Invalid 1=OK
HR7202	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set 1=Estimate
HR7203	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7206	TOLM1:TM> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7207	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7208	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7209	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7210	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR7211	TOLM1:Temp C or F deg	RW	1	0=C 1=F
HR7212	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing 1=NPS Biasing in use
HR7213	TOLM1:Time constants	RW	1	0=Single 1=Multiple
HR7214	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually 1=Estimate (on-line)

Holding Register	Parameter	Access	Length	Info
HR7215	TOLM1:T const dyn. balancing	RW	1	0=Fixed 1=Dynamic
HR7216	TOLM1:Dev.Temp (tmax)	RW	1	0=A 1=B 2=F 3=H 4=Manual set
HR7217	TOLM1:Amb. T Selection	RW	1	0=Manual set 1=RTD
HR7218	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR7219	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7220				HR7219 (Lo-16bits)
HR7221	LCR1:IS< condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR7222	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked
HR7223	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7224	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7225	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7226	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7227	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7228	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7229	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7230	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7231	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7232	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7233	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7234	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7235	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7244				HR7243 (Lo-16bits)
HR7245	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7252	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7253	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7254	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7255	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7256	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7257	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7258	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7259	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7271	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)
HR7285	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7300				HR7299 (Lo-16bits)
HR7301	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7324	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7325	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7326	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7327	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7328	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7329	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7330	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7331	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7332	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7333	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7334	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7335	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7336	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7337	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7338	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7339	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7340	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7341	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7342	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7343	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7344	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7345	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7346	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7347	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7348	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7349	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7350	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7351	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7352	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7353	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7354	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7355	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7356	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7357	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7374	OV2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7375	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7376	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7377	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7378	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7379	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7380	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7381	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)
HR7383	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)
HR7387	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7401	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)
HR7427	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)
HR7429	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7431	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7432				HR7431 (Lo-16bits)
HR7433	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7434				HR7433 (Lo-16bits)
HR7435	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7436				HR7435 (Lo-16bits)
HR7437	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7446	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7447	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7448	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7449	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7450	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7451	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7452	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7453	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7454	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7455	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7456	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7457	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7458	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7459	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7460	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7461	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7462	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7463	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7464	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7465	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7466	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7467	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7468	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7469	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7470	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7471	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7472	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7473	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7474	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7475	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7476	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7477	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7478	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7479	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7496	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7497	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7498	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7499	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7500	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7501	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7502	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7503	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7505	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)
HR7511	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7526				HR7525 (Lo-16bits)
HR7527	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7528				HR7527 (Lo-16bits)
HR7529	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7530				HR7529 (Lo-16bits)
HR7531	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7532				HR7531 (Lo-16bits)
HR7533	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7534				HR7533 (Lo-16bits)
HR7535	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7536				HR7535 (Lo-16bits)
HR7537	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)
HR7539	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7562				HR7561 (Lo-16bits)
HR7563	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7568	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7569	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7570	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7571	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7572	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7573	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7574	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7575	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7576	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7577	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7578	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7579	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7580	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7581	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7582	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7583	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7584	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7585	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7586	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7587	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7588	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7589	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7590	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7591	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7592	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7593	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7594	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7595	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7596	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7597	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7598	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7599	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7600	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7601	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7605	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7618	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7619	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7620	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7621	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7622	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7623	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7624	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7625	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7630				HR7629 (Lo-16bits)
HR7631	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7632				HR7631 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7633	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7634				HR7633 (Lo-16bits)
HR7635	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7636				HR7635 (Lo-16bits)
HR7637	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7638				HR7637 (Lo-16bits)
HR7639	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7640				HR7639 (Lo-16bits)
HR7641	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7642				HR7641 (Lo-16bits)
HR7643	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7644				HR7643 (Lo-16bits)
HR7645	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)
HR7659	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7661	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)
HR7689	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7690	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7691	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7692	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7693	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7694	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7695	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7696	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7697	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7698	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7699	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7700	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7701	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7702	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7703	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7704	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7705	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7706	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7707	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7708	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7709	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7710	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7711	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7712	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7713	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7714	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7715	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7716	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7717	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7718	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7719	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7720	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7721	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7722	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7723	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7735	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7744				HR7743 (Lo-16bits)
HR7745	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7746				HR7745 (Lo-16bits)
HR7747	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7748				HR7747 (Lo-16bits)
HR7749	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7750				HR7749 (Lo-16bits)
HR7751	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7752				HR7751 (Lo-16bits)
HR7753	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7754				HR7753 (Lo-16bits)
HR7755	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7756	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7757	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7758	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7759	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7760	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7761	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7762	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7763	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7765	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7792				HR7791 (Lo-16bits)
HR7793	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7822				HR7821 (Lo-16bits)
HR7823	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7828	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7829	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7830	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7831	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7832	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7833	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7834	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7835	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7836	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7837	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7838	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7839	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7840	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7841	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7842	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7843	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7844	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7845	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7846	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7847	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7848	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7849	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7850	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7851	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7852	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7853	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7854	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7855	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7856	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7857	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7858	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7859	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7860	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7861	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7865	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)
HR7875	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7890				HR7889 (Lo-16bits)
HR7891	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7894	UV2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7895	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7896	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7897	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7898	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7899	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7900	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7901	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7916				HR7915 (Lo-16bits)
HR7917	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7921	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7951	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7966	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7967	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7968	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7969	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7970	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7971	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7972	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7973	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7974	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7975	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7976	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7977	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7978	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7979	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7980	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7981	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7982	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7983	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7984	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7985	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7986	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7987	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7988	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7989	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7990	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7991	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7992	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7993	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7994	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7995	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7996	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7997	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7998	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7999	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8021	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8032	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8033	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8034	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8035	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8036	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8037	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8038	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8039	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8049	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8078				HR8077 (Lo-16bits)
HR8079	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)
HR8103	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8104	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8105	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8106	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8107	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8108	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8109	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8110	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8111	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8112	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8113	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8114	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8115	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8116	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8117	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8118	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8119	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8120	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8121	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8122	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8123	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8124	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8125	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8126	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8127	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR8128	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8129	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8130	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8131	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8132	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8133	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8134	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8135	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8136	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8137	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8148				HR8147 (Lo-16bits)
HR8149	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)
HR8169	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8170	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8171	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8172	UV4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8173	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8174	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8175	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8176	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8177	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8199	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8229	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8242	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8243	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8244	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8245	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8246	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8247	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8248	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8249	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8250	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8251	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8252	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8253	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8254	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8255	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8256	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8257	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8258	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8259	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8260	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8261	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8262	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8263	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8264	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8265	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8266	NOV1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8267	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8268	NOV2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8269	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8270	NOV3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8271	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8272	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8273	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8274	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8275	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8276	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8277	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8278	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8279	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8280	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8281	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8282	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8283	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8301	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8316	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8317	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8318	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8319	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8320	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8321	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8322	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8323	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8327	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8354				HR8353 (Lo-16bits)
HR8355	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8378				HR8377 (Lo-16bits)
HR8379	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8384				HR8383 (Lo-16bits)
HR8385	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8388	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8389	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8390	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8391	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8392	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8393	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8394	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8395	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8396	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8397	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8398	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8399	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8400	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8401	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8402	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8403	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8404	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8405	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8406	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8407	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8408	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8409	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8410	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8411	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8412	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8413	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8414	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8415	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8416	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8417	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8418	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8419	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8420	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8421	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8427	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8453	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8454	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8455	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8456	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8457	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8458	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8459	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8460	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8461	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8479	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8509	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8526	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8527	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8528	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8529	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8530	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8531	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8532	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8533	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8534	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8535	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8536	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8537	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8538	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8539	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8540	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8541	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8542	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8543	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8544	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8545	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8546	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8547	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8548	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8549	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8550	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8551	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <

Holding Register	Parameter	Access	Length	Info
HR8552	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8553	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8554	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8555	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8556	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8557	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8558	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8559	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8581	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8592	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8593	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8594	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8595	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8596	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8597	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8598	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8599	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8607	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8635	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)
HR8661	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8664	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8665	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8666	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8667	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8668	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8669	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8670	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8671	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8672	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8673	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8674	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8675	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8676	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8677	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8678	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8679	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8680	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8681	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8682	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8683	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8684	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8685	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8686	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8687	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8688	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8689	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8690	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8691	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8692	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8693	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8694	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8695	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8696	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8697	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8708				HR8707 (Lo-16bits)
HR8709	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8730	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8731	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8732	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8733	VUB4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8734	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8735	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8736	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8737	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8754				HR8753 (Lo-16bits)
HR8755	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8760				HR8759 (Lo-16bits)
HR8761	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8790				HR8789 (Lo-16bits)
HR8791	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8802	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8803	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8804	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8805	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8806	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8807	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8808	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8809	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8810	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8811	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8812	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8813	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8814	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8815	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8816	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8817	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8818	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8819	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8820	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8821	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8822	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8823	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8824	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8825	PGS1:PS1 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8826	PGS1:PS2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8827	PGS1:PS3 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8828	PGS1:PS4 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8829	PGS1:PS5 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8830	PGS1:PS6 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8831	PGS1:PS7 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8832	PGS1:PS8 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8833	PGS1:PS9 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8834	PGS1:PS10 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8835	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8836	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8837	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8838	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8839	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8840	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8841	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8842	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8843	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR8844	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR8845	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8846	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes
HR8847	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8848	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR8849	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8850	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR8851	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8852	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR8853	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8854	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR8855	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8856	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8857	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8858	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8859	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8860	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8861	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8862	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8863	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8864	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8865	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8866	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8867	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8868	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8869				HR8868 (Lo-16bits)
HR8870	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8871				HR8870 (Lo-16bits)
HR8872	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8873				HR8872 (Lo-16bits)
HR8874	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8875				HR8874 (Lo-16bits)
HR8876	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8877				HR8876 (Lo-16bits)
HR8878	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8879				HR8878 (Lo-16bits)
HR8880	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8881				HR8880 (Lo-16bits)
HR8882	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8883				HR8882 (Lo-16bits)
HR8884	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8885				HR8884 (Lo-16bits)
HR8886	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8887				HR8886 (Lo-16bits)
HR8888	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8889				HR8888 (Lo-16bits)
HR8890	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8891				HR8890 (Lo-16bits)
HR8892	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8893				HR8892 (Lo-16bits)
HR8894	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8895				HR8894 (Lo-16bits)
HR8896	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8897				HR8896 (Lo-16bits)
HR8898	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8899				HR8898 (Lo-16bits)
HR8900	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8901	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8902	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8903	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8904	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8905	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8906	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8907	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8908	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8909				HR8908 (Lo-16bits)
HR8910	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8911				HR8910 (Lo-16bits)
HR8912	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8913				HR8912 (Lo-16bits)
HR8914	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8915				HR8914 (Lo-16bits)
HR8916	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8917				HR8916 (Lo-16bits)
HR8918	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8919				HR8918 (Lo-16bits)
HR8920	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8921				HR8920 (Lo-16bits)
HR8922	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8923				HR8922 (Lo-16bits)
HR8924	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8925				HR8924 (Lo-16bits)
HR8926	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8927				HR8926 (Lo-16bits)
HR8928	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8929				HR8928 (Lo-16bits)
HR8930	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8931				HR8930 (Lo-16bits)
HR8932	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8933				HR8932 (Lo-16bits)
HR8934	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8935				HR8934 (Lo-16bits)
HR8936	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8937				HR8936 (Lo-16bits)
HR8938	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8939				HR8938 (Lo-16bits)
HR8940	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8941	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8942	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8943	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8944	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8945	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8946	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8947	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8948	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8949				HR8948 (Lo-16bits)
HR8950	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8951				HR8950 (Lo-16bits)
HR8952	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8953				HR8952 (Lo-16bits)
HR8954	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8955				HR8954 (Lo-16bits)
HR8956	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8957				HR8956 (Lo-16bits)
HR8958	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8959				HR8958 (Lo-16bits)
HR8960	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8961				HR8960 (Lo-16bits)
HR8962	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8963				HR8962 (Lo-16bits)
HR8964	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8965				HR8964 (Lo-16bits)
HR8966	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8967				HR8966 (Lo-16bits)
HR8968	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8969				HR8968 (Lo-16bits)
HR8970	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8971				HR8970 (Lo-16bits)
HR8972	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8973				HR8972 (Lo-16bits)
HR8974	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8975				HR8974 (Lo-16bits)
HR8976	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8977				HR8976 (Lo-16bits)
HR8978	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8979				HR8978 (Lo-16bits)
HR8980	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8981	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8982	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8983	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8984	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8985	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8986	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8987	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8988	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8989				HR8988 (Lo-16bits)
HR8990	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8991				HR8990 (Lo-16bits)
HR8992	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8993				HR8992 (Lo-16bits)
HR8994	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8995				HR8994 (Lo-16bits)
HR8996	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8997				HR8996 (Lo-16bits)
HR8998	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8999				HR8998 (Lo-16bits)
HR9000	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9001				HR9000 (Lo-16bits)
HR9002	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9003				HR9002 (Lo-16bits)
HR9004	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9005				HR9004 (Lo-16bits)
HR9006	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9007				HR9006 (Lo-16bits)
HR9008	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9009				HR9008 (Lo-16bits)
HR9010	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9011				HR9010 (Lo-16bits)
HR9012	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9013				HR9012 (Lo-16bits)
HR9014	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9015				HR9014 (Lo-16bits)
HR9016	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9017				HR9016 (Lo-16bits)
HR9018	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9019				HR9018 (Lo-16bits)
HR9020	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9021	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9022	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9023	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9024	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9025	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9026	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9027	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9028	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9029				HR9028 (Lo-16bits)
HR9030	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9031				HR9030 (Lo-16bits)
HR9032	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9033				HR9032 (Lo-16bits)
HR9034	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9035				HR9034 (Lo-16bits)
HR9036	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9037				HR9036 (Lo-16bits)
HR9038	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9039				HR9038 (Lo-16bits)
HR9040	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9041				HR9040 (Lo-16bits)
HR9042	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9043				HR9042 (Lo-16bits)
HR9044	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9045				HR9044 (Lo-16bits)
HR9046	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9047				HR9046 (Lo-16bits)
HR9048	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9049				HR9048 (Lo-16bits)
HR9050	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9051				HR9050 (Lo-16bits)
HR9052	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9053				HR9052 (Lo-16bits)
HR9054	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9055				HR9054 (Lo-16bits)
HR9056	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9057				HR9056 (Lo-16bits)
HR9058	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9059				HR9058 (Lo-16bits)
HR9060	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9061	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9062	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9063	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9064	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9065	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9066	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9067	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9068	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9069				HR9068 (Lo-16bits)
HR9070	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9071				HR9070 (Lo-16bits)
HR9072	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9073				HR9072 (Lo-16bits)
HR9074	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9075				HR9074 (Lo-16bits)
HR9076	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9077				HR9076 (Lo-16bits)
HR9078	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9079				HR9078 (Lo-16bits)
HR9080	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9081				HR9080 (Lo-16bits)
HR9082	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9083				HR9082 (Lo-16bits)
HR9084	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9085				HR9084 (Lo-16bits)
HR9086	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9087				HR9086 (Lo-16bits)
HR9088	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9089				HR9088 (Lo-16bits)
HR9090	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9091				HR9090 (Lo-16bits)
HR9092	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9093				HR9092 (Lo-16bits)
HR9094	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9095				HR9094 (Lo-16bits)
HR9096	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9097				HR9096 (Lo-16bits)
HR9098	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9099				HR9098 (Lo-16bits)
HR9100	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9101	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9102	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9103	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9104	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9105	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9106	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9107	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9108	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9109				HR9108 (Lo-16bits)
HR9110	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9111				HR9110 (Lo-16bits)
HR9112	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9113				HR9112 (Lo-16bits)
HR9114	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9115				HR9114 (Lo-16bits)
HR9116	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9117				HR9116 (Lo-16bits)
HR9118	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9119				HR9118 (Lo-16bits)
HR9120	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9121				HR9120 (Lo-16bits)
HR9122	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9123				HR9122 (Lo-16bits)
HR9124	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9125				HR9124 (Lo-16bits)
HR9126	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9127				HR9126 (Lo-16bits)
HR9128	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9129				HR9128 (Lo-16bits)
HR9130	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9131				HR9130 (Lo-16bits)
HR9132	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9133				HR9132 (Lo-16bits)
HR9134	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9135				HR9134 (Lo-16bits)
HR9136	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9137				HR9136 (Lo-16bits)
HR9138	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9139				HR9138 (Lo-16bits)
HR9140	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9141	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9142	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9143	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9144	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9145	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9146	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9147	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9148	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9149				HR9148 (Lo-16bits)
HR9150	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9151				HR9150 (Lo-16bits)
HR9152	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9153				HR9152 (Lo-16bits)
HR9154	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9155				HR9154 (Lo-16bits)
HR9156	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9157				HR9156 (Lo-16bits)
HR9158	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9159				HR9158 (Lo-16bits)
HR9160	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9161				HR9160 (Lo-16bits)
HR9162	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9163				HR9162 (Lo-16bits)
HR9164	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9165				HR9164 (Lo-16bits)
HR9166	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9167				HR9166 (Lo-16bits)
HR9168	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9169				HR9168 (Lo-16bits)
HR9170	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9171				HR9170 (Lo-16bits)
HR9172	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9173				HR9172 (Lo-16bits)
HR9174	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9175				HR9174 (Lo-16bits)
HR9176	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9177				HR9176 (Lo-16bits)
HR9178	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9179				HR9178 (Lo-16bits)
HR9180	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9181	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9182	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9183	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9184	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9185	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9186	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9187	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9188	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR9189	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9193	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR9194	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9195	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9196	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9197	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9198	OPW1:P> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9199	UPW1:P< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9200	RPW1:Prev> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9201	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9202	SEF1:U03rd< Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR9203	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9204	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9205	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9206	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9207	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR9208	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=VHz(1) Blocked 2=VHz(1) Start 3=VHz(1) Alarm 4=VHz(1) Trip
HR9209	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=VHz(2) Start 2=VHz(2) Alarm 3=VHz(2) Trip 4=VHz(2) Blocked
HR9210	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR9211	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)
HR9213	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9216	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9217				HR9216 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9218	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9219				HR9218 (Lo-16bits)
HR9220	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9221				HR9220 (Lo-16bits)
HR9222	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9223	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9224				HR9223 (Lo-16bits)
HR9225	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9226				HR9225 (Lo-16bits)
HR9227	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9228				HR9227 (Lo-16bits)
HR9229	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9230	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9231				HR9230 (Lo-16bits)
HR9232	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9233				HR9232 (Lo-16bits)
HR9234	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9235				HR9234 (Lo-16bits)
HR9236	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9237	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9243	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9244	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9245				HR9244 (Lo-16bits)
HR9246	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9247				HR9246 (Lo-16bits)
HR9248	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9249				HR9248 (Lo-16bits)
HR9250	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9251	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9260	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9261				HR9260 (Lo-16bits)
HR9262	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9263				HR9262 (Lo-16bits)
HR9264	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9265				HR9264 (Lo-16bits)
HR9266	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9267				HR9266 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9268	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9269	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9276	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9277				HR9276 (Lo-16bits)
HR9278	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9279				HR9278 (Lo-16bits)
HR9280	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9281				HR9280 (Lo-16bits)
HR9282	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9283	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9288				HR9287 (Lo-16bits)
HR9289	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9290	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9291				HR9290 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9292	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9293				HR9292 (Lo-16bits)
HR9294	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9295				HR9294 (Lo-16bits)
HR9296	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9297	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9302				HR9301 (Lo-16bits)
HR9303	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9304	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9305				HR9304 (Lo-16bits)
HR9306	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9307				HR9306 (Lo-16bits)
HR9308	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9309				HR9308 (Lo-16bits)
HR9310	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9311	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9317	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9320	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9321				HR9320 (Lo-16bits)
HR9322	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9323				HR9322 (Lo-16bits)
HR9324	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9325				HR9324 (Lo-16bits)
HR9326	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9327				HR9326 (Lo-16bits)
HR9328	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9329	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9341	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)
HR9361	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9366				HR9365 (Lo-16bits)
HR9367	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)
HR9391	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9396				HR9395 (Lo-16bits)
HR9397	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9426				HR9425 (Lo-16bits)
HR9427	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9456				HR9455 (Lo-16bits)
HR9457	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	ARC1:I/O Arc> condition	R	1	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked
HR9460	ARC1:I/O Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9461				HR9460 (Lo-16bits)
HR9462	ARC1:I/O Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9463				HR9462 (Lo-16bits)
HR9464	ARC1:I/O Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9465				HR9464 (Lo-16bits)
HR9466	ARC1:I/O Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9467				HR9466 (Lo-16bits)
HR9468	ARC1:I/O Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9469				HR9468 (Lo-16bits)
HR9470	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9471	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9472	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTBusfail 3=VTLinefail 4=Blocked
HR9473	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR9474	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR9475	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked
HR9476	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR9477	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR9478	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR9479	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9481	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9505	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	VJP1:<DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR9514	VJP1:Voltage meas selected	R	1	0=Selection Ok 1=Selection not available
HR9515	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9516	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9517	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9518	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9519	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9520	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9521	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9522	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9523	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9524	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9525	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9526	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR9527	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9528	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9529	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9530	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled 1=Activated
HR9531	AR1:AR Status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR9534	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR9535				HR9534 (Lo-16bits)
HR9536	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR9537				HR9536 (Lo-16bits)
HR9538	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR9539	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	VRG1:Voltage meas condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok

Holding Register	Parameter	Access	Length	Info
HR9544	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR9545				HR9544 (Lo-16bits)
HR9546	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR9547				HR9546 (Lo-16bits)
HR9548	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR9549	VRG1:Remote tap control input	RW	1	0=Stop 1=Lower 2=Higher
HR9550	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9551				HR9550 (Lo-16bits)
HR9552	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9553				HR9552 (Lo-16bits)
HR9554	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9555				HR9554 (Lo-16bits)
HR9556	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9557				HR9556 (Lo-16bits)
HR9558	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9559				HR9558 (Lo-16bits)
HR9560	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9561				HR9560 (Lo-16bits)
HR9562	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9563				HR9562 (Lo-16bits)
HR9564	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9565				HR9564 (Lo-16bits)
HR9566	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9567				HR9566 (Lo-16bits)
HR9568	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9569				HR9568 (Lo-16bits)
HR9570	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9571				HR9570 (Lo-16bits)
HR9572	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9573				HR9572 (Lo-16bits)
HR9574	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9575				HR9574 (Lo-16bits)
HR9576	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9577				HR9576 (Lo-16bits)
HR9578	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9579				HR9578 (Lo-16bits)
HR9580	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9587				HR9586 (Lo-16bits)
HR9588	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)
HR9594	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9599				HR9598 (Lo-16bits)
HR9600	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9617				HR9616 (Lo-16bits)
HR9618	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9619				HR9618 (Lo-16bits)
HR9620	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9621				HR9620 (Lo-16bits)
HR9622	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9623				HR9622 (Lo-16bits)
HR9624	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9625				HR9624 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9626	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9627				HR9626 (Lo-16bits)
HR9628	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9629				HR9628 (Lo-16bits)
HR9630	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLT detected
HR9631	EMON1:I0> (50N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9632	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLT detected
HR9633	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9634	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal 1=FLT FW 2=FLT REV
HR9635	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=No voltage
HR9636	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9637	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLT detected
HR9638	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR9639				HR9638 (Lo-16bits)
HR9640	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR9641				HR9640 (Lo-16bits)
HR9642	GSYN:Synchronizer mode	RW	1	0=Disabled 1=Activated
HR9643	GSYN:Synchronizing by	RW	1	0=Autosynchronizer 1=Manual commands
HR9644	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch 1=I know what I am doing
HR9645	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9672				HR9671 (Lo-16bits)
HR9673	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9690				HR9689 (Lo-16bits)
HR9691	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9697	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9722				HR9721 (Lo-16bits)
HR9723	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	VREC:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9726	VREC:Scale current values to primary	RW	1	0=No 1=Yes
HR9727	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	RW	1	0=No 1=Yes
HR9728	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9729				HR9728 (Lo-16bits)
HR9730	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR9731				HR9730 (Lo-16bits)
HR9732	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR9733				HR9732 (Lo-16bits)
HR9734	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR9735				HR9734 (Lo-16bits)
HR9736	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR9737				HR9736 (Lo-16bits)
HR9738	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR9739				HR9738 (Lo-16bits)
HR9740	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR9741				HR9740 (Lo-16bits)
HR9742	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR9743				HR9742 (Lo-16bits)
HR9744	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR9745				HR9744 (Lo-16bits)
HR9746	RTB:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9747	RTB:Scale current values to primary	RW	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9748	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9749				HR9748 (Lo-16bits)
HR9750	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9751				HR9750 (Lo-16bits)
HR9752	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9753				HR9752 (Lo-16bits)
HR9754	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9755				HR9754 (Lo-16bits)
HR9756	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9757				HR9756 (Lo-16bits)
HR9758	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9759				HR9758 (Lo-16bits)
HR9760	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9761				HR9760 (Lo-16bits)
HR9762	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9763				HR9762 (Lo-16bits)
HR9764	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9765				HR9764 (Lo-16bits)
HR9766	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9767				HR9766 (Lo-16bits)
HR9768	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR9769				HR9768 (Lo-16bits)
HR9770	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR9771				HR9770 (Lo-16bits)
HR9772	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9773				HR9772 (Lo-16bits)
HR9774	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9775				HR9774 (Lo-16bits)
HR9776	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9777				HR9776 (Lo-16bits)
HR9778	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9779				HR9778 (Lo-16bits)
HR9780	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9781				HR9780 (Lo-16bits)
HR9782	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9783				HR9782 (Lo-16bits)
HR9784	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9785				HR9784 (Lo-16bits)
HR9786	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9787				HR9786 (Lo-16bits)
HR9788	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9789				HR9788 (Lo-16bits)
HR9790	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9791				HR9790 (Lo-16bits)
HR9792	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9793				HR9792 (Lo-16bits)
HR9794	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9795				HR9794 (Lo-16bits)
HR9796	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9797				HR9796 (Lo-16bits)
HR9798	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9799				HR9798 (Lo-16bits)
HR9800	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9801				HR9800 (Lo-16bits)
HR9802	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9803				HR9802 (Lo-16bits)
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

1.3 M Modbus map

The following table lists modbus holding registers for AQ-210 frame units with firmware M_v2.1.1.3-247. The firmware is used by the following device types:

- AQ-M210
- AQ-M215



NOTICE!

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools* → *Communication* → *Modbus map*.

Table. 1.3 - 3. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter

Holding Register	Parameter	Access	Length	Info
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1:Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR27				HR26 (Lo-16bits)
HR28	MEAS1:Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1:Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1:Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1:Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1:Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1:Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1:Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1:Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR56	MEAS1: Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1: Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1: Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1: Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1: Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1: Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1: Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1: Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1: Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1: Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1: Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1: Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1: Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1: Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1: Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR445				HR444 (Lo-16bits)
HR446	MEAS2: Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2: Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2: Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2: Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2: Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2: calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2: Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2: Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2: Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2: I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2: I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2: I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2: I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2: I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR473				HR472 (Lo-16bits)
HR474	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR499				HR498 (Lo-16bits)
HR500	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I ¹ L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I ¹ L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I ¹ L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I ¹ L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I ¹ L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I ¹ L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I ¹ L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I ¹ L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I ¹ L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I ¹ L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I ¹ L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I ¹ L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I ¹ L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I ¹ L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I ¹ L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I ¹ L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I ¹ L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I ¹ L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I ¹ L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I ¹ L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I ¹ L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I ¹ L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I ¹ L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I ¹ L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I ¹ L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I ¹ L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I ¹ L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I ¹ L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I ¹ L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I ¹ L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR829				HR828 (Lo-16bits)
HR830	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR916	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR939				HR938 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR940	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR965				HR964 (Lo-16bits)
HR966	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR994	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1024	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1054	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1084	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1114	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1144	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1174	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1204	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1217				HR1216 (Lo-16bits)
HR1218	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)
HR1228	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1229				HR1228 (Lo-16bits)
HR1230	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1232	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)
HR1234	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)
HR1244	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)
HR1252	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1256	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)
HR1258	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1259				HR1258 (Lo-16bits)
HR1260	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)
HR1268	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)
HR1276	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1279				HR1278 (Lo-16bits)
HR1280	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1281				HR1280 (Lo-16bits)
HR1282	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)
HR1288	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1291				HR1290 (Lo-16bits)
HR1292	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1302	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	POW1:Reset 3ph Energies	RW	1	0=- 1=Reset
HR1317	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1325	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1338				HR1337 (Lo-16bits)
HR1339	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1340				HR1339 (Lo-16bits)
HR1341	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1342				HR1341 (Lo-16bits)
HR1343	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1344				HR1343 (Lo-16bits)
HR1345	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1346				HR1345 (Lo-16bits)
HR1347	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1348				HR1347 (Lo-16bits)
HR1349	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1350				HR1349 (Lo-16bits)
HR1351	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1352				HR1351 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1353	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1354				HR1353 (Lo-16bits)
HR1355	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1356				HR1355 (Lo-16bits)
HR1357	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1358				HR1357 (Lo-16bits)
HR1359	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1377	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)
HR1381	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1382				HR1381 (Lo-16bits)
HR1383	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)
HR1393	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1402				HR1401 (Lo-16bits)
HR1403	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1405	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)
HR1409	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1410				HR1409 (Lo-16bits)
HR1411	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)
HR1417	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)
HR1419	DIGI:DIs (cpu)	R	2	0b=DI1 1b=DI2 2b=DI3 (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	DIGI:DIs (input option 1)	R	2	0b=DI4 1b=DI5 2b=DI6 3b=DI7 4b=DI8 5b=DI9 6b=DI10 7b=DI11 (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	DIGI:DIs (input option 2)	R	2	0b=DI12 1b=DI13 2b=DI14 3b=DI15 4b=DI16 5b=DI17 6b=DI18 7b=DI19 (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	DIGI:DIs (input option 3)	R	2	0b=DI20 1b=DI21 2b=DI22 3b=DI23 4b=DI24 5b=DI25 6b=DI26 7b=DI27 (Hi-16bits)
HR1426				HR1425 (Lo-16bits)
HR1427	DIGI:DIs (input option 4)	R	2	0b=DI28 1b=DI29 2b=DI30 3b=DI31 4b=DI32 5b=DI33 6b=DI34 7b=DI35 (Hi-16bits)
HR1428				HR1427 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1429	DIGI:DIs (input option 5)	R	2	0b=DI36 1b=DI37 2b=DI38 3b=DI39 4b=DI40 5b=DI41 6b=DI42 7b=DI43 (Hi-16bits)
HR1430				HR1429 (Lo-16bits)
HR1431	DIGI:DIs (input option 6)	R	2	0b=DI44 1b=DI45 2b=DI46 3b=DI47 4b=DI48 5b=DI49 6b=DI50 7b=DI51 (Hi-16bits)
HR1432				HR1431 (Lo-16bits)
HR1433	DIGI:Dis 1-32	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1434				HR1433 (Lo-16bits)
HR1435	DIGI:Dis 33-64	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 (Hi-16bits)
HR1436				HR1435 (Lo-16bits)
HR1437	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1438				HR1437 (Lo-16bits)
HR1439	DIGI:DI1 Counter Clear	RW	1	0=- 1=Clear
HR1440	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1441				HR1440 (Lo-16bits)
HR1442	DIGI:DI2 Counter Clear	RW	1	0=- 1=Clear
HR1443	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1444				HR1443 (Lo-16bits)
HR1445	DIGI:DI3 Counter Clear	RW	1	0=- 1=Clear
HR1446	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1447				HR1446 (Lo-16bits)
HR1448	DIGI:DI4 Counter Clear	RW	1	0=- 1=Clear
HR1449	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1450				HR1449 (Lo-16bits)
HR1451	DIGI:DI5 Counter Clear	RW	1	0=- 1=Clear
HR1452	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1453				HR1452 (Lo-16bits)
HR1454	DIGI:DI6 Counter Clear	RW	1	0=- 1=Clear
HR1455	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1456				HR1455 (Lo-16bits)
HR1457	DIGI:DI7 Counter Clear	RW	1	0=- 1=Clear
HR1458	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1459				HR1458 (Lo-16bits)
HR1460	DIGI:DI8 Counter Clear	RW	1	0=- 1=Clear
HR1461	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1462				HR1461 (Lo-16bits)
HR1463	DIGI:DI9 Counter Clear	RW	1	0=- 1=Clear
HR1464	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1465				HR1464 (Lo-16bits)
HR1466	DIGI:DI10 Counter Clear	RW	1	0=- 1=Clear
HR1467	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)
HR1469	DIGI:DI11 Counter Clear	RW	1	0=- 1=Clear
HR1470	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1471				HR1470 (Lo-16bits)
HR1472	DIGI:DI12 Counter Clear	RW	1	0=- 1=Clear
HR1473	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1474				HR1473 (Lo-16bits)
HR1475	DIGI:DI13 Counter Clear	RW	1	0=- 1=Clear
HR1476	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1477				HR1476 (Lo-16bits)
HR1478	DIGI:DI14 Counter Clear	RW	1	0=- 1=Clear
HR1479	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1480				HR1479 (Lo-16bits)
HR1481	DIGI:DI15 Counter Clear	RW	1	0=- 1=Clear
HR1482	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1483				HR1482 (Lo-16bits)
HR1484	DIGI:DI16 Counter Clear	RW	1	0=- 1=Clear
HR1485	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1486				HR1485 (Lo-16bits)
HR1487	DIGI:DI17 Counter Clear	RW	1	0=- 1=Clear
HR1488	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1489				HR1488 (Lo-16bits)
HR1490	DIGI:DI18 Counter Clear	RW	1	0=- 1=Clear
HR1491	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1492				HR1491 (Lo-16bits)
HR1493	DIGI:DI19 Counter Clear	RW	1	0=- 1=Clear
HR1494	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1495				HR1494 (Lo-16bits)
HR1496	DIGI:DI20 Counter Clear	RW	1	0=- 1=Clear
HR1497	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1498				HR1497 (Lo-16bits)
HR1499	DIGI:DI21 Counter Clear	RW	1	0=- 1=Clear
HR1500	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1501				HR1500 (Lo-16bits)
HR1502	DIGI:DI22 Counter Clear	RW	1	0=- 1=Clear
HR1503	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1504				HR1503 (Lo-16bits)
HR1505	DIGI:DI23 Counter Clear	RW	1	0=- 1=Clear
HR1506	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1507				HR1506 (Lo-16bits)
HR1508	DIGI:DI24 Counter Clear	RW	1	0=- 1=Clear
HR1509	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1510				HR1509 (Lo-16bits)
HR1511	DIGI:DI25 Counter Clear	RW	1	0=- 1=Clear
HR1512	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1513				HR1512 (Lo-16bits)
HR1514	DIGI:DI26 Counter Clear	RW	1	0=- 1=Clear
HR1515	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIGI:DI27 Counter Clear	RW	1	0=- 1=Clear
HR1518	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	DIGI:DI28 Counter Clear	RW	1	0=- 1=Clear
HR1521	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIGI:DI29 Counter Clear	RW	1	0=- 1=Clear
HR1524	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	DIGI:DI30 Counter Clear	RW	1	0=- 1=Clear
HR1527	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	DIGI:DI31 Counter Clear	RW	1	0=- 1=Clear
HR1530	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1531				HR1530 (Lo-16bits)
HR1532	DIGI:DI32 Counter Clear	RW	1	0=- 1=Clear
HR1533	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	DIGI:DI33 Counter Clear	RW	1	0=- 1=Clear
HR1536	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	DIGI:DI34 Counter Clear	RW	1	0=- 1=Clear
HR1539	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1540				HR1539 (Lo-16bits)
HR1541	DIGI:DI35 Counter Clear	RW	1	0=- 1=Clear
HR1542	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1543				HR1542 (Lo-16bits)
HR1544	DIGI:DI36 Counter Clear	RW	1	0=- 1=Clear
HR1545	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	DIGI:DI37 Counter Clear	RW	1	0=- 1=Clear
HR1548	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	DIGI:DI38 Counter Clear	RW	1	0=- 1=Clear
HR1551	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	DIGI:DI39 Counter Clear	RW	1	0=- 1=Clear
HR1554	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	DIGI:DI40 Counter Clear	RW	1	0=- 1=Clear
HR1557	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	DIGI:DI41 Counter Clear	RW	1	0=- 1=Clear
HR1560	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1561				HR1560 (Lo-16bits)
HR1562	DIGI:DI42 Counter Clear	RW	1	0=- 1=Clear
HR1563	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	DIGI:DI43 Counter Clear	RW	1	0=- 1=Clear
HR1566	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	DIGI:DI44 Counter Clear	RW	1	0=- 1=Clear
HR1569	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1570				HR1569 (Lo-16bits)
HR1571	DIGI:DI45 Counter Clear	RW	1	0=- 1=Clear
HR1572	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1573				HR1572 (Lo-16bits)
HR1574	DIGI:DI46 Counter Clear	RW	1	0=- 1=Clear
HR1575	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	DIGI:DI47 Counter Clear	RW	1	0=- 1=Clear
HR1578	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	DIGI:DI48 Counter Clear	RW	1	0=- 1=Clear
HR1581	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	DIGI:DI49 Counter Clear	RW	1	0=- 1=Clear
HR1584	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	DIGI:DI50 Counter Clear	RW	1	0=- 1=Clear
HR1587	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	DIGI:DI51 Counter Clear	RW	1	0=- 1=Clear
HR1590	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1593	SGS:Force SG	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1594	SGS:Force SG change	RW	1	0=Disabled 1=Enabled
HR1595	SGS:Used setting groups	RW	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1596	SGS:Remote SG change	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1597	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR1598	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1600	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1605				HR1604 (Lo-16bits)
HR1606	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1617				HR1616 (Lo-16bits)
HR1618	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1627				HR1626 (Lo-16bits)
HR1628	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1630	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1635				HR1634 (Lo-16bits)
HR1636	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR1639	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1652				HR1651 (Lo-16bits)
HR1653	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1655	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1679	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1688	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1691	NOC1:I> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1692	NOC1:I> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1693	NOC1:I> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1694	NOC1:I> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1695	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1704				HR1703 (Lo-16bits)
HR1705	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)
HR1713	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1716				HR1715 (Lo-16bits)
HR1717	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1718	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1719	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1720	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1721	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1722	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1723	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1724	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1726				HR1725 (Lo-16bits)
HR1727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1732				HR1731 (Lo-16bits)
HR1733	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1736				HR1735 (Lo-16bits)
HR1737	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1738				HR1737 (Lo-16bits)
HR1739	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1742	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1743	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1744	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1745	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1746	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1747	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1748	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1749	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1750	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1751	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1752	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1753	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1754	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1755	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1756	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1757	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1758	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1759	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1760	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1761	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1762	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1763	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1764	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1765	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1766	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1767	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1768	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1769	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1770	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1771	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1772	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1776				HR1775 (Lo-16bits)
HR1777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1778				HR1777 (Lo-16bits)
HR1779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1780				HR1779 (Lo-16bits)
HR1781	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1784				HR1783 (Lo-16bits)
HR1785	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1790				HR1789 (Lo-16bits)
HR1791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)
HR1793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1794				HR1793 (Lo-16bits)
HR1795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1796				HR1795 (Lo-16bits)
HR1797	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1798				HR1797 (Lo-16bits)
HR1799	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1800				HR1799 (Lo-16bits)
HR1801	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)
HR1803	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1808				HR1807 (Lo-16bits)
HR1809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1838	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1839	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1840	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1841	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1842	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1843	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1844	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1845	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1852				HR1851 (Lo-16bits)
HR1853	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1854				HR1853 (Lo-16bits)
HR1855	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1856				HR1855 (Lo-16bits)
HR1857	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1858				HR1857 (Lo-16bits)
HR1859	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1860				HR1859 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1861	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1862	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1863	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1864	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1865	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1866	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1867	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1868	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1870				HR1869 (Lo-16bits)
HR1871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1872				HR1871 (Lo-16bits)
HR1873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1874				HR1873 (Lo-16bits)
HR1875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1876				HR1875 (Lo-16bits)
HR1877	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1878				HR1877 (Lo-16bits)
HR1879	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1880				HR1879 (Lo-16bits)
HR1881	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1882				HR1881 (Lo-16bits)
HR1883	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1884				HR1883 (Lo-16bits)
HR1885	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1886	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1887	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1888	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1889	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1890	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1891	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1892	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1893	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1894	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1895	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1896	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1897	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1898	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1899	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1900	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1901	NOC1:Pick-up setting lset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1902				HR1901 (Lo-16bits)
HR1903	NOC1:Delay type	RW	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR1904	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	NOC1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR1907	NOC1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1908	NOC1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1909	NOC1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR1910	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	NOC1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR1919	NOC1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR1920	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1921	NOC2:I>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1922	NOC2:I>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1923	NOC2:I>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1924	NOC2:I>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1925	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1926				HR1925 (Lo-16bits)
HR1927	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1928				HR1927 (Lo-16bits)
HR1929	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1948	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1949	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1950	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1951	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1952	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1953	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1954	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1955	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1957	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1958				HR1957 (Lo-16bits)
HR1959	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1962				HR1961 (Lo-16bits)
HR1963	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1972	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1973	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1974	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1975	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1976	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1977	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1978	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1979	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1980	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1981	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1982	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1983	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1984	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1985	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1986	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1987	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1988	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1989	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1990	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1991	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1992	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1993	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1994	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1995	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1996	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1997	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1998	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1999	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2000	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2001	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2002	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2003	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2032				HR2031 (Lo-16bits)
HR2033	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2044				HR2043 (Lo-16bits)
HR2045	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2046				HR2045 (Lo-16bits)
HR2047	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2048				HR2047 (Lo-16bits)
HR2049	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2050				HR2049 (Lo-16bits)
HR2051	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2052				HR2051 (Lo-16bits)
HR2053	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2054				HR2053 (Lo-16bits)
HR2055	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2056				HR2055 (Lo-16bits)
HR2057	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)
HR2059	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)
HR2061	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2062				HR2061 (Lo-16bits)
HR2063	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2068	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2069	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2070	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2071	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2072	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2073	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2074	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2075	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)
HR2081	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2085	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)
HR2091	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2092	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2093	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2094	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2095	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2096	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2097	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2098	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2099	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2110				HR2109 (Lo-16bits)
HR2111	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2116	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2117	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2118	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2119	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2120	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2121	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2122	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2123	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2124	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2125	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2126	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2127	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2128	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2129	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2130	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2131	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	NOC2:Delay type	RW	1	0=DT 1=IDMT
HR2134	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2135				HR2134 (Lo-16bits)
HR2136	NOC2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2137	NOC2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2138	NOC2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2139	NOC2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2140	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	NOC2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2149	NOC2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2150	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2151	NOC3:I>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2152	NOC3:I>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC

Holding Register	Parameter	Access	Length	Info
HR2153	NOC3: >>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2154	NOC3: >>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2155	NOC3: >>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	NOC3: >>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2158				HR2157 (Lo-16bits)
HR2159	NOC3: >>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)
HR2165	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2178	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2179	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2180	NOC3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2181	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2182	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2183	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2184	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2185	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2186				HR2185 (Lo-16bits)
HR2187	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)
HR2195	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)
HR2201	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2202	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2203	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2204	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2205	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2206	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2207	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2208	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2209	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2210	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2211	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2212	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2213	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2214	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2215	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2216	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2217	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2218	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2219	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2220	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2221	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2222	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2223	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2224	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2225	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2226	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2227	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2228	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2229	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2230	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2231	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2232	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2233	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2240				HR2239 (Lo-16bits)
HR2241	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2242				HR2241 (Lo-16bits)
HR2243	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2246				HR2245 (Lo-16bits)
HR2247	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)
HR2251	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)
HR2253	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2254				HR2253 (Lo-16bits)
HR2255	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2256				HR2255 (Lo-16bits)
HR2257	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)
HR2267	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2272				HR2271 (Lo-16bits)
HR2273	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2286				HR2285 (Lo-16bits)
HR2287	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)
HR2291	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2298	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2299	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2300	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2301	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2302	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2303	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2304	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2305	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2308				HR2307 (Lo-16bits)
HR2309	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2311	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2314				HR2313 (Lo-16bits)
HR2315	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2322	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2323	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2324	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2325	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2326	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2327	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2328	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2329	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2330				HR2329 (Lo-16bits)
HR2331	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2332				HR2331 (Lo-16bits)
HR2333	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2334				HR2333 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2335	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2336				HR2335 (Lo-16bits)
HR2337	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2338				HR2337 (Lo-16bits)
HR2339	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2340				HR2339 (Lo-16bits)
HR2341	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2342				HR2341 (Lo-16bits)
HR2343	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2344				HR2343 (Lo-16bits)
HR2345	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2346	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2347	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2348	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2349	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2350	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2351	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2352	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2353	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2354	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2355	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2356	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2357	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2358	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2359	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2360	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2361	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2362				HR2361 (Lo-16bits)
HR2363	NOC3:Delay type	RW	1	0=DT 1=IDMT
HR2364	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	NOC3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2367	NOC3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2368	NOC3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2369	NOC3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2370	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	NOC3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2379	NOC3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR2380	NOC4: >>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2381	NOC4: >>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2382	NOC4: >>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2383	NOC4: >>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2384	NOC4: >>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2385	NOC4: >>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2386				HR2385 (Lo-16bits)
HR2387	NOC4: >>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	NOC4: >>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)
HR2391	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)
HR2393	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2402				HR2401 (Lo-16bits)
HR2403	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2404				HR2403 (Lo-16bits)
HR2405	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2406				HR2405 (Lo-16bits)
HR2407	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2408	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2409	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2410	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2411	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2412	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2413	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2414	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2415	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2416				HR2415 (Lo-16bits)
HR2417	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2418				HR2417 (Lo-16bits)
HR2419	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2420				HR2419 (Lo-16bits)
HR2421	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2422				HR2421 (Lo-16bits)
HR2423	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2424				HR2423 (Lo-16bits)
HR2425	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2426				HR2425 (Lo-16bits)
HR2427	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2428				HR2427 (Lo-16bits)
HR2429	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2430				HR2429 (Lo-16bits)
HR2431	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2432	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2433	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2434	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2435	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2436	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2437	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2438	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2439	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2440	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2441	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2442	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2443	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2444	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2445	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2446	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2447	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2448	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2449	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2450	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2451	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2452	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2453	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2454	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2455	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2456	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2457	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2458	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2459	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2460	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2461	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2462	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2463	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2464				HR2463 (Lo-16bits)
HR2465	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2466				HR2465 (Lo-16bits)
HR2467	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2468				HR2467 (Lo-16bits)
HR2469	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2470				HR2469 (Lo-16bits)
HR2471	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2472				HR2471 (Lo-16bits)
HR2473	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2474				HR2473 (Lo-16bits)
HR2475	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2476				HR2475 (Lo-16bits)
HR2477	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2478				HR2477 (Lo-16bits)
HR2479	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2480				HR2479 (Lo-16bits)
HR2481	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2482				HR2481 (Lo-16bits)
HR2483	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2484				HR2483 (Lo-16bits)
HR2485	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2486				HR2485 (Lo-16bits)
HR2487	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2488				HR2487 (Lo-16bits)
HR2489	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2490				HR2489 (Lo-16bits)
HR2491	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2492				HR2491 (Lo-16bits)
HR2493	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2494				HR2493 (Lo-16bits)
HR2495	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2496				HR2495 (Lo-16bits)
HR2497	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2498				HR2497 (Lo-16bits)
HR2499	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2500				HR2499 (Lo-16bits)
HR2501	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2502				HR2501 (Lo-16bits)
HR2503	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2504				HR2503 (Lo-16bits)
HR2505	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2506				HR2505 (Lo-16bits)
HR2507	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2508				HR2507 (Lo-16bits)
HR2509	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2510				HR2509 (Lo-16bits)
HR2511	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2512				HR2511 (Lo-16bits)
HR2513	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2514				HR2513 (Lo-16bits)
HR2515	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2516				HR2515 (Lo-16bits)
HR2517	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2528	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2529	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2530	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2531	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2532	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2533	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2534	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2535	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2536				HR2535 (Lo-16bits)
HR2537	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2552	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2553	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2554	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2555	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2556	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2557	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2558	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2559	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)
HR2565	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2576	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2577	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2578	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2579	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2580	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2581	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2582	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2583	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2584	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2585	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2586	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2587	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2588	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2589	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2590	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2591	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2592				HR2591 (Lo-16bits)
HR2593	NOC4:Delay type	RW	1	0=DT 1=IDMT
HR2594	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2595				HR2594 (Lo-16bits)
HR2596	NOC4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2597	NOC4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2598	NOC4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2599	NOC4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2600	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2601				HR2600 (Lo-16bits)
HR2602	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2603				HR2602 (Lo-16bits)
HR2604	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2605				HR2604 (Lo-16bits)
HR2606	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2607				HR2606 (Lo-16bits)
HR2608	NOC4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2609	NOC4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2610	ROC1:ldrw> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2611	ROC1:ldrw> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2612	ROC2:ldrw>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2613	ROC2:ldrw>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2614	ROC3:ldrw>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2615	ROC3:ldrw>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2616	ROC4:ldrw>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2617	ROC4:ldrw>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2618	ROC5:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2619	ROC5:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2620	ROC6:ldrw>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2621	ROC6:ldrw>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2622	ROC7:ldrw>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2623	ROC7:ldrw>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2624	ROC8:ldrw>>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2625	ROC8:ldrw>>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2626	ROV1:Urw>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2627	ROV2:Urw>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR2628	ROV3:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2629	ROV4:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2630	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2631	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2632	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled 1=Activated
HR2633	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=Start On (Alarm) 3=Start On (Alarm & Trip) 4=Start On (Trip) 5=Alarm On 6=Start On 7=Start On Alarm On 8=Trip On 9=Alarm On Trip On 10=Multistatus On
HR2634	DOC1:ldir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2635	DOC1:ldir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2636	DOC1:ldir> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2637	DOC1:ldir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DOC1:ldir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2640				HR2639 (Lo-16bits)
HR2641	DOC1:ldir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2642				HR2641 (Lo-16bits)
HR2643	DOC1:ldir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2646	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2647	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2648	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2649	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2650	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2651	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2652	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2653	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2654				HR2653 (Lo-16bits)
HR2655	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2658				HR2657 (Lo-16bits)
HR2659	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2660				HR2659 (Lo-16bits)
HR2661	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2670				HR2669 (Lo-16bits)
HR2671	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2673	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2688				HR2687 (Lo-16bits)
HR2689	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2700				HR2699 (Lo-16bits)
HR2701	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2702	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2703	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2704	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2705	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2706	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2707	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2708	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2709	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2726	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2727	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2728	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2729	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2730	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2731	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2732	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2733	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2734	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2735	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2736	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2737	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2738	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2739	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2740	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2741	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2742	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2743	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2744	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2745	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2746	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2747	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2748	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2749	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2750	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2751	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2752	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2753	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2754	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2755	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2756	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2757	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)
HR2763	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2774				HR2773 (Lo-16bits)
HR2775	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2804				HR2803 (Lo-16bits)
HR2805	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2822	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2823	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2824	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2825	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2826	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2827	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2828	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2829	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2830				HR2829 (Lo-16bits)
HR2831	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2832				HR2831 (Lo-16bits)
HR2833	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)
HR2837	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2846	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2847	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2848	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2849	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2850	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2851	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2852	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2853	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)
HR2861	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2870	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2871	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2872	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2873	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2874	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2875	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2876	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2877	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2878	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2879	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2880	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2881	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2882	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2883	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2884	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2885	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2886	DOC2:ldir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2887	DOC2:ldir>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2888	DOC2:ldir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2889				HR2888 (Lo-16bits)
HR2890	DOC2:ldir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2891				HR2890 (Lo-16bits)
HR2892	DOC2:ldir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2893				HR2892 (Lo-16bits)
HR2894	DOC2:ldir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2895				HR2894 (Lo-16bits)
HR2896	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2897	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2898	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2899	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2900	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2901	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2902	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2903	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2904	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2905				HR2904 (Lo-16bits)
HR2906	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2907				HR2906 (Lo-16bits)
HR2908	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2909				HR2908 (Lo-16bits)
HR2910	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2911				HR2910 (Lo-16bits)
HR2912	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2913				HR2912 (Lo-16bits)
HR2914	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2915				HR2914 (Lo-16bits)
HR2916	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2917				HR2916 (Lo-16bits)
HR2918	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2919				HR2918 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2920	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2921				HR2920 (Lo-16bits)
HR2922	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2923				HR2922 (Lo-16bits)
HR2924	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2925				HR2924 (Lo-16bits)
HR2926	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2927				HR2926 (Lo-16bits)
HR2928	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2929				HR2928 (Lo-16bits)
HR2930	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2931				HR2930 (Lo-16bits)
HR2932	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2933				HR2932 (Lo-16bits)
HR2934	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2935				HR2934 (Lo-16bits)
HR2936	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2937				HR2936 (Lo-16bits)
HR2938	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2939				HR2938 (Lo-16bits)
HR2940	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2941				HR2940 (Lo-16bits)
HR2942	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2943				HR2942 (Lo-16bits)
HR2944	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2945				HR2944 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2946	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2947				HR2946 (Lo-16bits)
HR2948	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2949				HR2948 (Lo-16bits)
HR2950	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2951				HR2950 (Lo-16bits)
HR2952	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2953	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2954	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2955	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2956	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2957	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2958	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2959	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2960	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2961				HR2960 (Lo-16bits)
HR2962	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2963				HR2962 (Lo-16bits)
HR2964	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2965				HR2964 (Lo-16bits)
HR2966	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2967				HR2966 (Lo-16bits)
HR2968	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2969				HR2968 (Lo-16bits)
HR2970	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2971				HR2970 (Lo-16bits)
HR2972	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2973				HR2972 (Lo-16bits)
HR2974	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2975				HR2974 (Lo-16bits)
HR2976	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2977	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2978	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2979	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2980	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2981	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2982	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2983	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2984	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2985	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2986	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2987	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2988	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2989	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2990	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2991	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2992	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2993	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2994	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2995	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2996	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2997	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2998	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2999	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3000	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3001	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3002	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3003	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3004	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3005	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3006	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3007	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3008	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3009				HR3008 (Lo-16bits)
HR3010	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3011				HR3010 (Lo-16bits)
HR3012	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3013				HR3012 (Lo-16bits)
HR3014	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3015				HR3014 (Lo-16bits)
HR3016	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3017				HR3016 (Lo-16bits)
HR3018	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3019				HR3018 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3020	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3021				HR3020 (Lo-16bits)
HR3022	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3023				HR3022 (Lo-16bits)
HR3024	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3025				HR3024 (Lo-16bits)
HR3026	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3027				HR3026 (Lo-16bits)
HR3028	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3029				HR3028 (Lo-16bits)
HR3030	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3031				HR3030 (Lo-16bits)
HR3032	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3033				HR3032 (Lo-16bits)
HR3034	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3035				HR3034 (Lo-16bits)
HR3036	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3037				HR3036 (Lo-16bits)
HR3038	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3039				HR3038 (Lo-16bits)
HR3040	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3041				HR3040 (Lo-16bits)
HR3042	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3043				HR3042 (Lo-16bits)
HR3044	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3045				HR3044 (Lo-16bits)
HR3046	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3047				HR3046 (Lo-16bits)
HR3048	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3049				HR3048 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3050	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3051				HR3050 (Lo-16bits)
HR3052	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3053				HR3052 (Lo-16bits)
HR3054	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3055				HR3054 (Lo-16bits)
HR3056	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3057				HR3056 (Lo-16bits)
HR3058	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3059				HR3058 (Lo-16bits)
HR3060	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3061				HR3060 (Lo-16bits)
HR3062	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3063				HR3062 (Lo-16bits)
HR3064	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3065				HR3064 (Lo-16bits)
HR3066	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3067				HR3066 (Lo-16bits)
HR3068	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3069				HR3068 (Lo-16bits)
HR3070	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3071				HR3070 (Lo-16bits)
HR3072	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3073	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3074	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3075	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3076	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3077	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3078	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3079	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3080	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3081				HR3080 (Lo-16bits)
HR3082	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3083				HR3082 (Lo-16bits)
HR3084	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3085				HR3084 (Lo-16bits)
HR3086	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3087				HR3086 (Lo-16bits)
HR3088	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3089				HR3088 (Lo-16bits)
HR3090	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3091				HR3090 (Lo-16bits)
HR3092	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3093				HR3092 (Lo-16bits)
HR3094	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3095				HR3094 (Lo-16bits)
HR3096	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3097	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3098	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3099	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3100	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3101	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3102	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3103	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3104	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3105				HR3104 (Lo-16bits)
HR3106	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3107				HR3106 (Lo-16bits)
HR3108	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3109				HR3108 (Lo-16bits)
HR3110	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3111				HR3110 (Lo-16bits)
HR3112	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3113				HR3112 (Lo-16bits)
HR3114	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3115				HR3114 (Lo-16bits)
HR3116	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3117				HR3116 (Lo-16bits)
HR3118	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3119				HR3118 (Lo-16bits)
HR3120	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3121	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3122	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3123	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3124	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3125	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3126	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3127	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3128	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3129	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3130	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3131	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3132	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3133	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3134	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3135	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3136	DOC3:ldir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3137	DOC3:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3138	DOC3:ldir>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3139	DOC3:ldir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	DOC3:ldir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3143	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)
HR3147	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3148	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3149	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3150	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3151	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3152	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3153	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3154	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3155	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3156				HR3155 (Lo-16bits)
HR3157	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3165	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3190				HR3189 (Lo-16bits)
HR3191	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3204	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3205	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3206	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3207	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3208	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3209	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3210	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3211	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3218				HR3217 (Lo-16bits)
HR3219	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3228	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3229	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3230	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3231	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3232	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3233	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3234	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3235	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3236	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3237	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3238	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3239	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3240	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3241	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3242	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3243	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3244	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3245	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3246	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3247	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3248	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3249	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3250	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3251	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3252	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3253	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3254	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3255	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3256	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3257	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3258	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3259	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3264				HR3263 (Lo-16bits)
HR3265	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3294				HR3293 (Lo-16bits)
HR3295	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)
HR3315	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3324	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3325	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3326	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3327	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3328	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3329	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3330	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3331	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3347	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3348	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3349	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3350	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3351	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3352	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3353	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3354	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3355	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3372	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3373	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3374	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3375	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3376	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3377	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3378	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3379	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3380	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3381	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3382	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3383	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3384	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3385	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3386	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3387	DOC4:ldir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3388	DOC4:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3389	DOC4:ldir>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C

Holding Register	Parameter	Access	Length	Info
HR3390	DOC4:ldir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3391				HR3390 (Lo-16bits)
HR3392	DOC4:ldir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3393				HR3392 (Lo-16bits)
HR3394	DOC4:ldir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3395				HR3394 (Lo-16bits)
HR3396	DOC4:ldir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3397				HR3396 (Lo-16bits)
HR3398	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3399	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3400	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3401	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3402	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3403	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3404	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3405	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3406	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3407				HR3406 (Lo-16bits)
HR3408	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3409				HR3408 (Lo-16bits)
HR3410	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3411				HR3410 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3412	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3413				HR3412 (Lo-16bits)
HR3414	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3415				HR3414 (Lo-16bits)
HR3416	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3417				HR3416 (Lo-16bits)
HR3418	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3419				HR3418 (Lo-16bits)
HR3420	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3421				HR3420 (Lo-16bits)
HR3422	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3423				HR3422 (Lo-16bits)
HR3424	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3425				HR3424 (Lo-16bits)
HR3426	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3427				HR3426 (Lo-16bits)
HR3428	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3429				HR3428 (Lo-16bits)
HR3430	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3431				HR3430 (Lo-16bits)
HR3432	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3433				HR3432 (Lo-16bits)
HR3434	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3435				HR3434 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3436	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3437				HR3436 (Lo-16bits)
HR3438	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3439				HR3438 (Lo-16bits)
HR3440	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3441				HR3440 (Lo-16bits)
HR3442	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3443				HR3442 (Lo-16bits)
HR3444	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3445				HR3444 (Lo-16bits)
HR3446	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3447				HR3446 (Lo-16bits)
HR3448	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3449				HR3448 (Lo-16bits)
HR3450	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3451				HR3450 (Lo-16bits)
HR3452	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3453				HR3452 (Lo-16bits)
HR3454	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3455	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3456	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3457	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3458	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3459	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3460	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3461	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3462	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3463				HR3462 (Lo-16bits)
HR3464	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3465				HR3464 (Lo-16bits)
HR3466	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3467				HR3466 (Lo-16bits)
HR3468	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3469				HR3468 (Lo-16bits)
HR3470	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3471				HR3470 (Lo-16bits)
HR3472	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3473				HR3472 (Lo-16bits)
HR3474	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3475				HR3474 (Lo-16bits)
HR3476	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3477				HR3476 (Lo-16bits)
HR3478	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3479	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3480	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3481	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3482	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3483	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3484	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3485	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3486	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3487	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3488	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3489	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3490	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3491	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3492	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3493	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3494	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3495	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3496	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3497	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3498	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3499	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3500	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3501	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3502	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3503	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3504	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3505	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3506	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3507	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3508	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3509	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3510	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3511				HR3510 (Lo-16bits)
HR3512	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3513				HR3512 (Lo-16bits)
HR3514	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3515				HR3514 (Lo-16bits)
HR3516	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3517				HR3516 (Lo-16bits)
HR3518	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3519				HR3518 (Lo-16bits)
HR3520	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3521				HR3520 (Lo-16bits)
HR3522	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3523				HR3522 (Lo-16bits)
HR3524	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3525				HR3524 (Lo-16bits)
HR3526	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3527				HR3526 (Lo-16bits)
HR3528	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3529				HR3528 (Lo-16bits)
HR3530	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3531				HR3530 (Lo-16bits)
HR3532	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3533				HR3532 (Lo-16bits)
HR3534	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3535				HR3534 (Lo-16bits)
HR3536	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3537				HR3536 (Lo-16bits)
HR3538	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3539				HR3538 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3540	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3541				HR3540 (Lo-16bits)
HR3542	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3543				HR3542 (Lo-16bits)
HR3544	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3545				HR3544 (Lo-16bits)
HR3546	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3547				HR3546 (Lo-16bits)
HR3548	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3549				HR3548 (Lo-16bits)
HR3550	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3551				HR3550 (Lo-16bits)
HR3552	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3553				HR3552 (Lo-16bits)
HR3554	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3555				HR3554 (Lo-16bits)
HR3556	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3557				HR3556 (Lo-16bits)
HR3558	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3559				HR3558 (Lo-16bits)
HR3560	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3561				HR3560 (Lo-16bits)
HR3562	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3563				HR3562 (Lo-16bits)
HR3564	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3565				HR3564 (Lo-16bits)
HR3566	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3567				HR3566 (Lo-16bits)
HR3568	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3569				HR3568 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3570	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3571				HR3570 (Lo-16bits)
HR3572	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3573				HR3572 (Lo-16bits)
HR3574	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3575	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3576	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3577	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3578	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3579	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3580	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3581	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3582	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3583				HR3582 (Lo-16bits)
HR3584	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3585				HR3584 (Lo-16bits)
HR3586	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3587				HR3586 (Lo-16bits)
HR3588	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3589				HR3588 (Lo-16bits)
HR3590	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3591				HR3590 (Lo-16bits)
HR3592	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3593				HR3592 (Lo-16bits)
HR3594	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3595				HR3594 (Lo-16bits)
HR3596	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3597				HR3596 (Lo-16bits)
HR3598	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3599	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3600	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3601	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3602	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3603	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3604	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3605	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3606	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3607				HR3606 (Lo-16bits)
HR3608	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3609				HR3608 (Lo-16bits)
HR3610	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3611				HR3610 (Lo-16bits)
HR3612	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3613				HR3612 (Lo-16bits)
HR3614	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3615				HR3614 (Lo-16bits)
HR3616	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3617				HR3616 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3618	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3619				HR3618 (Lo-16bits)
HR3620	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3621				HR3620 (Lo-16bits)
HR3622	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3623	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3624	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3625	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3626	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3627	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3628	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3629	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3630	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3631	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3632	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3633	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3634	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3635	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3636	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3637	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3638	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3639	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3640	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3641	NEF1:I0> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3642	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3643				HR3642 (Lo-16bits)
HR3644	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3645				HR3644 (Lo-16bits)
HR3646	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3647				HR3646 (Lo-16bits)
HR3648	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3649				HR3648 (Lo-16bits)
HR3650	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3651				HR3650 (Lo-16bits)
HR3652	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3653				HR3652 (Lo-16bits)
HR3654	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3655				HR3654 (Lo-16bits)
HR3656	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3657				HR3656 (Lo-16bits)
HR3658	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3659				HR3658 (Lo-16bits)
HR3660	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3661	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3662	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3663	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3664	NEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3665	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3666	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3667	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3668	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3669				HR3668 (Lo-16bits)
HR3670	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3671				HR3670 (Lo-16bits)
HR3672	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3673				HR3672 (Lo-16bits)
HR3674	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3675				HR3674 (Lo-16bits)
HR3676	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3677				HR3676 (Lo-16bits)
HR3678	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3679				HR3678 (Lo-16bits)
HR3680	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3681				HR3680 (Lo-16bits)
HR3682	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3683				HR3682 (Lo-16bits)
HR3684	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3685	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3686	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3687	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3688	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3689	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3690	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3691	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3692	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3693	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3694	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3695	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3696	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3697	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3698	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3699	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3700	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3701	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3702	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3703	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3704	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3705	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3706	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3707	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3708	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3709	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3710	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3711	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3712	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3713	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3714	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3715	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3716	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3717				HR3716 (Lo-16bits)
HR3718	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3719				HR3718 (Lo-16bits)
HR3720	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3721				HR3720 (Lo-16bits)
HR3722	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3723				HR3722 (Lo-16bits)
HR3724	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3725				HR3724 (Lo-16bits)
HR3726	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3727				HR3726 (Lo-16bits)
HR3728	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3729				HR3728 (Lo-16bits)
HR3730	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3731				HR3730 (Lo-16bits)
HR3732	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3733				HR3732 (Lo-16bits)
HR3734	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3735				HR3734 (Lo-16bits)
HR3736	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3737				HR3736 (Lo-16bits)
HR3738	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3739				HR3738 (Lo-16bits)
HR3740	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3741				HR3740 (Lo-16bits)
HR3742	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3743				HR3742 (Lo-16bits)
HR3744	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3745				HR3744 (Lo-16bits)
HR3746	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3747				HR3746 (Lo-16bits)
HR3748	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3749				HR3748 (Lo-16bits)
HR3750	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3751				HR3750 (Lo-16bits)
HR3752	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3753				HR3752 (Lo-16bits)
HR3754	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3755				HR3754 (Lo-16bits)
HR3756	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3757				HR3756 (Lo-16bits)
HR3758	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3759				HR3758 (Lo-16bits)
HR3760	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3761				HR3760 (Lo-16bits)
HR3762	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3763				HR3762 (Lo-16bits)
HR3764	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3765				HR3764 (Lo-16bits)
HR3766	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3767				HR3766 (Lo-16bits)
HR3768	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3769				HR3768 (Lo-16bits)
HR3770	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3771				HR3770 (Lo-16bits)
HR3772	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3773				HR3772 (Lo-16bits)
HR3774	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3775				HR3774 (Lo-16bits)
HR3776	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3777				HR3776 (Lo-16bits)
HR3778	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3779				HR3778 (Lo-16bits)
HR3780	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3781	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3782	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3783	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3784	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3785	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3786	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3787	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3788	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3789				HR3788 (Lo-16bits)
HR3790	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3791				HR3790 (Lo-16bits)
HR3792	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3793				HR3792 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3794	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3795				HR3794 (Lo-16bits)
HR3796	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3797				HR3796 (Lo-16bits)
HR3798	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3799				HR3798 (Lo-16bits)
HR3800	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3801				HR3800 (Lo-16bits)
HR3802	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3803				HR3802 (Lo-16bits)
HR3804	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3805	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3806	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3807	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3808	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3809	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3810	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3811	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3812	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3813				HR3812 (Lo-16bits)
HR3814	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3815				HR3814 (Lo-16bits)
HR3816	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3817				HR3816 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3818	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3819				HR3818 (Lo-16bits)
HR3820	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3821				HR3820 (Lo-16bits)
HR3822	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3823				HR3822 (Lo-16bits)
HR3824	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3825				HR3824 (Lo-16bits)
HR3826	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3827				HR3826 (Lo-16bits)
HR3828	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3829	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3830	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3831	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3832	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3833	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3834	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3835	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3836	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3837	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3838	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3839	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3840	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3841	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3842	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3843	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3844	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR3845				HR3844 (Lo-16bits)
HR3846	NEF1:Delay type	RW	1	0=DT 1=IDMT
HR3847	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	NEF1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR3850	NEF1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3851	NEF1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3852	NEF1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR3853	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	NEF1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR3862	NEF1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR3863	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3864	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3865	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3866	NEF2:I0>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3867	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3886	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3887	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3888	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3889	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3890	NEF2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3891	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3892	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3893	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3910	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3911	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3912	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3913	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3914	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3915	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3916	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3917	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3918	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3919	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3920	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3921	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3922	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3923	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3924	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3925	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3926	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3927	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3928	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3929	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3930	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3931	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3932	NEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3933	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3934	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3935	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3936	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3937	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3938	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3939	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3940	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3941	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3965	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3966				HR3965 (Lo-16bits)
HR3967	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3968				HR3967 (Lo-16bits)
HR3969	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3970				HR3969 (Lo-16bits)
HR3971	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3972				HR3971 (Lo-16bits)
HR3973	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3974				HR3973 (Lo-16bits)
HR3975	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3976				HR3975 (Lo-16bits)
HR3977	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3978				HR3977 (Lo-16bits)
HR3979	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3980				HR3979 (Lo-16bits)
HR3981	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3995	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4006	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4007	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4008	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4009	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4010	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4011	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4012	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4013	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4019	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4030	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4031	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4032	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4033	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4034	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4035	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4036	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4037	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4043	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4054	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4055	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4056	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4057	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4058	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4059	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4060	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4061	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4062	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4063	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4064	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4065	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4066	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4067	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4068	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4069	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4070				HR4069 (Lo-16bits)
HR4071	NEF2:Delay type	RW	1	0=DT 1=IDMT
HR4072	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4073				HR4072 (Lo-16bits)
HR4074	NEF2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4075	NEF2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4076	NEF2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4077	NEF2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4078	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4079				HR4078 (Lo-16bits)
HR4080	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4081				HR4080 (Lo-16bits)
HR4082	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4083				HR4082 (Lo-16bits)
HR4084	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4085				HR4084 (Lo-16bits)
HR4086	NEF2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4087	NEF2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4088	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4089	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4090	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4091	NEF3:I0>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4092	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4093				HR4092 (Lo-16bits)
HR4094	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4095				HR4094 (Lo-16bits)
HR4096	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4097				HR4096 (Lo-16bits)
HR4098	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4099				HR4098 (Lo-16bits)
HR4100	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4101				HR4100 (Lo-16bits)
HR4102	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4103				HR4102 (Lo-16bits)
HR4104	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4105				HR4104 (Lo-16bits)
HR4106	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4107				HR4106 (Lo-16bits)
HR4108	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4109				HR4108 (Lo-16bits)
HR4110	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4111	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4112	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4113	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4114	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4115	NEF3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4116	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4117	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4118	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4119				HR4118 (Lo-16bits)
HR4120	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4121				HR4120 (Lo-16bits)
HR4122	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4123				HR4122 (Lo-16bits)
HR4124	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4125				HR4124 (Lo-16bits)
HR4126	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4127				HR4126 (Lo-16bits)
HR4128	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4129				HR4128 (Lo-16bits)
HR4130	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4131				HR4130 (Lo-16bits)
HR4132	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4133				HR4132 (Lo-16bits)
HR4134	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4135	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4136	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4137	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4138	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4139	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4140	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4141	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4142	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4143	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4144	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4145	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4146	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4147	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4148	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4149	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4150	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4151	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4152	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4153	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4154	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4155	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4156	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4157	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4158	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4159	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4160	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4161	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4162	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4163	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4164	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4165	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4166	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4167				HR4166 (Lo-16bits)
HR4168	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4169				HR4168 (Lo-16bits)
HR4170	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4171				HR4170 (Lo-16bits)
HR4172	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4173				HR4172 (Lo-16bits)
HR4174	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4175				HR4174 (Lo-16bits)
HR4176	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4177				HR4176 (Lo-16bits)
HR4178	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4179				HR4178 (Lo-16bits)
HR4180	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4181				HR4180 (Lo-16bits)
HR4182	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4183				HR4182 (Lo-16bits)
HR4184	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4185				HR4184 (Lo-16bits)
HR4186	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4187				HR4186 (Lo-16bits)
HR4188	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4189				HR4188 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4190	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4191				HR4190 (Lo-16bits)
HR4192	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4193				HR4192 (Lo-16bits)
HR4194	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4195				HR4194 (Lo-16bits)
HR4196	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4197				HR4196 (Lo-16bits)
HR4198	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4199				HR4198 (Lo-16bits)
HR4200	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4201				HR4200 (Lo-16bits)
HR4202	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4203				HR4202 (Lo-16bits)
HR4204	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4205				HR4204 (Lo-16bits)
HR4206	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4207				HR4206 (Lo-16bits)
HR4208	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4209				HR4208 (Lo-16bits)
HR4210	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4211				HR4210 (Lo-16bits)
HR4212	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4213				HR4212 (Lo-16bits)
HR4214	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4215				HR4214 (Lo-16bits)
HR4216	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4217				HR4216 (Lo-16bits)
HR4218	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4219				HR4218 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4220	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4221				HR4220 (Lo-16bits)
HR4222	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4223				HR4222 (Lo-16bits)
HR4224	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4225				HR4224 (Lo-16bits)
HR4226	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4227				HR4226 (Lo-16bits)
HR4228	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4229				HR4228 (Lo-16bits)
HR4230	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4231	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4232	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4233	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4234	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4235	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4236	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4237	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4238	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4239				HR4238 (Lo-16bits)
HR4240	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4241				HR4240 (Lo-16bits)
HR4242	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4243				HR4242 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4244	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4245				HR4244 (Lo-16bits)
HR4246	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4247				HR4246 (Lo-16bits)
HR4248	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4249				HR4248 (Lo-16bits)
HR4250	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4251				HR4250 (Lo-16bits)
HR4252	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4253				HR4252 (Lo-16bits)
HR4254	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4255	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4256	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4257	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4258	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4259	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4260	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4261	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4262	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4263				HR4262 (Lo-16bits)
HR4264	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4265				HR4264 (Lo-16bits)
HR4266	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4267				HR4266 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4268	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4269				HR4268 (Lo-16bits)
HR4270	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4271				HR4270 (Lo-16bits)
HR4272	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4273				HR4272 (Lo-16bits)
HR4274	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4275				HR4274 (Lo-16bits)
HR4276	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4277				HR4276 (Lo-16bits)
HR4278	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4279	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4280	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4281	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4282	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4283	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4284	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4285	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4286	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4287	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4288	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4289	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4290	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4291	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4292	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4293	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4294	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4295				HR4294 (Lo-16bits)
HR4296	NEF3:Delay type	RW	1	0=DT 1=IDMT
HR4297	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	NEF3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4300	NEF3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4301	NEF3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4302	NEF3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4303	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4310				HR4309 (Lo-16bits)
HR4311	NEF3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4312	NEF3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4313	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4314	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4315	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4316	NEF4:I0>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4317	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4336	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4337	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4338	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4339	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4340	NEF4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4341	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4342	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4343	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4360	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4361	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4362	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4363	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4364	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4365	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4366	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4367	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4368	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4369	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4370	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4371	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4372	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4373	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4374	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4375	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4376	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4377	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4378	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4379	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4380	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4381	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4382	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4383	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4384	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4385	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4386	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4387	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4388	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4389	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4390	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4391	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4394				HR4393 (Lo-16bits)
HR4395	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4396				HR4395 (Lo-16bits)
HR4397	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4415	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4418				HR4417 (Lo-16bits)
HR4419	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4420				HR4419 (Lo-16bits)
HR4421	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4445	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4456	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4457	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4458	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4459	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4460	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4461	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4462	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4463	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4469	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4480	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4481	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4482	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4483	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4484	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4485	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4486	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4487	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4493	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)
HR4503	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4504	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4505	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4506	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4507	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4508	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4509	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4510	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4511	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4512	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4513	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4514	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4515	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4516	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4517	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4518	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4519	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF4:Delay type	RW	1	0=DT 1=IDMT
HR4522	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4523				HR4522 (Lo-16bits)
HR4524	NEF4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4525	NEF4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4526	NEF4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4527	NEF4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4528	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4529				HR4528 (Lo-16bits)
HR4530	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4531				HR4530 (Lo-16bits)
HR4532	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4533				HR4532 (Lo-16bits)
HR4534	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4535				HR4534 (Lo-16bits)
HR4536	NEF4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4537	NEF4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4538	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4539	DEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4540	DEF1:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4541	DEF1:I0dir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4542	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4543				HR4542 (Lo-16bits)
HR4544	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4545				HR4544 (Lo-16bits)
HR4546	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4547				HR4546 (Lo-16bits)
HR4548	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4549	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4550	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4551	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4552	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4553	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4554	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4555	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4556	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4557				HR4556 (Lo-16bits)
HR4558	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4559				HR4558 (Lo-16bits)
HR4560	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4561				HR4560 (Lo-16bits)
HR4562	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4563				HR4562 (Lo-16bits)
HR4564	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4565				HR4564 (Lo-16bits)
HR4566	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4567				HR4566 (Lo-16bits)
HR4568	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4569				HR4568 (Lo-16bits)
HR4570	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4571				HR4570 (Lo-16bits)
HR4572	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4573				HR4572 (Lo-16bits)
HR4574	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4575				HR4574 (Lo-16bits)
HR4576	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4577				HR4576 (Lo-16bits)
HR4578	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4579				HR4578 (Lo-16bits)
HR4580	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4581				HR4580 (Lo-16bits)
HR4582	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4583				HR4582 (Lo-16bits)
HR4584	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4585				HR4584 (Lo-16bits)
HR4586	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4587				HR4586 (Lo-16bits)
HR4588	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4589				HR4588 (Lo-16bits)
HR4590	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4591				HR4590 (Lo-16bits)
HR4592	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4593				HR4592 (Lo-16bits)
HR4594	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4595				HR4594 (Lo-16bits)
HR4596	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4597				HR4596 (Lo-16bits)
HR4598	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4599				HR4598 (Lo-16bits)
HR4600	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4601				HR4600 (Lo-16bits)
HR4602	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4603				HR4602 (Lo-16bits)
HR4604	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4605				HR4604 (Lo-16bits)
HR4606	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4607				HR4606 (Lo-16bits)
HR4608	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4609				HR4608 (Lo-16bits)
HR4610	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4611				HR4610 (Lo-16bits)
HR4612	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4613				HR4612 (Lo-16bits)
HR4614	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4615				HR4614 (Lo-16bits)
HR4616	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4617				HR4616 (Lo-16bits)
HR4618	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4619				HR4618 (Lo-16bits)
HR4620	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4621				HR4620 (Lo-16bits)
HR4622	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4623				HR4622 (Lo-16bits)
HR4624	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4625				HR4624 (Lo-16bits)
HR4626	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4627				HR4626 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4628	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4629				HR4628 (Lo-16bits)
HR4630	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4631				HR4630 (Lo-16bits)
HR4632	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4633				HR4632 (Lo-16bits)
HR4634	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4635				HR4634 (Lo-16bits)
HR4636	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4637				HR4636 (Lo-16bits)
HR4638	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4639				HR4638 (Lo-16bits)
HR4640	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4641				HR4640 (Lo-16bits)
HR4642	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4643				HR4642 (Lo-16bits)
HR4644	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4645				HR4644 (Lo-16bits)
HR4646	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4647				HR4646 (Lo-16bits)
HR4648	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4649				HR4648 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4650	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4651				HR4650 (Lo-16bits)
HR4652	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4653	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4654	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4655	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4656	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4657	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4658	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4659	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4660	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4661				HR4660 (Lo-16bits)
HR4662	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4663				HR4662 (Lo-16bits)
HR4664	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4665				HR4664 (Lo-16bits)
HR4666	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4667				HR4666 (Lo-16bits)
HR4668	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4669				HR4668 (Lo-16bits)
HR4670	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4671				HR4670 (Lo-16bits)
HR4672	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4673				HR4672 (Lo-16bits)
HR4674	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4675				HR4674 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4676	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4677				HR4676 (Lo-16bits)
HR4678	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4679				HR4678 (Lo-16bits)
HR4680	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4681				HR4680 (Lo-16bits)
HR4682	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4683				HR4682 (Lo-16bits)
HR4684	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4685				HR4684 (Lo-16bits)
HR4686	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4687				HR4686 (Lo-16bits)
HR4688	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4689				HR4688 (Lo-16bits)
HR4690	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4691				HR4690 (Lo-16bits)
HR4692	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4693				HR4692 (Lo-16bits)
HR4694	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4695				HR4694 (Lo-16bits)
HR4696	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4697				HR4696 (Lo-16bits)
HR4698	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4699				HR4698 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4700	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4701				HR4700 (Lo-16bits)
HR4702	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4703				HR4702 (Lo-16bits)
HR4704	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4705				HR4704 (Lo-16bits)
HR4706	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4707				HR4706 (Lo-16bits)
HR4708	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4709				HR4708 (Lo-16bits)
HR4710	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4711				HR4710 (Lo-16bits)
HR4712	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4713				HR4712 (Lo-16bits)
HR4714	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4715				HR4714 (Lo-16bits)
HR4716	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4717				HR4716 (Lo-16bits)
HR4718	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4719				HR4718 (Lo-16bits)
HR4720	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4721				HR4720 (Lo-16bits)
HR4722	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4723				HR4722 (Lo-16bits)
HR4724	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4725				HR4724 (Lo-16bits)
HR4726	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4727				HR4726 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4728	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4729				HR4728 (Lo-16bits)
HR4730	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4731				HR4730 (Lo-16bits)
HR4732	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4733				HR4732 (Lo-16bits)
HR4734	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4735				HR4734 (Lo-16bits)
HR4736	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4737				HR4736 (Lo-16bits)
HR4738	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4739				HR4738 (Lo-16bits)
HR4740	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4741	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4742	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4743	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4744	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4745	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4746	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4747	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4748	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4749	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4750	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4751	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4752	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4753	DEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4754	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4755	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4756	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4757				HR4756 (Lo-16bits)
HR4758	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4759				HR4758 (Lo-16bits)
HR4760	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4761				HR4760 (Lo-16bits)
HR4762	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4763				HR4762 (Lo-16bits)
HR4764	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4765				HR4764 (Lo-16bits)
HR4766	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4767				HR4766 (Lo-16bits)
HR4768	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4769				HR4768 (Lo-16bits)
HR4770	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4771				HR4770 (Lo-16bits)
HR4772	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4773				HR4772 (Lo-16bits)
HR4774	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4775				HR4774 (Lo-16bits)
HR4776	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4777				HR4776 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4778	DEF1:Definite operating time delay(Uneearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4779				HR4778 (Lo-16bits)
HR4780	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4781				HR4780 (Lo-16bits)
HR4782	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4783				HR4782 (Lo-16bits)
HR4784	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4785				HR4784 (Lo-16bits)
HR4786	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4787				HR4786 (Lo-16bits)
HR4788	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4789				HR4788 (Lo-16bits)
HR4790	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4791				HR4790 (Lo-16bits)
HR4792	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4793				HR4792 (Lo-16bits)
HR4794	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4795				HR4794 (Lo-16bits)
HR4796	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4797				HR4796 (Lo-16bits)
HR4798	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4799				HR4798 (Lo-16bits)
HR4800	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4801				HR4800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4802	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4803				HR4802 (Lo-16bits)
HR4804	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4805	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4806	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4807	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4808	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4809	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4810	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4811	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4812	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4813	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4814	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4815	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4816	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4817	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4818	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4819	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4820	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4821	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4822	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4823	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4824	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4825	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4826	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4827	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4828	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4829	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4830	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4831	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4832	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4833	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4834	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4835	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4836	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4837				HR4836 (Lo-16bits)
HR4838	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4839				HR4838 (Lo-16bits)
HR4840	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4841				HR4840 (Lo-16bits)
HR4842	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4843				HR4842 (Lo-16bits)
HR4844	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4845				HR4844 (Lo-16bits)
HR4846	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4847				HR4846 (Lo-16bits)
HR4848	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4849				HR4848 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4850	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4851				HR4850 (Lo-16bits)
HR4852	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4853				HR4852 (Lo-16bits)
HR4854	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4855				HR4854 (Lo-16bits)
HR4856	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4857				HR4856 (Lo-16bits)
HR4858	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4859				HR4858 (Lo-16bits)
HR4860	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4861				HR4860 (Lo-16bits)
HR4862	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4863				HR4862 (Lo-16bits)
HR4864	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4865				HR4864 (Lo-16bits)
HR4866	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4867				HR4866 (Lo-16bits)
HR4868	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4869				HR4868 (Lo-16bits)
HR4870	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4871				HR4870 (Lo-16bits)
HR4872	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4873				HR4872 (Lo-16bits)
HR4874	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4875				HR4874 (Lo-16bits)
HR4876	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4877				HR4876 (Lo-16bits)
HR4878	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4879				HR4878 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4880	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4881				HR4880 (Lo-16bits)
HR4882	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4883				HR4882 (Lo-16bits)
HR4884	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4885				HR4884 (Lo-16bits)
HR4886	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4887				HR4886 (Lo-16bits)
HR4888	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4889				HR4888 (Lo-16bits)
HR4890	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4891				HR4890 (Lo-16bits)
HR4892	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4893				HR4892 (Lo-16bits)
HR4894	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4895				HR4894 (Lo-16bits)
HR4896	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4897				HR4896 (Lo-16bits)
HR4898	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4899				HR4898 (Lo-16bits)
HR4900	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4901	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4902	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4903	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4904	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4905	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4906	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4907	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4908	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4909				HR4908 (Lo-16bits)
HR4910	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4911				HR4910 (Lo-16bits)
HR4912	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4913				HR4912 (Lo-16bits)
HR4914	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4915				HR4914 (Lo-16bits)
HR4916	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4917				HR4916 (Lo-16bits)
HR4918	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4919				HR4918 (Lo-16bits)
HR4920	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4921				HR4920 (Lo-16bits)
HR4922	DEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4923				HR4922 (Lo-16bits)
HR4924	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4925	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4926	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4927	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4928	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4929	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4930	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4931	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4932	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4933				HR4932 (Lo-16bits)
HR4934	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4935				HR4934 (Lo-16bits)
HR4936	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4937				HR4936 (Lo-16bits)
HR4938	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4939				HR4938 (Lo-16bits)
HR4940	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4941				HR4940 (Lo-16bits)
HR4942	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4943				HR4942 (Lo-16bits)
HR4944	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4945				HR4944 (Lo-16bits)
HR4946	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4947				HR4946 (Lo-16bits)
HR4948	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4949	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4950	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4951	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4952	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4953	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4954	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4955	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4956	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4957	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4958	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4959	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4960	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4961	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4962	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4963	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4964	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4965	DEF2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4966	DEF2:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4967	DEF2:I0dir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4968	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4969				HR4968 (Lo-16bits)
HR4970	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4971				HR4970 (Lo-16bits)
HR4972	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4973				HR4972 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4974	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4975	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4976	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4977	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4978	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4979	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4980	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4981	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4982	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4983				HR4982 (Lo-16bits)
HR4984	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4985				HR4984 (Lo-16bits)
HR4986	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4987				HR4986 (Lo-16bits)
HR4988	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4989				HR4988 (Lo-16bits)
HR4990	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4991				HR4990 (Lo-16bits)
HR4992	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4993				HR4992 (Lo-16bits)
HR4994	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4995				HR4994 (Lo-16bits)
HR4996	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4997				HR4996 (Lo-16bits)
HR4998	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4999				HR4998 (Lo-16bits)
HR5000	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5001				HR5000 (Lo-16bits)
HR5002	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5003				HR5002 (Lo-16bits)
HR5004	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5005				HR5004 (Lo-16bits)
HR5006	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5007				HR5006 (Lo-16bits)
HR5008	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5009				HR5008 (Lo-16bits)
HR5010	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5011				HR5010 (Lo-16bits)
HR5012	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5013				HR5012 (Lo-16bits)
HR5014	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5015				HR5014 (Lo-16bits)
HR5016	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5017				HR5016 (Lo-16bits)
HR5018	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5019				HR5018 (Lo-16bits)
HR5020	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5021				HR5020 (Lo-16bits)
HR5022	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5023				HR5022 (Lo-16bits)
HR5024	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5025				HR5024 (Lo-16bits)
HR5026	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5027				HR5026 (Lo-16bits)
HR5028	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5029				HR5028 (Lo-16bits)
HR5030	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5031				HR5030 (Lo-16bits)
HR5032	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5033				HR5032 (Lo-16bits)
HR5034	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5035				HR5034 (Lo-16bits)
HR5036	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5037				HR5036 (Lo-16bits)
HR5038	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5039				HR5038 (Lo-16bits)
HR5040	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5041				HR5040 (Lo-16bits)
HR5042	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5043				HR5042 (Lo-16bits)
HR5044	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5045				HR5044 (Lo-16bits)
HR5046	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5047				HR5046 (Lo-16bits)
HR5048	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5049				HR5048 (Lo-16bits)
HR5050	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5051				HR5050 (Lo-16bits)
HR5052	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5053				HR5052 (Lo-16bits)
HR5054	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5055				HR5054 (Lo-16bits)
HR5056	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5057				HR5056 (Lo-16bits)
HR5058	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5059				HR5058 (Lo-16bits)
HR5060	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5061				HR5060 (Lo-16bits)
HR5062	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5063				HR5062 (Lo-16bits)
HR5064	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5065				HR5064 (Lo-16bits)
HR5066	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5067				HR5066 (Lo-16bits)
HR5068	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5069				HR5068 (Lo-16bits)
HR5070	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5071				HR5070 (Lo-16bits)
HR5072	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5073				HR5072 (Lo-16bits)
HR5074	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5075				HR5074 (Lo-16bits)
HR5076	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5077				HR5076 (Lo-16bits)
HR5078	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5079	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5080	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5081	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5082	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5083	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5084	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5085	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5086	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5087				HR5086 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5088	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5089				HR5088 (Lo-16bits)
HR5090	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5091				HR5090 (Lo-16bits)
HR5092	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5093				HR5092 (Lo-16bits)
HR5094	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5095				HR5094 (Lo-16bits)
HR5096	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5097				HR5096 (Lo-16bits)
HR5098	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5099				HR5098 (Lo-16bits)
HR5100	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5101				HR5100 (Lo-16bits)
HR5102	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5103				HR5102 (Lo-16bits)
HR5104	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5105				HR5104 (Lo-16bits)
HR5106	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5107				HR5106 (Lo-16bits)
HR5108	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5109				HR5108 (Lo-16bits)
HR5110	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5111				HR5110 (Lo-16bits)
HR5112	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5113				HR5112 (Lo-16bits)
HR5114	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5115				HR5114 (Lo-16bits)
HR5116	DEF2:Unearth. Pick-up setting I0set(I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5117				HR5116 (Lo-16bits)
HR5118	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5119				HR5118 (Lo-16bits)
HR5120	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5121				HR5120 (Lo-16bits)
HR5122	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5123				HR5122 (Lo-16bits)
HR5124	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5125				HR5124 (Lo-16bits)
HR5126	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5127				HR5126 (Lo-16bits)
HR5128	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5129				HR5128 (Lo-16bits)
HR5130	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5131				HR5130 (Lo-16bits)
HR5132	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5133				HR5132 (Lo-16bits)
HR5134	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5135				HR5134 (Lo-16bits)
HR5136	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5137				HR5136 (Lo-16bits)
HR5138	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5139				HR5138 (Lo-16bits)
HR5140	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5141				HR5140 (Lo-16bits)
HR5142	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5143				HR5142 (Lo-16bits)
HR5144	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5145				HR5144 (Lo-16bits)
HR5146	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5147				HR5146 (Lo-16bits)
HR5148	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5149				HR5148 (Lo-16bits)
HR5150	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5151				HR5150 (Lo-16bits)
HR5152	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5153				HR5152 (Lo-16bits)
HR5154	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5155				HR5154 (Lo-16bits)
HR5156	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5157				HR5156 (Lo-16bits)
HR5158	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5159				HR5158 (Lo-16bits)
HR5160	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5161				HR5160 (Lo-16bits)
HR5162	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5163				HR5162 (Lo-16bits)
HR5164	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5165				HR5164 (Lo-16bits)
HR5166	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5167	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5168	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR5169	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5170	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5171	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5172	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5173	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5174	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5175	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5176	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5177	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5178	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5179	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5180	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5181	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5182	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5183				HR5182 (Lo-16bits)
HR5184	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5185				HR5184 (Lo-16bits)
HR5186	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5187				HR5186 (Lo-16bits)
HR5188	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5189				HR5188 (Lo-16bits)
HR5190	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5191				HR5190 (Lo-16bits)
HR5192	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5193				HR5192 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5194	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5195				HR5194 (Lo-16bits)
HR5196	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5197				HR5196 (Lo-16bits)
HR5198	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5199				HR5198 (Lo-16bits)
HR5200	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5201				HR5200 (Lo-16bits)
HR5202	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5203				HR5202 (Lo-16bits)
HR5204	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5205				HR5204 (Lo-16bits)
HR5206	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5207				HR5206 (Lo-16bits)
HR5208	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5209				HR5208 (Lo-16bits)
HR5210	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5211				HR5210 (Lo-16bits)
HR5212	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5213				HR5212 (Lo-16bits)
HR5214	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5215				HR5214 (Lo-16bits)
HR5216	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5217				HR5216 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5218	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5219				HR5218 (Lo-16bits)
HR5220	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5221				HR5220 (Lo-16bits)
HR5222	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5223				HR5222 (Lo-16bits)
HR5224	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5225				HR5224 (Lo-16bits)
HR5226	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5227				HR5226 (Lo-16bits)
HR5228	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5229				HR5228 (Lo-16bits)
HR5230	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5231	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5232	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5233	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5234	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5235	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5236	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5237	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5238	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5239	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5240	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5241	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5242	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR5243	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5244	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5245	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5246	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5247	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5248	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5249	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5250	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5251	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5252	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5253	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5254	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5255	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5256	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5257	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5258	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5259	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5260	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5261	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5262	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5263				HR5262 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5264	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5265				HR5264 (Lo-16bits)
HR5266	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5267				HR5266 (Lo-16bits)
HR5268	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5269				HR5268 (Lo-16bits)
HR5270	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5271				HR5270 (Lo-16bits)
HR5272	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5273				HR5272 (Lo-16bits)
HR5274	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5275				HR5274 (Lo-16bits)
HR5276	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5277				HR5276 (Lo-16bits)
HR5278	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5279				HR5278 (Lo-16bits)
HR5280	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5281				HR5280 (Lo-16bits)
HR5282	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5283				HR5282 (Lo-16bits)
HR5284	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5285				HR5284 (Lo-16bits)
HR5286	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5287				HR5286 (Lo-16bits)
HR5288	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5289				HR5288 (Lo-16bits)
HR5290	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5291				HR5290 (Lo-16bits)
HR5292	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5293				HR5292 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5294	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5295				HR5294 (Lo-16bits)
HR5296	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5297				HR5296 (Lo-16bits)
HR5298	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5299				HR5298 (Lo-16bits)
HR5300	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5301				HR5300 (Lo-16bits)
HR5302	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5303				HR5302 (Lo-16bits)
HR5304	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5305				HR5304 (Lo-16bits)
HR5306	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5307				HR5306 (Lo-16bits)
HR5308	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5309				HR5308 (Lo-16bits)
HR5310	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5311				HR5310 (Lo-16bits)
HR5312	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5313				HR5312 (Lo-16bits)
HR5314	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5315				HR5314 (Lo-16bits)
HR5316	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5317				HR5316 (Lo-16bits)
HR5318	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5319				HR5318 (Lo-16bits)
HR5320	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5321				HR5320 (Lo-16bits)
HR5322	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5323				HR5322 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5324	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5325				HR5324 (Lo-16bits)
HR5326	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5327	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5328	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5329	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5330	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5331	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5332	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5333	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5334	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5335				HR5334 (Lo-16bits)
HR5336	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5337				HR5336 (Lo-16bits)
HR5338	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5339				HR5338 (Lo-16bits)
HR5340	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5341				HR5340 (Lo-16bits)
HR5342	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5343				HR5342 (Lo-16bits)
HR5344	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5345				HR5344 (Lo-16bits)
HR5346	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5347				HR5346 (Lo-16bits)
HR5348	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5349				HR5348 (Lo-16bits)
HR5350	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5351	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5352	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5353	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5354	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5355	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5356	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5357	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5358	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5359				HR5358 (Lo-16bits)
HR5360	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5361				HR5360 (Lo-16bits)
HR5362	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5363				HR5362 (Lo-16bits)
HR5364	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5365				HR5364 (Lo-16bits)
HR5366	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5367				HR5366 (Lo-16bits)
HR5368	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5369				HR5368 (Lo-16bits)
HR5370	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5371				HR5370 (Lo-16bits)
HR5372	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5373				HR5372 (Lo-16bits)
HR5374	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5375	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5376	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5377	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5378	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5379	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5380	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5381	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5382	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5383	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5384	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5385	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5386	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5387	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5388	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5389	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5390	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR5391	DEF3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5392	DEF3:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5393	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5394	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5395				HR5394 (Lo-16bits)
HR5396	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5397				HR5396 (Lo-16bits)
HR5398	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5399				HR5398 (Lo-16bits)
HR5400	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5401	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5402	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5403	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5404	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5405	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5406	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5407	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5408	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5409				HR5408 (Lo-16bits)
HR5410	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5411				HR5410 (Lo-16bits)
HR5412	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5413				HR5412 (Lo-16bits)
HR5414	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5415				HR5414 (Lo-16bits)
HR5416	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5417				HR5416 (Lo-16bits)
HR5418	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5419				HR5418 (Lo-16bits)
HR5420	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5421				HR5420 (Lo-16bits)
HR5422	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5423				HR5422 (Lo-16bits)
HR5424	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5425				HR5424 (Lo-16bits)
HR5426	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5427				HR5426 (Lo-16bits)
HR5428	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5429				HR5428 (Lo-16bits)
HR5430	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5431				HR5430 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5432	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5433				HR5432 (Lo-16bits)
HR5434	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5435				HR5434 (Lo-16bits)
HR5436	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5437				HR5436 (Lo-16bits)
HR5438	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5439				HR5438 (Lo-16bits)
HR5440	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5441				HR5440 (Lo-16bits)
HR5442	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5443				HR5442 (Lo-16bits)
HR5444	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5445				HR5444 (Lo-16bits)
HR5446	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5447				HR5446 (Lo-16bits)
HR5448	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5449				HR5448 (Lo-16bits)
HR5450	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5451				HR5450 (Lo-16bits)
HR5452	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5453				HR5452 (Lo-16bits)
HR5454	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5455				HR5454 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5456	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5457				HR5456 (Lo-16bits)
HR5458	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5459				HR5458 (Lo-16bits)
HR5460	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5461				HR5460 (Lo-16bits)
HR5462	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5463				HR5462 (Lo-16bits)
HR5464	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5465				HR5464 (Lo-16bits)
HR5466	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5467				HR5466 (Lo-16bits)
HR5468	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5469				HR5468 (Lo-16bits)
HR5470	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5471				HR5470 (Lo-16bits)
HR5472	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5473				HR5472 (Lo-16bits)
HR5474	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5475				HR5474 (Lo-16bits)
HR5476	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5477				HR5476 (Lo-16bits)
HR5478	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5479				HR5478 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5480	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5481				HR5480 (Lo-16bits)
HR5482	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5483				HR5482 (Lo-16bits)
HR5484	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5485				HR5484 (Lo-16bits)
HR5486	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5487				HR5486 (Lo-16bits)
HR5488	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5489				HR5488 (Lo-16bits)
HR5490	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5491				HR5490 (Lo-16bits)
HR5492	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5493				HR5492 (Lo-16bits)
HR5494	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5495				HR5494 (Lo-16bits)
HR5496	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5497				HR5496 (Lo-16bits)
HR5498	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5499				HR5498 (Lo-16bits)
HR5500	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5501				HR5500 (Lo-16bits)
HR5502	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5503				HR5502 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5504	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5505	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5506	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5507	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5508	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5509	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5510	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5511	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5512	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5513				HR5512 (Lo-16bits)
HR5514	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5515				HR5514 (Lo-16bits)
HR5516	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5517				HR5516 (Lo-16bits)
HR5518	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5519				HR5518 (Lo-16bits)
HR5520	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5521				HR5520 (Lo-16bits)
HR5522	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5523				HR5522 (Lo-16bits)
HR5524	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5525				HR5524 (Lo-16bits)
HR5526	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5527				HR5526 (Lo-16bits)
HR5528	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5529				HR5528 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5530	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5531				HR5530 (Lo-16bits)
HR5532	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5533				HR5532 (Lo-16bits)
HR5534	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5535				HR5534 (Lo-16bits)
HR5536	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5537				HR5536 (Lo-16bits)
HR5538	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5539				HR5538 (Lo-16bits)
HR5540	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5541				HR5540 (Lo-16bits)
HR5542	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5543				HR5542 (Lo-16bits)
HR5544	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5545				HR5544 (Lo-16bits)
HR5546	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5547				HR5546 (Lo-16bits)
HR5548	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5549				HR5548 (Lo-16bits)
HR5550	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5551				HR5550 (Lo-16bits)
HR5552	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5553				HR5552 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5554	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5555				HR5554 (Lo-16bits)
HR5556	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5557				HR5556 (Lo-16bits)
HR5558	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5559				HR5558 (Lo-16bits)
HR5560	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5561				HR5560 (Lo-16bits)
HR5562	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5563				HR5562 (Lo-16bits)
HR5564	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5565				HR5564 (Lo-16bits)
HR5566	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5567				HR5566 (Lo-16bits)
HR5568	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5569				HR5568 (Lo-16bits)
HR5570	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5571				HR5570 (Lo-16bits)
HR5572	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5573				HR5572 (Lo-16bits)
HR5574	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5575				HR5574 (Lo-16bits)
HR5576	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5577				HR5576 (Lo-16bits)
HR5578	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5579				HR5578 (Lo-16bits)
HR5580	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5581				HR5580 (Lo-16bits)
HR5582	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5583				HR5582 (Lo-16bits)
HR5584	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5585				HR5584 (Lo-16bits)
HR5586	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5587				HR5586 (Lo-16bits)
HR5588	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5589				HR5588 (Lo-16bits)
HR5590	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5591				HR5590 (Lo-16bits)
HR5592	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5593	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5594	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5595	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5596	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5597	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5598	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5599	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5600	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5601	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5602	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5603	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5604	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5605	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5606	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5607	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5608	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5609				HR5608 (Lo-16bits)
HR5610	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5611				HR5610 (Lo-16bits)
HR5612	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5613				HR5612 (Lo-16bits)
HR5614	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5615				HR5614 (Lo-16bits)
HR5616	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5617				HR5616 (Lo-16bits)
HR5618	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5619				HR5618 (Lo-16bits)
HR5620	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5621				HR5620 (Lo-16bits)
HR5622	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5623				HR5622 (Lo-16bits)
HR5624	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5625				HR5624 (Lo-16bits)
HR5626	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5627				HR5626 (Lo-16bits)
HR5628	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5629				HR5628 (Lo-16bits)
HR5630	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5631				HR5630 (Lo-16bits)
HR5632	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5633				HR5632 (Lo-16bits)
HR5634	DEF3:Definite operating time delay(Uneearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5635				HR5634 (Lo-16bits)
HR5636	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5637				HR5636 (Lo-16bits)
HR5638	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5639				HR5638 (Lo-16bits)
HR5640	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5641				HR5640 (Lo-16bits)
HR5642	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5643				HR5642 (Lo-16bits)
HR5644	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5645				HR5644 (Lo-16bits)
HR5646	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5647				HR5646 (Lo-16bits)
HR5648	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5649				HR5648 (Lo-16bits)
HR5650	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5651				HR5650 (Lo-16bits)
HR5652	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5653				HR5652 (Lo-16bits)
HR5654	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5655				HR5654 (Lo-16bits)
HR5656	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5657	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5658	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5659	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5660	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5661	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5662	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5663	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5664	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5665	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5666	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5667	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5668	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5669	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5670	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5671	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5672	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5673	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5674	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5675	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5676	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5677	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5678	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5679	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5680	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5681	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5682	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5683	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5684	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5685	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5686	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5687	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5688	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5689				HR5688 (Lo-16bits)
HR5690	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5691				HR5690 (Lo-16bits)
HR5692	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5693				HR5692 (Lo-16bits)
HR5694	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5695				HR5694 (Lo-16bits)
HR5696	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5697				HR5696 (Lo-16bits)
HR5698	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5699				HR5698 (Lo-16bits)
HR5700	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5701				HR5700 (Lo-16bits)
HR5702	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5703				HR5702 (Lo-16bits)
HR5704	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5705				HR5704 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5706	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5707				HR5706 (Lo-16bits)
HR5708	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5709				HR5708 (Lo-16bits)
HR5710	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5711				HR5710 (Lo-16bits)
HR5712	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5713				HR5712 (Lo-16bits)
HR5714	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5715				HR5714 (Lo-16bits)
HR5716	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5717				HR5716 (Lo-16bits)
HR5718	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5719				HR5718 (Lo-16bits)
HR5720	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5721				HR5720 (Lo-16bits)
HR5722	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5723				HR5722 (Lo-16bits)
HR5724	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5725				HR5724 (Lo-16bits)
HR5726	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5727				HR5726 (Lo-16bits)
HR5728	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5729				HR5728 (Lo-16bits)
HR5730	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5731				HR5730 (Lo-16bits)
HR5732	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5733				HR5732 (Lo-16bits)
HR5734	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5735				HR5734 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5736	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5737				HR5736 (Lo-16bits)
HR5738	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5739				HR5738 (Lo-16bits)
HR5740	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5741				HR5740 (Lo-16bits)
HR5742	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5743				HR5742 (Lo-16bits)
HR5744	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5745				HR5744 (Lo-16bits)
HR5746	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5747				HR5746 (Lo-16bits)
HR5748	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5749				HR5748 (Lo-16bits)
HR5750	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5751				HR5750 (Lo-16bits)
HR5752	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5753	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5754	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5755	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5756	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5757	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5758	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5759	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5760	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5761				HR5760 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5762	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5763				HR5762 (Lo-16bits)
HR5764	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5765				HR5764 (Lo-16bits)
HR5766	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5767				HR5766 (Lo-16bits)
HR5768	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5769				HR5768 (Lo-16bits)
HR5770	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5771				HR5770 (Lo-16bits)
HR5772	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5773				HR5772 (Lo-16bits)
HR5774	DEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5775				HR5774 (Lo-16bits)
HR5776	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5777	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5778	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5779	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5780	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5781	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5782	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5783	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5784	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5785				HR5784 (Lo-16bits)
HR5786	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5787				HR5786 (Lo-16bits)
HR5788	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5789				HR5788 (Lo-16bits)
HR5790	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5791				HR5790 (Lo-16bits)
HR5792	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5793				HR5792 (Lo-16bits)
HR5794	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5795				HR5794 (Lo-16bits)
HR5796	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5797				HR5796 (Lo-16bits)
HR5798	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5799				HR5798 (Lo-16bits)
HR5800	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5801	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5802	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5803	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5804	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5805	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5806	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5807	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5808	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5809	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5810	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5811	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5812	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5813	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5814	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5815	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5816	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5817	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5818	DEF4:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5819	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5820	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5821				HR5820 (Lo-16bits)
HR5822	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5823				HR5822 (Lo-16bits)
HR5824	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5825				HR5824 (Lo-16bits)
HR5826	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5827	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5828	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5829	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5830	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5831	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5832	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5833	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5834	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5835				HR5834 (Lo-16bits)
HR5836	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5837				HR5836 (Lo-16bits)
HR5838	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5839				HR5838 (Lo-16bits)
HR5840	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5841				HR5840 (Lo-16bits)
HR5842	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5843				HR5842 (Lo-16bits)
HR5844	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5845				HR5844 (Lo-16bits)
HR5846	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5847				HR5846 (Lo-16bits)
HR5848	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5849				HR5848 (Lo-16bits)
HR5850	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5851				HR5850 (Lo-16bits)
HR5852	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5853				HR5852 (Lo-16bits)
HR5854	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5855				HR5854 (Lo-16bits)
HR5856	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5857				HR5856 (Lo-16bits)
HR5858	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5859				HR5858 (Lo-16bits)
HR5860	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5861				HR5860 (Lo-16bits)
HR5862	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5863				HR5862 (Lo-16bits)
HR5864	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5865				HR5864 (Lo-16bits)
HR5866	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5867				HR5866 (Lo-16bits)
HR5868	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5869				HR5868 (Lo-16bits)
HR5870	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5871				HR5870 (Lo-16bits)
HR5872	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5873				HR5872 (Lo-16bits)
HR5874	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5875				HR5874 (Lo-16bits)
HR5876	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5877				HR5876 (Lo-16bits)
HR5878	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5879				HR5878 (Lo-16bits)
HR5880	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5881				HR5880 (Lo-16bits)
HR5882	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5883				HR5882 (Lo-16bits)
HR5884	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5885				HR5884 (Lo-16bits)
HR5886	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5887				HR5886 (Lo-16bits)
HR5888	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5889				HR5888 (Lo-16bits)
HR5890	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5891				HR5890 (Lo-16bits)
HR5892	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5893				HR5892 (Lo-16bits)
HR5894	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5895				HR5894 (Lo-16bits)
HR5896	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5897				HR5896 (Lo-16bits)
HR5898	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5899				HR5898 (Lo-16bits)
HR5900	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5901				HR5900 (Lo-16bits)
HR5902	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5903				HR5902 (Lo-16bits)
HR5904	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5905				HR5904 (Lo-16bits)
HR5906	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5907				HR5906 (Lo-16bits)
HR5908	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5909				HR5908 (Lo-16bits)
HR5910	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5911				HR5910 (Lo-16bits)
HR5912	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5913				HR5912 (Lo-16bits)
HR5914	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5915				HR5914 (Lo-16bits)
HR5916	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5917				HR5916 (Lo-16bits)
HR5918	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5919				HR5918 (Lo-16bits)
HR5920	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5921				HR5920 (Lo-16bits)
HR5922	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5923				HR5922 (Lo-16bits)
HR5924	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5925				HR5924 (Lo-16bits)
HR5926	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5927				HR5926 (Lo-16bits)
HR5928	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5929				HR5928 (Lo-16bits)
HR5930	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5931	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5932	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5933	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5934	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5935	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5936	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5937	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5938	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5939				HR5938 (Lo-16bits)
HR5940	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5941				HR5940 (Lo-16bits)
HR5942	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5943				HR5942 (Lo-16bits)
HR5944	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5945				HR5944 (Lo-16bits)
HR5946	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5947				HR5946 (Lo-16bits)
HR5948	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5949				HR5948 (Lo-16bits)
HR5950	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5951				HR5950 (Lo-16bits)
HR5952	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5953				HR5952 (Lo-16bits)
HR5954	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5955				HR5954 (Lo-16bits)
HR5956	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5957				HR5956 (Lo-16bits)
HR5958	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5959				HR5958 (Lo-16bits)
HR5960	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5961				HR5960 (Lo-16bits)
HR5962	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5963				HR5962 (Lo-16bits)
HR5964	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5965				HR5964 (Lo-16bits)
HR5966	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5967				HR5966 (Lo-16bits)
HR5968	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5969				HR5968 (Lo-16bits)
HR5970	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5971				HR5970 (Lo-16bits)
HR5972	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5973				HR5972 (Lo-16bits)
HR5974	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5975				HR5974 (Lo-16bits)
HR5976	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5977				HR5976 (Lo-16bits)
HR5978	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5979				HR5978 (Lo-16bits)
HR5980	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5981				HR5980 (Lo-16bits)
HR5982	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5983				HR5982 (Lo-16bits)
HR5984	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5985				HR5984 (Lo-16bits)
HR5986	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5987				HR5986 (Lo-16bits)
HR5988	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5989				HR5988 (Lo-16bits)
HR5990	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5991				HR5990 (Lo-16bits)
HR5992	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5993				HR5992 (Lo-16bits)
HR5994	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5995				HR5994 (Lo-16bits)
HR5996	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5997				HR5996 (Lo-16bits)
HR5998	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5999				HR5998 (Lo-16bits)
HR6000	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6001				HR6000 (Lo-16bits)
HR6002	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6003				HR6002 (Lo-16bits)
HR6004	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6005				HR6004 (Lo-16bits)
HR6006	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6007				HR6006 (Lo-16bits)
HR6008	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6009				HR6008 (Lo-16bits)
HR6010	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6011				HR6010 (Lo-16bits)
HR6012	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6013				HR6012 (Lo-16bits)
HR6014	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6015				HR6014 (Lo-16bits)
HR6016	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6017				HR6016 (Lo-16bits)
HR6018	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6019	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6020	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6021	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6022	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR6023	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6024	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6025	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6026	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6027	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6028	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6029	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6030	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6031	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6032	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6033	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6034	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6035				HR6034 (Lo-16bits)
HR6036	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6037				HR6036 (Lo-16bits)
HR6038	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6039				HR6038 (Lo-16bits)
HR6040	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6041				HR6040 (Lo-16bits)
HR6042	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6043				HR6042 (Lo-16bits)
HR6044	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6045				HR6044 (Lo-16bits)
HR6046	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6047				HR6046 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6048	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6049				HR6048 (Lo-16bits)
HR6050	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6051				HR6050 (Lo-16bits)
HR6052	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6053				HR6052 (Lo-16bits)
HR6054	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6055				HR6054 (Lo-16bits)
HR6056	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6057				HR6056 (Lo-16bits)
HR6058	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6059				HR6058 (Lo-16bits)
HR6060	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6061				HR6060 (Lo-16bits)
HR6062	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6063				HR6062 (Lo-16bits)
HR6064	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6065				HR6064 (Lo-16bits)
HR6066	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6067				HR6066 (Lo-16bits)
HR6068	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6069				HR6068 (Lo-16bits)
HR6070	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6071				HR6070 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6072	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6073				HR6072 (Lo-16bits)
HR6074	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6075				HR6074 (Lo-16bits)
HR6076	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6077				HR6076 (Lo-16bits)
HR6078	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6079				HR6078 (Lo-16bits)
HR6080	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6081				HR6080 (Lo-16bits)
HR6082	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6083	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6084	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6085	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6086	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6087	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6088	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6089	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6090	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6091	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6092	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6093	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6094	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6095	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6096	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6097	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6098	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6099	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6100	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6101	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6102	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6103	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6104	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6105	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6106	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6107	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6108	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6109	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6110	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6111	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6112	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6113	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6114	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6115				HR6114 (Lo-16bits)
HR6116	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6117				HR6116 (Lo-16bits)
HR6118	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6119				HR6118 (Lo-16bits)
HR6120	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6121				HR6120 (Lo-16bits)
HR6122	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6123				HR6122 (Lo-16bits)
HR6124	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6125				HR6124 (Lo-16bits)
HR6126	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6127				HR6126 (Lo-16bits)
HR6128	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6129				HR6128 (Lo-16bits)
HR6130	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6131				HR6130 (Lo-16bits)
HR6132	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6133				HR6132 (Lo-16bits)
HR6134	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6135				HR6134 (Lo-16bits)
HR6136	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6137				HR6136 (Lo-16bits)
HR6138	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6139				HR6138 (Lo-16bits)
HR6140	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6141				HR6140 (Lo-16bits)
HR6142	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6143				HR6142 (Lo-16bits)
HR6144	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6145				HR6144 (Lo-16bits)
HR6146	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6147				HR6146 (Lo-16bits)
HR6148	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6149				HR6148 (Lo-16bits)
HR6150	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6151				HR6150 (Lo-16bits)
HR6152	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6153				HR6152 (Lo-16bits)
HR6154	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6155				HR6154 (Lo-16bits)
HR6156	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6157				HR6156 (Lo-16bits)
HR6158	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6159				HR6158 (Lo-16bits)
HR6160	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6161				HR6160 (Lo-16bits)
HR6162	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6163				HR6162 (Lo-16bits)
HR6164	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6165				HR6164 (Lo-16bits)
HR6166	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6167				HR6166 (Lo-16bits)
HR6168	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6169				HR6168 (Lo-16bits)
HR6170	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6171				HR6170 (Lo-16bits)
HR6172	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6173				HR6172 (Lo-16bits)
HR6174	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6175				HR6174 (Lo-16bits)
HR6176	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6177				HR6176 (Lo-16bits)
HR6178	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6179	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6180	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6181	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6182	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6183	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6184	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6185	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6186	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6187				HR6186 (Lo-16bits)
HR6188	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6189				HR6188 (Lo-16bits)
HR6190	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6191				HR6190 (Lo-16bits)
HR6192	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6193				HR6192 (Lo-16bits)
HR6194	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6195				HR6194 (Lo-16bits)
HR6196	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6197				HR6196 (Lo-16bits)
HR6198	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6199				HR6198 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6200	DEF4:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6201				HR6200 (Lo-16bits)
HR6202	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6203	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6204	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6205	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6206	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6207	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6208	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6209	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6210	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6211				HR6210 (Lo-16bits)
HR6212	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6213				HR6212 (Lo-16bits)
HR6214	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6215				HR6214 (Lo-16bits)
HR6216	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6217				HR6216 (Lo-16bits)
HR6218	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6219				HR6218 (Lo-16bits)
HR6220	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6221				HR6220 (Lo-16bits)
HR6222	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6223				HR6222 (Lo-16bits)
HR6224	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6225				HR6224 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6226	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6227	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6228	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6229	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6230	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6231	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6232	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6233	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6234	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6235	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6236	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6237	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6238	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6239	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6240	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6241	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6242	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR6243	IEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input

Holding Register	Parameter	Access	Length	Info
HR6244	CUB1:I2> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6245	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6246	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6247				HR6246 (Lo-16bits)
HR6248	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6249				HR6248 (Lo-16bits)
HR6250	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6251				HR6250 (Lo-16bits)
HR6252	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6253				HR6252 (Lo-16bits)
HR6254	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6255				HR6254 (Lo-16bits)
HR6256	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6257				HR6256 (Lo-16bits)
HR6258	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6259				HR6258 (Lo-16bits)
HR6260	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6261				HR6260 (Lo-16bits)
HR6262	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6263				HR6262 (Lo-16bits)
HR6264	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6265				HR6264 (Lo-16bits)
HR6266	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6267				HR6266 (Lo-16bits)
HR6268	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6269				HR6268 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6270	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6271				HR6270 (Lo-16bits)
HR6272	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6273				HR6272 (Lo-16bits)
HR6274	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6275				HR6274 (Lo-16bits)
HR6276	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6277				HR6276 (Lo-16bits)
HR6278	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6279				HR6278 (Lo-16bits)
HR6280	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6281				HR6280 (Lo-16bits)
HR6282	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6283				HR6282 (Lo-16bits)
HR6284	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6285				HR6284 (Lo-16bits)
HR6286	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6287				HR6286 (Lo-16bits)
HR6288	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6289				HR6288 (Lo-16bits)
HR6290	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6291				HR6290 (Lo-16bits)
HR6292	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6293				HR6292 (Lo-16bits)
HR6294	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6295	CUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6296	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6297	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6298	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6299	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6300	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6301	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6302	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6303				HR6302 (Lo-16bits)
HR6304	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6305				HR6304 (Lo-16bits)
HR6306	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6307				HR6306 (Lo-16bits)
HR6308	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6309				HR6308 (Lo-16bits)
HR6310	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6311				HR6310 (Lo-16bits)
HR6312	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6313				HR6312 (Lo-16bits)
HR6314	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6315				HR6314 (Lo-16bits)
HR6316	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6317				HR6316 (Lo-16bits)
HR6318	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6319	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6320	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6321	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6322	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6323	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6324	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6325	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6326	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6327	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6328	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6329	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6330	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6331	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6332	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6333	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6334	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6335	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6336	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6337	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6338	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6339	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6340	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6341	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6342	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6343	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6344	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6345	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6346	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6347	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6348	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6349	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6350	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6351				HR6350 (Lo-16bits)
HR6352	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6353				HR6352 (Lo-16bits)
HR6354	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6355				HR6354 (Lo-16bits)
HR6356	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6357				HR6356 (Lo-16bits)
HR6358	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6359				HR6358 (Lo-16bits)
HR6360	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6361				HR6360 (Lo-16bits)
HR6362	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6363				HR6362 (Lo-16bits)
HR6364	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6365				HR6364 (Lo-16bits)
HR6366	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6367				HR6366 (Lo-16bits)
HR6368	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6369				HR6368 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6370	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6371				HR6370 (Lo-16bits)
HR6372	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6373				HR6372 (Lo-16bits)
HR6374	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6375				HR6374 (Lo-16bits)
HR6376	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6377				HR6376 (Lo-16bits)
HR6378	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6379				HR6378 (Lo-16bits)
HR6380	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6381				HR6380 (Lo-16bits)
HR6382	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6383				HR6382 (Lo-16bits)
HR6384	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6385				HR6384 (Lo-16bits)
HR6386	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6387				HR6386 (Lo-16bits)
HR6388	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6389				HR6388 (Lo-16bits)
HR6390	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6391				HR6390 (Lo-16bits)
HR6392	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6393				HR6392 (Lo-16bits)
HR6394	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6395				HR6394 (Lo-16bits)
HR6396	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6397				HR6396 (Lo-16bits)
HR6398	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6399				HR6398 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6400	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6401				HR6400 (Lo-16bits)
HR6402	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6403				HR6402 (Lo-16bits)
HR6404	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6405				HR6404 (Lo-16bits)
HR6406	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6407				HR6406 (Lo-16bits)
HR6408	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6409				HR6408 (Lo-16bits)
HR6410	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6411				HR6410 (Lo-16bits)
HR6412	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6413				HR6412 (Lo-16bits)
HR6414	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6415	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6416	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6417	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6418	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6419	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6420	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6421	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6422	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6423				HR6422 (Lo-16bits)
HR6424	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6425				HR6424 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6426	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6427				HR6426 (Lo-16bits)
HR6428	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6429				HR6428 (Lo-16bits)
HR6430	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6431				HR6430 (Lo-16bits)
HR6432	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6433				HR6432 (Lo-16bits)
HR6434	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6435				HR6434 (Lo-16bits)
HR6436	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6437				HR6436 (Lo-16bits)
HR6438	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6439	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6440	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6441	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6442	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6443	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6444	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6445	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6446	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6447	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6448	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6449	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6450	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6451	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6452	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6453	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6454	CUB2:I2>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6455	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6456	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6457				HR6456 (Lo-16bits)
HR6458	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6459				HR6458 (Lo-16bits)
HR6460	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6461				HR6460 (Lo-16bits)
HR6462	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6463				HR6462 (Lo-16bits)
HR6464	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6465				HR6464 (Lo-16bits)
HR6466	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6467				HR6466 (Lo-16bits)
HR6468	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6469				HR6468 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6470	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6471				HR6470 (Lo-16bits)
HR6472	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6473				HR6472 (Lo-16bits)
HR6474	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6475				HR6474 (Lo-16bits)
HR6476	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6477				HR6476 (Lo-16bits)
HR6478	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6479				HR6478 (Lo-16bits)
HR6480	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6481				HR6480 (Lo-16bits)
HR6482	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6483				HR6482 (Lo-16bits)
HR6484	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6485				HR6484 (Lo-16bits)
HR6486	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6487				HR6486 (Lo-16bits)
HR6488	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6489				HR6488 (Lo-16bits)
HR6490	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6491				HR6490 (Lo-16bits)
HR6492	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6493				HR6492 (Lo-16bits)
HR6494	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6495				HR6494 (Lo-16bits)
HR6496	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6497				HR6496 (Lo-16bits)
HR6498	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6499				HR6498 (Lo-16bits)
HR6500	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6501				HR6500 (Lo-16bits)
HR6502	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6503				HR6502 (Lo-16bits)
HR6504	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6505	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6506	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6507	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6508	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6509	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6510	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6511	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6512	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6513				HR6512 (Lo-16bits)
HR6514	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6515				HR6514 (Lo-16bits)
HR6516	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6517				HR6516 (Lo-16bits)
HR6518	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6519				HR6518 (Lo-16bits)
HR6520	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6521				HR6520 (Lo-16bits)
HR6522	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6523				HR6522 (Lo-16bits)
HR6524	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6525				HR6524 (Lo-16bits)
HR6526	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6527				HR6526 (Lo-16bits)
HR6528	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6529	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6530	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6531	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6532	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6533	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6534	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6535	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6536	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6537	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6538	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6539	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6540	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6541	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6542	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6543	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6544	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6545	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6546	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6547	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6548	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6549	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6550	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6551	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6552	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6553	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6554	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6555	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6556	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6557	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6558	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6559	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6560	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6561				HR6560 (Lo-16bits)
HR6562	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6563				HR6562 (Lo-16bits)
HR6564	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6565				HR6564 (Lo-16bits)
HR6566	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6567				HR6566 (Lo-16bits)
HR6568	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6569				HR6568 (Lo-16bits)
HR6570	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6571				HR6570 (Lo-16bits)
HR6572	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6573				HR6572 (Lo-16bits)
HR6574	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6575				HR6574 (Lo-16bits)
HR6576	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6577				HR6576 (Lo-16bits)
HR6578	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6579				HR6578 (Lo-16bits)
HR6580	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6581				HR6580 (Lo-16bits)
HR6582	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6583				HR6582 (Lo-16bits)
HR6584	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6585				HR6584 (Lo-16bits)
HR6586	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6587				HR6586 (Lo-16bits)
HR6588	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6589				HR6588 (Lo-16bits)
HR6590	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6591				HR6590 (Lo-16bits)
HR6592	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6593				HR6592 (Lo-16bits)
HR6594	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6595				HR6594 (Lo-16bits)
HR6596	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6597				HR6596 (Lo-16bits)
HR6598	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6599				HR6598 (Lo-16bits)
HR6600	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6601				HR6600 (Lo-16bits)
HR6602	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6603				HR6602 (Lo-16bits)
HR6604	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6605				HR6604 (Lo-16bits)
HR6606	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6607				HR6606 (Lo-16bits)
HR6608	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6609				HR6608 (Lo-16bits)
HR6610	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6611				HR6610 (Lo-16bits)
HR6612	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6613				HR6612 (Lo-16bits)
HR6614	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6615				HR6614 (Lo-16bits)
HR6616	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6617				HR6616 (Lo-16bits)
HR6618	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6619				HR6618 (Lo-16bits)
HR6620	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6621				HR6620 (Lo-16bits)
HR6622	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6623				HR6622 (Lo-16bits)
HR6624	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6625	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6626	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6627	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6628	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6629	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6630	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6631	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6632	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6633				HR6632 (Lo-16bits)
HR6634	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6635				HR6634 (Lo-16bits)
HR6636	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6637				HR6636 (Lo-16bits)
HR6638	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6639				HR6638 (Lo-16bits)
HR6640	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6641				HR6640 (Lo-16bits)
HR6642	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6643				HR6642 (Lo-16bits)
HR6644	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6645				HR6644 (Lo-16bits)
HR6646	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6647				HR6646 (Lo-16bits)
HR6648	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6649	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6650	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6651	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6652	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6653	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6654	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6655	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6656	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6657	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6658	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6659	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6660	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6661	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6662	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6663	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6664	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6665	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6666	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6667				HR6666 (Lo-16bits)
HR6668	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6669				HR6668 (Lo-16bits)
HR6670	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6671				HR6670 (Lo-16bits)
HR6672	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6673				HR6672 (Lo-16bits)
HR6674	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6675				HR6674 (Lo-16bits)
HR6676	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6677				HR6676 (Lo-16bits)
HR6678	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6679				HR6678 (Lo-16bits)
HR6680	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6681				HR6680 (Lo-16bits)
HR6682	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6683				HR6682 (Lo-16bits)
HR6684	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6685				HR6684 (Lo-16bits)
HR6686	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6687				HR6686 (Lo-16bits)
HR6688	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6689				HR6688 (Lo-16bits)
HR6690	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6691				HR6690 (Lo-16bits)
HR6692	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6693				HR6692 (Lo-16bits)
HR6694	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6695				HR6694 (Lo-16bits)
HR6696	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6697				HR6696 (Lo-16bits)
HR6698	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6699				HR6698 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6700	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6701				HR6700 (Lo-16bits)
HR6702	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6703				HR6702 (Lo-16bits)
HR6704	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6705				HR6704 (Lo-16bits)
HR6706	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6707				HR6706 (Lo-16bits)
HR6708	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6709				HR6708 (Lo-16bits)
HR6710	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6711				HR6710 (Lo-16bits)
HR6712	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6713				HR6712 (Lo-16bits)
HR6714	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6715	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6716	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6717	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6718	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6719	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6720	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6721	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6722	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6723				HR6722 (Lo-16bits)
HR6724	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6725				HR6724 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6726	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6727				HR6726 (Lo-16bits)
HR6728	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6729				HR6728 (Lo-16bits)
HR6730	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6731				HR6730 (Lo-16bits)
HR6732	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6733				HR6732 (Lo-16bits)
HR6734	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6735				HR6734 (Lo-16bits)
HR6736	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6737				HR6736 (Lo-16bits)
HR6738	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6739	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6740	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6741	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6742	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6743	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6744	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6745	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6746	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6747	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6748	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6749	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6750	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6751	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6752	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6753	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6754	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6755	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6756	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6757	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6758	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6759	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6760	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6761	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6762	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6763	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6764	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6765	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6766	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6767	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6768	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6769	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6770	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6771				HR6770 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6772	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6773				HR6772 (Lo-16bits)
HR6774	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6775				HR6774 (Lo-16bits)
HR6776	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6777				HR6776 (Lo-16bits)
HR6778	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6779				HR6778 (Lo-16bits)
HR6780	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6781				HR6780 (Lo-16bits)
HR6782	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6783				HR6782 (Lo-16bits)
HR6784	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6785				HR6784 (Lo-16bits)
HR6786	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6787				HR6786 (Lo-16bits)
HR6788	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6789				HR6788 (Lo-16bits)
HR6790	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6791				HR6790 (Lo-16bits)
HR6792	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6793				HR6792 (Lo-16bits)
HR6794	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6795				HR6794 (Lo-16bits)
HR6796	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6797				HR6796 (Lo-16bits)
HR6798	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6799				HR6798 (Lo-16bits)
HR6800	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6801				HR6800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6802	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6803				HR6802 (Lo-16bits)
HR6804	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6805				HR6804 (Lo-16bits)
HR6806	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6807				HR6806 (Lo-16bits)
HR6808	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6809				HR6808 (Lo-16bits)
HR6810	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6811				HR6810 (Lo-16bits)
HR6812	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6813				HR6812 (Lo-16bits)
HR6814	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6815				HR6814 (Lo-16bits)
HR6816	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6817				HR6816 (Lo-16bits)
HR6818	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6819				HR6818 (Lo-16bits)
HR6820	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6821				HR6820 (Lo-16bits)
HR6822	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6823				HR6822 (Lo-16bits)
HR6824	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6825				HR6824 (Lo-16bits)
HR6826	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6827				HR6826 (Lo-16bits)
HR6828	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6829				HR6828 (Lo-16bits)
HR6830	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6831				HR6830 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6832	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6833				HR6832 (Lo-16bits)
HR6834	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6835	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6836	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6837	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6838	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6839	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6840	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6841	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6842	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6843				HR6842 (Lo-16bits)
HR6844	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6845				HR6844 (Lo-16bits)
HR6846	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6847				HR6846 (Lo-16bits)
HR6848	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6849				HR6848 (Lo-16bits)
HR6850	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6851				HR6850 (Lo-16bits)
HR6852	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6853				HR6852 (Lo-16bits)
HR6854	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6855				HR6854 (Lo-16bits)
HR6856	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6857				HR6856 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6858	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6859	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6860	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6861	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6862	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6863	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6864	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6865	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6866	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6867	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6868	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6869	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6870	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6871	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6872	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6873	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6874	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6875	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR6876	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6877				HR6876 (Lo-16bits)
HR6878	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6879				HR6878 (Lo-16bits)
HR6880	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6881				HR6880 (Lo-16bits)
HR6882	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6883				HR6882 (Lo-16bits)
HR6884	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6885				HR6884 (Lo-16bits)
HR6886	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6887				HR6886 (Lo-16bits)
HR6888	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6889				HR6888 (Lo-16bits)
HR6890	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6891				HR6890 (Lo-16bits)
HR6892	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6893				HR6892 (Lo-16bits)
HR6894	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6895				HR6894 (Lo-16bits)
HR6896	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6897				HR6896 (Lo-16bits)
HR6898	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6899				HR6898 (Lo-16bits)
HR6900	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6901				HR6900 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6902	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6903				HR6902 (Lo-16bits)
HR6904	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6905				HR6904 (Lo-16bits)
HR6906	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6907				HR6906 (Lo-16bits)
HR6908	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6909				HR6908 (Lo-16bits)
HR6910	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6911				HR6910 (Lo-16bits)
HR6912	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6913				HR6912 (Lo-16bits)
HR6914	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6915				HR6914 (Lo-16bits)
HR6916	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6917				HR6916 (Lo-16bits)
HR6918	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6919				HR6918 (Lo-16bits)
HR6920	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6921				HR6920 (Lo-16bits)
HR6922	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6923				HR6922 (Lo-16bits)
HR6924	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6925	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6926	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6927	CUB4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6928	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6929	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6930	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6931	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6932	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6933				HR6932 (Lo-16bits)
HR6934	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6935				HR6934 (Lo-16bits)
HR6936	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6937				HR6936 (Lo-16bits)
HR6938	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6939				HR6938 (Lo-16bits)
HR6940	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6941				HR6940 (Lo-16bits)
HR6942	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6943				HR6942 (Lo-16bits)
HR6944	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6945				HR6944 (Lo-16bits)
HR6946	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6947				HR6946 (Lo-16bits)
HR6948	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6949	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6950	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6951	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6952	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6953	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6954	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6955	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6956	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6957	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6958	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6959	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6960	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6961	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6962	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6963	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6964	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6965	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6966	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6967	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6968	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6969	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6970	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6971	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6972	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6973	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6974	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6975	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6976	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6977	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6978	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6979	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6980	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6981				HR6980 (Lo-16bits)
HR6982	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6983				HR6982 (Lo-16bits)
HR6984	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6985				HR6984 (Lo-16bits)
HR6986	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6987				HR6986 (Lo-16bits)
HR6988	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6989				HR6988 (Lo-16bits)
HR6990	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6991				HR6990 (Lo-16bits)
HR6992	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6993				HR6992 (Lo-16bits)
HR6994	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6995				HR6994 (Lo-16bits)
HR6996	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6997				HR6996 (Lo-16bits)
HR6998	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6999				HR6998 (Lo-16bits)
HR7000	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7001				HR7000 (Lo-16bits)
HR7002	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7003				HR7002 (Lo-16bits)
HR7004	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7005				HR7004 (Lo-16bits)
HR7006	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7007				HR7006 (Lo-16bits)
HR7008	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7009				HR7008 (Lo-16bits)
HR7010	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7011				HR7010 (Lo-16bits)
HR7012	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7013				HR7012 (Lo-16bits)
HR7014	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7015				HR7014 (Lo-16bits)
HR7016	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7017				HR7016 (Lo-16bits)
HR7018	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7019				HR7018 (Lo-16bits)
HR7020	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7021				HR7020 (Lo-16bits)
HR7022	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7023				HR7022 (Lo-16bits)
HR7024	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7025				HR7024 (Lo-16bits)
HR7026	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7027				HR7026 (Lo-16bits)
HR7028	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7029				HR7028 (Lo-16bits)
HR7030	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7031				HR7030 (Lo-16bits)
HR7032	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7033				HR7032 (Lo-16bits)
HR7034	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7035				HR7034 (Lo-16bits)
HR7036	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7037				HR7036 (Lo-16bits)
HR7038	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7039				HR7038 (Lo-16bits)
HR7040	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7041				HR7040 (Lo-16bits)
HR7042	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7043				HR7042 (Lo-16bits)
HR7044	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7045	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7046	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7047	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7048	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7049	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7050	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7051	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7052	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7053				HR7052 (Lo-16bits)
HR7054	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7055				HR7054 (Lo-16bits)
HR7056	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7057				HR7056 (Lo-16bits)
HR7058	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7059				HR7058 (Lo-16bits)
HR7060	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7061				HR7060 (Lo-16bits)
HR7062	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7063				HR7062 (Lo-16bits)
HR7064	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7065				HR7064 (Lo-16bits)
HR7066	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7067				HR7066 (Lo-16bits)
HR7068	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7069	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7070	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7071	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7072	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7073	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7074	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7075	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7076	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7077	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7078	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7079	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7080	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7081	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7082	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7083	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7084	HOC1:Ih> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7085	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)
HR7089	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7106	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7107				HR7106 (Lo-16bits)
HR7108	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7109				HR7108 (Lo-16bits)
HR7110	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7111				HR7110 (Lo-16bits)
HR7112	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7113				HR7112 (Lo-16bits)
HR7114	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7115				HR7114 (Lo-16bits)
HR7116	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7117				HR7116 (Lo-16bits)
HR7118	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7119				HR7118 (Lo-16bits)
HR7120	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7121				HR7120 (Lo-16bits)
HR7122	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7123				HR7122 (Lo-16bits)
HR7124	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7125				HR7124 (Lo-16bits)
HR7126	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7127	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7136				HR7135 (Lo-16bits)
HR7137	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7148	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7149				HR7148 (Lo-16bits)
HR7150	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7151				HR7150 (Lo-16bits)
HR7152	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7153				HR7152 (Lo-16bits)
HR7154	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7155				HR7154 (Lo-16bits)
HR7156	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7157				HR7156 (Lo-16bits)
HR7158	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7159				HR7158 (Lo-16bits)
HR7160	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7161				HR7160 (Lo-16bits)
HR7162	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7163				HR7162 (Lo-16bits)
HR7164	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7165				HR7164 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7166	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7167				HR7166 (Lo-16bits)
HR7168	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR7169	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked
HR7170	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR7171	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR7172	DIF1:Id< condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR7173				HR7172 (Lo-16bits)
HR7174	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR7175				HR7174 (Lo-16bits)
HR7176	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)
HR7177				HR7176 (Lo-16bits)
HR7178	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7179	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7180	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7181	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR7182	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7183	TOLT1:TT> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7184	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7185	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7186	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7187	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7188	TOLT1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7189	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7190	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR7191	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7194	TOLF1:TF> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7195	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7196	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7197	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7198	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7199	TOLF1:Temp C or F deg	RW	1	0=C 1=F
HR7200	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7201	TOLF1:RTD Sensor	R	1	0=Invalid 1=OK
HR7202	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set 1=Estimate
HR7203	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7206	TOLM1:TM> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7207	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7208	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7209	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7210	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR7211	TOLM1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7212	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing 1=NPS Biasing in use
HR7213	TOLM1:Time constants	RW	1	0=Single 1=Multiple
HR7214	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually 1=Estimate (on-line)
HR7215	TOLM1:T const dyn. balancing	RW	1	0=Fixed 1=Dynamic
HR7216	TOLM1:Dev.Temp (tmax)	RW	1	0=A 1=B 2=F 3=H 4=Manual set
HR7217	TOLM1:Amb. T Selection	RW	1	0=Manual set 1=RTD
HR7218	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR7219	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7220				HR7219 (Lo-16bits)
HR7221	LCR1:ISt> condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR7222	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked
HR7223	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7224	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7225	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7226	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7227	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7228	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7229	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7230	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7231	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7232	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7233	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7234	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7235	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7240				HR7239 (Lo-16bits)
HR7241	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7252	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7253	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7254	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7255	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7256	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7257	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7258	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7259	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7268				HR7267 (Lo-16bits)
HR7269	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)
HR7285	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7297	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7324	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7325	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7326	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7327	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7328	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7329	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7330	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7331	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7332	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7333	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7334	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7335	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7336	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7337	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7338	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7339	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7340	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7341	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7342	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7343	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7344	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7345	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7346	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7347	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7348	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7349	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7350	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7351	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7352	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7353	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7354	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7355	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7356	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7357	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7371	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7374	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7375	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7376	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7377	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7378	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7379	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7380	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7381	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)
HR7383	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)
HR7387	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7397	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7427	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)
HR7429	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)
HR7431	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7432				HR7431 (Lo-16bits)
HR7433	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7434				HR7433 (Lo-16bits)
HR7435	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7436				HR7435 (Lo-16bits)
HR7437	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7446	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7447	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7448	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7449	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7450	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7451	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7452	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7453	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7454	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7455	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7456	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7457	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7458	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7459	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7460	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7461	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7462	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7463	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7464	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7465	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7466	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7467	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7468	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7469	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7470	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7471	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7472	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7473	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7474	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7475	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7476	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7477	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7478	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7479	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7496	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7497	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7498	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7499	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7500	OV3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7501	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7502	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7503	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)
HR7511	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7526				HR7525 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7527	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7528				HR7527 (Lo-16bits)
HR7529	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7530				HR7529 (Lo-16bits)
HR7531	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7532				HR7531 (Lo-16bits)
HR7533	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7534				HR7533 (Lo-16bits)
HR7535	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7536				HR7535 (Lo-16bits)
HR7537	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)
HR7539	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7557	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7568	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7569	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7570	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7571	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7572	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7573	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7574	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7575	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7576	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7577	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7578	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7579	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7580	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7581	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7582	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7583	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7584	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7585	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7586	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7587	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7588	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7589	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7590	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7591	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7592	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7593	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7594	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7595	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7596	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7597	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7598	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7599	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7600	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7601	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7618	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7619	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7620	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7621	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7622	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7623	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7624	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7625	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7630				HR7629 (Lo-16bits)
HR7631	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7632				HR7631 (Lo-16bits)
HR7633	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7634				HR7633 (Lo-16bits)
HR7635	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7636				HR7635 (Lo-16bits)
HR7637	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7638				HR7637 (Lo-16bits)
HR7639	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7640				HR7639 (Lo-16bits)
HR7641	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7642				HR7641 (Lo-16bits)
HR7643	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7644				HR7643 (Lo-16bits)
HR7645	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7658				HR7657 (Lo-16bits)
HR7659	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7688				HR7687 (Lo-16bits)
HR7689	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7690	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7691	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7692	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7693	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7694	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7695	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7696	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7697	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7698	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7699	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7700	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7701	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7702	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7703	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7704	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7705	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7706	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7707	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7708	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7709	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7710	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7711	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7712	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7713	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7714	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7715	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7716	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7717	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7718	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7719	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7720	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7721	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7722	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7723	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7731	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7744				HR7743 (Lo-16bits)
HR7745	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7746				HR7745 (Lo-16bits)
HR7747	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7748				HR7747 (Lo-16bits)
HR7749	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7750				HR7749 (Lo-16bits)
HR7751	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7752				HR7751 (Lo-16bits)
HR7753	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7754				HR7753 (Lo-16bits)
HR7755	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7756	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7757	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7758	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7759	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7760	UV1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7761	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7762	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7763	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7787	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7817	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7828	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7829	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7830	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7831	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7832	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7833	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7834	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7835	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7836	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7837	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7838	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7839	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7840	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7841	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7842	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7843	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7844	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7845	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7846	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7847	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7848	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7849	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7850	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7851	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7852	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7853	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7854	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7855	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7856	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7857	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7858	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7859	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7860	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7861	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)
HR7865	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)
HR7875	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7890				HR7889 (Lo-16bits)
HR7891	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7894	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7895	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7896	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7897	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7898	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7899	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7900	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7901	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7916				HR7915 (Lo-16bits)
HR7917	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7946				HR7945 (Lo-16bits)
HR7947	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7966	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7967	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7968	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7969	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7970	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7971	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7972	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7973	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7974	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7975	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7976	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7977	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7978	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7979	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7980	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7981	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7982	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7983	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7984	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7985	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7986	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7987	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7988	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7989	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR7990	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7991	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7992	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7993	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7994	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7995	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7996	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7997	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7998	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7999	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8016				HR8015 (Lo-16bits)
HR8017	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8032	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8033	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8034	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8035	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8036	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8037	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8038	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8039	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8045	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8073	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8103	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8104	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8105	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8106	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8107	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8108	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8109	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8110	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8111	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8112	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8113	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8114	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8115	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8116	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8117	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8118	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8119	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8120	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8121	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8122	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8123	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8124	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8125	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8126	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8127	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR8128	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8129	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8130	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8131	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8132	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8133	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8134	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8135	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8136	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8137	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8145	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8169	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8170	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8171	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8172	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8173	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8174	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8175	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8176	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8177	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8195	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8225	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)
HR8229	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8242	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8243	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8244	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8245	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8246	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8247	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8248	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8249	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8250	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8251	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8252	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8253	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8254	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8255	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8256	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8257	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8258	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8259	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8260	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8261	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8262	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8263	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8264	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8265	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8266	NOV1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8267	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8268	NOV2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8269	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8270	NOV3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8271	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8272	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8273	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8274	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8275	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8276	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8277	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8278	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8279	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8280	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8281	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8282	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8283	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8296				HR8295 (Lo-16bits)
HR8297	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8316	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8317	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8318	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8319	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8320	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8321	VUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8322	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8323	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8348				HR8347 (Lo-16bits)
HR8349	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8378				HR8377 (Lo-16bits)
HR8379	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8388	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8389	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8390	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8391	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8392	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8393	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8394	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8395	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8396	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8397	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8398	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8399	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8400	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8401	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8402	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8403	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8404	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8405	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8406	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8407	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8408	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8409	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8410	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8411	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8412	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8413	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8414	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8415	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8416	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8417	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8418	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8419	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8420	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8421	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8422				HR8421 (Lo-16bits)
HR8423	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8449	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8454	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8455	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8456	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8457	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8458	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8459	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8460	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8461	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8475	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8505	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8526	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8527	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8528	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8529	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8530	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8531	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8532	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8533	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8534	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8535	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8536	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8537	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8538	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8539	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8540	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8541	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8542	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8543	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8544	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8545	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8546	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8547	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8548	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8549	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8550	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8551	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8552	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8553	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8554	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8555	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8556	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8557	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8558	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8559	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8577	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8592	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8593	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8594	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8595	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8596	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8597	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8598	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8599	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8603	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8630				HR8629 (Lo-16bits)
HR8631	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8660				HR8659 (Lo-16bits)
HR8661	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8664	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8665	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8666	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8667	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8668	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8669	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8670	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8671	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8672	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8673	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8674	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8675	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8676	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8677	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8678	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8679	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8680	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8681	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8682	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8683	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8684	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8685	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8686	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8687	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8688	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8689	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8690	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8691	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8692	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8693	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8694	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8695	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8696	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8697	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8702				HR8701 (Lo-16bits)
HR8703	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8728				HR8727 (Lo-16bits)
HR8729	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8730	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8731	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8732	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8733	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8734	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8735	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8736	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8737	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8754				HR8753 (Lo-16bits)
HR8755	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8784				HR8783 (Lo-16bits)
HR8785	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8802	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8803	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8804	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8805	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8806	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8807	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8808	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8809	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8810	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8811	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8812	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8813	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8814	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8815	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8816	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8817	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8818	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8819	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8820	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8821	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8822	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8823	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8824	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8825	PGS1:PS1 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8826	PGS1:PS2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8827	PGS1:PS3 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8828	PGS1:PS4 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8829	PGS1:PS5 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8830	PGS1:PS6 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8831	PGS1:PS7 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8832	PGS1:PS8 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8833	PGS1:PS9 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8834	PGS1:PS10 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8835	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8836	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8837	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8838	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8839	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8840	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8841	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8842	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8843	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR8844	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR8845	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8846	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes
HR8847	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8848	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR8849	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8850	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR8851	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8852	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR8853	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8854	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR8855	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8856	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8857	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8858	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8859	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8860	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8861	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8862	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8863	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8864	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8865	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8866	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8867	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8868	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8869				HR8868 (Lo-16bits)
HR8870	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8871				HR8870 (Lo-16bits)
HR8872	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8873				HR8872 (Lo-16bits)
HR8874	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8875				HR8874 (Lo-16bits)
HR8876	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8877				HR8876 (Lo-16bits)
HR8878	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8879				HR8878 (Lo-16bits)
HR8880	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8881				HR8880 (Lo-16bits)
HR8882	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8883				HR8882 (Lo-16bits)
HR8884	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8885				HR8884 (Lo-16bits)
HR8886	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8887				HR8886 (Lo-16bits)
HR8888	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8889				HR8888 (Lo-16bits)
HR8890	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8891				HR8890 (Lo-16bits)
HR8892	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8893				HR8892 (Lo-16bits)
HR8894	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8895				HR8894 (Lo-16bits)
HR8896	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8897				HR8896 (Lo-16bits)
HR8898	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8899				HR8898 (Lo-16bits)
HR8900	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8901	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8902	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8903	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8904	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8905	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8906	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8907	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8908	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8909				HR8908 (Lo-16bits)
HR8910	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8911				HR8910 (Lo-16bits)
HR8912	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8913				HR8912 (Lo-16bits)
HR8914	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8915				HR8914 (Lo-16bits)
HR8916	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8917				HR8916 (Lo-16bits)
HR8918	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8919				HR8918 (Lo-16bits)
HR8920	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8921				HR8920 (Lo-16bits)
HR8922	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8923				HR8922 (Lo-16bits)
HR8924	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8925				HR8924 (Lo-16bits)
HR8926	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8927				HR8926 (Lo-16bits)
HR8928	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8929				HR8928 (Lo-16bits)
HR8930	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8931				HR8930 (Lo-16bits)
HR8932	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8933				HR8932 (Lo-16bits)
HR8934	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8935				HR8934 (Lo-16bits)
HR8936	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8937				HR8936 (Lo-16bits)
HR8938	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8939				HR8938 (Lo-16bits)
HR8940	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8941	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8942	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8943	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8944	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8945	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8946	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8947	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8948	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8949				HR8948 (Lo-16bits)
HR8950	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8951				HR8950 (Lo-16bits)
HR8952	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8953				HR8952 (Lo-16bits)
HR8954	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8955				HR8954 (Lo-16bits)
HR8956	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8957				HR8956 (Lo-16bits)
HR8958	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8959				HR8958 (Lo-16bits)
HR8960	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8961				HR8960 (Lo-16bits)
HR8962	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8963				HR8962 (Lo-16bits)
HR8964	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8965				HR8964 (Lo-16bits)
HR8966	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8967				HR8966 (Lo-16bits)
HR8968	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8969				HR8968 (Lo-16bits)
HR8970	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8971				HR8970 (Lo-16bits)
HR8972	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8973				HR8972 (Lo-16bits)
HR8974	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8975				HR8974 (Lo-16bits)
HR8976	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8977				HR8976 (Lo-16bits)
HR8978	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8979				HR8978 (Lo-16bits)
HR8980	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8981	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8982	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8983	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8984	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8985	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8986	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8987	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8988	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8989				HR8988 (Lo-16bits)
HR8990	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8991				HR8990 (Lo-16bits)
HR8992	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8993				HR8992 (Lo-16bits)
HR8994	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8995				HR8994 (Lo-16bits)
HR8996	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8997				HR8996 (Lo-16bits)
HR8998	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8999				HR8998 (Lo-16bits)
HR9000	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9001				HR9000 (Lo-16bits)
HR9002	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9003				HR9002 (Lo-16bits)
HR9004	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9005				HR9004 (Lo-16bits)
HR9006	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9007				HR9006 (Lo-16bits)
HR9008	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9009				HR9008 (Lo-16bits)
HR9010	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9011				HR9010 (Lo-16bits)
HR9012	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9013				HR9012 (Lo-16bits)
HR9014	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9015				HR9014 (Lo-16bits)
HR9016	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9017				HR9016 (Lo-16bits)
HR9018	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9019				HR9018 (Lo-16bits)
HR9020	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9021	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9022	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9023	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9024	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9025	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9026	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9027	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9028	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9029				HR9028 (Lo-16bits)
HR9030	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9031				HR9030 (Lo-16bits)
HR9032	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9033				HR9032 (Lo-16bits)
HR9034	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9035				HR9034 (Lo-16bits)
HR9036	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9037				HR9036 (Lo-16bits)
HR9038	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9039				HR9038 (Lo-16bits)
HR9040	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9041				HR9040 (Lo-16bits)
HR9042	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9043				HR9042 (Lo-16bits)
HR9044	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9045				HR9044 (Lo-16bits)
HR9046	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9047				HR9046 (Lo-16bits)
HR9048	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9049				HR9048 (Lo-16bits)
HR9050	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9051				HR9050 (Lo-16bits)
HR9052	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9053				HR9052 (Lo-16bits)
HR9054	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9055				HR9054 (Lo-16bits)
HR9056	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9057				HR9056 (Lo-16bits)
HR9058	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9059				HR9058 (Lo-16bits)
HR9060	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9061	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9062	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9063	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9064	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9065	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9066	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9067	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9068	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9069				HR9068 (Lo-16bits)
HR9070	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9071				HR9070 (Lo-16bits)
HR9072	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9073				HR9072 (Lo-16bits)
HR9074	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9075				HR9074 (Lo-16bits)
HR9076	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9077				HR9076 (Lo-16bits)
HR9078	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9079				HR9078 (Lo-16bits)
HR9080	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9081				HR9080 (Lo-16bits)
HR9082	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9083				HR9082 (Lo-16bits)
HR9084	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9085				HR9084 (Lo-16bits)
HR9086	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9087				HR9086 (Lo-16bits)
HR9088	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9089				HR9088 (Lo-16bits)
HR9090	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9091				HR9090 (Lo-16bits)
HR9092	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9093				HR9092 (Lo-16bits)
HR9094	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9095				HR9094 (Lo-16bits)
HR9096	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9097				HR9096 (Lo-16bits)
HR9098	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9099				HR9098 (Lo-16bits)
HR9100	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9101	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9102	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9103	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9104	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9105	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9106	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9107	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9108	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9109				HR9108 (Lo-16bits)
HR9110	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9111				HR9110 (Lo-16bits)
HR9112	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9113				HR9112 (Lo-16bits)
HR9114	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9115				HR9114 (Lo-16bits)
HR9116	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9117				HR9116 (Lo-16bits)
HR9118	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9119				HR9118 (Lo-16bits)
HR9120	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9121				HR9120 (Lo-16bits)
HR9122	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9123				HR9122 (Lo-16bits)
HR9124	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9125				HR9124 (Lo-16bits)
HR9126	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9127				HR9126 (Lo-16bits)
HR9128	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9129				HR9128 (Lo-16bits)
HR9130	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9131				HR9130 (Lo-16bits)
HR9132	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9133				HR9132 (Lo-16bits)
HR9134	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9135				HR9134 (Lo-16bits)
HR9136	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9137				HR9136 (Lo-16bits)
HR9138	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9139				HR9138 (Lo-16bits)
HR9140	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9141	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9142	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9143	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9144	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9145	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9146	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9147	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9148	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9149				HR9148 (Lo-16bits)
HR9150	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9151				HR9150 (Lo-16bits)
HR9152	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9153				HR9152 (Lo-16bits)
HR9154	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9155				HR9154 (Lo-16bits)
HR9156	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9157				HR9156 (Lo-16bits)
HR9158	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9159				HR9158 (Lo-16bits)
HR9160	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9161				HR9160 (Lo-16bits)
HR9162	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9163				HR9162 (Lo-16bits)
HR9164	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9165				HR9164 (Lo-16bits)
HR9166	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9167				HR9166 (Lo-16bits)
HR9168	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9169				HR9168 (Lo-16bits)
HR9170	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9171				HR9170 (Lo-16bits)
HR9172	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9173				HR9172 (Lo-16bits)
HR9174	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9175				HR9174 (Lo-16bits)
HR9176	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9177				HR9176 (Lo-16bits)
HR9178	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9179				HR9178 (Lo-16bits)
HR9180	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9181	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9182	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9183	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9184	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9185	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9186	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9187	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9188	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR9189	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR9194	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9195	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9196	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9197	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9198	OPW1:P> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9199	UPW1:P< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9200	RPW1:Prev> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9201	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9202	SEF1:U03rd< Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR9203	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9204	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9205	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9206	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9207	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR9208	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=VHz(1) Blocked 2=VHz(1) Start 3=VHz(1) Alarm 4=VHz(1) Trip
HR9209	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=VHz(2) Start 2=VHz(2) Alarm 3=VHz(2) Trip 4=VHz(2) Blocked
HR9210	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR9211	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9213	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9216	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9217				HR9216 (Lo-16bits)
HR9218	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9219				HR9218 (Lo-16bits)
HR9220	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9221				HR9220 (Lo-16bits)
HR9222	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9223	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9224				HR9223 (Lo-16bits)
HR9225	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9226				HR9225 (Lo-16bits)
HR9227	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9228				HR9227 (Lo-16bits)
HR9229	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9230	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9231				HR9230 (Lo-16bits)
HR9232	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9233				HR9232 (Lo-16bits)
HR9234	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9235				HR9234 (Lo-16bits)
HR9236	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9237	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9238				HR9237 (Lo-16bits)
HR9239	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9244	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9245				HR9244 (Lo-16bits)
HR9246	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9247				HR9246 (Lo-16bits)
HR9248	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9249				HR9248 (Lo-16bits)
HR9250	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9251	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9260	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9261				HR9260 (Lo-16bits)
HR9262	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9263				HR9262 (Lo-16bits)
HR9264	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9265				HR9264 (Lo-16bits)
HR9266	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9267				HR9266 (Lo-16bits)
HR9268	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9269	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9276	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9277				HR9276 (Lo-16bits)
HR9278	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9279				HR9278 (Lo-16bits)
HR9280	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9281				HR9280 (Lo-16bits)
HR9282	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9283	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9288				HR9287 (Lo-16bits)
HR9289	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9290	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9291				HR9290 (Lo-16bits)
HR9292	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9293				HR9292 (Lo-16bits)
HR9294	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9295				HR9294 (Lo-16bits)
HR9296	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9297	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9302				HR9301 (Lo-16bits)
HR9303	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9304	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9305				HR9304 (Lo-16bits)
HR9306	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9307				HR9306 (Lo-16bits)
HR9308	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9309				HR9308 (Lo-16bits)
HR9310	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9311	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9313	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)
HR9317	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9320	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9321				HR9320 (Lo-16bits)
HR9322	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9323				HR9322 (Lo-16bits)
HR9324	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9325				HR9324 (Lo-16bits)
HR9326	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9327				HR9326 (Lo-16bits)
HR9328	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9329	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9337	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9361	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9366				HR9365 (Lo-16bits)
HR9367	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9391	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9421	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9451	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	ARC1:I/O Arc> condition	R	1	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked
HR9460	ARC1:I/O Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9461				HR9460 (Lo-16bits)
HR9462	ARC1:I/O Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9463				HR9462 (Lo-16bits)
HR9464	ARC1:I/O Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9465				HR9464 (Lo-16bits)
HR9466	ARC1:I/O Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9467				HR9466 (Lo-16bits)
HR9468	ARC1:I/O Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9469				HR9468 (Lo-16bits)
HR9470	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9471	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9472	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTBusfail 3=VTLinefail 4=Blocked
HR9473	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR9474	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR9475	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9476	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR9477	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR9478	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR9479	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9501	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	VJP1:<DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR9514	VJP1:Voltage meas selected	R	1	0=Selection Ok 1=Selection not available
HR9515	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9516	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9517	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9518	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9519	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9520	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9521	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9522	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR9523	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9524	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9525	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9526	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9527	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9528	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9529	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9530	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled 1=Activated
HR9531	AR1:AR Status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR9534	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR9535				HR9534 (Lo-16bits)
HR9536	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR9537				HR9536 (Lo-16bits)
HR9538	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR9539	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9540				HR9539 (Lo-16bits)
HR9541	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	VRG1:Voltage meas condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok
HR9544	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR9545				HR9544 (Lo-16bits)
HR9546	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR9547				HR9546 (Lo-16bits)
HR9548	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR9549	VRG1:Remote tap control input	RW	1	0=Stop 1=Lower 2=Higher
HR9550	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9551				HR9550 (Lo-16bits)
HR9552	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9553				HR9552 (Lo-16bits)
HR9554	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9555				HR9554 (Lo-16bits)
HR9556	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9557				HR9556 (Lo-16bits)
HR9558	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9559				HR9558 (Lo-16bits)
HR9560	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9561				HR9560 (Lo-16bits)
HR9562	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9563				HR9562 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9564	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9565				HR9564 (Lo-16bits)
HR9566	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9567				HR9566 (Lo-16bits)
HR9568	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9569				HR9568 (Lo-16bits)
HR9570	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9571				HR9570 (Lo-16bits)
HR9572	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9573				HR9572 (Lo-16bits)
HR9574	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9575				HR9574 (Lo-16bits)
HR9576	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9577				HR9576 (Lo-16bits)
HR9578	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9579				HR9578 (Lo-16bits)
HR9580	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9587				HR9586 (Lo-16bits)
HR9588	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9594	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9599				HR9598 (Lo-16bits)
HR9600	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9617				HR9616 (Lo-16bits)
HR9618	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9619				HR9618 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9620	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9621				HR9620 (Lo-16bits)
HR9622	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9623				HR9622 (Lo-16bits)
HR9624	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9625				HR9624 (Lo-16bits)
HR9626	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9627				HR9626 (Lo-16bits)
HR9628	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9629				HR9628 (Lo-16bits)
HR9630	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLT detected
HR9631	EMON1:I0> (50N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9632	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLT detected
HR9633	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9634	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal 1=FLT FW 2=FLT REV
HR9635	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=No voltage
HR9636	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9637	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLT detected
HR9638	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR9639				HR9638 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9640	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR9641				HR9640 (Lo-16bits)
HR9642	GSYN:Synchronizer mode	RW	1	0=Disabled 1=Activated
HR9643	GSYN:Synchronizing by	RW	1	0=Autosynchronizer 1=Manual commands
HR9644	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch 1=I know what I am doing
HR9645	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9665	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9690				HR9689 (Lo-16bits)
HR9691	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)
HR9697	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9715	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	VREC:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9726	VREC:Scale current values to primary	RW	1	0=No 1=Yes
HR9727	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	RW	1	0=No 1=Yes
HR9728	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9729				HR9728 (Lo-16bits)
HR9730	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR9731				HR9730 (Lo-16bits)
HR9732	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR9733				HR9732 (Lo-16bits)
HR9734	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR9735				HR9734 (Lo-16bits)
HR9736	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR9737				HR9736 (Lo-16bits)
HR9738	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR9739				HR9738 (Lo-16bits)
HR9740	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9741				HR9740 (Lo-16bits)
HR9742	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR9743				HR9742 (Lo-16bits)
HR9744	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR9745				HR9744 (Lo-16bits)
HR9746	RTB:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9747	RTB:Scale current values to primary	RW	1	0=No 1=Yes
HR9748	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9749				HR9748 (Lo-16bits)
HR9750	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9751				HR9750 (Lo-16bits)
HR9752	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9753				HR9752 (Lo-16bits)
HR9754	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9755				HR9754 (Lo-16bits)
HR9756	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9757				HR9756 (Lo-16bits)
HR9758	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9759				HR9758 (Lo-16bits)
HR9760	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9761				HR9760 (Lo-16bits)
HR9762	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9763				HR9762 (Lo-16bits)
HR9764	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9765				HR9764 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9766	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9767				HR9766 (Lo-16bits)
HR9768	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR9769				HR9768 (Lo-16bits)
HR9770	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR9771				HR9770 (Lo-16bits)
HR9772	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9773				HR9772 (Lo-16bits)
HR9774	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9775				HR9774 (Lo-16bits)
HR9776	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9777				HR9776 (Lo-16bits)
HR9778	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9779				HR9778 (Lo-16bits)
HR9780	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9781				HR9780 (Lo-16bits)
HR9782	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9783				HR9782 (Lo-16bits)
HR9784	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9785				HR9784 (Lo-16bits)
HR9786	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9787				HR9786 (Lo-16bits)
HR9788	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9789				HR9788 (Lo-16bits)
HR9790	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9791				HR9790 (Lo-16bits)
HR9792	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9793				HR9792 (Lo-16bits)
HR9794	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9795				HR9794 (Lo-16bits)
HR9796	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9797				HR9796 (Lo-16bits)
HR9798	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9799				HR9798 (Lo-16bits)
HR9800	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9801				HR9800 (Lo-16bits)
HR9802	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9803				HR9802 (Lo-16bits)
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

1.4 R Modbus map

The following table lists modbus holding registers for AQ-R215 devices with firmware R_v2.1.1.3-247.

**NOTICE!**

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools* → *Communication* → *Modbus map*.

Table. 1.4 - 4. AQ-21x frame Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index

Holding Register	Parameter	Access	Length	Info
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1: Pri.Pha.curr.IL1 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1: Pri.Pha.curr.IL2 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1: Pri.Pha.curr.IL3 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1: Pri.Pha.curr.IL1 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1: Pri.Pha.curr.IL2 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1: Pri.Pha.curr.IL3 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1: Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1: Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1: Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1: Pri.Res.curr.I01 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1: Pri.Res.curr.I01 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1: Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1: Pri.Res.curr.I02 ff.	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR52	MEAS1: Pri. Res. curr. I02 3h.	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1: Res. curr. angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1: Pri. calc. I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1: calc. I0 Pha. angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1: Pha. curr. IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1: Pha. curr. IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1: Pha. curr. IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1: Pri. Pos. seq. curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1: Pri. Neg. seq. curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1: Pri. Zero seq. curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1: Pos. seq. curr. angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1: Neg. seq. curr. angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1: Zero seq. curr. angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1: Res. curr. I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR79				HR78 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR80	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR109				HR108 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR110	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR140	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR170	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR200	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR230	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR260	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR290	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR320	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR350	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR379				HR378 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR380	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR410	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR440	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR469				HR468 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR470	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR495				HR494 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR496	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR523				HR522 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR524	MEAS2:I"L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR554	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR584	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I"L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I"L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I"L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I"L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I"L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I"L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I"L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I"L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I"L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I"L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR614	MEAS2:I"L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR644	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I"L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I"L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I"L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I"L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I"L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I"L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I"L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I"L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR674	MEAS2:I ³ L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I ³ L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I ³ L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I ³ L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I ³ L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I ³ L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I ³ L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I ³ L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I ³ L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I ³ L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I ³ L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I ³ L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I ³ L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I ³ L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I ³ L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR704	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR734	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR764	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR794	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR823				HR822 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR824	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR853				HR852 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR854	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U1 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U1 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U1 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:U2 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:U2 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:U2 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:U3 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:U3 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR883				HR882 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR884	MEAS3:U3 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U4 Volt pri ff	R	2	FLOAT [0...1000000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U4 Volt sec ff	R	2	FLOAT [0...500] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U4 Angle ff	R	2	FLOAT [-360...360] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:U1 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:U1 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:U1 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:U2 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:U2 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:U2 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:U3 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:U3 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:U3 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:U4 Volt pri 3h	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:U4 Volt sec 3h	R	2	FLOAT [0...500] (Hi-16bits)
HR913				HR912 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR914	MEAS3:U4 Angle 3h	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR943				HR942 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR944	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR971				HR970 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR972	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:System volt UL1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR995				HR994 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR996	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1217				HR1216 (Lo-16bits)
HR1218	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)
HR1228	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1229				HR1228 (Lo-16bits)
HR1230	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)
HR1232	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)
HR1234	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)
HR1244	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)
HR1252	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)
HR1256	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)
HR1258	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1259				HR1258 (Lo-16bits)
HR1260	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)
HR1268	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)
HR1276	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1279				HR1278 (Lo-16bits)
HR1280	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1281				HR1280 (Lo-16bits)
HR1282	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1288	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1291				HR1290 (Lo-16bits)
HR1292	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1312	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1317				HR1316 (Lo-16bits)
HR1318	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1319				HR1318 (Lo-16bits)
HR1320	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1321				HR1320 (Lo-16bits)
HR1322	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1323				HR1322 (Lo-16bits)
HR1324	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1325				HR1324 (Lo-16bits)
HR1326	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1327				HR1326 (Lo-16bits)
HR1328	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1329				HR1328 (Lo-16bits)
HR1330	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1331				HR1330 (Lo-16bits)
HR1332	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1333				HR1332 (Lo-16bits)
HR1334	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1335				HR1334 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1336	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1337				HR1336 (Lo-16bits)
HR1338	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)
HR1342	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1349				HR1348 (Lo-16bits)
HR1350	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1358	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1359				HR1358 (Lo-16bits)
HR1360	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1361				HR1360 (Lo-16bits)
HR1362	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1363				HR1362 (Lo-16bits)
HR1364	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1365				HR1364 (Lo-16bits)
HR1366	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1367				HR1366 (Lo-16bits)
HR1368	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1369				HR1368 (Lo-16bits)
HR1370	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1371				HR1370 (Lo-16bits)
HR1372	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1373				HR1372 (Lo-16bits)
HR1374	POW1:Reset 3ph Energies	RW	1	0=- 1=Reset
HR1375	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1381	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1382				HR1381 (Lo-16bits)
HR1383	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)
HR1393	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1402				HR1401 (Lo-16bits)
HR1403	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)
HR1405	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1409	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1410				HR1409 (Lo-16bits)
HR1411	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)
HR1417	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)
HR1419	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1426				HR1425 (Lo-16bits)
HR1427	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1428				HR1427 (Lo-16bits)
HR1429	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1430				HR1429 (Lo-16bits)
HR1431	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1432				HR1431 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1433	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1434				HR1433 (Lo-16bits)
HR1435	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1436				HR1435 (Lo-16bits)
HR1437	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1438				HR1437 (Lo-16bits)
HR1439	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1440				HR1439 (Lo-16bits)
HR1441	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1442				HR1441 (Lo-16bits)
HR1443	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1444				HR1443 (Lo-16bits)
HR1445	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1446				HR1445 (Lo-16bits)
HR1447	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1448				HR1447 (Lo-16bits)
HR1449	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1450				HR1449 (Lo-16bits)
HR1451	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1452				HR1451 (Lo-16bits)
HR1453	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1454				HR1453 (Lo-16bits)
HR1455	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1456				HR1455 (Lo-16bits)
HR1457	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1458				HR1457 (Lo-16bits)
HR1459	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1460				HR1459 (Lo-16bits)
HR1461	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1462				HR1461 (Lo-16bits)
HR1463	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1464				HR1463 (Lo-16bits)
HR1465	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1466				HR1465 (Lo-16bits)
HR1467	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)
HR1469	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1470				HR1469 (Lo-16bits)
HR1471	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1472				HR1471 (Lo-16bits)
HR1473	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1474				HR1473 (Lo-16bits)
HR1475	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1476				HR1475 (Lo-16bits)
HR1477	DIGI:DIs (cpu)	R	2	0b=DI1 1b=DI2 2b=DI3 (Hi-16bits)
HR1478				HR1477 (Lo-16bits)
HR1479	DIGI:DIs (input option 1)	R	2	0b=DI4 1b=DI5 2b=DI6 3b=DI7 4b=DI8 5b=DI9 6b=DI10 7b=DI11 (Hi-16bits)
HR1480				HR1479 (Lo-16bits)
HR1481	DIGI:DIs (input option 2)	R	2	0b=DI12 1b=DI13 2b=DI14 3b=DI15 4b=DI16 5b=DI17 6b=DI18 7b=DI19 (Hi-16bits)
HR1482				HR1481 (Lo-16bits)
HR1483	DIGI:DIs (input option 3)	R	2	0b=DI20 1b=DI21 2b=DI22 3b=DI23 4b=DI24 5b=DI25 6b=DI26 7b=DI27 (Hi-16bits)
HR1484				HR1483 (Lo-16bits)
HR1485	DIGI:DIs (input option 4)	R	2	0b=D28 1b=DI29 2b=DI30 3b=DI31 4b=DI32 5b=DI33 6b=DI34 7b=DI35 (Hi-16bits)
HR1486				HR1485 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1487	DIGI:DIs (input option 5)	R	2	0b=DI36 1b=DI37 2b=DI38 3b=DI39 4b=DI40 5b=DI41 6b=DI42 7b=DI43 (Hi-16bits)
HR1488				HR1487 (Lo-16bits)
HR1489	DIGI:DIs (input option 6)	R	2	0b=DI44 1b=DI45 2b=DI46 3b=DI47 4b=DI48 5b=DI49 6b=DI50 7b=DI51 (Hi-16bits)
HR1490				HR1489 (Lo-16bits)
HR1491	DIGI:Dis 1-32	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1492				HR1491 (Lo-16bits)
HR1493	DIGI:Dis 33-64	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 (Hi-16bits)
HR1494				HR1493 (Lo-16bits)
HR1495	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1496				HR1495 (Lo-16bits)
HR1497	DIGI:DI1 Counter Clear	RW	1	0=- 1=Clear
HR1498	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1499				HR1498 (Lo-16bits)
HR1500	DIGI:DI2 Counter Clear	RW	1	0=- 1=Clear
HR1501	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1502				HR1501 (Lo-16bits)
HR1503	DIGI:DI3 Counter Clear	RW	1	0=- 1=Clear
HR1504	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1505				HR1504 (Lo-16bits)
HR1506	DIGI:DI4 Counter Clear	RW	1	0=- 1=Clear
HR1507	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1508				HR1507 (Lo-16bits)
HR1509	DIGI:DI5 Counter Clear	RW	1	0=- 1=Clear
HR1510	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1511				HR1510 (Lo-16bits)
HR1512	DIGI:DI6 Counter Clear	RW	1	0=- 1=Clear
HR1513	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1514				HR1513 (Lo-16bits)
HR1515	DIGI:DI7 Counter Clear	RW	1	0=- 1=Clear
HR1516	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1517				HR1516 (Lo-16bits)
HR1518	DIGI:DI8 Counter Clear	RW	1	0=- 1=Clear
HR1519	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1520				HR1519 (Lo-16bits)
HR1521	DIGI:DI9 Counter Clear	RW	1	0=- 1=Clear
HR1522	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1523				HR1522 (Lo-16bits)
HR1524	DIGI:DI10 Counter Clear	RW	1	0=- 1=Clear
HR1525	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1526				HR1525 (Lo-16bits)
HR1527	DIGI:DI11 Counter Clear	RW	1	0=- 1=Clear
HR1528	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1529				HR1528 (Lo-16bits)
HR1530	DIGI:DI12 Counter Clear	RW	1	0=- 1=Clear
HR1531	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1532				HR1531 (Lo-16bits)
HR1533	DIGI:DI13 Counter Clear	RW	1	0=- 1=Clear
HR1534	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1535				HR1534 (Lo-16bits)
HR1536	DIGI:DI14 Counter Clear	RW	1	0=- 1=Clear
HR1537	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1538				HR1537 (Lo-16bits)
HR1539	DIGI:DI15 Counter Clear	RW	1	0=- 1=Clear
HR1540	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1541				HR1540 (Lo-16bits)
HR1542	DIGI:DI16 Counter Clear	RW	1	0=- 1=Clear
HR1543	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1544				HR1543 (Lo-16bits)
HR1545	DIGI:DI17 Counter Clear	RW	1	0=- 1=Clear
HR1546	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1547				HR1546 (Lo-16bits)
HR1548	DIGI:DI18 Counter Clear	RW	1	0=- 1=Clear
HR1549	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1550				HR1549 (Lo-16bits)
HR1551	DIGI:DI19 Counter Clear	RW	1	0=- 1=Clear
HR1552	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1553				HR1552 (Lo-16bits)
HR1554	DIGI:DI20 Counter Clear	RW	1	0=- 1=Clear
HR1555	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1556				HR1555 (Lo-16bits)
HR1557	DIGI:DI21 Counter Clear	RW	1	0=- 1=Clear
HR1558	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1559				HR1558 (Lo-16bits)
HR1560	DIGI:DI22 Counter Clear	RW	1	0=- 1=Clear
HR1561	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1562				HR1561 (Lo-16bits)
HR1563	DIGI:DI23 Counter Clear	RW	1	0=- 1=Clear
HR1564	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1565				HR1564 (Lo-16bits)
HR1566	DIGI:DI24 Counter Clear	RW	1	0=- 1=Clear
HR1567	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1568				HR1567 (Lo-16bits)
HR1569	DIGI:DI25 Counter Clear	RW	1	0=- 1=Clear
HR1570	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1571				HR1570 (Lo-16bits)
HR1572	DIGI:DI26 Counter Clear	RW	1	0=- 1=Clear
HR1573	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1574				HR1573 (Lo-16bits)
HR1575	DIGI:DI27 Counter Clear	RW	1	0=- 1=Clear
HR1576	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1577				HR1576 (Lo-16bits)
HR1578	DIGI:DI28 Counter Clear	RW	1	0=- 1=Clear
HR1579	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1580				HR1579 (Lo-16bits)
HR1581	DIGI:DI29 Counter Clear	RW	1	0=- 1=Clear
HR1582	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1583				HR1582 (Lo-16bits)
HR1584	DIGI:DI30 Counter Clear	RW	1	0=- 1=Clear
HR1585	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1586				HR1585 (Lo-16bits)
HR1587	DIGI:DI31 Counter Clear	RW	1	0=- 1=Clear
HR1588	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1589				HR1588 (Lo-16bits)
HR1590	DIGI:DI32 Counter Clear	RW	1	0=- 1=Clear
HR1591	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1592				HR1591 (Lo-16bits)
HR1593	DIGI:DI33 Counter Clear	RW	1	0=- 1=Clear
HR1594	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1595				HR1594 (Lo-16bits)
HR1596	DIGI:DI34 Counter Clear	RW	1	0=- 1=Clear
HR1597	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1598				HR1597 (Lo-16bits)
HR1599	DIGI:DI35 Counter Clear	RW	1	0=- 1=Clear
HR1600	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1601				HR1600 (Lo-16bits)
HR1602	DIGI:DI36 Counter Clear	RW	1	0=- 1=Clear
HR1603	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1604				HR1603 (Lo-16bits)
HR1605	DIGI:DI37 Counter Clear	RW	1	0=- 1=Clear
HR1606	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	DIGI:DI38 Counter Clear	RW	1	0=- 1=Clear
HR1609	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1610				HR1609 (Lo-16bits)
HR1611	DIGI:DI39 Counter Clear	RW	1	0=- 1=Clear
HR1612	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	DIGI:DI40 Counter Clear	RW	1	0=- 1=Clear
HR1615	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1616				HR1615 (Lo-16bits)
HR1617	DIGI:DI41 Counter Clear	RW	1	0=- 1=Clear
HR1618	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	DIGI:DI42 Counter Clear	RW	1	0=- 1=Clear
HR1621	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1622				HR1621 (Lo-16bits)
HR1623	DIGI:DI43 Counter Clear	RW	1	0=- 1=Clear
HR1624	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	DIGI:DI44 Counter Clear	RW	1	0=- 1=Clear
HR1627	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1628				HR1627 (Lo-16bits)
HR1629	DIGI:DI45 Counter Clear	RW	1	0=- 1=Clear
HR1630	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1631				HR1630 (Lo-16bits)
HR1632	DIGI:DI46 Counter Clear	RW	1	0=- 1=Clear
HR1633	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1634				HR1633 (Lo-16bits)
HR1635	DIGI:DI47 Counter Clear	RW	1	0=- 1=Clear
HR1636	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	DIGI:DI48 Counter Clear	RW	1	0=- 1=Clear
HR1639	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	DIGI:DI49 Counter Clear	RW	1	0=- 1=Clear
HR1642	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1643				HR1642 (Lo-16bits)
HR1644	DIGI:DI50 Counter Clear	RW	1	0=- 1=Clear
HR1645	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	DIGI:DI51 Counter Clear	RW	1	0=- 1=Clear
HR1648	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 (Hi-16bits)
HR1649				HR1648 (Lo-16bits)
HR1650	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1651	SGS:Force SG	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1652	SGS:Force SG change	RW	1	0=Disabled 1=Enabled
HR1653	SGS:Used setting groups	RW	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1654	SGS:Remote SG change	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1655	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR1656	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1657				HR1656 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1658	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1659				HR1658 (Lo-16bits)
HR1660	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1661				HR1660 (Lo-16bits)
HR1662	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1663				HR1662 (Lo-16bits)
HR1664	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1665				HR1664 (Lo-16bits)
HR1666	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1667				HR1666 (Lo-16bits)
HR1668	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1669				HR1668 (Lo-16bits)
HR1670	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1671				HR1670 (Lo-16bits)
HR1672	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1673				HR1672 (Lo-16bits)
HR1674	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1675				HR1674 (Lo-16bits)
HR1676	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1677				HR1676 (Lo-16bits)
HR1678	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1679				HR1678 (Lo-16bits)
HR1680	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1681				HR1680 (Lo-16bits)
HR1682	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1683				HR1682 (Lo-16bits)
HR1684	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1685				HR1684 (Lo-16bits)
HR1686	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1687				HR1686 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1688	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1691				HR1690 (Lo-16bits)
HR1692	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1693				HR1692 (Lo-16bits)
HR1694	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1695				HR1694 (Lo-16bits)
HR1696	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR1697	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1700				HR1699 (Lo-16bits)
HR1701	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1713	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1716				HR1715 (Lo-16bits)
HR1717	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1718				HR1717 (Lo-16bits)
HR1719	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1720				HR1719 (Lo-16bits)
HR1721	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1722				HR1721 (Lo-16bits)
HR1723	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1724				HR1723 (Lo-16bits)
HR1725	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1726				HR1725 (Lo-16bits)
HR1727	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)
HR1729	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1732				HR1731 (Lo-16bits)
HR1733	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1736				HR1735 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1737	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1738				HR1737 (Lo-16bits)
HR1739	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1742				HR1741 (Lo-16bits)
HR1743	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1744				HR1743 (Lo-16bits)
HR1745	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1746	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1747				HR1746 (Lo-16bits)
HR1748	NOC1:I> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1749	NOC1:I> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1750	NOC1:I> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1751	NOC1:I> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1752	NOC1:I> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1753	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1754				HR1753 (Lo-16bits)
HR1755	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1756				HR1755 (Lo-16bits)
HR1757	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1758				HR1757 (Lo-16bits)
HR1759	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1760				HR1759 (Lo-16bits)
HR1761	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1762				HR1761 (Lo-16bits)
HR1763	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1764				HR1763 (Lo-16bits)
HR1765	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1766				HR1765 (Lo-16bits)
HR1767	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1768				HR1767 (Lo-16bits)
HR1769	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1770				HR1769 (Lo-16bits)
HR1771	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1772				HR1771 (Lo-16bits)
HR1773	NOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1776	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1777	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1778	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1779	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1780	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1781	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1782	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1783	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1784				HR1783 (Lo-16bits)
HR1785	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1790				HR1789 (Lo-16bits)
HR1791	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)
HR1793	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1794				HR1793 (Lo-16bits)
HR1795	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1796				HR1795 (Lo-16bits)
HR1797	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1798				HR1797 (Lo-16bits)
HR1799	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1800	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1801	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1802	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1803	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1804	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1805	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1806	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1807	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1808	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1809	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1810	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1811	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1812	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1813	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1814	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1815	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1816	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1817	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1818	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1819	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1820	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1821	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1822	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1823	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1824	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1825	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1826	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1827	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1828	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1829	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1830	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1831	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)
HR1835	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1836				HR1835 (Lo-16bits)
HR1837	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1838				HR1837 (Lo-16bits)
HR1839	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1840				HR1839 (Lo-16bits)
HR1841	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1842				HR1841 (Lo-16bits)
HR1843	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1844				HR1843 (Lo-16bits)
HR1845	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1852				HR1851 (Lo-16bits)
HR1853	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1854				HR1853 (Lo-16bits)
HR1855	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1856				HR1855 (Lo-16bits)
HR1857	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1858				HR1857 (Lo-16bits)
HR1859	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1860				HR1859 (Lo-16bits)
HR1861	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1862				HR1861 (Lo-16bits)
HR1863	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1864				HR1863 (Lo-16bits)
HR1865	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1866				HR1865 (Lo-16bits)
HR1867	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1868				HR1867 (Lo-16bits)
HR1869	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1870				HR1869 (Lo-16bits)
HR1871	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1872				HR1871 (Lo-16bits)
HR1873	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1874				HR1873 (Lo-16bits)
HR1875	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1876				HR1875 (Lo-16bits)
HR1877	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1878				HR1877 (Lo-16bits)
HR1879	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1880				HR1879 (Lo-16bits)
HR1881	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1882				HR1881 (Lo-16bits)
HR1883	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1884				HR1883 (Lo-16bits)
HR1885	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1886				HR1885 (Lo-16bits)
HR1887	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1888				HR1887 (Lo-16bits)
HR1889	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1890				HR1889 (Lo-16bits)
HR1891	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1892				HR1891 (Lo-16bits)
HR1893	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1894				HR1893 (Lo-16bits)
HR1895	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1896	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1897	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1898	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1899	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1900	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1901	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1902	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1903	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1904				HR1903 (Lo-16bits)
HR1905	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1906				HR1905 (Lo-16bits)
HR1907	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1908				HR1907 (Lo-16bits)
HR1909	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1910				HR1909 (Lo-16bits)
HR1911	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1912				HR1911 (Lo-16bits)
HR1913	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1914				HR1913 (Lo-16bits)
HR1915	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1916				HR1915 (Lo-16bits)
HR1917	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1918				HR1917 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1919	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1920	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1921	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1922	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1923	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1924	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1925	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1926	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1927	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1944	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1945	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1946	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1947	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1948	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1949	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1950	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1951	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1952	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1953	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1954	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1955	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1956	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1957	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1958	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1959	NOC1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	NOC1:Delay type	RW	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR1962	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1963				HR1962 (Lo-16bits)
HR1964	NOC1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR1965	NOC1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1966	NOC1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1967	NOC1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR1968	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1969				HR1968 (Lo-16bits)
HR1970	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1971				HR1970 (Lo-16bits)
HR1972	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1973				HR1972 (Lo-16bits)
HR1974	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1975				HR1974 (Lo-16bits)
HR1976	NOC1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR1977	NOC1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR1978	NOC2:I>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1979	NOC2:I>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1980	NOC2:I>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1981	NOC2:I>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1982	NOC2:I>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1983	NOC2:I>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1984				HR1983 (Lo-16bits)
HR1985	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1986				HR1985 (Lo-16bits)
HR1987	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1988				HR1987 (Lo-16bits)
HR1989	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1990				HR1989 (Lo-16bits)
HR1991	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1992				HR1991 (Lo-16bits)
HR1993	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1994				HR1993 (Lo-16bits)
HR1995	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1996				HR1995 (Lo-16bits)
HR1997	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1998				HR1997 (Lo-16bits)
HR1999	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2000				HR1999 (Lo-16bits)
HR2001	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2002				HR2001 (Lo-16bits)
HR2003	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2006	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2007	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2008	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2009	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2010	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2011	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2012	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2013	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2015	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2030	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2031	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2032	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2033	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2034	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2035	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2036	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2037	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2038	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2039	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR2040	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2041	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2042	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2043	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2044	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2045	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2046	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2047	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2048	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2049	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2050	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2051	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2052	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2053	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2054	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2055	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2056	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2057	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2058	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2059	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2060	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2061	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2068				HR2067 (Lo-16bits)
HR2069	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2070				HR2069 (Lo-16bits)
HR2071	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2072				HR2071 (Lo-16bits)
HR2073	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2074				HR2073 (Lo-16bits)
HR2075	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)
HR2081	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2090				HR2089 (Lo-16bits)
HR2091	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2092				HR2091 (Lo-16bits)
HR2093	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2094				HR2093 (Lo-16bits)
HR2095	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2096				HR2095 (Lo-16bits)
HR2097	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2098				HR2097 (Lo-16bits)
HR2099	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)
HR2107	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2116				HR2115 (Lo-16bits)
HR2117	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2118				HR2117 (Lo-16bits)
HR2119	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2120				HR2119 (Lo-16bits)
HR2121	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2122				HR2121 (Lo-16bits)
HR2123	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2124				HR2123 (Lo-16bits)
HR2125	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2126	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2127	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2128	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2129	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2130	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2131	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2132	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2133	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2134				HR2133 (Lo-16bits)
HR2135	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2136				HR2135 (Lo-16bits)
HR2137	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2138				HR2137 (Lo-16bits)
HR2139	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2140				HR2139 (Lo-16bits)
HR2141	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2142				HR2141 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2143	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2144				HR2143 (Lo-16bits)
HR2145	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2146				HR2145 (Lo-16bits)
HR2147	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2148				HR2147 (Lo-16bits)
HR2149	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2150	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2151	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2152	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2153	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2154	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2155	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2156	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2157	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2158				HR2157 (Lo-16bits)
HR2159	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)
HR2165	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2168				HR2167 (Lo-16bits)
HR2169	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2174	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2175	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2176	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2177	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2178	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2179	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2180	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2181	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2182	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2183	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2184	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2185	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2186	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2187	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2188	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2189	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NOC2:Delay type	RW	1	0=DT 1=IDMT
HR2192	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2193				HR2192 (Lo-16bits)
HR2194	NOC2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2195	NOC2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2196	NOC2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2197	NOC2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2198	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2199				HR2198 (Lo-16bits)
HR2200	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2201				HR2200 (Lo-16bits)
HR2202	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2203				HR2202 (Lo-16bits)
HR2204	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)
HR2206	NOC2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2207	NOC2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2208	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2209	NOC3:I>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2210	NOC3:I>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC

Holding Register	Parameter	Access	Length	Info
HR2211	NOC3: >>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2212	NOC3: >>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2213	NOC3: >>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2214				HR2213 (Lo-16bits)
HR2215	NOC3: >>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	NOC3: >>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2218				HR2217 (Lo-16bits)
HR2219	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2220				HR2219 (Lo-16bits)
HR2221	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2222				HR2221 (Lo-16bits)
HR2223	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2224				HR2223 (Lo-16bits)
HR2225	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2226				HR2225 (Lo-16bits)
HR2227	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2228				HR2227 (Lo-16bits)
HR2229	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2230				HR2229 (Lo-16bits)
HR2231	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2232				HR2231 (Lo-16bits)
HR2233	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2236	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2237	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2238	NOC3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2239	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2240	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2241	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2242	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2243	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2246				HR2245 (Lo-16bits)
HR2247	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)
HR2251	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)
HR2253	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2254				HR2253 (Lo-16bits)
HR2255	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2256				HR2255 (Lo-16bits)
HR2257	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2260	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2261	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2262	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2263	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2264	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2265	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2266	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2267	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2268	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2269	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2270	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2271	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2272	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2273	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2274	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2275	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2276	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2277	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2278	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2279	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2280	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2281	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2282	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2283	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2284	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2285	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2286	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2287	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2288	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2289	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2290	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2291	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2298				HR2297 (Lo-16bits)
HR2299	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2300				HR2299 (Lo-16bits)
HR2301	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2302				HR2301 (Lo-16bits)
HR2303	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2304				HR2303 (Lo-16bits)
HR2305	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2308				HR2307 (Lo-16bits)
HR2309	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)
HR2311	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2314				HR2313 (Lo-16bits)
HR2315	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2322				HR2321 (Lo-16bits)
HR2323	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2324				HR2323 (Lo-16bits)
HR2325	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2326				HR2325 (Lo-16bits)
HR2327	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2328				HR2327 (Lo-16bits)
HR2329	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2330				HR2329 (Lo-16bits)
HR2331	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2332				HR2331 (Lo-16bits)
HR2333	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2334				HR2333 (Lo-16bits)
HR2335	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2336				HR2335 (Lo-16bits)
HR2337	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2338				HR2337 (Lo-16bits)
HR2339	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2340				HR2339 (Lo-16bits)
HR2341	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2342				HR2341 (Lo-16bits)
HR2343	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2344				HR2343 (Lo-16bits)
HR2345	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2346				HR2345 (Lo-16bits)
HR2347	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2348				HR2347 (Lo-16bits)
HR2349	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2350				HR2349 (Lo-16bits)
HR2351	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2352				HR2351 (Lo-16bits)
HR2353	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2354				HR2353 (Lo-16bits)
HR2355	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2356	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2357	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2358	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2359	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2360	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2361	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2362	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2363	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2364				HR2363 (Lo-16bits)
HR2365	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2366				HR2365 (Lo-16bits)
HR2367	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2368				HR2367 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2369	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2370				HR2369 (Lo-16bits)
HR2371	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2372				HR2371 (Lo-16bits)
HR2373	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2374				HR2373 (Lo-16bits)
HR2375	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2376				HR2375 (Lo-16bits)
HR2377	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2378				HR2377 (Lo-16bits)
HR2379	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2380	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2381	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2382	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2383	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2384	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2385	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2386	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2387	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)
HR2391	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2393	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2402				HR2401 (Lo-16bits)
HR2403	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2404	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2405	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2406	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2407	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2408	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2409	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2410	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2411	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2412	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2413	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2414	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2415	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2416	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2417	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2418	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2419	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2420				HR2419 (Lo-16bits)
HR2421	NOC3:Delay type	RW	1	0=DT 1=IDMT
HR2422	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	NOC3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2425	NOC3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2426	NOC3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2427	NOC3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2428	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2433				HR2432 (Lo-16bits)
HR2434	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	NOC3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2437	NOC3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR2438	NOC4:I>>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2439	NOC4:I>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2440	NOC4:I>>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2441	NOC4:I>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2442	NOC4:I>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2443	NOC4:I>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2444				HR2443 (Lo-16bits)
HR2445	NOC4:I>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2446				HR2445 (Lo-16bits)
HR2447	NOC4:I>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2448				HR2447 (Lo-16bits)
HR2449	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2450				HR2449 (Lo-16bits)
HR2451	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2452				HR2451 (Lo-16bits)
HR2453	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2454				HR2453 (Lo-16bits)
HR2455	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2456				HR2455 (Lo-16bits)
HR2457	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2458				HR2457 (Lo-16bits)
HR2459	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2460				HR2459 (Lo-16bits)
HR2461	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2462				HR2461 (Lo-16bits)
HR2463	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2464				HR2463 (Lo-16bits)
HR2465	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2466	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2467	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2468	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2469	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2470	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2471	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2472	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2473	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2474				HR2473 (Lo-16bits)
HR2475	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2476				HR2475 (Lo-16bits)
HR2477	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2478				HR2477 (Lo-16bits)
HR2479	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2480				HR2479 (Lo-16bits)
HR2481	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2482				HR2481 (Lo-16bits)
HR2483	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2484				HR2483 (Lo-16bits)
HR2485	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2486				HR2485 (Lo-16bits)
HR2487	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2488				HR2487 (Lo-16bits)
HR2489	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2490	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2491	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2492	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2493	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2494	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2495	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2496	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2497	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2498	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2499	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2500	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2501	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2502	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2503	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2504	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2505	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2506	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2507	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2508	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2509	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2510	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2511	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2512	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2513	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2514	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2515	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2516	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2517	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2518	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2519	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2520	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2521	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2528				HR2527 (Lo-16bits)
HR2529	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2530				HR2529 (Lo-16bits)
HR2531	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2532				HR2531 (Lo-16bits)
HR2533	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2534				HR2533 (Lo-16bits)
HR2535	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)
HR2537	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2538				HR2537 (Lo-16bits)
HR2539	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2552				HR2551 (Lo-16bits)
HR2553	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2556				HR2555 (Lo-16bits)
HR2557	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2558				HR2557 (Lo-16bits)
HR2559	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)
HR2565	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2568				HR2567 (Lo-16bits)
HR2569	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2576				HR2575 (Lo-16bits)
HR2577	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2580				HR2579 (Lo-16bits)
HR2581	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2582				HR2581 (Lo-16bits)
HR2583	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2586	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2587	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2588	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2589	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2590	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2591	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2592	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2593	NOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2594				HR2593 (Lo-16bits)
HR2595	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2598				HR2597 (Lo-16bits)
HR2599	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2600				HR2599 (Lo-16bits)
HR2601	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2604				HR2603 (Lo-16bits)
HR2605	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2606				HR2605 (Lo-16bits)
HR2607	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2608				HR2607 (Lo-16bits)
HR2609	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2610	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2611	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2612	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2613	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2614	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2615	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2616	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2617	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2618				HR2617 (Lo-16bits)
HR2619	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2620				HR2619 (Lo-16bits)
HR2621	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2622				HR2621 (Lo-16bits)
HR2623	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2624				HR2623 (Lo-16bits)
HR2625	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2628				HR2627 (Lo-16bits)
HR2629	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2630				HR2629 (Lo-16bits)
HR2631	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2632				HR2631 (Lo-16bits)
HR2633	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2634	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2635	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2636	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2637	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2638	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2639	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2640	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2641	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2642	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2643	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2644	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2645	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2646	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2647	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2648	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2649	NOC4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2650				HR2649 (Lo-16bits)
HR2651	NOC4:Delay type	RW	1	0=DT 1=IDMT
HR2652	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2653				HR2652 (Lo-16bits)
HR2654	NOC4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2655	NOC4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2656	NOC4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2657	NOC4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2658	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2659				HR2658 (Lo-16bits)
HR2660	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2661				HR2660 (Lo-16bits)
HR2662	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2663				HR2662 (Lo-16bits)
HR2664	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2665				HR2664 (Lo-16bits)
HR2666	NOC4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2667	NOC4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2668	ROC1:ldrw> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2669	ROC1:ldrw> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2670	ROC2:ldrw>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2671	ROC2:ldrw>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2672	ROC3:ldrw>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2673	ROC3:ldrw>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2674	ROC4:ldrw>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2675	ROC4:ldrw>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2676	ROC5:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2677	ROC5:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2678	ROC6:ldrw>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2679	ROC6:ldrw>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2680	ROC7:ldrw>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2681	ROC7:ldrw>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2682	ROC8:ldrw>>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2683	ROC8:ldrw>>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2684	ROV1:Urw>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2685	ROV2:Urw>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR2686	ROV3:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2687	ROV4:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2688	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2689	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2690	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled 1=Activated
HR2691	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=Start On (Alarm) 3=Start On (Alarm & Trip) 4=Start On (Trip) 5=Alarm On 6=Start On 7=Start On Alarm On 8=Trip On 9=Alarm On Trip On 10=Multistatus On
HR2692	DOC1:ldir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2693	DOC1:ldir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2694	DOC1:ldir> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2695	DOC1:ldir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DOC1:ldir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	DOC1:ldir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	DOC1:ldir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)
HR2703	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2704	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2705	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2706	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2707	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2708	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2709	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2710	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2711	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2726				HR2725 (Lo-16bits)
HR2727	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2728				HR2727 (Lo-16bits)
HR2729	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2730				HR2729 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2731	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2732				HR2731 (Lo-16bits)
HR2733	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2734				HR2733 (Lo-16bits)
HR2735	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2736				HR2735 (Lo-16bits)
HR2737	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2738				HR2737 (Lo-16bits)
HR2739	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2740				HR2739 (Lo-16bits)
HR2741	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2742				HR2741 (Lo-16bits)
HR2743	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2744				HR2743 (Lo-16bits)
HR2745	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2746				HR2745 (Lo-16bits)
HR2747	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2748				HR2747 (Lo-16bits)
HR2749	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2750				HR2749 (Lo-16bits)
HR2751	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2752				HR2751 (Lo-16bits)
HR2753	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2754				HR2753 (Lo-16bits)
HR2755	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2756				HR2755 (Lo-16bits)
HR2757	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2758				HR2757 (Lo-16bits)
HR2759	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2760	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2761	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2762	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2763	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2764	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2765	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2766	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2767	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2774				HR2773 (Lo-16bits)
HR2775	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2784	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2785	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2786	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2787	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2788	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2789	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2790	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2791	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2792	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2793	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2794	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2795	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2796	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2797	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2798	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2799	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2800	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2801	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2802	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2803	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2804	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2805	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2806	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2807	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2808	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2809	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2810	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2811	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2812	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2813	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2814	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2815	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2830				HR2829 (Lo-16bits)
HR2831	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2832				HR2831 (Lo-16bits)
HR2833	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)
HR2837	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2846				HR2845 (Lo-16bits)
HR2847	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2848				HR2847 (Lo-16bits)
HR2849	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2850				HR2849 (Lo-16bits)
HR2851	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2852				HR2851 (Lo-16bits)
HR2853	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)
HR2861	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2862				HR2861 (Lo-16bits)
HR2863	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2878				HR2877 (Lo-16bits)
HR2879	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2880	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2881	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2882	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2883	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2884	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2885	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2886	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2887	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2888				HR2887 (Lo-16bits)
HR2889	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2890				HR2889 (Lo-16bits)
HR2891	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2892				HR2891 (Lo-16bits)
HR2893	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2904	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2905	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2906	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2907	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2908	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2909	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2910	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2911	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2912				HR2911 (Lo-16bits)
HR2913	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2914				HR2913 (Lo-16bits)
HR2915	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2916				HR2915 (Lo-16bits)
HR2917	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2926				HR2925 (Lo-16bits)
HR2927	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2928	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2929	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2930	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2931	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2932	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2933	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2934	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2935	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2936	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2937	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2938	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2939	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2940	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2941	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2942	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2943	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2944	DOC2:ldir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2945	DOC2:ldir>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2946	DOC2:ldir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2947				HR2946 (Lo-16bits)
HR2948	DOC2:ldir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2949				HR2948 (Lo-16bits)
HR2950	DOC2:ldir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2951				HR2950 (Lo-16bits)
HR2952	DOC2:ldir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2953				HR2952 (Lo-16bits)
HR2954	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2955	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2956	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2957	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2958	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2959	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2960	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2961	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2962	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2963				HR2962 (Lo-16bits)
HR2964	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2965				HR2964 (Lo-16bits)
HR2966	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2967				HR2966 (Lo-16bits)
HR2968	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2969				HR2968 (Lo-16bits)
HR2970	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2971				HR2970 (Lo-16bits)
HR2972	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2973				HR2972 (Lo-16bits)
HR2974	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2975				HR2974 (Lo-16bits)
HR2976	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2977				HR2976 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2978	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2979				HR2978 (Lo-16bits)
HR2980	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2981				HR2980 (Lo-16bits)
HR2982	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2983				HR2982 (Lo-16bits)
HR2984	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2985				HR2984 (Lo-16bits)
HR2986	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2987				HR2986 (Lo-16bits)
HR2988	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2989				HR2988 (Lo-16bits)
HR2990	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2991				HR2990 (Lo-16bits)
HR2992	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2993				HR2992 (Lo-16bits)
HR2994	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2995				HR2994 (Lo-16bits)
HR2996	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2997				HR2996 (Lo-16bits)
HR2998	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2999				HR2998 (Lo-16bits)
HR3000	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3001				HR3000 (Lo-16bits)
HR3002	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3003				HR3002 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3004	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3005				HR3004 (Lo-16bits)
HR3006	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3007				HR3006 (Lo-16bits)
HR3008	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3009				HR3008 (Lo-16bits)
HR3010	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3011	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3012	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3013	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3014	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3015	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3016	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3017	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3018	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3019				HR3018 (Lo-16bits)
HR3020	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3021				HR3020 (Lo-16bits)
HR3022	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3023				HR3022 (Lo-16bits)
HR3024	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3025				HR3024 (Lo-16bits)
HR3026	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3027				HR3026 (Lo-16bits)
HR3028	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3029				HR3028 (Lo-16bits)
HR3030	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3031				HR3030 (Lo-16bits)
HR3032	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3033				HR3032 (Lo-16bits)
HR3034	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3035	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3036	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3037	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3038	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3039	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3040	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3041	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3042	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3043	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3044	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3045	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3046	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3047	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3048	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3049	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3050	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3051	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3052	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3053	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3054	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3055	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3056	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3057	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3058	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3059	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3060	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3061	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3062	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3063	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3064	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3065	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3066	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3067				HR3066 (Lo-16bits)
HR3068	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3069				HR3068 (Lo-16bits)
HR3070	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3071				HR3070 (Lo-16bits)
HR3072	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3073				HR3072 (Lo-16bits)
HR3074	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3075				HR3074 (Lo-16bits)
HR3076	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3077				HR3076 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3078	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3079				HR3078 (Lo-16bits)
HR3080	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3081				HR3080 (Lo-16bits)
HR3082	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3083				HR3082 (Lo-16bits)
HR3084	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3085				HR3084 (Lo-16bits)
HR3086	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3087				HR3086 (Lo-16bits)
HR3088	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3089				HR3088 (Lo-16bits)
HR3090	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3091				HR3090 (Lo-16bits)
HR3092	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3093				HR3092 (Lo-16bits)
HR3094	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3095				HR3094 (Lo-16bits)
HR3096	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3097				HR3096 (Lo-16bits)
HR3098	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3099				HR3098 (Lo-16bits)
HR3100	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3101				HR3100 (Lo-16bits)
HR3102	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3103				HR3102 (Lo-16bits)
HR3104	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3105				HR3104 (Lo-16bits)
HR3106	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3107				HR3106 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3108	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3109				HR3108 (Lo-16bits)
HR3110	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3111				HR3110 (Lo-16bits)
HR3112	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3113				HR3112 (Lo-16bits)
HR3114	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3115				HR3114 (Lo-16bits)
HR3116	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3117				HR3116 (Lo-16bits)
HR3118	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3119				HR3118 (Lo-16bits)
HR3120	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3121				HR3120 (Lo-16bits)
HR3122	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3123				HR3122 (Lo-16bits)
HR3124	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3125				HR3124 (Lo-16bits)
HR3126	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3127				HR3126 (Lo-16bits)
HR3128	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3129				HR3128 (Lo-16bits)
HR3130	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3131	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3132	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3133	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3134	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3135	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3136	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3137	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3138	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3139				HR3138 (Lo-16bits)
HR3140	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3141				HR3140 (Lo-16bits)
HR3142	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3143				HR3142 (Lo-16bits)
HR3144	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3145				HR3144 (Lo-16bits)
HR3146	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3147				HR3146 (Lo-16bits)
HR3148	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3149				HR3148 (Lo-16bits)
HR3150	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3151				HR3150 (Lo-16bits)
HR3152	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3153				HR3152 (Lo-16bits)
HR3154	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3155	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3156	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3157	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3158	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3159	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3160	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3161	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3162	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3163				HR3162 (Lo-16bits)
HR3164	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3165				HR3164 (Lo-16bits)
HR3166	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3167				HR3166 (Lo-16bits)
HR3168	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3169				HR3168 (Lo-16bits)
HR3170	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3171				HR3170 (Lo-16bits)
HR3172	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3173				HR3172 (Lo-16bits)
HR3174	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3175				HR3174 (Lo-16bits)
HR3176	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3177				HR3176 (Lo-16bits)
HR3178	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3179	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3180	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3181	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3182	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3183	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3184	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3185	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3186	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3187	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3188	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3189	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3190	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3191	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3192	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3193	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3194	DOC3:Idir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3195	DOC3:Idir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3196	DOC3:Idir>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3197	DOC3:Idir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	DOC3:Idir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3201	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3206	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3207	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3208	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3209	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3210	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3211	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3212	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3213	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3223	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3230				HR3229 (Lo-16bits)
HR3231	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3232				HR3231 (Lo-16bits)
HR3233	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3234				HR3233 (Lo-16bits)
HR3235	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3236				HR3235 (Lo-16bits)
HR3237	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3238				HR3237 (Lo-16bits)
HR3239	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3240				HR3239 (Lo-16bits)
HR3241	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3242				HR3241 (Lo-16bits)
HR3243	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3244				HR3243 (Lo-16bits)
HR3245	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3246				HR3245 (Lo-16bits)
HR3247	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3248				HR3247 (Lo-16bits)
HR3249	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3250				HR3249 (Lo-16bits)
HR3251	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3252				HR3251 (Lo-16bits)
HR3253	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3254				HR3253 (Lo-16bits)
HR3255	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3256				HR3255 (Lo-16bits)
HR3257	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3258				HR3257 (Lo-16bits)
HR3259	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3262	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3263	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3264	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3265	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3266	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3267	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3268	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3269	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3276				HR3275 (Lo-16bits)
HR3277	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3286	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3287	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3288	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3289	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3290	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3291	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3292	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3293	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3294	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3295	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3296	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3297	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3298	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3299	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3300	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3301	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3302	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3303	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3304	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3305	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3306	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3307	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3308	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3309	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3310	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3311	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3312	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3313	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3314	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3315	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3316	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3317	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3322				HR3321 (Lo-16bits)
HR3323	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3326				HR3325 (Lo-16bits)
HR3327	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3328				HR3327 (Lo-16bits)
HR3329	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3330				HR3329 (Lo-16bits)
HR3331	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)
HR3347	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3348				HR3347 (Lo-16bits)
HR3349	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3350				HR3349 (Lo-16bits)
HR3351	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3352				HR3351 (Lo-16bits)
HR3353	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3354				HR3353 (Lo-16bits)
HR3355	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3374				HR3373 (Lo-16bits)
HR3375	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3376				HR3375 (Lo-16bits)
HR3377	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3378				HR3377 (Lo-16bits)
HR3379	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3380				HR3379 (Lo-16bits)
HR3381	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3382	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3383	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3384	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3385	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3386	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3387	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3388	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3389	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3390				HR3389 (Lo-16bits)
HR3391	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3392				HR3391 (Lo-16bits)
HR3393	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3394				HR3393 (Lo-16bits)
HR3395	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3396				HR3395 (Lo-16bits)
HR3397	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3405	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3406	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3407	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3408	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3409	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3410	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3411	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3412	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3413	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)
HR3415	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3430	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3431	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3432	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3433	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3434	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3435	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3436	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3437	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3438	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3439	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3440	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3441	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3442	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3443	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3444	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3445	DOC4:Idir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3446	DOC4:Idir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3447	DOC4:Idir>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C

Holding Register	Parameter	Access	Length	Info
HR3448	DOC4:ldir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3449				HR3448 (Lo-16bits)
HR3450	DOC4:ldir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3451				HR3450 (Lo-16bits)
HR3452	DOC4:ldir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3453				HR3452 (Lo-16bits)
HR3454	DOC4:ldir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3455				HR3454 (Lo-16bits)
HR3456	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3457	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3458	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3459	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3460	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3461	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3462	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3463	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3464	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3465				HR3464 (Lo-16bits)
HR3466	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3467				HR3466 (Lo-16bits)
HR3468	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3469				HR3468 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3470	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3471				HR3470 (Lo-16bits)
HR3472	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3473				HR3472 (Lo-16bits)
HR3474	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3475				HR3474 (Lo-16bits)
HR3476	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3477				HR3476 (Lo-16bits)
HR3478	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3479				HR3478 (Lo-16bits)
HR3480	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3481				HR3480 (Lo-16bits)
HR3482	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3483				HR3482 (Lo-16bits)
HR3484	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3485				HR3484 (Lo-16bits)
HR3486	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3487				HR3486 (Lo-16bits)
HR3488	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3489				HR3488 (Lo-16bits)
HR3490	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3491				HR3490 (Lo-16bits)
HR3492	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3493				HR3492 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3494	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3495				HR3494 (Lo-16bits)
HR3496	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3497				HR3496 (Lo-16bits)
HR3498	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3499				HR3498 (Lo-16bits)
HR3500	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3501				HR3500 (Lo-16bits)
HR3502	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3503				HR3502 (Lo-16bits)
HR3504	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3505				HR3504 (Lo-16bits)
HR3506	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3507				HR3506 (Lo-16bits)
HR3508	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3509				HR3508 (Lo-16bits)
HR3510	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3511				HR3510 (Lo-16bits)
HR3512	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3513	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3514	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3515	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3516	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3517	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3518	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3519	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3520	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3521				HR3520 (Lo-16bits)
HR3522	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3523				HR3522 (Lo-16bits)
HR3524	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3525				HR3524 (Lo-16bits)
HR3526	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3527				HR3526 (Lo-16bits)
HR3528	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3529				HR3528 (Lo-16bits)
HR3530	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3531				HR3530 (Lo-16bits)
HR3532	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3533				HR3532 (Lo-16bits)
HR3534	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3535				HR3534 (Lo-16bits)
HR3536	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3537	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3538	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3539	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3540	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3541	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3542	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3543	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3544	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3545	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3546	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3547	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3548	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3549	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3550	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3551	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3552	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3553	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3554	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3555	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3556	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3557	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3558	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3559	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3560	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3561	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3562	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3563	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3564	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3565	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3566	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3567	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3568	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3569				HR3568 (Lo-16bits)
HR3570	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3571				HR3570 (Lo-16bits)
HR3572	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3573				HR3572 (Lo-16bits)
HR3574	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3575				HR3574 (Lo-16bits)
HR3576	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3577				HR3576 (Lo-16bits)
HR3578	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3579				HR3578 (Lo-16bits)
HR3580	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3581				HR3580 (Lo-16bits)
HR3582	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3583				HR3582 (Lo-16bits)
HR3584	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3585				HR3584 (Lo-16bits)
HR3586	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3587				HR3586 (Lo-16bits)
HR3588	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3589				HR3588 (Lo-16bits)
HR3590	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3591				HR3590 (Lo-16bits)
HR3592	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3593				HR3592 (Lo-16bits)
HR3594	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3595				HR3594 (Lo-16bits)
HR3596	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3597				HR3596 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3598	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3599				HR3598 (Lo-16bits)
HR3600	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3601				HR3600 (Lo-16bits)
HR3602	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3603				HR3602 (Lo-16bits)
HR3604	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3605				HR3604 (Lo-16bits)
HR3606	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3607				HR3606 (Lo-16bits)
HR3608	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3609				HR3608 (Lo-16bits)
HR3610	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3611				HR3610 (Lo-16bits)
HR3612	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3613				HR3612 (Lo-16bits)
HR3614	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3615				HR3614 (Lo-16bits)
HR3616	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3617				HR3616 (Lo-16bits)
HR3618	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3619				HR3618 (Lo-16bits)
HR3620	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3621				HR3620 (Lo-16bits)
HR3622	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3623				HR3622 (Lo-16bits)
HR3624	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3625				HR3624 (Lo-16bits)
HR3626	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3627				HR3626 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3628	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3629				HR3628 (Lo-16bits)
HR3630	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3631				HR3630 (Lo-16bits)
HR3632	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3633	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3634	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3635	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3636	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3637	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3638	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3639	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3640	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3641				HR3640 (Lo-16bits)
HR3642	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3643				HR3642 (Lo-16bits)
HR3644	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3645				HR3644 (Lo-16bits)
HR3646	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3647				HR3646 (Lo-16bits)
HR3648	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3649				HR3648 (Lo-16bits)
HR3650	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3651				HR3650 (Lo-16bits)
HR3652	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3653				HR3652 (Lo-16bits)
HR3654	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3655				HR3654 (Lo-16bits)
HR3656	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3657	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3658	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3659	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3660	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3661	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3662	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3663	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3664	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3665				HR3664 (Lo-16bits)
HR3666	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3667				HR3666 (Lo-16bits)
HR3668	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3669				HR3668 (Lo-16bits)
HR3670	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3671				HR3670 (Lo-16bits)
HR3672	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3673				HR3672 (Lo-16bits)
HR3674	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3675				HR3674 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3676	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3677				HR3676 (Lo-16bits)
HR3678	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3679				HR3678 (Lo-16bits)
HR3680	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3681	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3682	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3683	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3684	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3685	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3686	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3687	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3688	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3689	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3690	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3691	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3692	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3693	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3694	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3695	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3696	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3697	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3698	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3699	NEF1:I0> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3700	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3701				HR3700 (Lo-16bits)
HR3702	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3703				HR3702 (Lo-16bits)
HR3704	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3705				HR3704 (Lo-16bits)
HR3706	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3707				HR3706 (Lo-16bits)
HR3708	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3709				HR3708 (Lo-16bits)
HR3710	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3711				HR3710 (Lo-16bits)
HR3712	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3713				HR3712 (Lo-16bits)
HR3714	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3715				HR3714 (Lo-16bits)
HR3716	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3717				HR3716 (Lo-16bits)
HR3718	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3719	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3720	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3721	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3722	NEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3723	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3724	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3725	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3726	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3727				HR3726 (Lo-16bits)
HR3728	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3729				HR3728 (Lo-16bits)
HR3730	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3731				HR3730 (Lo-16bits)
HR3732	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3733				HR3732 (Lo-16bits)
HR3734	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3735				HR3734 (Lo-16bits)
HR3736	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3737				HR3736 (Lo-16bits)
HR3738	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3739				HR3738 (Lo-16bits)
HR3740	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3741				HR3740 (Lo-16bits)
HR3742	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3743	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3744	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3745	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3746	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3747	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3748	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3749	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3750	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3751	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3752	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3753	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3754	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3755	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3756	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3757	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3758	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3759	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3760	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3761	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3762	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3763	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3764	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3765	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3766	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3767	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3768	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3769	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3770	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3771	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3772	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3773	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3774	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3775				HR3774 (Lo-16bits)
HR3776	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3777				HR3776 (Lo-16bits)
HR3778	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3779				HR3778 (Lo-16bits)
HR3780	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3781				HR3780 (Lo-16bits)
HR3782	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3783				HR3782 (Lo-16bits)
HR3784	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3785				HR3784 (Lo-16bits)
HR3786	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3787				HR3786 (Lo-16bits)
HR3788	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3789				HR3788 (Lo-16bits)
HR3790	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3791				HR3790 (Lo-16bits)
HR3792	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3793				HR3792 (Lo-16bits)
HR3794	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3795				HR3794 (Lo-16bits)
HR3796	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3797				HR3796 (Lo-16bits)
HR3798	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3799				HR3798 (Lo-16bits)
HR3800	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3801				HR3800 (Lo-16bits)
HR3802	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3803				HR3802 (Lo-16bits)
HR3804	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3805				HR3804 (Lo-16bits)
HR3806	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3807				HR3806 (Lo-16bits)
HR3808	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3809				HR3808 (Lo-16bits)
HR3810	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3811				HR3810 (Lo-16bits)
HR3812	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3813				HR3812 (Lo-16bits)
HR3814	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3815				HR3814 (Lo-16bits)
HR3816	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3817				HR3816 (Lo-16bits)
HR3818	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3819				HR3818 (Lo-16bits)
HR3820	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3821				HR3820 (Lo-16bits)
HR3822	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3823				HR3822 (Lo-16bits)
HR3824	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3825				HR3824 (Lo-16bits)
HR3826	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3827				HR3826 (Lo-16bits)
HR3828	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3829				HR3828 (Lo-16bits)
HR3830	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3831				HR3830 (Lo-16bits)
HR3832	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3833				HR3832 (Lo-16bits)
HR3834	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3835				HR3834 (Lo-16bits)
HR3836	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3837				HR3836 (Lo-16bits)
HR3838	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3839	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3840	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3841	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3842	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3843	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3844	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3845	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3846	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3847				HR3846 (Lo-16bits)
HR3848	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3849				HR3848 (Lo-16bits)
HR3850	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3851				HR3850 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3852	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3853				HR3852 (Lo-16bits)
HR3854	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3855				HR3854 (Lo-16bits)
HR3856	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3857				HR3856 (Lo-16bits)
HR3858	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3859				HR3858 (Lo-16bits)
HR3860	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3861				HR3860 (Lo-16bits)
HR3862	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3863	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3864	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3865	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3866	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3867	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3868	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3869	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3870	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3871				HR3870 (Lo-16bits)
HR3872	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3873				HR3872 (Lo-16bits)
HR3874	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3875				HR3874 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3876	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3877				HR3876 (Lo-16bits)
HR3878	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3879				HR3878 (Lo-16bits)
HR3880	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3881				HR3880 (Lo-16bits)
HR3882	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3883				HR3882 (Lo-16bits)
HR3884	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3885				HR3884 (Lo-16bits)
HR3886	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3887	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3888	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3889	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3890	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3891	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3892	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3893	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3894	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3895	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3896	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3897	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3898	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3899	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3900	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3901	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3902	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR3903				HR3902 (Lo-16bits)
HR3904	NEF1:Delay type	RW	1	0=DT 1=IDMT
HR3905	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	NEF1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR3908	NEF1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3909	NEF1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3910	NEF1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR3911	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR3912				HR3911 (Lo-16bits)
HR3913	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR3914				HR3913 (Lo-16bits)
HR3915	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR3916				HR3915 (Lo-16bits)
HR3917	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	NEF1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR3920	NEF1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR3921	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3922	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3923	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3924	NEF2:I0>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3925	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3926				HR3925 (Lo-16bits)
HR3927	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3928				HR3927 (Lo-16bits)
HR3929	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3930				HR3929 (Lo-16bits)
HR3931	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3932				HR3931 (Lo-16bits)
HR3933	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3934				HR3933 (Lo-16bits)
HR3935	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3936				HR3935 (Lo-16bits)
HR3937	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3938				HR3937 (Lo-16bits)
HR3939	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3940				HR3939 (Lo-16bits)
HR3941	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3944	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3945	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3946	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3947	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3948	NEF2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3949	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3950	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3951	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)
HR3965	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3966				HR3965 (Lo-16bits)
HR3967	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3968	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3969	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3970	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3971	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3972	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3973	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3974	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3975	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3976	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3977	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3978	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3979	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3980	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3981	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3982	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3983	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3984	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3985	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3986	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3987	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3988	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3989	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3990	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3991	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3992	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3993	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3994	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3995	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3996	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3997	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3998	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3999	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)
HR4007	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4023	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4030				HR4029 (Lo-16bits)
HR4031	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4032				HR4031 (Lo-16bits)
HR4033	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4034				HR4033 (Lo-16bits)
HR4035	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4036				HR4035 (Lo-16bits)
HR4037	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4053	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4054				HR4053 (Lo-16bits)
HR4055	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4056				HR4055 (Lo-16bits)
HR4057	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4058				HR4057 (Lo-16bits)
HR4059	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4060				HR4059 (Lo-16bits)
HR4061	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4062				HR4061 (Lo-16bits)
HR4063	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4064	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4065	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4066	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4067	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4068	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4069	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4070	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4071	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4072				HR4071 (Lo-16bits)
HR4073	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4074				HR4073 (Lo-16bits)
HR4075	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4076				HR4075 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4077	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4078				HR4077 (Lo-16bits)
HR4079	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4080				HR4079 (Lo-16bits)
HR4081	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4082				HR4081 (Lo-16bits)
HR4083	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4084				HR4083 (Lo-16bits)
HR4085	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4088	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4089	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4090	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4091	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4092	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4093	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4094	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4095	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4096				HR4095 (Lo-16bits)
HR4097	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4098				HR4097 (Lo-16bits)
HR4099	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4100				HR4099 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4101	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4102				HR4101 (Lo-16bits)
HR4103	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4104				HR4103 (Lo-16bits)
HR4105	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4106				HR4105 (Lo-16bits)
HR4107	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4108				HR4107 (Lo-16bits)
HR4109	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4110				HR4109 (Lo-16bits)
HR4111	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4112	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4113	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4114	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4115	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4116	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4117	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4118	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4119	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4120	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4121	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4122	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4123	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4124	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4125	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4126	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4127	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	NEF2:Delay type	RW	1	0=DT 1=IDMT
HR4130	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4131				HR4130 (Lo-16bits)
HR4132	NEF2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4133	NEF2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4134	NEF2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4135	NEF2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4136	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4137				HR4136 (Lo-16bits)
HR4138	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4139				HR4138 (Lo-16bits)
HR4140	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4141				HR4140 (Lo-16bits)
HR4142	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4143				HR4142 (Lo-16bits)
HR4144	NEF2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4145	NEF2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4146	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4147	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4148	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4149	NEF3:I0>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4150	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4151				HR4150 (Lo-16bits)
HR4152	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4153				HR4152 (Lo-16bits)
HR4154	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4155				HR4154 (Lo-16bits)
HR4156	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4157				HR4156 (Lo-16bits)
HR4158	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4159				HR4158 (Lo-16bits)
HR4160	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4161				HR4160 (Lo-16bits)
HR4162	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4163				HR4162 (Lo-16bits)
HR4164	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4165				HR4164 (Lo-16bits)
HR4166	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4167				HR4166 (Lo-16bits)
HR4168	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4169	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4170	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4171	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4172	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4173	NEF3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4174	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4175	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4176	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4177				HR4176 (Lo-16bits)
HR4178	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4179				HR4178 (Lo-16bits)
HR4180	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4181				HR4180 (Lo-16bits)
HR4182	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4183				HR4182 (Lo-16bits)
HR4184	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4185				HR4184 (Lo-16bits)
HR4186	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4187				HR4186 (Lo-16bits)
HR4188	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4189				HR4188 (Lo-16bits)
HR4190	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4191				HR4190 (Lo-16bits)
HR4192	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4193	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4194	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4195	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4196	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4197	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4198	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4199	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4200	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4201	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4202	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4203	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4204	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4205	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4206	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4207	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4208	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4209	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4210	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4211	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4212	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4213	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4214	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4215	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4216	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4217	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4218	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4219	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4220	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4221	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4222	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4223	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4224	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4225				HR4224 (Lo-16bits)
HR4226	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4227				HR4226 (Lo-16bits)
HR4228	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4229				HR4228 (Lo-16bits)
HR4230	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4231				HR4230 (Lo-16bits)
HR4232	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4233				HR4232 (Lo-16bits)
HR4234	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4235				HR4234 (Lo-16bits)
HR4236	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4237				HR4236 (Lo-16bits)
HR4238	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4239				HR4238 (Lo-16bits)
HR4240	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4241				HR4240 (Lo-16bits)
HR4242	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4243				HR4242 (Lo-16bits)
HR4244	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4245				HR4244 (Lo-16bits)
HR4246	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4247				HR4246 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4248	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4249				HR4248 (Lo-16bits)
HR4250	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4251				HR4250 (Lo-16bits)
HR4252	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4253				HR4252 (Lo-16bits)
HR4254	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4255				HR4254 (Lo-16bits)
HR4256	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4257				HR4256 (Lo-16bits)
HR4258	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4259				HR4258 (Lo-16bits)
HR4260	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4261				HR4260 (Lo-16bits)
HR4262	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4263				HR4262 (Lo-16bits)
HR4264	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4265				HR4264 (Lo-16bits)
HR4266	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4267				HR4266 (Lo-16bits)
HR4268	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4269				HR4268 (Lo-16bits)
HR4270	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4271				HR4270 (Lo-16bits)
HR4272	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4273				HR4272 (Lo-16bits)
HR4274	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4275				HR4274 (Lo-16bits)
HR4276	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4277				HR4276 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4278	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4279				HR4278 (Lo-16bits)
HR4280	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4281				HR4280 (Lo-16bits)
HR4282	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4283				HR4282 (Lo-16bits)
HR4284	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4285				HR4284 (Lo-16bits)
HR4286	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4287				HR4286 (Lo-16bits)
HR4288	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4289	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4290	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4291	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4292	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4293	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4294	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4295	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4296	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4297				HR4296 (Lo-16bits)
HR4298	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4299				HR4298 (Lo-16bits)
HR4300	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4301				HR4300 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4302	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4303				HR4302 (Lo-16bits)
HR4304	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4305				HR4304 (Lo-16bits)
HR4306	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4307				HR4306 (Lo-16bits)
HR4308	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4309				HR4308 (Lo-16bits)
HR4310	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4311				HR4310 (Lo-16bits)
HR4312	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4313	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4314	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4315	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4316	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4317	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4318	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4319	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4320	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4321				HR4320 (Lo-16bits)
HR4322	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4323				HR4322 (Lo-16bits)
HR4324	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4325				HR4324 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4326	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4327				HR4326 (Lo-16bits)
HR4328	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4329				HR4328 (Lo-16bits)
HR4330	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4331				HR4330 (Lo-16bits)
HR4332	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4333				HR4332 (Lo-16bits)
HR4334	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4335				HR4334 (Lo-16bits)
HR4336	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4337	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4338	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4339	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4340	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4341	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4342	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4343	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4344	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4345	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4346	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4347	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4348	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4349	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4350	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4351	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4352	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4353				HR4352 (Lo-16bits)
HR4354	NEF3:Delay type	RW	1	0=DT 1=IDMT
HR4355	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4358	NEF3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4359	NEF3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4360	NEF3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4361	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4366				HR4365 (Lo-16bits)
HR4367	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4368				HR4367 (Lo-16bits)
HR4369	NEF3>Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4370	NEF3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4371	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4372	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4373	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4374	NEF4:I0>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4375	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4382				HR4381 (Lo-16bits)
HR4383	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4390				HR4389 (Lo-16bits)
HR4391	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4394	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4395	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4396	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4397	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4398	NEF4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4399	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4400	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4401	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)
HR4415	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4418	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4419	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4420	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4421	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4422	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4423	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4424	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4425	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4426	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4427	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4428	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4429	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4430	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4431	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4432	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4433	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4434	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4435	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4436	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4437	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4438	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4439	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4440	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4441	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4442	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4443	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4444	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4445	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4446	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4447	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4448	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4449	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)
HR4463	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4473	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4486				HR4485 (Lo-16bits)
HR4487	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)
HR4493	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4503	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4504				HR4503 (Lo-16bits)
HR4505	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4506				HR4505 (Lo-16bits)
HR4507	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4508				HR4507 (Lo-16bits)
HR4509	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4510				HR4509 (Lo-16bits)
HR4511	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4512				HR4511 (Lo-16bits)
HR4513	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4514	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4515	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4516	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4517	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4518	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4519	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4520	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4521	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4522				HR4521 (Lo-16bits)
HR4523	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4524				HR4523 (Lo-16bits)
HR4525	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4527	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4534				HR4533 (Lo-16bits)
HR4535	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4536				HR4535 (Lo-16bits)
HR4537	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4538	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4539	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4540	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4541	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4542	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4543	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4544	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4545	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4546				HR4545 (Lo-16bits)
HR4547	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4548				HR4547 (Lo-16bits)
HR4549	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4551	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4562	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4563	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4564	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4565	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4566	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4567	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4568	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4569	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4570	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4571	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4572	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4573	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4574	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4575	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4576	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4577	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF4:Delay type	RW	1	0=DT 1=IDMT
HR4580	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4581				HR4580 (Lo-16bits)
HR4582	NEF4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4583	NEF4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4584	NEF4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4585	NEF4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4586	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4587				HR4586 (Lo-16bits)
HR4588	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4589				HR4588 (Lo-16bits)
HR4590	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4591				HR4590 (Lo-16bits)
HR4592	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4593				HR4592 (Lo-16bits)
HR4594	NEF4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4595	NEF4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4596	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4597	DEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4598	DEF1:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4599	DEF1:I0dir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4600	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4601				HR4600 (Lo-16bits)
HR4602	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4603				HR4602 (Lo-16bits)
HR4604	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4605				HR4604 (Lo-16bits)
HR4606	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4607	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4608	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4609	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4610	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4611	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4612	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4613	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4614	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4615				HR4614 (Lo-16bits)
HR4616	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4617				HR4616 (Lo-16bits)
HR4618	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4619				HR4618 (Lo-16bits)
HR4620	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4621				HR4620 (Lo-16bits)
HR4622	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4623				HR4622 (Lo-16bits)
HR4624	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4625				HR4624 (Lo-16bits)
HR4626	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4627				HR4626 (Lo-16bits)
HR4628	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4629				HR4628 (Lo-16bits)
HR4630	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4631				HR4630 (Lo-16bits)
HR4632	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4633				HR4632 (Lo-16bits)
HR4634	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4635				HR4634 (Lo-16bits)
HR4636	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4637				HR4636 (Lo-16bits)
HR4638	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4639				HR4638 (Lo-16bits)
HR4640	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4641				HR4640 (Lo-16bits)
HR4642	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4643				HR4642 (Lo-16bits)
HR4644	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4645				HR4644 (Lo-16bits)
HR4646	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4647				HR4646 (Lo-16bits)
HR4648	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4649				HR4648 (Lo-16bits)
HR4650	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4651				HR4650 (Lo-16bits)
HR4652	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4653				HR4652 (Lo-16bits)
HR4654	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4655				HR4654 (Lo-16bits)
HR4656	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4657				HR4656 (Lo-16bits)
HR4658	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4659				HR4658 (Lo-16bits)
HR4660	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4661				HR4660 (Lo-16bits)
HR4662	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4663				HR4662 (Lo-16bits)
HR4664	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4665				HR4664 (Lo-16bits)
HR4666	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4667				HR4666 (Lo-16bits)
HR4668	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4669				HR4668 (Lo-16bits)
HR4670	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4671				HR4670 (Lo-16bits)
HR4672	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4673				HR4672 (Lo-16bits)
HR4674	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4675				HR4674 (Lo-16bits)
HR4676	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4677				HR4676 (Lo-16bits)
HR4678	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4679				HR4678 (Lo-16bits)
HR4680	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4681				HR4680 (Lo-16bits)
HR4682	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4683				HR4682 (Lo-16bits)
HR4684	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4685				HR4684 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4686	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4687				HR4686 (Lo-16bits)
HR4688	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4689				HR4688 (Lo-16bits)
HR4690	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4691				HR4690 (Lo-16bits)
HR4692	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4693				HR4692 (Lo-16bits)
HR4694	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4695				HR4694 (Lo-16bits)
HR4696	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4697				HR4696 (Lo-16bits)
HR4698	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4699				HR4698 (Lo-16bits)
HR4700	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4701				HR4700 (Lo-16bits)
HR4702	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4703				HR4702 (Lo-16bits)
HR4704	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4705				HR4704 (Lo-16bits)
HR4706	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4707				HR4706 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4708	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4709				HR4708 (Lo-16bits)
HR4710	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4711	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4712	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4713	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4714	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4715	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4716	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4717	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4718	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4719				HR4718 (Lo-16bits)
HR4720	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4721				HR4720 (Lo-16bits)
HR4722	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4723				HR4722 (Lo-16bits)
HR4724	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4725				HR4724 (Lo-16bits)
HR4726	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4727				HR4726 (Lo-16bits)
HR4728	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4729				HR4728 (Lo-16bits)
HR4730	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4731				HR4730 (Lo-16bits)
HR4732	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4733				HR4732 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4734	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4735				HR4734 (Lo-16bits)
HR4736	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4737				HR4736 (Lo-16bits)
HR4738	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4739				HR4738 (Lo-16bits)
HR4740	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4741				HR4740 (Lo-16bits)
HR4742	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4743				HR4742 (Lo-16bits)
HR4744	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4745				HR4744 (Lo-16bits)
HR4746	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4747				HR4746 (Lo-16bits)
HR4748	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4749				HR4748 (Lo-16bits)
HR4750	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4751				HR4750 (Lo-16bits)
HR4752	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4753				HR4752 (Lo-16bits)
HR4754	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4755				HR4754 (Lo-16bits)
HR4756	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4757				HR4756 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4758	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4759				HR4758 (Lo-16bits)
HR4760	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4761				HR4760 (Lo-16bits)
HR4762	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4763				HR4762 (Lo-16bits)
HR4764	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4765				HR4764 (Lo-16bits)
HR4766	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4767				HR4766 (Lo-16bits)
HR4768	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4769				HR4768 (Lo-16bits)
HR4770	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4771				HR4770 (Lo-16bits)
HR4772	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4773				HR4772 (Lo-16bits)
HR4774	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4775				HR4774 (Lo-16bits)
HR4776	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4777				HR4776 (Lo-16bits)
HR4778	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4779				HR4778 (Lo-16bits)
HR4780	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4781				HR4780 (Lo-16bits)
HR4782	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4783				HR4782 (Lo-16bits)
HR4784	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4785				HR4784 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4786	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4787				HR4786 (Lo-16bits)
HR4788	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4789				HR4788 (Lo-16bits)
HR4790	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4791				HR4790 (Lo-16bits)
HR4792	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4793				HR4792 (Lo-16bits)
HR4794	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4795				HR4794 (Lo-16bits)
HR4796	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4797				HR4796 (Lo-16bits)
HR4798	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4799	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4800	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4801	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4802	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4803	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4804	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4805	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR4806	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4807	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4808	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4809	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4810	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4811	DEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4812	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4813	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4814	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4815				HR4814 (Lo-16bits)
HR4816	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4817				HR4816 (Lo-16bits)
HR4818	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4819				HR4818 (Lo-16bits)
HR4820	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4821				HR4820 (Lo-16bits)
HR4822	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4823				HR4822 (Lo-16bits)
HR4824	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4825				HR4824 (Lo-16bits)
HR4826	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4827				HR4826 (Lo-16bits)
HR4828	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4829				HR4828 (Lo-16bits)
HR4830	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4831				HR4830 (Lo-16bits)
HR4832	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4833				HR4832 (Lo-16bits)
HR4834	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4835				HR4834 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4836	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4837				HR4836 (Lo-16bits)
HR4838	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4839				HR4838 (Lo-16bits)
HR4840	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4841				HR4840 (Lo-16bits)
HR4842	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4843				HR4842 (Lo-16bits)
HR4844	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4845				HR4844 (Lo-16bits)
HR4846	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4847				HR4846 (Lo-16bits)
HR4848	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4849				HR4848 (Lo-16bits)
HR4850	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4851				HR4850 (Lo-16bits)
HR4852	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4853				HR4852 (Lo-16bits)
HR4854	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4855				HR4854 (Lo-16bits)
HR4856	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4857				HR4856 (Lo-16bits)
HR4858	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4859				HR4858 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4860	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4861				HR4860 (Lo-16bits)
HR4862	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4863	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4864	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4865	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4866	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4867	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4868	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4869	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4870	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4871	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4872	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4873	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4874	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4875	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4876	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4877	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4878	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4879	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4880	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4881	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4882	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4883	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4884	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4885	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4886	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4887	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4888	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4889	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4890	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4891	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4892	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4893	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4894	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4895				HR4894 (Lo-16bits)
HR4896	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4897				HR4896 (Lo-16bits)
HR4898	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4899				HR4898 (Lo-16bits)
HR4900	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4901				HR4900 (Lo-16bits)
HR4902	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4903				HR4902 (Lo-16bits)
HR4904	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4905				HR4904 (Lo-16bits)
HR4906	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4907				HR4906 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4908	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4909				HR4908 (Lo-16bits)
HR4910	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4911				HR4910 (Lo-16bits)
HR4912	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4913				HR4912 (Lo-16bits)
HR4914	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4915				HR4914 (Lo-16bits)
HR4916	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4917				HR4916 (Lo-16bits)
HR4918	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4919				HR4918 (Lo-16bits)
HR4920	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4921				HR4920 (Lo-16bits)
HR4922	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4923				HR4922 (Lo-16bits)
HR4924	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4925				HR4924 (Lo-16bits)
HR4926	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4927				HR4926 (Lo-16bits)
HR4928	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4929				HR4928 (Lo-16bits)
HR4930	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4931				HR4930 (Lo-16bits)
HR4932	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4933				HR4932 (Lo-16bits)
HR4934	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4935				HR4934 (Lo-16bits)
HR4936	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4937				HR4936 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4938	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4939				HR4938 (Lo-16bits)
HR4940	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4941				HR4940 (Lo-16bits)
HR4942	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4943				HR4942 (Lo-16bits)
HR4944	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4945				HR4944 (Lo-16bits)
HR4946	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4947				HR4946 (Lo-16bits)
HR4948	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4949				HR4948 (Lo-16bits)
HR4950	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4951				HR4950 (Lo-16bits)
HR4952	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4953				HR4952 (Lo-16bits)
HR4954	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4955				HR4954 (Lo-16bits)
HR4956	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4957				HR4956 (Lo-16bits)
HR4958	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4959	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4960	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4961	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4962	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4963	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4964	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4965	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4966	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4967				HR4966 (Lo-16bits)
HR4968	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4969				HR4968 (Lo-16bits)
HR4970	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4971				HR4970 (Lo-16bits)
HR4972	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4973				HR4972 (Lo-16bits)
HR4974	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4975				HR4974 (Lo-16bits)
HR4976	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4977				HR4976 (Lo-16bits)
HR4978	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4979				HR4978 (Lo-16bits)
HR4980	DEF1:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4981				HR4980 (Lo-16bits)
HR4982	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4983	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4984	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4985	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4986	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4987	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4988	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4989	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4990	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4991				HR4990 (Lo-16bits)
HR4992	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4993				HR4992 (Lo-16bits)
HR4994	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4995				HR4994 (Lo-16bits)
HR4996	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4997				HR4996 (Lo-16bits)
HR4998	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4999				HR4998 (Lo-16bits)
HR5000	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5001				HR5000 (Lo-16bits)
HR5002	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5003				HR5002 (Lo-16bits)
HR5004	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5005				HR5004 (Lo-16bits)
HR5006	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5007	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5008	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5009	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5010	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5011	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5012	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5013	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5014	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5015	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5016	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5017	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5018	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5019	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5020	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5021	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5022	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5023	DEF2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5024	DEF2:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5025	DEF2:I0dir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5026	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5027				HR5026 (Lo-16bits)
HR5028	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5029				HR5028 (Lo-16bits)
HR5030	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5031				HR5030 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5032	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5033	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5034	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5035	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5036	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5037	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5038	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5039	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5040	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5041				HR5040 (Lo-16bits)
HR5042	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5043				HR5042 (Lo-16bits)
HR5044	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5045				HR5044 (Lo-16bits)
HR5046	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5047				HR5046 (Lo-16bits)
HR5048	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5049				HR5048 (Lo-16bits)
HR5050	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5051				HR5050 (Lo-16bits)
HR5052	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5053				HR5052 (Lo-16bits)
HR5054	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5055				HR5054 (Lo-16bits)
HR5056	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5057				HR5056 (Lo-16bits)
HR5058	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5059				HR5058 (Lo-16bits)
HR5060	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5061				HR5060 (Lo-16bits)
HR5062	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5063				HR5062 (Lo-16bits)
HR5064	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5065				HR5064 (Lo-16bits)
HR5066	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5067				HR5066 (Lo-16bits)
HR5068	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5069				HR5068 (Lo-16bits)
HR5070	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5071				HR5070 (Lo-16bits)
HR5072	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5073				HR5072 (Lo-16bits)
HR5074	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5075				HR5074 (Lo-16bits)
HR5076	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5077				HR5076 (Lo-16bits)
HR5078	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5079				HR5078 (Lo-16bits)
HR5080	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5081				HR5080 (Lo-16bits)
HR5082	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5083				HR5082 (Lo-16bits)
HR5084	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5085				HR5084 (Lo-16bits)
HR5086	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5087				HR5086 (Lo-16bits)
HR5088	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5089				HR5088 (Lo-16bits)
HR5090	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5091				HR5090 (Lo-16bits)
HR5092	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5093				HR5092 (Lo-16bits)
HR5094	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5095				HR5094 (Lo-16bits)
HR5096	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5097				HR5096 (Lo-16bits)
HR5098	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5099				HR5098 (Lo-16bits)
HR5100	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5101				HR5100 (Lo-16bits)
HR5102	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5103				HR5102 (Lo-16bits)
HR5104	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5105				HR5104 (Lo-16bits)
HR5106	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5107				HR5106 (Lo-16bits)
HR5108	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5109				HR5108 (Lo-16bits)
HR5110	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5111				HR5110 (Lo-16bits)
HR5112	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5113				HR5112 (Lo-16bits)
HR5114	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5115				HR5114 (Lo-16bits)
HR5116	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5117				HR5116 (Lo-16bits)
HR5118	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5119				HR5118 (Lo-16bits)
HR5120	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5121				HR5120 (Lo-16bits)
HR5122	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5123				HR5122 (Lo-16bits)
HR5124	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5125				HR5124 (Lo-16bits)
HR5126	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5127				HR5126 (Lo-16bits)
HR5128	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5129				HR5128 (Lo-16bits)
HR5130	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5131				HR5130 (Lo-16bits)
HR5132	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5133				HR5132 (Lo-16bits)
HR5134	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5135				HR5134 (Lo-16bits)
HR5136	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5137	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5138	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5139	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5140	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5141	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5142	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5143	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5144	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5145				HR5144 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5146	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5147				HR5146 (Lo-16bits)
HR5148	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5149				HR5148 (Lo-16bits)
HR5150	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5151				HR5150 (Lo-16bits)
HR5152	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5153				HR5152 (Lo-16bits)
HR5154	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5155				HR5154 (Lo-16bits)
HR5156	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5157				HR5156 (Lo-16bits)
HR5158	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5159				HR5158 (Lo-16bits)
HR5160	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5161				HR5160 (Lo-16bits)
HR5162	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5163				HR5162 (Lo-16bits)
HR5164	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5165				HR5164 (Lo-16bits)
HR5166	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5167				HR5166 (Lo-16bits)
HR5168	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5169				HR5168 (Lo-16bits)
HR5170	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5171				HR5170 (Lo-16bits)
HR5172	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5173				HR5172 (Lo-16bits)
HR5174	DEF2:Unearth. Pick-up setting I0set(I0Sinfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5175				HR5174 (Lo-16bits)
HR5176	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5177				HR5176 (Lo-16bits)
HR5178	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5179				HR5178 (Lo-16bits)
HR5180	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5181				HR5180 (Lo-16bits)
HR5182	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5183				HR5182 (Lo-16bits)
HR5184	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5185				HR5184 (Lo-16bits)
HR5186	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5187				HR5186 (Lo-16bits)
HR5188	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5189				HR5188 (Lo-16bits)
HR5190	DEF2:Unearth. Pick-up setting U0set(I0Sinfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5191				HR5190 (Lo-16bits)
HR5192	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5193				HR5192 (Lo-16bits)
HR5194	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5195				HR5194 (Lo-16bits)
HR5196	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5197				HR5196 (Lo-16bits)
HR5198	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5199				HR5198 (Lo-16bits)
HR5200	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5201				HR5200 (Lo-16bits)
HR5202	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5203				HR5202 (Lo-16bits)
HR5204	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5205				HR5204 (Lo-16bits)
HR5206	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5207				HR5206 (Lo-16bits)
HR5208	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5209				HR5208 (Lo-16bits)
HR5210	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5211				HR5210 (Lo-16bits)
HR5212	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5213				HR5212 (Lo-16bits)
HR5214	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5215				HR5214 (Lo-16bits)
HR5216	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5217				HR5216 (Lo-16bits)
HR5218	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5219				HR5218 (Lo-16bits)
HR5220	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5221				HR5220 (Lo-16bits)
HR5222	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5223				HR5222 (Lo-16bits)
HR5224	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5225	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5226	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR5227	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5228	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5229	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5230	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5231	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5232	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5233	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5234	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5235	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5236	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5237	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5238	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5239	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5240	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5241				HR5240 (Lo-16bits)
HR5242	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5243				HR5242 (Lo-16bits)
HR5244	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5245				HR5244 (Lo-16bits)
HR5246	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5247				HR5246 (Lo-16bits)
HR5248	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5249				HR5248 (Lo-16bits)
HR5250	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5251				HR5250 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5252	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5253				HR5252 (Lo-16bits)
HR5254	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5255				HR5254 (Lo-16bits)
HR5256	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5257				HR5256 (Lo-16bits)
HR5258	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5259				HR5258 (Lo-16bits)
HR5260	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5261				HR5260 (Lo-16bits)
HR5262	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5263				HR5262 (Lo-16bits)
HR5264	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5265				HR5264 (Lo-16bits)
HR5266	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5267				HR5266 (Lo-16bits)
HR5268	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5269				HR5268 (Lo-16bits)
HR5270	DEF2:Definite operating time delay(Uneared)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5271				HR5270 (Lo-16bits)
HR5272	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5273				HR5272 (Lo-16bits)
HR5274	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5275				HR5274 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5276	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5277				HR5276 (Lo-16bits)
HR5278	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5279				HR5278 (Lo-16bits)
HR5280	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5281				HR5280 (Lo-16bits)
HR5282	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5283				HR5282 (Lo-16bits)
HR5284	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5285				HR5284 (Lo-16bits)
HR5286	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5287				HR5286 (Lo-16bits)
HR5288	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5289	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5290	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5291	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5292	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5293	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5294	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5295	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5296	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5297	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5298	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5299	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5300	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR5301	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5302	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5303	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5304	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5305	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5306	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5307	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5308	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5309	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5310	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5311	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5312	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5313	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5314	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5315	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5316	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5317	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5318	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5319	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5320	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5321				HR5320 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5322	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5323				HR5322 (Lo-16bits)
HR5324	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5325				HR5324 (Lo-16bits)
HR5326	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5327				HR5326 (Lo-16bits)
HR5328	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5329				HR5328 (Lo-16bits)
HR5330	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5331				HR5330 (Lo-16bits)
HR5332	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5333				HR5332 (Lo-16bits)
HR5334	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5335				HR5334 (Lo-16bits)
HR5336	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5337				HR5336 (Lo-16bits)
HR5338	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5339				HR5338 (Lo-16bits)
HR5340	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5341				HR5340 (Lo-16bits)
HR5342	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5343				HR5342 (Lo-16bits)
HR5344	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5345				HR5344 (Lo-16bits)
HR5346	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5347				HR5346 (Lo-16bits)
HR5348	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5349				HR5348 (Lo-16bits)
HR5350	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5351				HR5350 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5352	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5353				HR5352 (Lo-16bits)
HR5354	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5355				HR5354 (Lo-16bits)
HR5356	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5357				HR5356 (Lo-16bits)
HR5358	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5359				HR5358 (Lo-16bits)
HR5360	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5361				HR5360 (Lo-16bits)
HR5362	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5363				HR5362 (Lo-16bits)
HR5364	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5365				HR5364 (Lo-16bits)
HR5366	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5367				HR5366 (Lo-16bits)
HR5368	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5369				HR5368 (Lo-16bits)
HR5370	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5371				HR5370 (Lo-16bits)
HR5372	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5373				HR5372 (Lo-16bits)
HR5374	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5375				HR5374 (Lo-16bits)
HR5376	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5377				HR5376 (Lo-16bits)
HR5378	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5379				HR5378 (Lo-16bits)
HR5380	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5381				HR5380 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5382	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5383				HR5382 (Lo-16bits)
HR5384	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5385	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5386	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5387	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5388	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5389	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5390	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5391	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5392	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5393				HR5392 (Lo-16bits)
HR5394	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5395				HR5394 (Lo-16bits)
HR5396	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5397				HR5396 (Lo-16bits)
HR5398	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5399				HR5398 (Lo-16bits)
HR5400	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5401				HR5400 (Lo-16bits)
HR5402	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5403				HR5402 (Lo-16bits)
HR5404	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5405				HR5404 (Lo-16bits)
HR5406	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5407				HR5406 (Lo-16bits)
HR5408	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5409	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5410	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5411	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5412	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5413	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5414	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5415	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5416	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5417				HR5416 (Lo-16bits)
HR5418	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5419				HR5418 (Lo-16bits)
HR5420	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5421				HR5420 (Lo-16bits)
HR5422	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5423				HR5422 (Lo-16bits)
HR5424	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5425				HR5424 (Lo-16bits)
HR5426	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5427				HR5426 (Lo-16bits)
HR5428	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5429				HR5428 (Lo-16bits)
HR5430	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5431				HR5430 (Lo-16bits)
HR5432	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5433	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5434	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5435	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5436	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5437	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5438	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5439	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5440	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5441	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5442	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5443	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5444	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5445	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5446	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5447	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5448	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR5449	DEF3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5450	DEF3:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5451	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5452	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5453				HR5452 (Lo-16bits)
HR5454	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5455				HR5454 (Lo-16bits)
HR5456	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5457				HR5456 (Lo-16bits)
HR5458	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5459	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5460	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5461	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5462	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5463	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5464	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5465	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5466	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5467				HR5466 (Lo-16bits)
HR5468	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5469				HR5468 (Lo-16bits)
HR5470	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5471				HR5470 (Lo-16bits)
HR5472	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5473				HR5472 (Lo-16bits)
HR5474	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5475				HR5474 (Lo-16bits)
HR5476	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5477				HR5476 (Lo-16bits)
HR5478	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5479				HR5478 (Lo-16bits)
HR5480	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5481				HR5480 (Lo-16bits)
HR5482	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5483				HR5482 (Lo-16bits)
HR5484	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5485				HR5484 (Lo-16bits)
HR5486	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5487				HR5486 (Lo-16bits)
HR5488	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5489				HR5488 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5490	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5491				HR5490 (Lo-16bits)
HR5492	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5493				HR5492 (Lo-16bits)
HR5494	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5495				HR5494 (Lo-16bits)
HR5496	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5497				HR5496 (Lo-16bits)
HR5498	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5499				HR5498 (Lo-16bits)
HR5500	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5501				HR5500 (Lo-16bits)
HR5502	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5503				HR5502 (Lo-16bits)
HR5504	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5505				HR5504 (Lo-16bits)
HR5506	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5507				HR5506 (Lo-16bits)
HR5508	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5509				HR5508 (Lo-16bits)
HR5510	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5511				HR5510 (Lo-16bits)
HR5512	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5513				HR5512 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5514	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5515				HR5514 (Lo-16bits)
HR5516	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5517				HR5516 (Lo-16bits)
HR5518	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5519				HR5518 (Lo-16bits)
HR5520	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5521				HR5520 (Lo-16bits)
HR5522	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5523				HR5522 (Lo-16bits)
HR5524	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5525				HR5524 (Lo-16bits)
HR5526	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5527				HR5526 (Lo-16bits)
HR5528	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5529				HR5528 (Lo-16bits)
HR5530	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5531				HR5530 (Lo-16bits)
HR5532	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5533				HR5532 (Lo-16bits)
HR5534	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5535				HR5534 (Lo-16bits)
HR5536	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5537				HR5536 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5538	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5539				HR5538 (Lo-16bits)
HR5540	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5541				HR5540 (Lo-16bits)
HR5542	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5543				HR5542 (Lo-16bits)
HR5544	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5545				HR5544 (Lo-16bits)
HR5546	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5547				HR5546 (Lo-16bits)
HR5548	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5549				HR5548 (Lo-16bits)
HR5550	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5551				HR5550 (Lo-16bits)
HR5552	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5553				HR5552 (Lo-16bits)
HR5554	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5555				HR5554 (Lo-16bits)
HR5556	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5557				HR5556 (Lo-16bits)
HR5558	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5559				HR5558 (Lo-16bits)
HR5560	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5561				HR5560 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5562	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5563	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5564	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5565	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5566	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5567	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5568	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5569	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5570	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5571				HR5570 (Lo-16bits)
HR5572	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5573				HR5572 (Lo-16bits)
HR5574	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5575				HR5574 (Lo-16bits)
HR5576	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5577				HR5576 (Lo-16bits)
HR5578	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5579				HR5578 (Lo-16bits)
HR5580	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5581				HR5580 (Lo-16bits)
HR5582	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5583				HR5582 (Lo-16bits)
HR5584	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5585				HR5584 (Lo-16bits)
HR5586	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5587				HR5586 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5588	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5589				HR5588 (Lo-16bits)
HR5590	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5591				HR5590 (Lo-16bits)
HR5592	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5593				HR5592 (Lo-16bits)
HR5594	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5595				HR5594 (Lo-16bits)
HR5596	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5597				HR5596 (Lo-16bits)
HR5598	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5599				HR5598 (Lo-16bits)
HR5600	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5601				HR5600 (Lo-16bits)
HR5602	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5603				HR5602 (Lo-16bits)
HR5604	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5605				HR5604 (Lo-16bits)
HR5606	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5607				HR5606 (Lo-16bits)
HR5608	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5609				HR5608 (Lo-16bits)
HR5610	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5611				HR5610 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5612	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5613				HR5612 (Lo-16bits)
HR5614	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5615				HR5614 (Lo-16bits)
HR5616	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5617				HR5616 (Lo-16bits)
HR5618	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5619				HR5618 (Lo-16bits)
HR5620	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5621				HR5620 (Lo-16bits)
HR5622	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5623				HR5622 (Lo-16bits)
HR5624	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5625				HR5624 (Lo-16bits)
HR5626	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5627				HR5626 (Lo-16bits)
HR5628	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5629				HR5628 (Lo-16bits)
HR5630	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5631				HR5630 (Lo-16bits)
HR5632	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5633				HR5632 (Lo-16bits)
HR5634	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5635				HR5634 (Lo-16bits)
HR5636	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5637				HR5636 (Lo-16bits)
HR5638	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5639				HR5638 (Lo-16bits)
HR5640	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5641				HR5640 (Lo-16bits)
HR5642	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5643				HR5642 (Lo-16bits)
HR5644	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5645				HR5644 (Lo-16bits)
HR5646	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5647				HR5646 (Lo-16bits)
HR5648	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5649				HR5648 (Lo-16bits)
HR5650	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5651	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5652	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5653	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5654	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5655	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5656	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5657	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5658	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5659	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5660	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5661	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5662	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5663	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5664	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5665	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5666	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5667				HR5666 (Lo-16bits)
HR5668	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5669				HR5668 (Lo-16bits)
HR5670	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5671				HR5670 (Lo-16bits)
HR5672	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5673				HR5672 (Lo-16bits)
HR5674	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5675				HR5674 (Lo-16bits)
HR5676	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5677				HR5676 (Lo-16bits)
HR5678	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5679				HR5678 (Lo-16bits)
HR5680	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5681				HR5680 (Lo-16bits)
HR5682	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5683				HR5682 (Lo-16bits)
HR5684	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5685				HR5684 (Lo-16bits)
HR5686	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5687				HR5686 (Lo-16bits)
HR5688	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5689				HR5688 (Lo-16bits)
HR5690	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5691				HR5690 (Lo-16bits)
HR5692	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5693				HR5692 (Lo-16bits)
HR5694	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5695				HR5694 (Lo-16bits)
HR5696	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5697				HR5696 (Lo-16bits)
HR5698	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5699				HR5698 (Lo-16bits)
HR5700	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5701				HR5700 (Lo-16bits)
HR5702	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5703				HR5702 (Lo-16bits)
HR5704	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5705				HR5704 (Lo-16bits)
HR5706	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5707				HR5706 (Lo-16bits)
HR5708	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5709				HR5708 (Lo-16bits)
HR5710	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5711				HR5710 (Lo-16bits)
HR5712	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5713				HR5712 (Lo-16bits)
HR5714	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5715	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5716	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5717	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5718	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5719	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5720	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5721	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5722	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5723	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5724	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5725	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5726	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5727	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5728	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5729	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5730	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5731	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5732	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5733	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5734	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5735	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5736	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5737	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5738	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5739	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5740	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5741	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5742	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5743	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5744	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5745	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5746	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5747				HR5746 (Lo-16bits)
HR5748	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5749				HR5748 (Lo-16bits)
HR5750	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5751				HR5750 (Lo-16bits)
HR5752	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5753				HR5752 (Lo-16bits)
HR5754	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5755				HR5754 (Lo-16bits)
HR5756	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5757				HR5756 (Lo-16bits)
HR5758	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5759				HR5758 (Lo-16bits)
HR5760	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5761				HR5760 (Lo-16bits)
HR5762	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5763				HR5762 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5764	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5765				HR5764 (Lo-16bits)
HR5766	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5767				HR5766 (Lo-16bits)
HR5768	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5769				HR5768 (Lo-16bits)
HR5770	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5771				HR5770 (Lo-16bits)
HR5772	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5773				HR5772 (Lo-16bits)
HR5774	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5775				HR5774 (Lo-16bits)
HR5776	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5777				HR5776 (Lo-16bits)
HR5778	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5779				HR5778 (Lo-16bits)
HR5780	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5781				HR5780 (Lo-16bits)
HR5782	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5783				HR5782 (Lo-16bits)
HR5784	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5785				HR5784 (Lo-16bits)
HR5786	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5787				HR5786 (Lo-16bits)
HR5788	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5789				HR5788 (Lo-16bits)
HR5790	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5791				HR5790 (Lo-16bits)
HR5792	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5793				HR5792 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5794	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5795				HR5794 (Lo-16bits)
HR5796	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5797				HR5796 (Lo-16bits)
HR5798	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5799				HR5798 (Lo-16bits)
HR5800	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5801				HR5800 (Lo-16bits)
HR5802	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5803				HR5802 (Lo-16bits)
HR5804	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5805				HR5804 (Lo-16bits)
HR5806	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5807				HR5806 (Lo-16bits)
HR5808	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5809				HR5808 (Lo-16bits)
HR5810	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5811	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5812	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5813	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5814	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5815	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5816	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5817	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5818	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5819				HR5818 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5820	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5821				HR5820 (Lo-16bits)
HR5822	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5823				HR5822 (Lo-16bits)
HR5824	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5825				HR5824 (Lo-16bits)
HR5826	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5827				HR5826 (Lo-16bits)
HR5828	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5829				HR5828 (Lo-16bits)
HR5830	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5831				HR5830 (Lo-16bits)
HR5832	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5833				HR5832 (Lo-16bits)
HR5834	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5835	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5836	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5837	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5838	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5839	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5840	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5841	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5842	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5843				HR5842 (Lo-16bits)
HR5844	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5845				HR5844 (Lo-16bits)
HR5846	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5847				HR5846 (Lo-16bits)
HR5848	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5849				HR5848 (Lo-16bits)
HR5850	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5851				HR5850 (Lo-16bits)
HR5852	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5853				HR5852 (Lo-16bits)
HR5854	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5855				HR5854 (Lo-16bits)
HR5856	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5857				HR5856 (Lo-16bits)
HR5858	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5859	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5860	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5861	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5862	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5863	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5864	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5865	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5866	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5867	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5868	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5869	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5870	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5871	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5872	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5873	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5874	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5875	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5876	DEF4:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5877	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5878	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5879				HR5878 (Lo-16bits)
HR5880	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5881				HR5880 (Lo-16bits)
HR5882	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5883				HR5882 (Lo-16bits)
HR5884	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5885	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5886	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5887	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5888	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5889	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5890	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5891	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5892	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5893				HR5892 (Lo-16bits)
HR5894	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5895				HR5894 (Lo-16bits)
HR5896	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5897				HR5896 (Lo-16bits)
HR5898	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5899				HR5898 (Lo-16bits)
HR5900	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5901				HR5900 (Lo-16bits)
HR5902	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5903				HR5902 (Lo-16bits)
HR5904	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5905				HR5904 (Lo-16bits)
HR5906	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5907				HR5906 (Lo-16bits)
HR5908	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5909				HR5908 (Lo-16bits)
HR5910	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5911				HR5910 (Lo-16bits)
HR5912	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5913				HR5912 (Lo-16bits)
HR5914	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5915				HR5914 (Lo-16bits)
HR5916	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5917				HR5916 (Lo-16bits)
HR5918	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5919				HR5918 (Lo-16bits)
HR5920	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5921				HR5920 (Lo-16bits)
HR5922	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5923				HR5922 (Lo-16bits)
HR5924	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5925				HR5924 (Lo-16bits)
HR5926	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5927				HR5926 (Lo-16bits)
HR5928	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5929				HR5928 (Lo-16bits)
HR5930	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5931				HR5930 (Lo-16bits)
HR5932	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5933				HR5932 (Lo-16bits)
HR5934	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5935				HR5934 (Lo-16bits)
HR5936	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5937				HR5936 (Lo-16bits)
HR5938	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5939				HR5938 (Lo-16bits)
HR5940	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5941				HR5940 (Lo-16bits)
HR5942	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5943				HR5942 (Lo-16bits)
HR5944	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5945				HR5944 (Lo-16bits)
HR5946	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5947				HR5946 (Lo-16bits)
HR5948	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5949				HR5948 (Lo-16bits)
HR5950	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5951				HR5950 (Lo-16bits)
HR5952	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5953				HR5952 (Lo-16bits)
HR5954	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5955				HR5954 (Lo-16bits)
HR5956	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5957				HR5956 (Lo-16bits)
HR5958	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5959				HR5958 (Lo-16bits)
HR5960	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5961				HR5960 (Lo-16bits)
HR5962	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5963				HR5962 (Lo-16bits)
HR5964	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5965				HR5964 (Lo-16bits)
HR5966	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5967				HR5966 (Lo-16bits)
HR5968	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5969				HR5968 (Lo-16bits)
HR5970	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5971				HR5970 (Lo-16bits)
HR5972	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5973				HR5972 (Lo-16bits)
HR5974	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5975				HR5974 (Lo-16bits)
HR5976	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5977				HR5976 (Lo-16bits)
HR5978	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5979				HR5978 (Lo-16bits)
HR5980	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5981				HR5980 (Lo-16bits)
HR5982	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5983				HR5982 (Lo-16bits)
HR5984	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5985				HR5984 (Lo-16bits)
HR5986	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5987				HR5986 (Lo-16bits)
HR5988	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5989	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5990	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5991	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5992	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5993	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5994	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5995	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5996	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5997				HR5996 (Lo-16bits)
HR5998	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5999				HR5998 (Lo-16bits)
HR6000	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6001				HR6000 (Lo-16bits)
HR6002	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6003				HR6002 (Lo-16bits)
HR6004	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6005				HR6004 (Lo-16bits)
HR6006	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6007				HR6006 (Lo-16bits)
HR6008	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6009				HR6008 (Lo-16bits)
HR6010	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6011				HR6010 (Lo-16bits)
HR6012	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6013				HR6012 (Lo-16bits)
HR6014	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6015				HR6014 (Lo-16bits)
HR6016	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6017				HR6016 (Lo-16bits)
HR6018	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6019				HR6018 (Lo-16bits)
HR6020	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6021				HR6020 (Lo-16bits)
HR6022	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6023				HR6022 (Lo-16bits)
HR6024	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6025				HR6024 (Lo-16bits)
HR6026	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6027				HR6026 (Lo-16bits)
HR6028	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6029				HR6028 (Lo-16bits)
HR6030	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6031				HR6030 (Lo-16bits)
HR6032	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6033				HR6032 (Lo-16bits)
HR6034	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6035				HR6034 (Lo-16bits)
HR6036	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6037				HR6036 (Lo-16bits)
HR6038	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6039				HR6038 (Lo-16bits)
HR6040	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6041				HR6040 (Lo-16bits)
HR6042	DEF4:Unearth. Pick-up setting U0set(I0Sinf)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6043				HR6042 (Lo-16bits)
HR6044	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6045				HR6044 (Lo-16bits)
HR6046	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6047				HR6046 (Lo-16bits)
HR6048	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6049				HR6048 (Lo-16bits)
HR6050	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6051				HR6050 (Lo-16bits)
HR6052	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6053				HR6052 (Lo-16bits)
HR6054	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6055				HR6054 (Lo-16bits)
HR6056	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6057				HR6056 (Lo-16bits)
HR6058	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6059				HR6058 (Lo-16bits)
HR6060	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6061				HR6060 (Lo-16bits)
HR6062	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6063				HR6062 (Lo-16bits)
HR6064	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6065				HR6064 (Lo-16bits)
HR6066	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6067				HR6066 (Lo-16bits)
HR6068	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6069				HR6068 (Lo-16bits)
HR6070	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6071				HR6070 (Lo-16bits)
HR6072	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6073				HR6072 (Lo-16bits)
HR6074	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6075				HR6074 (Lo-16bits)
HR6076	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6077	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6078	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6079	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6080	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR6081	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6082	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6083	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6084	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6085	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6086	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6087	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6088	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6089	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6090	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6091	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6092	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6093				HR6092 (Lo-16bits)
HR6094	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6095				HR6094 (Lo-16bits)
HR6096	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6097				HR6096 (Lo-16bits)
HR6098	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6099				HR6098 (Lo-16bits)
HR6100	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6101				HR6100 (Lo-16bits)
HR6102	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6103				HR6102 (Lo-16bits)
HR6104	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6105				HR6104 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6106	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6107				HR6106 (Lo-16bits)
HR6108	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6109				HR6108 (Lo-16bits)
HR6110	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6111				HR6110 (Lo-16bits)
HR6112	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6113				HR6112 (Lo-16bits)
HR6114	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6115				HR6114 (Lo-16bits)
HR6116	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6117				HR6116 (Lo-16bits)
HR6118	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6119				HR6118 (Lo-16bits)
HR6120	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6121				HR6120 (Lo-16bits)
HR6122	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6123				HR6122 (Lo-16bits)
HR6124	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6125				HR6124 (Lo-16bits)
HR6126	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6127				HR6126 (Lo-16bits)
HR6128	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6129				HR6128 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6130	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6131				HR6130 (Lo-16bits)
HR6132	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6133				HR6132 (Lo-16bits)
HR6134	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6135				HR6134 (Lo-16bits)
HR6136	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6137				HR6136 (Lo-16bits)
HR6138	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6139				HR6138 (Lo-16bits)
HR6140	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6141	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6142	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6143	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6144	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6145	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6146	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6147	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6148	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6149	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6150	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6151	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6152	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6153	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6154	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6155	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6156	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6157	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6158	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6159	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6160	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6161	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6162	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6163	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6164	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6165	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6166	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6167	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6168	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6169	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6170	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6171	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6172	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6173				HR6172 (Lo-16bits)
HR6174	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6175				HR6174 (Lo-16bits)
HR6176	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6177				HR6176 (Lo-16bits)
HR6178	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6179				HR6178 (Lo-16bits)
HR6180	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6181				HR6180 (Lo-16bits)
HR6182	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6183				HR6182 (Lo-16bits)
HR6184	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6185				HR6184 (Lo-16bits)
HR6186	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6187				HR6186 (Lo-16bits)
HR6188	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6189				HR6188 (Lo-16bits)
HR6190	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6191				HR6190 (Lo-16bits)
HR6192	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6193				HR6192 (Lo-16bits)
HR6194	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6195				HR6194 (Lo-16bits)
HR6196	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6197				HR6196 (Lo-16bits)
HR6198	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6199				HR6198 (Lo-16bits)
HR6200	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6201				HR6200 (Lo-16bits)
HR6202	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6203				HR6202 (Lo-16bits)
HR6204	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6205				HR6204 (Lo-16bits)
HR6206	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6207				HR6206 (Lo-16bits)
HR6208	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6209				HR6208 (Lo-16bits)
HR6210	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6211				HR6210 (Lo-16bits)
HR6212	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6213				HR6212 (Lo-16bits)
HR6214	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6215				HR6214 (Lo-16bits)
HR6216	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6217				HR6216 (Lo-16bits)
HR6218	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6219				HR6218 (Lo-16bits)
HR6220	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6221				HR6220 (Lo-16bits)
HR6222	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6223				HR6222 (Lo-16bits)
HR6224	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6225				HR6224 (Lo-16bits)
HR6226	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6227				HR6226 (Lo-16bits)
HR6228	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6229				HR6228 (Lo-16bits)
HR6230	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6231				HR6230 (Lo-16bits)
HR6232	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6233				HR6232 (Lo-16bits)
HR6234	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6235				HR6234 (Lo-16bits)
HR6236	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6237	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6238	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6239	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6240	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6241	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6242	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6243	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6244	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6245				HR6244 (Lo-16bits)
HR6246	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6247				HR6246 (Lo-16bits)
HR6248	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6249				HR6248 (Lo-16bits)
HR6250	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6251				HR6250 (Lo-16bits)
HR6252	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6253				HR6252 (Lo-16bits)
HR6254	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6255				HR6254 (Lo-16bits)
HR6256	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6257				HR6256 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6258	DEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6259				HR6258 (Lo-16bits)
HR6260	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6261	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6262	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6263	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6264	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6265	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6266	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6267	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6268	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6269				HR6268 (Lo-16bits)
HR6270	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6271				HR6270 (Lo-16bits)
HR6272	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6273				HR6272 (Lo-16bits)
HR6274	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6275				HR6274 (Lo-16bits)
HR6276	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6277				HR6276 (Lo-16bits)
HR6278	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6279				HR6278 (Lo-16bits)
HR6280	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6281				HR6280 (Lo-16bits)
HR6282	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6283				HR6282 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6284	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6285	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6286	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6287	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6288	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6289	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6290	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6291	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6292	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6293	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6294	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6295	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6296	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6297	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6298	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6299	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6300	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR6301	IEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input

Holding Register	Parameter	Access	Length	Info
HR6302	CUB1:I2> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6303	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6304	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6305				HR6304 (Lo-16bits)
HR6306	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6307				HR6306 (Lo-16bits)
HR6308	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6309				HR6308 (Lo-16bits)
HR6310	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6311				HR6310 (Lo-16bits)
HR6312	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6313				HR6312 (Lo-16bits)
HR6314	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6315				HR6314 (Lo-16bits)
HR6316	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6317				HR6316 (Lo-16bits)
HR6318	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6319				HR6318 (Lo-16bits)
HR6320	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6321				HR6320 (Lo-16bits)
HR6322	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6323				HR6322 (Lo-16bits)
HR6324	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6325				HR6324 (Lo-16bits)
HR6326	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6327				HR6326 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6328	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6329				HR6328 (Lo-16bits)
HR6330	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6331				HR6330 (Lo-16bits)
HR6332	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6333				HR6332 (Lo-16bits)
HR6334	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6335				HR6334 (Lo-16bits)
HR6336	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6337				HR6336 (Lo-16bits)
HR6338	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6339				HR6338 (Lo-16bits)
HR6340	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6341				HR6340 (Lo-16bits)
HR6342	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6343				HR6342 (Lo-16bits)
HR6344	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6345				HR6344 (Lo-16bits)
HR6346	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6347				HR6346 (Lo-16bits)
HR6348	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6349				HR6348 (Lo-16bits)
HR6350	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6351				HR6350 (Lo-16bits)
HR6352	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6353	CUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6354	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6355	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6356	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6357	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6358	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6359	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6360	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6361				HR6360 (Lo-16bits)
HR6362	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6363				HR6362 (Lo-16bits)
HR6364	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6365				HR6364 (Lo-16bits)
HR6366	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6367				HR6366 (Lo-16bits)
HR6368	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6369				HR6368 (Lo-16bits)
HR6370	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6371				HR6370 (Lo-16bits)
HR6372	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6373				HR6372 (Lo-16bits)
HR6374	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6375				HR6374 (Lo-16bits)
HR6376	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6377	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6378	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6379	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6380	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6381	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6382	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6383	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6384	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6385	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6386	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6387	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6388	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6389	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6390	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6391	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6392	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6393	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6394	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6395	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6396	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6397	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6398	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6399	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6400	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6401	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6402	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6403	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6404	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6405	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6406	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6407	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6408	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6409				HR6408 (Lo-16bits)
HR6410	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6411				HR6410 (Lo-16bits)
HR6412	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6413				HR6412 (Lo-16bits)
HR6414	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6415				HR6414 (Lo-16bits)
HR6416	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6417				HR6416 (Lo-16bits)
HR6418	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6419				HR6418 (Lo-16bits)
HR6420	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6421				HR6420 (Lo-16bits)
HR6422	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6423				HR6422 (Lo-16bits)
HR6424	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6425				HR6424 (Lo-16bits)
HR6426	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6427				HR6426 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6428	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6429				HR6428 (Lo-16bits)
HR6430	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6431				HR6430 (Lo-16bits)
HR6432	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6433				HR6432 (Lo-16bits)
HR6434	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6435				HR6434 (Lo-16bits)
HR6436	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6437				HR6436 (Lo-16bits)
HR6438	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6439				HR6438 (Lo-16bits)
HR6440	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6441				HR6440 (Lo-16bits)
HR6442	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6443				HR6442 (Lo-16bits)
HR6444	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6445				HR6444 (Lo-16bits)
HR6446	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6447				HR6446 (Lo-16bits)
HR6448	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6449				HR6448 (Lo-16bits)
HR6450	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6451				HR6450 (Lo-16bits)
HR6452	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6453				HR6452 (Lo-16bits)
HR6454	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6455				HR6454 (Lo-16bits)
HR6456	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6457				HR6456 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6458	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6459				HR6458 (Lo-16bits)
HR6460	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6461				HR6460 (Lo-16bits)
HR6462	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6463				HR6462 (Lo-16bits)
HR6464	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6465				HR6464 (Lo-16bits)
HR6466	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6467				HR6466 (Lo-16bits)
HR6468	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6469				HR6468 (Lo-16bits)
HR6470	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6471				HR6470 (Lo-16bits)
HR6472	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6473	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6474	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6475	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6476	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6477	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6478	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6479	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6480	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6481				HR6480 (Lo-16bits)
HR6482	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6483				HR6482 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6484	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6485				HR6484 (Lo-16bits)
HR6486	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6487				HR6486 (Lo-16bits)
HR6488	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6489				HR6488 (Lo-16bits)
HR6490	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6491				HR6490 (Lo-16bits)
HR6492	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6493				HR6492 (Lo-16bits)
HR6494	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6495				HR6494 (Lo-16bits)
HR6496	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6497	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6498	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6499	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6500	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6501	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6502	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6503	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6504	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6505	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6506	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6507	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6508	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6509	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6510	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6511	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6512	CUB2:I2>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6513	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6514	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6515				HR6514 (Lo-16bits)
HR6516	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6517				HR6516 (Lo-16bits)
HR6518	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6519				HR6518 (Lo-16bits)
HR6520	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6521				HR6520 (Lo-16bits)
HR6522	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6523				HR6522 (Lo-16bits)
HR6524	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6525				HR6524 (Lo-16bits)
HR6526	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6527				HR6526 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6528	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6529				HR6528 (Lo-16bits)
HR6530	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6531				HR6530 (Lo-16bits)
HR6532	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6533				HR6532 (Lo-16bits)
HR6534	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6535				HR6534 (Lo-16bits)
HR6536	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6537				HR6536 (Lo-16bits)
HR6538	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6539				HR6538 (Lo-16bits)
HR6540	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6541				HR6540 (Lo-16bits)
HR6542	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6543				HR6542 (Lo-16bits)
HR6544	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6545				HR6544 (Lo-16bits)
HR6546	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6547				HR6546 (Lo-16bits)
HR6548	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6549				HR6548 (Lo-16bits)
HR6550	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6551				HR6550 (Lo-16bits)
HR6552	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6553				HR6552 (Lo-16bits)
HR6554	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6555				HR6554 (Lo-16bits)
HR6556	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6557				HR6556 (Lo-16bits)
HR6558	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6559				HR6558 (Lo-16bits)
HR6560	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6561				HR6560 (Lo-16bits)
HR6562	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6563	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6564	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6565	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6566	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6567	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6568	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6569	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6570	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6571				HR6570 (Lo-16bits)
HR6572	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6573				HR6572 (Lo-16bits)
HR6574	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6575				HR6574 (Lo-16bits)
HR6576	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6577				HR6576 (Lo-16bits)
HR6578	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6579				HR6578 (Lo-16bits)
HR6580	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6581				HR6580 (Lo-16bits)
HR6582	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6583				HR6582 (Lo-16bits)
HR6584	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6585				HR6584 (Lo-16bits)
HR6586	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6587	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6588	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6589	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6590	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6591	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6592	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6593	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6594	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6595	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6596	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6597	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6598	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6599	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6600	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6601	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6602	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6603	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6604	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6605	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6606	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6607	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6608	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6609	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6610	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6611	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6612	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6613	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6614	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6615	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6616	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6617	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6618	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6619				HR6618 (Lo-16bits)
HR6620	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6621				HR6620 (Lo-16bits)
HR6622	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6623				HR6622 (Lo-16bits)
HR6624	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6625				HR6624 (Lo-16bits)
HR6626	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6627				HR6626 (Lo-16bits)
HR6628	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6629				HR6628 (Lo-16bits)
HR6630	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6631				HR6630 (Lo-16bits)
HR6632	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6633				HR6632 (Lo-16bits)
HR6634	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6635				HR6634 (Lo-16bits)
HR6636	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6637				HR6636 (Lo-16bits)
HR6638	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6639				HR6638 (Lo-16bits)
HR6640	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6641				HR6640 (Lo-16bits)
HR6642	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6643				HR6642 (Lo-16bits)
HR6644	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6645				HR6644 (Lo-16bits)
HR6646	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6647				HR6646 (Lo-16bits)
HR6648	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6649				HR6648 (Lo-16bits)
HR6650	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6651				HR6650 (Lo-16bits)
HR6652	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6653				HR6652 (Lo-16bits)
HR6654	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6655				HR6654 (Lo-16bits)
HR6656	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6657				HR6656 (Lo-16bits)
HR6658	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6659				HR6658 (Lo-16bits)
HR6660	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6661				HR6660 (Lo-16bits)
HR6662	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6663				HR6662 (Lo-16bits)
HR6664	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6665				HR6664 (Lo-16bits)
HR6666	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6667				HR6666 (Lo-16bits)
HR6668	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6669				HR6668 (Lo-16bits)
HR6670	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6671				HR6670 (Lo-16bits)
HR6672	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6673				HR6672 (Lo-16bits)
HR6674	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6675				HR6674 (Lo-16bits)
HR6676	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6677				HR6676 (Lo-16bits)
HR6678	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6679				HR6678 (Lo-16bits)
HR6680	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6681				HR6680 (Lo-16bits)
HR6682	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6683	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6684	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6685	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6686	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6687	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6688	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6689	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6690	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6691				HR6690 (Lo-16bits)
HR6692	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6693				HR6692 (Lo-16bits)
HR6694	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6695				HR6694 (Lo-16bits)
HR6696	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6697				HR6696 (Lo-16bits)
HR6698	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6699				HR6698 (Lo-16bits)
HR6700	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6701				HR6700 (Lo-16bits)
HR6702	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6703				HR6702 (Lo-16bits)
HR6704	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6705				HR6704 (Lo-16bits)
HR6706	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6707	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6708	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6709	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6710	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6711	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6712	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6713	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6714	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6715	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6716	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6717	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6718	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6719	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6720	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6721	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6722	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6723	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6724	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6725				HR6724 (Lo-16bits)
HR6726	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6727				HR6726 (Lo-16bits)
HR6728	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6729				HR6728 (Lo-16bits)
HR6730	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6731				HR6730 (Lo-16bits)
HR6732	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6733				HR6732 (Lo-16bits)
HR6734	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6735				HR6734 (Lo-16bits)
HR6736	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6737				HR6736 (Lo-16bits)
HR6738	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6739				HR6738 (Lo-16bits)
HR6740	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6741				HR6740 (Lo-16bits)
HR6742	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6743				HR6742 (Lo-16bits)
HR6744	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6745				HR6744 (Lo-16bits)
HR6746	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6747				HR6746 (Lo-16bits)
HR6748	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6749				HR6748 (Lo-16bits)
HR6750	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6751				HR6750 (Lo-16bits)
HR6752	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6753				HR6752 (Lo-16bits)
HR6754	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6755				HR6754 (Lo-16bits)
HR6756	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6757				HR6756 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6758	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6759				HR6758 (Lo-16bits)
HR6760	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6761				HR6760 (Lo-16bits)
HR6762	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6763				HR6762 (Lo-16bits)
HR6764	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6765				HR6764 (Lo-16bits)
HR6766	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6767				HR6766 (Lo-16bits)
HR6768	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6769				HR6768 (Lo-16bits)
HR6770	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6771				HR6770 (Lo-16bits)
HR6772	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6773	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6774	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6775	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6776	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6777	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6778	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6779	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6780	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6781				HR6780 (Lo-16bits)
HR6782	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6783				HR6782 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6784	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6785				HR6784 (Lo-16bits)
HR6786	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6787				HR6786 (Lo-16bits)
HR6788	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6789				HR6788 (Lo-16bits)
HR6790	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6791				HR6790 (Lo-16bits)
HR6792	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6793				HR6792 (Lo-16bits)
HR6794	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6795				HR6794 (Lo-16bits)
HR6796	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6797	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6798	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6799	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6800	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6801	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6802	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6803	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6804	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6805	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6806	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6807	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6808	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6809	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6810	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6811	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6812	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6813	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6814	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6815	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6816	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6817	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6818	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6819	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6820	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6821	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6822	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6823	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6824	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6825	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6826	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6827	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6828	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6829				HR6828 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6830	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6831				HR6830 (Lo-16bits)
HR6832	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6833				HR6832 (Lo-16bits)
HR6834	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6835				HR6834 (Lo-16bits)
HR6836	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6837				HR6836 (Lo-16bits)
HR6838	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6839				HR6838 (Lo-16bits)
HR6840	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6841				HR6840 (Lo-16bits)
HR6842	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6843				HR6842 (Lo-16bits)
HR6844	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6845				HR6844 (Lo-16bits)
HR6846	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6847				HR6846 (Lo-16bits)
HR6848	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6849				HR6848 (Lo-16bits)
HR6850	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6851				HR6850 (Lo-16bits)
HR6852	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6853				HR6852 (Lo-16bits)
HR6854	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6855				HR6854 (Lo-16bits)
HR6856	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6857				HR6856 (Lo-16bits)
HR6858	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6859				HR6858 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6860	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6861				HR6860 (Lo-16bits)
HR6862	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6863				HR6862 (Lo-16bits)
HR6864	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6865				HR6864 (Lo-16bits)
HR6866	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6867				HR6866 (Lo-16bits)
HR6868	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6869				HR6868 (Lo-16bits)
HR6870	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6871				HR6870 (Lo-16bits)
HR6872	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6873				HR6872 (Lo-16bits)
HR6874	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6875				HR6874 (Lo-16bits)
HR6876	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6877				HR6876 (Lo-16bits)
HR6878	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6879				HR6878 (Lo-16bits)
HR6880	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6881				HR6880 (Lo-16bits)
HR6882	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6883				HR6882 (Lo-16bits)
HR6884	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6885				HR6884 (Lo-16bits)
HR6886	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6887				HR6886 (Lo-16bits)
HR6888	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6889				HR6888 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6890	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6891				HR6890 (Lo-16bits)
HR6892	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6893	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6894	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6895	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6896	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6897	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6898	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6899	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6900	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6901				HR6900 (Lo-16bits)
HR6902	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6903				HR6902 (Lo-16bits)
HR6904	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6905				HR6904 (Lo-16bits)
HR6906	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6907				HR6906 (Lo-16bits)
HR6908	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6909				HR6908 (Lo-16bits)
HR6910	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6911				HR6910 (Lo-16bits)
HR6912	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6913				HR6912 (Lo-16bits)
HR6914	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6915				HR6914 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6916	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6917	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6918	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6919	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6920	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6921	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6922	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6923	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6924	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6925	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6926	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6927	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6928	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6929	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6930	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6931	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6932	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6933	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR6934	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6935				HR6934 (Lo-16bits)
HR6936	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6937				HR6936 (Lo-16bits)
HR6938	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6939				HR6938 (Lo-16bits)
HR6940	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6941				HR6940 (Lo-16bits)
HR6942	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6943				HR6942 (Lo-16bits)
HR6944	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6945				HR6944 (Lo-16bits)
HR6946	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6947				HR6946 (Lo-16bits)
HR6948	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6949				HR6948 (Lo-16bits)
HR6950	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6951				HR6950 (Lo-16bits)
HR6952	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6953				HR6952 (Lo-16bits)
HR6954	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6955				HR6954 (Lo-16bits)
HR6956	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6957				HR6956 (Lo-16bits)
HR6958	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6959				HR6958 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6960	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6961				HR6960 (Lo-16bits)
HR6962	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6963				HR6962 (Lo-16bits)
HR6964	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6965				HR6964 (Lo-16bits)
HR6966	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6967				HR6966 (Lo-16bits)
HR6968	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6969				HR6968 (Lo-16bits)
HR6970	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6971				HR6970 (Lo-16bits)
HR6972	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6973				HR6972 (Lo-16bits)
HR6974	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6975				HR6974 (Lo-16bits)
HR6976	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6977				HR6976 (Lo-16bits)
HR6978	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6979				HR6978 (Lo-16bits)
HR6980	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6981				HR6980 (Lo-16bits)
HR6982	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6983	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6984	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6985	CUB4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6986	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6987	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6988	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6989	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6990	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6991				HR6990 (Lo-16bits)
HR6992	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6993				HR6992 (Lo-16bits)
HR6994	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6995				HR6994 (Lo-16bits)
HR6996	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6997				HR6996 (Lo-16bits)
HR6998	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6999				HR6998 (Lo-16bits)
HR7000	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7001				HR7000 (Lo-16bits)
HR7002	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7003				HR7002 (Lo-16bits)
HR7004	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7005				HR7004 (Lo-16bits)
HR7006	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7007	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7008	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7009	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7010	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7011	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR7012	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7013	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7014	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7015	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7016	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7017	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7018	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7019	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7020	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7021	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7022	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7023	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7024	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7025	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7026	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7027	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7028	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7029	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7030	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7031	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7032	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR7033	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7034	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7035	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7036	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7037	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7038	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7039				HR7038 (Lo-16bits)
HR7040	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7041				HR7040 (Lo-16bits)
HR7042	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7043				HR7042 (Lo-16bits)
HR7044	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7045				HR7044 (Lo-16bits)
HR7046	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7047				HR7046 (Lo-16bits)
HR7048	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7049				HR7048 (Lo-16bits)
HR7050	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7051				HR7050 (Lo-16bits)
HR7052	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7053				HR7052 (Lo-16bits)
HR7054	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7055				HR7054 (Lo-16bits)
HR7056	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7057				HR7056 (Lo-16bits)
HR7058	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7059				HR7058 (Lo-16bits)
HR7060	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7061				HR7060 (Lo-16bits)
HR7062	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7063				HR7062 (Lo-16bits)
HR7064	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7065				HR7064 (Lo-16bits)
HR7066	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7067				HR7066 (Lo-16bits)
HR7068	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7069				HR7068 (Lo-16bits)
HR7070	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7071				HR7070 (Lo-16bits)
HR7072	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7073				HR7072 (Lo-16bits)
HR7074	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7075				HR7074 (Lo-16bits)
HR7076	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7077				HR7076 (Lo-16bits)
HR7078	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7079				HR7078 (Lo-16bits)
HR7080	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7081				HR7080 (Lo-16bits)
HR7082	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7083				HR7082 (Lo-16bits)
HR7084	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7085				HR7084 (Lo-16bits)
HR7086	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7087				HR7086 (Lo-16bits)
HR7088	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7089				HR7088 (Lo-16bits)
HR7090	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7091				HR7090 (Lo-16bits)
HR7092	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7093				HR7092 (Lo-16bits)
HR7094	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7095				HR7094 (Lo-16bits)
HR7096	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7097				HR7096 (Lo-16bits)
HR7098	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7099				HR7098 (Lo-16bits)
HR7100	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7101				HR7100 (Lo-16bits)
HR7102	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7103	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7104	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7105	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7106	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7107	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7108	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7109	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7110	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7111				HR7110 (Lo-16bits)
HR7112	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7113				HR7112 (Lo-16bits)
HR7114	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7115				HR7114 (Lo-16bits)
HR7116	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7117				HR7116 (Lo-16bits)
HR7118	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7119				HR7118 (Lo-16bits)
HR7120	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7121				HR7120 (Lo-16bits)
HR7122	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7123				HR7122 (Lo-16bits)
HR7124	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7125				HR7124 (Lo-16bits)
HR7126	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7127	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7128	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7129	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7130	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7131	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7132	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7133	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7134	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7135	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7136	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7137	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7138	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7139	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7140	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7141	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7142	HOC1:Ih> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7143	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)
HR7155	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7164	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7165				HR7164 (Lo-16bits)
HR7166	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7167				HR7166 (Lo-16bits)
HR7168	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7169				HR7168 (Lo-16bits)
HR7170	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7171				HR7170 (Lo-16bits)
HR7172	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7173				HR7172 (Lo-16bits)
HR7174	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7175				HR7174 (Lo-16bits)
HR7176	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7177				HR7176 (Lo-16bits)
HR7178	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7179				HR7178 (Lo-16bits)
HR7180	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7181				HR7180 (Lo-16bits)
HR7182	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7183				HR7182 (Lo-16bits)
HR7184	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7185	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7190				HR7189 (Lo-16bits)
HR7191	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7194				HR7193 (Lo-16bits)
HR7195	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7196				HR7195 (Lo-16bits)
HR7197	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7198				HR7197 (Lo-16bits)
HR7199	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7200				HR7199 (Lo-16bits)
HR7201	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7202				HR7201 (Lo-16bits)
HR7203	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7206	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7207				HR7206 (Lo-16bits)
HR7208	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7209				HR7208 (Lo-16bits)
HR7210	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7211				HR7210 (Lo-16bits)
HR7212	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7213				HR7212 (Lo-16bits)
HR7214	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7215				HR7214 (Lo-16bits)
HR7216	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7217				HR7216 (Lo-16bits)
HR7218	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7219				HR7218 (Lo-16bits)
HR7220	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7221				HR7220 (Lo-16bits)
HR7222	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7223				HR7222 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7224	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7225				HR7224 (Lo-16bits)
HR7226	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR7227	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked
HR7228	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR7229	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR7230	DIF1:Id< condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR7231				HR7230 (Lo-16bits)
HR7232	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR7233				HR7232 (Lo-16bits)
HR7234	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)
HR7235				HR7234 (Lo-16bits)
HR7236	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7237	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7238	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7239	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR7240	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7241	TOLT1:TT> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7242	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7243	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7244	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7245	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7246	TOLT1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7247	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7248	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR7249	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7252	TOLF1:TF> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7253	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7254	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7255	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7256	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7257	TOLF1:Temp C or F deg	RW	1	0=C 1=F
HR7258	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7259	TOLF1:RTD Sensor	R	1	0=Invalid 1=OK
HR7260	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set 1=Estimate
HR7261	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7264	TOLM1:TM> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7265	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7266	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7267	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7268	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR7269	TOLM1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7270	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing 1=NPS Biasing in use
HR7271	TOLM1:Time constants	RW	1	0=Single 1=Multiple
HR7272	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually 1=Estimate (on-line)
HR7273	TOLM1:T const dyn. balancing	RW	1	0=Fixed 1=Dynamic
HR7274	TOLM1:Dev.Temp (tmax)	RW	1	0=A 1=B 2=F 3=H 4=Manual set
HR7275	TOLM1:Amb. T Selection	RW	1	0=Manual set 1=RTD
HR7276	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR7277	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	LCR1:ISt> condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR7280	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked
HR7281	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7282	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7283	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7284	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7285	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7286	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7287	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7288	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7289	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7290	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7291	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7292	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7293	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7298				HR7297 (Lo-16bits)
HR7299	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7310	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7311	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7312	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7313	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7314	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7315	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7316	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7317	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7324				HR7323 (Lo-16bits)
HR7325	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7326				HR7325 (Lo-16bits)
HR7327	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7328				HR7327 (Lo-16bits)
HR7329	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7330				HR7329 (Lo-16bits)
HR7331	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7332				HR7331 (Lo-16bits)
HR7333	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7355	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)
HR7357	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)
HR7381	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7382	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7383	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7384	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7385	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7386	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7387	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7388	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7389	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7390	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7391	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7392	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7393	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7394	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7395	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7396	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7397	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7398	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7399	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7400	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7401	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7402	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7403	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7404	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7405	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7406	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7407	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7408	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7409	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7410	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7411	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7412	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7413	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7414	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7415	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)
HR7427	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7429	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)
HR7431	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7432	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7433	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7434	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7435	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7436	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7437	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7438	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7439	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7455	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)
HR7469	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7485	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7498				HR7497 (Lo-16bits)
HR7499	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7502				HR7501 (Lo-16bits)
HR7503	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7504	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7505	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7506	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7507	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7508	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7509	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7510	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7511	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7512	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7513	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7514	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7515	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7516	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7517	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7518	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7519	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7520	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7521	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7522	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7523	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7524	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7525	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7526	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7527	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7528	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7529	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7530	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7531	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7532	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7533	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7534	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7535	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7536	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7537	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)
HR7539	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7554	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7555	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7556	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7557	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7558	OV3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7559	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7560	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7561	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)
HR7577	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7582				HR7581 (Lo-16bits)
HR7583	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7585	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)
HR7589	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7590				HR7589 (Lo-16bits)
HR7591	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7592				HR7591 (Lo-16bits)
HR7593	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7594				HR7593 (Lo-16bits)
HR7595	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7596				HR7595 (Lo-16bits)
HR7597	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7615	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7618				HR7617 (Lo-16bits)
HR7619	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7620				HR7619 (Lo-16bits)
HR7621	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7622				HR7621 (Lo-16bits)
HR7623	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7624				HR7623 (Lo-16bits)
HR7625	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7626	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7627	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7628	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7629	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7630	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7631	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7632	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7633	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7634	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7635	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7636	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7637	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7638	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7639	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7640	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7641	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7642	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7643	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7644	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7645	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7646	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7647	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7648	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7649	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7650	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7651	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7652	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7653	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7654	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7655	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7656	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7657	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7658	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7659	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7676	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7677	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7678	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7679	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7680	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7681	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7682	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7683	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7688				HR7687 (Lo-16bits)
HR7689	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7710				HR7709 (Lo-16bits)
HR7711	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7712				HR7711 (Lo-16bits)
HR7713	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7714				HR7713 (Lo-16bits)
HR7715	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7716				HR7715 (Lo-16bits)
HR7717	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)
HR7719	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7744				HR7743 (Lo-16bits)
HR7745	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7746				HR7745 (Lo-16bits)
HR7747	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7748	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7749	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7750	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7751	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7752	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7753	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7754	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7755	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7756	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7757	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7758	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7759	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7760	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7761	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7762	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7763	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7764	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7765	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7766	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7767	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7768	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7769	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7770	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7771	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7772	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7773	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7774	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7775	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7776	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7777	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7778	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7779	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7780	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7781	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7789	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7814	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7815	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7816	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7817	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7818	UV1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7819	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7820	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7821	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)
HR7831	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7845	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7846				HR7845 (Lo-16bits)
HR7847	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7848				HR7847 (Lo-16bits)
HR7849	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7850				HR7849 (Lo-16bits)
HR7851	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7852				HR7851 (Lo-16bits)
HR7853	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7854				HR7853 (Lo-16bits)
HR7855	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7856				HR7855 (Lo-16bits)
HR7857	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7858				HR7857 (Lo-16bits)
HR7859	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7860				HR7859 (Lo-16bits)
HR7861	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)
HR7865	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7875	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7886	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7887	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7888	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7889	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7890	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7891	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7892	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7893	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7894	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7895	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7896	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7897	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7898	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7899	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7900	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7901	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7902	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7903	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7904	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7905	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7906	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7907	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7908	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7909	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7910	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7911	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7912	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7913	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7914	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7915	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7916	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7917	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7918	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7919	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7948				HR7947 (Lo-16bits)
HR7949	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7952	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7953	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7954	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7955	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7956	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7957	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7958	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7959	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7966				HR7965 (Lo-16bits)
HR7967	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7968				HR7967 (Lo-16bits)
HR7969	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7970				HR7969 (Lo-16bits)
HR7971	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7972				HR7971 (Lo-16bits)
HR7973	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7974				HR7973 (Lo-16bits)
HR7975	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7976				HR7975 (Lo-16bits)
HR7977	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7978				HR7977 (Lo-16bits)
HR7979	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7980				HR7979 (Lo-16bits)
HR7981	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7982				HR7981 (Lo-16bits)
HR7983	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7984				HR7983 (Lo-16bits)
HR7985	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7986				HR7985 (Lo-16bits)
HR7987	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7988				HR7987 (Lo-16bits)
HR7989	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7990				HR7989 (Lo-16bits)
HR7991	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7992				HR7991 (Lo-16bits)
HR7993	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7994				HR7993 (Lo-16bits)
HR7995	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7996				HR7995 (Lo-16bits)
HR7997	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7998				HR7997 (Lo-16bits)
HR7999	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8004				HR8003 (Lo-16bits)
HR8005	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8024	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8025	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8026	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8027	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8028	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8029	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8030	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8031	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8032	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8033	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8034	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8035	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8036	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8037	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8038	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8039	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8040	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8041	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8042	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8043	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8044	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8045	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8046	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8047	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR8048	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8049	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8050	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8051	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8052	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8053	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8054	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8055	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8056	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8057	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8074				HR8073 (Lo-16bits)
HR8075	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8090	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8091	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8092	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8093	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8094	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8095	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8096	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8097	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8103	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8104				HR8103 (Lo-16bits)
HR8105	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8106				HR8105 (Lo-16bits)
HR8107	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8108				HR8107 (Lo-16bits)
HR8109	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8110				HR8109 (Lo-16bits)
HR8111	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8112				HR8111 (Lo-16bits)
HR8113	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8114				HR8113 (Lo-16bits)
HR8115	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8116				HR8115 (Lo-16bits)
HR8117	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8118				HR8117 (Lo-16bits)
HR8119	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8120				HR8119 (Lo-16bits)
HR8121	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8122				HR8121 (Lo-16bits)
HR8123	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8124				HR8123 (Lo-16bits)
HR8125	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8126				HR8125 (Lo-16bits)
HR8127	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8128				HR8127 (Lo-16bits)
HR8129	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8130				HR8129 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8131	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8132				HR8131 (Lo-16bits)
HR8133	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8134				HR8133 (Lo-16bits)
HR8135	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8136				HR8135 (Lo-16bits)
HR8137	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8161	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8162	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8163	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8164	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8165	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8166	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8167	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8168	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8169	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8170	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8171	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8172	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8173	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8174	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8175	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8176	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8177	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8178	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8179	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8180	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8181	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8182	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8183	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8184	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8185	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR8186	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8187	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8188	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8189	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8190	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8191	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8192	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8193	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8194	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8195	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8203	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8227	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8228	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8229	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8230	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8231	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8232	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8233	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8234	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8235	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8242				HR8241 (Lo-16bits)
HR8243	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8244				HR8243 (Lo-16bits)
HR8245	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8246				HR8245 (Lo-16bits)
HR8247	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8248				HR8247 (Lo-16bits)
HR8249	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8250				HR8249 (Lo-16bits)
HR8251	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8252				HR8251 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8253	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8254				HR8253 (Lo-16bits)
HR8255	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8256				HR8255 (Lo-16bits)
HR8257	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8258				HR8257 (Lo-16bits)
HR8259	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8260				HR8259 (Lo-16bits)
HR8261	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8262				HR8261 (Lo-16bits)
HR8263	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8264				HR8263 (Lo-16bits)
HR8265	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8266				HR8265 (Lo-16bits)
HR8267	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8268				HR8267 (Lo-16bits)
HR8269	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8270				HR8269 (Lo-16bits)
HR8271	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8272				HR8271 (Lo-16bits)
HR8273	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8274				HR8273 (Lo-16bits)
HR8275	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8276				HR8275 (Lo-16bits)
HR8277	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)
HR8281	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8282				HR8281 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8283	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8300	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8301	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8302	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8303	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8304	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8305	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8306	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8307	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8308	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8309	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8310	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8311	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8312	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8313	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8314	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8315	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8316	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8317	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8318	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8319	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8320	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8321	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8322	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8323	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8324	NOV1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8325	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8326	NOV2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8327	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8328	NOV3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8329	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8330	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8331	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8332	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8333	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8334	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8335	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8336	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8337	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8338	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8339	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8340	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8341	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8354				HR8353 (Lo-16bits)
HR8355	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8374	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8375	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8376	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8377	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8378	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8379	VUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8380	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8381	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8388				HR8387 (Lo-16bits)
HR8389	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8390				HR8389 (Lo-16bits)
HR8391	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8392				HR8391 (Lo-16bits)
HR8393	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8394				HR8393 (Lo-16bits)
HR8395	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8396				HR8395 (Lo-16bits)
HR8397	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8398				HR8397 (Lo-16bits)
HR8399	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8400				HR8399 (Lo-16bits)
HR8401	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8402				HR8401 (Lo-16bits)
HR8403	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8404				HR8403 (Lo-16bits)
HR8405	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8406				HR8405 (Lo-16bits)
HR8407	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8408				HR8407 (Lo-16bits)
HR8409	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8410				HR8409 (Lo-16bits)
HR8411	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8412				HR8411 (Lo-16bits)
HR8413	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)
HR8423	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8436				HR8435 (Lo-16bits)
HR8437	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8446	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8447	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8448	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8449	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8450	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8451	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8452	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8453	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8454	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8455	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8456	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8457	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8458	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8459	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8460	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8461	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8462	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8463	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8464	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8465	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8466	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8467	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8468	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8469	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8470	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8471	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8472	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8473	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8474	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8475	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8476	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8477	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8478	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8479	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8480				HR8479 (Lo-16bits)
HR8481	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8507	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8512	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8513	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8514	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8515	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8516	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8517	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8518	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8519	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8526				HR8525 (Lo-16bits)
HR8527	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8528				HR8527 (Lo-16bits)
HR8529	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8530				HR8529 (Lo-16bits)
HR8531	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8532				HR8531 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8533	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8534				HR8533 (Lo-16bits)
HR8535	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8536				HR8535 (Lo-16bits)
HR8537	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8538				HR8537 (Lo-16bits)
HR8539	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8540				HR8539 (Lo-16bits)
HR8541	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8542				HR8541 (Lo-16bits)
HR8543	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8544				HR8543 (Lo-16bits)
HR8545	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8546				HR8545 (Lo-16bits)
HR8547	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8548				HR8547 (Lo-16bits)
HR8549	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8563	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)
HR8577	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8584	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8585	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8586	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8587	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8588	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8589	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8590	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8591	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8592	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8593	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8594	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8595	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8596	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8597	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8598	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8599	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8600	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8601	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8602	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8603	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8604	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8605	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8606	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8607	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8608	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8609	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8610	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8611	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8612	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8613	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8614	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8615	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8616	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8617	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8635	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8650	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8651	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8652	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8653	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8654	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8655	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8656	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8657	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8660				HR8659 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8661	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8664				HR8663 (Lo-16bits)
HR8665	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8666				HR8665 (Lo-16bits)
HR8667	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8668				HR8667 (Lo-16bits)
HR8669	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8670				HR8669 (Lo-16bits)
HR8671	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8672				HR8671 (Lo-16bits)
HR8673	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8674				HR8673 (Lo-16bits)
HR8675	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8676				HR8675 (Lo-16bits)
HR8677	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8678				HR8677 (Lo-16bits)
HR8679	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8680				HR8679 (Lo-16bits)
HR8681	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8682				HR8681 (Lo-16bits)
HR8683	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8684				HR8683 (Lo-16bits)
HR8685	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8688				HR8687 (Lo-16bits)
HR8689	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8692				HR8691 (Lo-16bits)
HR8693	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8718				HR8717 (Lo-16bits)
HR8719	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8722	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8723	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8724	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8725	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8726	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8727	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8728	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8729	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8730	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8731	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8732	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8733	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8734	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8735	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8736	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8737	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8738	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8739	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8740	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8741	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8742	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8743	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8744	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8745	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8746	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8747	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8748	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8749	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8750	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8751	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8752	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8753	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8754	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8755	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8760				HR8759 (Lo-16bits)
HR8761	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8784				HR8783 (Lo-16bits)
HR8785	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8786				HR8785 (Lo-16bits)
HR8787	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8788	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8789	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8790	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8791	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8792	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8793	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8794	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8795	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8802				HR8801 (Lo-16bits)
HR8803	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8804				HR8803 (Lo-16bits)
HR8805	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8806				HR8805 (Lo-16bits)
HR8807	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8808				HR8807 (Lo-16bits)
HR8809	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8810				HR8809 (Lo-16bits)
HR8811	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8812				HR8811 (Lo-16bits)
HR8813	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8814				HR8813 (Lo-16bits)
HR8815	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8816				HR8815 (Lo-16bits)
HR8817	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8818				HR8817 (Lo-16bits)
HR8819	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8820				HR8819 (Lo-16bits)
HR8821	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8842				HR8841 (Lo-16bits)
HR8843	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8846				HR8845 (Lo-16bits)
HR8847	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8848				HR8847 (Lo-16bits)
HR8849	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8850				HR8849 (Lo-16bits)
HR8851	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8852				HR8851 (Lo-16bits)
HR8853	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8860	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8861	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8862	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8863	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8864	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8865	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8866	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8867	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8868	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8869	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8870	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8871	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8872	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8873	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8874	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8875	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8876	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8877	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8878	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8879	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8880	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8881	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8882	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8883	PGS1:PS1 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8884	PGS1:PS2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8885	PGS1:PS3 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8886	PGS1:PS4 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8887	PGS1:PS5 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8888	PGS1:PS6 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8889	PGS1:PS7 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8890	PGS1:PS8 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8891	PGS1:PS9 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8892	PGS1:PS10 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8893	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8894	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8895	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8896	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8897	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8898	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8899	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8900	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8901	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR8902	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR8903	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8904	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes
HR8905	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8906	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR8907	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8908	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR8909	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8910	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR8911	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8912	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR8913	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8914	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8915	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8916	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8917	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8918	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8919	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8920	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8921	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8922	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8923	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8924	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8925	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8926	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8927				HR8926 (Lo-16bits)
HR8928	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8929				HR8928 (Lo-16bits)
HR8930	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8931				HR8930 (Lo-16bits)
HR8932	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8933				HR8932 (Lo-16bits)
HR8934	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8935				HR8934 (Lo-16bits)
HR8936	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8937				HR8936 (Lo-16bits)
HR8938	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8939				HR8938 (Lo-16bits)
HR8940	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8941				HR8940 (Lo-16bits)
HR8942	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8943				HR8942 (Lo-16bits)
HR8944	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8945				HR8944 (Lo-16bits)
HR8946	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8947				HR8946 (Lo-16bits)
HR8948	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8949				HR8948 (Lo-16bits)
HR8950	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8951				HR8950 (Lo-16bits)
HR8952	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8953				HR8952 (Lo-16bits)
HR8954	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8955				HR8954 (Lo-16bits)
HR8956	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8957				HR8956 (Lo-16bits)
HR8958	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8959	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8960	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8961	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8962	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8963	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8964	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8965	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8966	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8967				HR8966 (Lo-16bits)
HR8968	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8969				HR8968 (Lo-16bits)
HR8970	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8971				HR8970 (Lo-16bits)
HR8972	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8973				HR8972 (Lo-16bits)
HR8974	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8975				HR8974 (Lo-16bits)
HR8976	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8977				HR8976 (Lo-16bits)
HR8978	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8979				HR8978 (Lo-16bits)
HR8980	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8981				HR8980 (Lo-16bits)
HR8982	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8983				HR8982 (Lo-16bits)
HR8984	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8985				HR8984 (Lo-16bits)
HR8986	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8987				HR8986 (Lo-16bits)
HR8988	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8989				HR8988 (Lo-16bits)
HR8990	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8991				HR8990 (Lo-16bits)
HR8992	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8993				HR8992 (Lo-16bits)
HR8994	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8995				HR8994 (Lo-16bits)
HR8996	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8997				HR8996 (Lo-16bits)
HR8998	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8999	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9000	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9001	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9002	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9003	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9004	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9005	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR9006	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9007				HR9006 (Lo-16bits)
HR9008	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9009				HR9008 (Lo-16bits)
HR9010	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9011				HR9010 (Lo-16bits)
HR9012	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9013				HR9012 (Lo-16bits)
HR9014	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9015				HR9014 (Lo-16bits)
HR9016	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9017				HR9016 (Lo-16bits)
HR9018	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9019				HR9018 (Lo-16bits)
HR9020	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9021				HR9020 (Lo-16bits)
HR9022	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9023				HR9022 (Lo-16bits)
HR9024	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9025				HR9024 (Lo-16bits)
HR9026	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9027				HR9026 (Lo-16bits)
HR9028	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9029				HR9028 (Lo-16bits)
HR9030	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9031				HR9030 (Lo-16bits)
HR9032	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9033				HR9032 (Lo-16bits)
HR9034	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9035				HR9034 (Lo-16bits)
HR9036	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9037				HR9036 (Lo-16bits)
HR9038	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9039	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9040	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9041	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9042	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9043	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9044	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9045	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR9046	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9047				HR9046 (Lo-16bits)
HR9048	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9049				HR9048 (Lo-16bits)
HR9050	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9051				HR9050 (Lo-16bits)
HR9052	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9053				HR9052 (Lo-16bits)
HR9054	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9055				HR9054 (Lo-16bits)
HR9056	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9057				HR9056 (Lo-16bits)
HR9058	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9059				HR9058 (Lo-16bits)
HR9060	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9061				HR9060 (Lo-16bits)
HR9062	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9063				HR9062 (Lo-16bits)
HR9064	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9065				HR9064 (Lo-16bits)
HR9066	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9067				HR9066 (Lo-16bits)
HR9068	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9069				HR9068 (Lo-16bits)
HR9070	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9071				HR9070 (Lo-16bits)
HR9072	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9073				HR9072 (Lo-16bits)
HR9074	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9075				HR9074 (Lo-16bits)
HR9076	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9077				HR9076 (Lo-16bits)
HR9078	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9079	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9080	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9081	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9082	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9083	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9084	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9085	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9086	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9087				HR9086 (Lo-16bits)
HR9088	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9089				HR9088 (Lo-16bits)
HR9090	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9091				HR9090 (Lo-16bits)
HR9092	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9093				HR9092 (Lo-16bits)
HR9094	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9095				HR9094 (Lo-16bits)
HR9096	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9097				HR9096 (Lo-16bits)
HR9098	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9099				HR9098 (Lo-16bits)
HR9100	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9101				HR9100 (Lo-16bits)
HR9102	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9103				HR9102 (Lo-16bits)
HR9104	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9105				HR9104 (Lo-16bits)
HR9106	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9107				HR9106 (Lo-16bits)
HR9108	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9109				HR9108 (Lo-16bits)
HR9110	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9111				HR9110 (Lo-16bits)
HR9112	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9113				HR9112 (Lo-16bits)
HR9114	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9115				HR9114 (Lo-16bits)
HR9116	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9117				HR9116 (Lo-16bits)
HR9118	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9119	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9120	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9121	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9122	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9123	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9124	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9125	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9126	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9127				HR9126 (Lo-16bits)
HR9128	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9129				HR9128 (Lo-16bits)
HR9130	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9131				HR9130 (Lo-16bits)
HR9132	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9133				HR9132 (Lo-16bits)
HR9134	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9135				HR9134 (Lo-16bits)
HR9136	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9137				HR9136 (Lo-16bits)
HR9138	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9139				HR9138 (Lo-16bits)
HR9140	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9141				HR9140 (Lo-16bits)
HR9142	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9143				HR9142 (Lo-16bits)
HR9144	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9145				HR9144 (Lo-16bits)
HR9146	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9147				HR9146 (Lo-16bits)
HR9148	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9149				HR9148 (Lo-16bits)
HR9150	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9151				HR9150 (Lo-16bits)
HR9152	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9153				HR9152 (Lo-16bits)
HR9154	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9155				HR9154 (Lo-16bits)
HR9156	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9157				HR9156 (Lo-16bits)
HR9158	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9159	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9160	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9161	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9162	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9163	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9164	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9165	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9166	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9167				HR9166 (Lo-16bits)
HR9168	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9169				HR9168 (Lo-16bits)
HR9170	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9171				HR9170 (Lo-16bits)
HR9172	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9173				HR9172 (Lo-16bits)
HR9174	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9175				HR9174 (Lo-16bits)
HR9176	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9177				HR9176 (Lo-16bits)
HR9178	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9179				HR9178 (Lo-16bits)
HR9180	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9181				HR9180 (Lo-16bits)
HR9182	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9183				HR9182 (Lo-16bits)
HR9184	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9185				HR9184 (Lo-16bits)
HR9186	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9187				HR9186 (Lo-16bits)
HR9188	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9189				HR9188 (Lo-16bits)
HR9190	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9191				HR9190 (Lo-16bits)
HR9192	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9193				HR9192 (Lo-16bits)
HR9194	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9195				HR9194 (Lo-16bits)
HR9196	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9197				HR9196 (Lo-16bits)
HR9198	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9199	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9200	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9201	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9202	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9203	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9204	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9205	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9206	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9207				HR9206 (Lo-16bits)
HR9208	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9209				HR9208 (Lo-16bits)
HR9210	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9211				HR9210 (Lo-16bits)
HR9212	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9213				HR9212 (Lo-16bits)
HR9214	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9215				HR9214 (Lo-16bits)
HR9216	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9217				HR9216 (Lo-16bits)
HR9218	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9219				HR9218 (Lo-16bits)
HR9220	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9221				HR9220 (Lo-16bits)
HR9222	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9223				HR9222 (Lo-16bits)
HR9224	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9225				HR9224 (Lo-16bits)
HR9226	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9227				HR9226 (Lo-16bits)
HR9228	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9229				HR9228 (Lo-16bits)
HR9230	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9231				HR9230 (Lo-16bits)
HR9232	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9233				HR9232 (Lo-16bits)
HR9234	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9235				HR9234 (Lo-16bits)
HR9236	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9237				HR9236 (Lo-16bits)
HR9238	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9239	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9240	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9241	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9242	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9243	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9244	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9245	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9246	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR9247	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR9252	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9253	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9254	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9255	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9256	OPW1:P> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9257	UPW1:P< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9258	RPW1:Prev> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9259	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9260	SEF1:U03rd< Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR9261	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9262	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9263	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9264	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9265	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR9266	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=VHz(1) Blocked 2=VHz(1) Start 3=VHz(1) Alarm 4=VHz(1) Trip
HR9267	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=VHz(2) Start 2=VHz(2) Alarm 3=VHz(2) Trip 4=VHz(2) Blocked
HR9268	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR9269	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9271	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9274	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9275				HR9274 (Lo-16bits)
HR9276	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9277				HR9276 (Lo-16bits)
HR9278	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9279				HR9278 (Lo-16bits)
HR9280	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9281	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9288	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9289				HR9288 (Lo-16bits)
HR9290	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9291				HR9290 (Lo-16bits)
HR9292	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9293				HR9292 (Lo-16bits)
HR9294	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9295	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9296				HR9295 (Lo-16bits)
HR9297	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9302	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9303				HR9302 (Lo-16bits)
HR9304	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9305				HR9304 (Lo-16bits)
HR9306	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9307				HR9306 (Lo-16bits)
HR9308	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9309	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9310				HR9309 (Lo-16bits)
HR9311	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)
HR9317	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9318	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9319				HR9318 (Lo-16bits)
HR9320	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9321				HR9320 (Lo-16bits)
HR9322	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9323				HR9322 (Lo-16bits)
HR9324	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9325				HR9324 (Lo-16bits)
HR9326	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9327	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9328				HR9327 (Lo-16bits)
HR9329	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9334	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9335				HR9334 (Lo-16bits)
HR9336	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9337				HR9336 (Lo-16bits)
HR9338	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9339				HR9338 (Lo-16bits)
HR9340	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9341	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9346				HR9345 (Lo-16bits)
HR9347	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9348	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9349				HR9348 (Lo-16bits)
HR9350	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9351				HR9350 (Lo-16bits)
HR9352	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9353				HR9352 (Lo-16bits)
HR9354	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9355	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)
HR9361	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9362	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9363				HR9362 (Lo-16bits)
HR9364	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9365				HR9364 (Lo-16bits)
HR9366	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9367				HR9366 (Lo-16bits)
HR9368	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9369	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9371	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9378	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9379				HR9378 (Lo-16bits)
HR9380	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9381				HR9380 (Lo-16bits)
HR9382	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9383				HR9382 (Lo-16bits)
HR9384	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9385				HR9384 (Lo-16bits)
HR9386	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9387	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)
HR9391	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9395	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9419	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9449	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9462				HR9461 (Lo-16bits)
HR9463	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9464				HR9463 (Lo-16bits)
HR9465	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9466				HR9465 (Lo-16bits)
HR9467	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9468				HR9467 (Lo-16bits)
HR9469	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9479	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9509	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)
HR9515	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	ARC1:I/O Arc> condition	R	1	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked
HR9518	ARC1:I/O Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9519				HR9518 (Lo-16bits)
HR9520	ARC1:I/O Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9521				HR9520 (Lo-16bits)
HR9522	ARC1:I/O Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9523				HR9522 (Lo-16bits)
HR9524	ARC1:I/O Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9525				HR9524 (Lo-16bits)
HR9526	ARC1:I/O Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9527				HR9526 (Lo-16bits)
HR9528	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9529	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9530	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTBusfail 3=VTLinefail 4=Blocked
HR9531	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR9532	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR9533	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9534	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR9535	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR9536	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR9537	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)
HR9545	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9559	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9566				HR9565 (Lo-16bits)
HR9567	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9568				HR9567 (Lo-16bits)
HR9569	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9570				HR9569 (Lo-16bits)
HR9571	VJP1:<DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR9572	VJP1:Voltage meas selected	R	1	0=Selection Ok 1=Selection not available
HR9573	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9574	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9575	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9576	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9577	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9578	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9579	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9580	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR9581	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9582	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9583	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9584	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9585	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9586	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9587	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9588	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled 1=Activated
HR9589	AR1:AR Status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR9590				HR9589 (Lo-16bits)
HR9591	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR9592	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR9593				HR9592 (Lo-16bits)
HR9594	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR9597	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9598				HR9597 (Lo-16bits)
HR9599	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR9600				HR9599 (Lo-16bits)
HR9601	VRG1:Voltage meas condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok
HR9602	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR9607	VRG1:Remote tap control input	RW	1	0=Stop 1=Lower 2=Higher
HR9608	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9617				HR9616 (Lo-16bits)
HR9618	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9619				HR9618 (Lo-16bits)
HR9620	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9621				HR9620 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9622	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9623				HR9622 (Lo-16bits)
HR9624	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9625				HR9624 (Lo-16bits)
HR9626	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9627				HR9626 (Lo-16bits)
HR9628	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9629				HR9628 (Lo-16bits)
HR9630	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9631				HR9630 (Lo-16bits)
HR9632	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9633				HR9632 (Lo-16bits)
HR9634	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9635				HR9634 (Lo-16bits)
HR9636	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9637				HR9636 (Lo-16bits)
HR9638	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9639				HR9638 (Lo-16bits)
HR9640	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9641				HR9640 (Lo-16bits)
HR9642	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9643				HR9642 (Lo-16bits)
HR9644	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9645				HR9644 (Lo-16bits)
HR9646	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9647				HR9646 (Lo-16bits)
HR9648	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9649				HR9648 (Lo-16bits)
HR9650	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9651				HR9650 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9652	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9653				HR9652 (Lo-16bits)
HR9654	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9655				HR9654 (Lo-16bits)
HR9656	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9657				HR9656 (Lo-16bits)
HR9658	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9659				HR9658 (Lo-16bits)
HR9660	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9661				HR9660 (Lo-16bits)
HR9662	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9663				HR9662 (Lo-16bits)
HR9664	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9665				HR9664 (Lo-16bits)
HR9666	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9667				HR9666 (Lo-16bits)
HR9668	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9669				HR9668 (Lo-16bits)
HR9670	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9671				HR9670 (Lo-16bits)
HR9672	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9673				HR9672 (Lo-16bits)
HR9674	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9675				HR9674 (Lo-16bits)
HR9676	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9677				HR9676 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9678	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9679				HR9678 (Lo-16bits)
HR9680	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9681				HR9680 (Lo-16bits)
HR9682	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9683				HR9682 (Lo-16bits)
HR9684	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9685				HR9684 (Lo-16bits)
HR9686	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9687				HR9686 (Lo-16bits)
HR9688	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLT detected
HR9689	EMON1:I0> (50N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9690	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLT detected
HR9691	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9692	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal 1=FLT FW 2=FLT REV
HR9693	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=No voltage
HR9694	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9695	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLT detected
HR9696	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR9697				HR9696 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9698	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR9699				HR9698 (Lo-16bits)
HR9700	GSYN:Synchronizer mode	RW	1	0=Disabled 1=Activated
HR9701	GSYN:Synchronizing by	RW	1	0=Autosynchronizer 1=Manual commands
HR9702	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch 1=I know what I am doing
HR9703	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9723	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR9732				HR9731 (Lo-16bits)
HR9733	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)
HR9743	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9748				HR9747 (Lo-16bits)
HR9749	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9750				HR9749 (Lo-16bits)
HR9751	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9752				HR9751 (Lo-16bits)
HR9753	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9754				HR9753 (Lo-16bits)
HR9755	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)
HR9769	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9772				HR9771 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9773	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9774				HR9773 (Lo-16bits)
HR9775	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9776				HR9775 (Lo-16bits)
HR9777	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9778				HR9777 (Lo-16bits)
HR9779	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9780				HR9779 (Lo-16bits)
HR9781	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9782				HR9781 (Lo-16bits)
HR9783	VREC:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9784	VREC:Scale current values to primary	RW	1	0=No 1=Yes
HR9785	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	RW	1	0=No 1=Yes
HR9786	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9787				HR9786 (Lo-16bits)
HR9788	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR9789				HR9788 (Lo-16bits)
HR9790	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR9791				HR9790 (Lo-16bits)
HR9792	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR9793				HR9792 (Lo-16bits)
HR9794	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR9795				HR9794 (Lo-16bits)
HR9796	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR9797				HR9796 (Lo-16bits)
HR9798	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9799				HR9798 (Lo-16bits)
HR9800	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR9801				HR9800 (Lo-16bits)
HR9802	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR9803				HR9802 (Lo-16bits)
HR9804	RTB:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9805	RTB:Scale current values to primary	RW	1	0=No 1=Yes
HR9806	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9807				HR9806 (Lo-16bits)
HR9808	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9809				HR9808 (Lo-16bits)
HR9810	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9811				HR9810 (Lo-16bits)
HR9812	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9813				HR9812 (Lo-16bits)
HR9814	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9815				HR9814 (Lo-16bits)
HR9816	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9817				HR9816 (Lo-16bits)
HR9818	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9819				HR9818 (Lo-16bits)
HR9820	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9821				HR9820 (Lo-16bits)
HR9822	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9823				HR9822 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9824	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9825				HR9824 (Lo-16bits)
HR9826	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR9827				HR9826 (Lo-16bits)
HR9828	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR9829				HR9828 (Lo-16bits)
HR9830	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9831				HR9830 (Lo-16bits)
HR9832	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9833				HR9832 (Lo-16bits)
HR9834	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9835				HR9834 (Lo-16bits)
HR9836	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9837				HR9836 (Lo-16bits)
HR9838	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9839				HR9838 (Lo-16bits)
HR9840	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9841				HR9840 (Lo-16bits)
HR9842	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9843				HR9842 (Lo-16bits)
HR9844	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9845				HR9844 (Lo-16bits)
HR9846	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9847				HR9846 (Lo-16bits)
HR9848	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9849				HR9848 (Lo-16bits)
HR9850	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9851				HR9850 (Lo-16bits)
HR9852	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9853				HR9852 (Lo-16bits)
HR9854	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9855				HR9854 (Lo-16bits)
HR9856	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9857				HR9856 (Lo-16bits)
HR9858	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9859				HR9858 (Lo-16bits)
HR9860	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9861				HR9860 (Lo-16bits)
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

1.5 G Modbus map

The following table lists Modbus holding registers for AQ-G215 devices with firmware G_v2.1.1.3-247.

**NOTICE!**

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools* → *Communication* → *Modbus map*.

Table. 1.5 - 5. Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index

Holding Register	Parameter	Access	Length	Info
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1: Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1: Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1: Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1: Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1: Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1: Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1: Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1: Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1: Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR43				HR42 (Lo-16bits)
HR44	MEAS1: Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1: Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1: calc.I0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1: Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)
HR102	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR110	MEAS1:IL1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)
HR132	MEAS1:IL1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR140	MEAS1:IL1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)
HR162	MEAS1:IL1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR169				HR168 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR170	MEAS1:IL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)
HR192	MEAS1:IL2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR200	MEAS1:IL2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)
HR222	MEAS1:IL2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR230	MEAS1:IL2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)
HR252	MEAS1:IL3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR260	MEAS1:IL3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)
HR282	MEAS1:IL3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:IL3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR290	MEAS1:IL3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:IL3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:IL3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:IL3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:IL3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)
HR312	MEAS1:I01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR320	MEAS1:I01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)
HR342	MEAS1:I01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR350	MEAS1:I01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)
HR372	MEAS1:I02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR380	MEAS1:I02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)
HR402	MEAS1:I02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR410	MEAS1:I02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS1:I02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS1:I02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS1:I02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS1:I02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS1:I02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS1:I02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS1:I02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS1:I02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS1:I02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS1:I02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR431				HR430 (Lo-16bits)
HR432	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR440	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2:Pri.calc.I"0	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2:calc.I"0 Pha.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2:Pha.curr.I"L1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR459				HR458 (Lo-16bits)
HR460	MEAS2:Pha.curr.I"L2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.curr.I"L3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:I" Pri.Pos.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:I" Pri.Neg.seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:I" Pri.Zero seq.curr.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR469				HR468 (Lo-16bits)
HR470	MEAS2:I" Pos.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:I" Neg.seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:I" Zero seq.curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.curr.I"01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:Res.curr.I"02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:Pha.I"L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR489				HR488 (Lo-16bits)
HR490	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR495				HR494 (Lo-16bits)
HR496	MEAS2:Res.I ⁰ 2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:Res.I ⁰ 2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:P-P curr.I ⁰ L1	R	2	FLOAT [0...500] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:P-P curr.I ⁰ L2	R	2	FLOAT [0...500] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:P-P curr.I ⁰ L3	R	2	FLOAT [0...500] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:P-P curr.I ⁰ 01	R	2	FLOAT [0...500] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:P-P curr.I ⁰ 02	R	2	FLOAT [0...500] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I ⁰ L1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I ⁰ L1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I ⁰ L1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I ⁰ L1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I ⁰ L1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)
HR520	MEAS2:I ⁰ L1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:I ⁰ L1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR524	MEAS2:I"L1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:I"L1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:I"L1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:I"L1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:I"L1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:I"L1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:I"L1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:I"L1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:I"L1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:I"L1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:I"L1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:I"L1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:I"L1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)
HR550	MEAS2:I"L1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I"L1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR554	MEAS2:I"L1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I"L1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I"L1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I"L1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I"L1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I"L1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I"L1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I"L1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I"L1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I"L1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I"L1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR579				HR578 (Lo-16bits)
HR580	MEAS2:I"L2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I"L2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR584	MEAS2:I ² L2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I ² L2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I ² L2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I ² L2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I ² L2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I ² L2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I ² L2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I ² L2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I ² L2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I ² L2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I ² L2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I ² L2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I ² L2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)
HR610	MEAS2:I ² L2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I ² L2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR614	MEAS2:I"L2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I"L2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I"L2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I"L2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I"L2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I"L2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I"L2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I"L2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I"L2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I"L2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I"L2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I"L2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I"L2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)
HR640	MEAS2:I"L2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR643				HR642 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR644	MEAS2:I ³ L3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I ³ L3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I ³ L3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I ³ L3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I ³ L3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I ³ L3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I ³ L3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I ³ L3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I ³ L3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I ³ L3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I ³ L3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I ³ L3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I ³ L3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)
HR670	MEAS2:I ³ L3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I ³ L3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR674	MEAS2:I"L3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"L3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"L3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"L3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"L3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"L3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"L3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"L3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"L3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"L3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"L3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"L3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"L3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)
HR700	MEAS2:I"L3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"L3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR704	MEAS2:I"L3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"L3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)
HR730	MEAS2:I"01 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR734	MEAS2:I"01 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"01 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"01 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"01 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"01 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"01 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"01 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"01 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"01 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"01 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"01 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"01 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"01 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)
HR760	MEAS2:I"01 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"01 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR764	MEAS2:I"01 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"01 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"01 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"01 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"01 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)
HR790	MEAS2:I"02 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR794	MEAS2:I"02 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS2:I"02 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS2:I"02 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS2:I"02 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS2:I"02 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS2:I"02 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS2:I"02 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS2:I"02 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS2:I"02 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS2:I"02 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS2:I"02 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS2:I"02 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR819				HR818 (Lo-16bits)
HR820	MEAS2:I"02 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS2:I"02 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR823				HR822 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR824	MEAS2:I"02 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS2:I"02 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS2:I"02 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS2:I"02 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS2:I"02 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS2:I"02 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS2:I"02 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS2:I"02 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U1Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U1Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U2Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR849				HR848 (Lo-16bits)
HR850	MEAS3:U3Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U3Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR853				HR852 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR854	MEAS3:U4Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:U4Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:U1Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:U1Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:U2Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:U2Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:U3Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:U3Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:U4Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:U4Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:Pos.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:Pos.seq.Volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:Neg.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)
HR880	MEAS3:Neg.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:Zero.seq.Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR883				HR882 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR884	MEAS3:Zero.seq.Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:U0 Calc. pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:Neg.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:Zero.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt UL12 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)
HR910	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR912	MEAS3:System volt UL23 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL31 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:System volt UL 1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:System volt UL 1 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:System volt UL 1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:System volt UL2 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR935				HR934 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR936	MEAS3:System volt UL3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR939				HR938 (Lo-16bits)
HR940	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:System volt U0 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:System volt U3 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:System volt U4 mag(kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR961				HR960 (Lo-16bits)
HR962	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:UL1 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR969				HR968 (Lo-16bits)
HR970	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:UL2 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:UL3 Power THD	R	2	FLOAT [0...100] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U1 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U1 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U1 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U1 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U1 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U1 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U1 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U1 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)
HR1000	MEAS3:U1 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U1 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U1 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U1 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U1 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U1 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U1 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U1 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U1 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U1 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U1 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U1 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U1 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U1 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U1 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)
HR1030	MEAS3:U1 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U1 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U1 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U1 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U1 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U2 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U2 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U2 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U2 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U2 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U2 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U2 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)
HR1060	MEAS3:U2 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U2 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U2 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U2 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U2 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U2 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U2 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U2 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U2 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U2 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U2 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U2 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U2 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U2 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U2 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)
HR1090	MEAS3:U2 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U2 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U2 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U2 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U2 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U2 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U3 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U3 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U3 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U3 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U3 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U3 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)
HR1120	MEAS3:U3 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U3 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U3 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U3 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U3 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U3 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U3 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U3 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U3 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U3 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U3 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U3 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U3 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U3 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U3 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)
HR1150	MEAS3:U3 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U3 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U3 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U3 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U3 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U3 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U3 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 Fund	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 2.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 3.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1169				HR1168 (Lo-16bits)
HR1170	MEAS3:U4 4.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	MEAS3:U4 5.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	MEAS3:U4 6.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	MEAS3:U4 7.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)
HR1178	MEAS3:U4 8.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	MEAS3:U4 9.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	MEAS3:U4 10.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	MEAS3:U4 11.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	MEAS3:U4 12.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	MEAS3:U4 13.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	MEAS3:U4 14.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	MEAS3:U4 15.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	MEAS3:U4 16.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	MEAS3:U4 17.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	MEAS3:U4 18.harm	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1199				HR1198 (Lo-16bits)
HR1200	MEAS3:U4 19.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)
HR1202	MEAS3:U4 20.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	MEAS3:U4 21.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	MEAS3:U4 22.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1207				HR1206 (Lo-16bits)
HR1208	MEAS3:U4 23.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1209				HR1208 (Lo-16bits)
HR1210	MEAS3:U4 24.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1211				HR1210 (Lo-16bits)
HR1212	MEAS3:U4 25.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1213				HR1212 (Lo-16bits)
HR1214	MEAS3:U4 26.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1215				HR1214 (Lo-16bits)
HR1216	MEAS3:U4 27.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1217				HR1216 (Lo-16bits)
HR1218	MEAS3:U4 28.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1219				HR1218 (Lo-16bits)
HR1220	MEAS3:U4 29.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1221				HR1220 (Lo-16bits)
HR1222	MEAS3:U4 30.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1223				HR1222 (Lo-16bits)
HR1224	MEAS3:U4 31.harm	R	2	FLOAT [0...100000] (Hi-16bits)
HR1225				HR1224 (Lo-16bits)
HR1226	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1227				HR1226 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1228	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1229				HR1228 (Lo-16bits)
HR1230	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1231				HR1230 (Lo-16bits)
HR1232	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1233				HR1232 (Lo-16bits)
HR1234	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1235				HR1234 (Lo-16bits)
HR1236	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1237				HR1236 (Lo-16bits)
HR1238	IMERX:Pos.seq. Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1239				HR1238 (Lo-16bits)
HR1240	IMERX:Pos.Seq. Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1241				HR1240 (Lo-16bits)
HR1242	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1243				HR1242 (Lo-16bits)
HR1244	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1245				HR1244 (Lo-16bits)
HR1246	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1247				HR1246 (Lo-16bits)
HR1248	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1249				HR1248 (Lo-16bits)
HR1250	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1251				HR1250 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1252	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1253				HR1252 (Lo-16bits)
HR1254	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1255				HR1254 (Lo-16bits)
HR1256	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR1257				HR1256 (Lo-16bits)
HR1258	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1259				HR1258 (Lo-16bits)
HR1260	POW1:Export Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1261				HR1260 (Lo-16bits)
HR1262	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1263				HR1262 (Lo-16bits)
HR1264	POW1:Import Active Energy (P) kWh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1265				HR1264 (Lo-16bits)
HR1266	POW1:Active Energy (P) Export/Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1267				HR1266 (Lo-16bits)
HR1268	POW1:Active Energy (P) Export/Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1269				HR1268 (Lo-16bits)
HR1270	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1271				HR1270 (Lo-16bits)
HR1272	POW1:Exported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1273				HR1272 (Lo-16bits)
HR1274	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1275				HR1274 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1276	POW1:Imported (Q) while Export (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1277				HR1276 (Lo-16bits)
HR1278	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1279				HR1278 (Lo-16bits)
HR1280	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1281				HR1280 (Lo-16bits)
HR1282	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1283				HR1282 (Lo-16bits)
HR1284	POW1:Exported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1285				HR1284 (Lo-16bits)
HR1286	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1287				HR1286 (Lo-16bits)
HR1288	POW1:Imported (Q) while Import (P) kVarh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1289				HR1288 (Lo-16bits)
HR1290	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1291				HR1290 (Lo-16bits)
HR1292	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1293				HR1292 (Lo-16bits)
HR1294	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1295				HR1294 (Lo-16bits)
HR1296	POW1:Apparent Energy (S) while Export (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1298	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	POW1:Apparent Energy (S) while Import (P) kVAh	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	POW1:Real Energy (P) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	POW1:Reactive Energy (Q) Net	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	POW1:Real Energy (P) Demand (Default demand direction from busbar)	RW	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	POW1:Reset 3ph Energies	RW	1	0=- 1=Reset
HR1317	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)
HR1319	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1321	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	POW1:3PH PF	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1338				HR1337 (Lo-16bits)
HR1339	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1340				HR1339 (Lo-16bits)
HR1341	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1342				HR1341 (Lo-16bits)
HR1343	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1344				HR1343 (Lo-16bits)
HR1345	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1346				HR1345 (Lo-16bits)
HR1347	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1348				HR1347 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1349	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1350				HR1349 (Lo-16bits)
HR1351	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1352				HR1351 (Lo-16bits)
HR1353	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1354				HR1353 (Lo-16bits)
HR1355	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1356				HR1355 (Lo-16bits)
HR1357	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1358				HR1357 (Lo-16bits)
HR1359	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1373	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)
HR1379	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1380				HR1379 (Lo-16bits)
HR1381	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1382				HR1381 (Lo-16bits)
HR1383	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1384				HR1383 (Lo-16bits)
HR1385	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1386				HR1385 (Lo-16bits)
HR1387	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR1388				HR1387 (Lo-16bits)
HR1389	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...20] (Hi-16bits)
HR1390				HR1389 (Lo-16bits)
HR1391	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1392				HR1391 (Lo-16bits)
HR1393	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR1394				HR1393 (Lo-16bits)
HR1395	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR1396				HR1395 (Lo-16bits)
HR1397	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR1398				HR1397 (Lo-16bits)
HR1399	M1VT1:VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1400				HR1399 (Lo-16bits)
HR1401	M1VT1:VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1402				HR1401 (Lo-16bits)
HR1403	M1VT1:U3 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1404				HR1403 (Lo-16bits)
HR1405	M1VT1:U3 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1406				HR1405 (Lo-16bits)
HR1407	M1VT1:U4 Res/SS VT primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR1408				HR1407 (Lo-16bits)
HR1409	M1VT1:U4 Res/SS VT secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR1410				HR1409 (Lo-16bits)
HR1411	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1412				HR1411 (Lo-16bits)
HR1413	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1414				HR1413 (Lo-16bits)
HR1415	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1416				HR1415 (Lo-16bits)
HR1417	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR1418				HR1417 (Lo-16bits)
HR1419	DIGI:DIs (cpu)	R	2	0b=DI1 1b=DI2 2b=DI3 (Hi-16bits)
HR1420				HR1419 (Lo-16bits)
HR1421	DIGI:DIs (input option 1)	R	2	0b=DI4 1b=DI5 2b=DI6 3b=DI7 4b=DI8 5b=DI9 6b=DI10 7b=DI11 (Hi-16bits)
HR1422				HR1421 (Lo-16bits)
HR1423	DIGI:DIs (input option 2)	R	2	0b=DI12 1b=DI13 2b=DI14 3b=DI15 4b=DI16 5b=DI17 6b=DI18 7b=DI19 (Hi-16bits)
HR1424				HR1423 (Lo-16bits)
HR1425	DIGI:DIs (input option 3)	R	2	0b=DI20 1b=DI21 2b=DI22 3b=DI23 4b=DI24 5b=DI25 6b=DI26 7b=DI27 (Hi-16bits)
HR1426				HR1425 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1427	DIGI:DIs (input option 4)	R	2	0b=D28 1b=DI29 2b=DI30 3b=DI31 4b=DI32 5b=DI33 6b=DI34 7b=DI35 (Hi-16bits)
HR1428				HR1427 (Lo-16bits)
HR1429	DIGI:DIs (input option 5)	R	2	0b=DI36 1b=DI37 2b=DI38 3b=DI39 4b=DI40 5b=DI41 6b=DI42 7b=DI43 (Hi-16bits)
HR1430				HR1429 (Lo-16bits)
HR1431	DIGI:DIs (input option 6)	R	2	0b=DI44 1b=DI45 2b=DI46 3b=DI47 4b=DI48 5b=DI49 6b=DI50 7b=DI51 (Hi-16bits)
HR1432				HR1431 (Lo-16bits)
HR1433	DIGI:Dis 1-32	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1434				HR1433 (Lo-16bits)
HR1435	DIGI:Dis 33-64	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 (Hi-16bits)
HR1436				HR1435 (Lo-16bits)
HR1437	DIGI:DI1 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1438				HR1437 (Lo-16bits)
HR1439	DIGI:DI1 Counter Clear	RW	1	0=- 1=Clear
HR1440	DIGI:DI2 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1441				HR1440 (Lo-16bits)
HR1442	DIGI:DI2 Counter Clear	RW	1	0=- 1=Clear
HR1443	DIGI:DI3 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1444				HR1443 (Lo-16bits)
HR1445	DIGI:DI3 Counter Clear	RW	1	0=- 1=Clear
HR1446	DIGI:DI4 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1447				HR1446 (Lo-16bits)
HR1448	DIGI:DI4 Counter Clear	RW	1	0=- 1=Clear
HR1449	DIGI:DI5 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1450				HR1449 (Lo-16bits)
HR1451	DIGI:DI5 Counter Clear	RW	1	0=- 1=Clear
HR1452	DIGI:DI6 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1453				HR1452 (Lo-16bits)
HR1454	DIGI:DI6 Counter Clear	RW	1	0=- 1=Clear
HR1455	DIGI:DI7 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1456				HR1455 (Lo-16bits)
HR1457	DIGI:DI7 Counter Clear	RW	1	0=- 1=Clear
HR1458	DIGI:DI8 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1459				HR1458 (Lo-16bits)
HR1460	DIGI:DI8 Counter Clear	RW	1	0=- 1=Clear
HR1461	DIGI:DI9 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1462				HR1461 (Lo-16bits)
HR1463	DIGI:DI9 Counter Clear	RW	1	0=- 1=Clear
HR1464	DIGI:DI10 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1465				HR1464 (Lo-16bits)
HR1466	DIGI:DI10 Counter Clear	RW	1	0=- 1=Clear
HR1467	DIGI:DI11 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1468				HR1467 (Lo-16bits)
HR1469	DIGI:DI11 Counter Clear	RW	1	0=- 1=Clear
HR1470	DIGI:DI12 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1471				HR1470 (Lo-16bits)
HR1472	DIGI:DI12 Counter Clear	RW	1	0=- 1=Clear
HR1473	DIGI:DI13 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1474				HR1473 (Lo-16bits)
HR1475	DIGI:DI13 Counter Clear	RW	1	0=- 1=Clear
HR1476	DIGI:DI14 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1477				HR1476 (Lo-16bits)
HR1478	DIGI:DI14 Counter Clear	RW	1	0=- 1=Clear
HR1479	DIGI:DI15 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1480				HR1479 (Lo-16bits)
HR1481	DIGI:DI15 Counter Clear	RW	1	0=- 1=Clear
HR1482	DIGI:DI16 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1483				HR1482 (Lo-16bits)
HR1484	DIGI:DI16 Counter Clear	RW	1	0=- 1=Clear
HR1485	DIGI:DI17 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1486				HR1485 (Lo-16bits)
HR1487	DIGI:DI17 Counter Clear	RW	1	0=- 1=Clear
HR1488	DIGI:DI18 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1489				HR1488 (Lo-16bits)
HR1490	DIGI:DI18 Counter Clear	RW	1	0=- 1=Clear
HR1491	DIGI:DI19 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1492				HR1491 (Lo-16bits)
HR1493	DIGI:DI19 Counter Clear	RW	1	0=- 1=Clear
HR1494	DIGI:DI20 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1495				HR1494 (Lo-16bits)
HR1496	DIGI:DI20 Counter Clear	RW	1	0=- 1=Clear
HR1497	DIGI:DI21 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1498				HR1497 (Lo-16bits)
HR1499	DIGI:DI21 Counter Clear	RW	1	0=- 1=Clear
HR1500	DIGI:DI22 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1501				HR1500 (Lo-16bits)
HR1502	DIGI:DI22 Counter Clear	RW	1	0=- 1=Clear
HR1503	DIGI:DI23 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1504				HR1503 (Lo-16bits)
HR1505	DIGI:DI23 Counter Clear	RW	1	0=- 1=Clear
HR1506	DIGI:DI24 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1507				HR1506 (Lo-16bits)
HR1508	DIGI:DI24 Counter Clear	RW	1	0=- 1=Clear
HR1509	DIGI:DI25 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1510				HR1509 (Lo-16bits)
HR1511	DIGI:DI25 Counter Clear	RW	1	0=- 1=Clear
HR1512	DIGI:DI26 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1513				HR1512 (Lo-16bits)
HR1514	DIGI:DI26 Counter Clear	RW	1	0=- 1=Clear
HR1515	DIGI:DI27 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1516				HR1515 (Lo-16bits)
HR1517	DIGI:DI27 Counter Clear	RW	1	0=- 1=Clear
HR1518	DIGI:DI28 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	DIGI:DI28 Counter Clear	RW	1	0=- 1=Clear
HR1521	DIGI:DI29 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1522				HR1521 (Lo-16bits)
HR1523	DIGI:DI29 Counter Clear	RW	1	0=- 1=Clear
HR1524	DIGI:DI30 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	DIGI:DI30 Counter Clear	RW	1	0=- 1=Clear
HR1527	DIGI:DI31 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1528				HR1527 (Lo-16bits)
HR1529	DIGI:DI31 Counter Clear	RW	1	0=- 1=Clear
HR1530	DIGI:DI32 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1531				HR1530 (Lo-16bits)
HR1532	DIGI:DI32 Counter Clear	RW	1	0=- 1=Clear
HR1533	DIGI:DI33 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1534				HR1533 (Lo-16bits)
HR1535	DIGI:DI33 Counter Clear	RW	1	0=- 1=Clear
HR1536	DIGI:DI34 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	DIGI:DI34 Counter Clear	RW	1	0=- 1=Clear
HR1539	DIGI:DI35 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1540				HR1539 (Lo-16bits)
HR1541	DIGI:DI35 Counter Clear	RW	1	0=- 1=Clear
HR1542	DIGI:DI36 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1543				HR1542 (Lo-16bits)
HR1544	DIGI:DI36 Counter Clear	RW	1	0=- 1=Clear
HR1545	DIGI:DI37 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1546				HR1545 (Lo-16bits)
HR1547	DIGI:DI37 Counter Clear	RW	1	0=- 1=Clear
HR1548	DIGI:DI38 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	DIGI:DI38 Counter Clear	RW	1	0=- 1=Clear
HR1551	DIGI:DI39 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1552				HR1551 (Lo-16bits)
HR1553	DIGI:DI39 Counter Clear	RW	1	0=- 1=Clear
HR1554	DIGI:DI40 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	DIGI:DI40 Counter Clear	RW	1	0=- 1=Clear
HR1557	DIGI:DI41 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1558				HR1557 (Lo-16bits)
HR1559	DIGI:DI41 Counter Clear	RW	1	0=- 1=Clear
HR1560	DIGI:DI42 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1561				HR1560 (Lo-16bits)
HR1562	DIGI:DI42 Counter Clear	RW	1	0=- 1=Clear
HR1563	DIGI:DI43 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1564				HR1563 (Lo-16bits)
HR1565	DIGI:DI43 Counter Clear	RW	1	0=- 1=Clear
HR1566	DIGI:DI44 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	DIGI:DI44 Counter Clear	RW	1	0=- 1=Clear
HR1569	DIGI:DI45 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1570				HR1569 (Lo-16bits)
HR1571	DIGI:DI45 Counter Clear	RW	1	0=- 1=Clear
HR1572	DIGI:DI46 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	DIGI:DI46 Counter Clear	RW	1	0=- 1=Clear
HR1575	DIGI:DI47 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1576				HR1575 (Lo-16bits)
HR1577	DIGI:DI47 Counter Clear	RW	1	0=- 1=Clear
HR1578	DIGI:DI48 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	DIGI:DI48 Counter Clear	RW	1	0=- 1=Clear
HR1581	DIGI:DI49 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1582				HR1581 (Lo-16bits)
HR1583	DIGI:DI49 Counter Clear	RW	1	0=- 1=Clear
HR1584	DIGI:DI50 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	DIGI:DI50 Counter Clear	RW	1	0=- 1=Clear
HR1587	DIGI:DI51 Counter	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1588				HR1587 (Lo-16bits)
HR1589	DIGI:DI51 Counter Clear	RW	1	0=- 1=Clear
HR1590	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1593	SGS:Force SG	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1594	SGS:Force SG change	RW	1	0=Disabled 1=Enabled
HR1595	SGS:Used setting groups	RW	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1596	SGS:Remote SG change	RW	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8

Holding Register	Parameter	Access	Length	Info
HR1597	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR1598	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)
HR1600	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1605				HR1604 (Lo-16bits)
HR1606	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)
HR1614	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	DEV:SW version	R	2	STRING [...32] (Hi-16bits)
HR1617				HR1616 (Lo-16bits)
HR1618	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1627				HR1626 (Lo-16bits)
HR1628	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)
HR1630	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	DEV:HW conf.	R	2	STRING [0...32] (Hi-16bits)
HR1635				HR1634 (Lo-16bits)
HR1636	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1637				HR1636 (Lo-16bits)
HR1638	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR1639	MBIO:MBIO ModA Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1640				HR1639 (Lo-16bits)
HR1641	MBIO:MBIO ModA Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1642				HR1641 (Lo-16bits)
HR1643	MBIO:MBIO ModA Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1644				HR1643 (Lo-16bits)
HR1645	MBIO:MBIO ModA Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1646				HR1645 (Lo-16bits)
HR1647	MBIO:MBIO ModA Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1648				HR1647 (Lo-16bits)
HR1649	MBIO:MBIO ModA Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1650				HR1649 (Lo-16bits)
HR1651	MBIO:MBIO ModA Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1652				HR1651 (Lo-16bits)
HR1653	MBIO:MBIO ModA Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1654				HR1653 (Lo-16bits)
HR1655	MBIO:MBIO ModB Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1656				HR1655 (Lo-16bits)
HR1657	MBIO:MBIO ModB Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1658				HR1657 (Lo-16bits)
HR1659	MBIO:MBIO ModB Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1660				HR1659 (Lo-16bits)
HR1661	MBIO:MBIO ModB Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1662				HR1661 (Lo-16bits)
HR1663	MBIO:MBIO ModB Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1664				HR1663 (Lo-16bits)
HR1665	MBIO:MBIO ModB Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1666				HR1665 (Lo-16bits)
HR1667	MBIO:MBIO ModB Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1668				HR1667 (Lo-16bits)
HR1669	MBIO:MBIO ModB Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1670				HR1669 (Lo-16bits)
HR1671	MBIO:MBIO ModC Ch1 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1672				HR1671 (Lo-16bits)
HR1673	MBIO:MBIO ModC Ch2 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1674				HR1673 (Lo-16bits)
HR1675	MBIO:MBIO ModC Ch3 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1676				HR1675 (Lo-16bits)
HR1677	MBIO:MBIO ModC Ch4 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1678				HR1677 (Lo-16bits)
HR1679	MBIO:MBIO ModC Ch5 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1680				HR1679 (Lo-16bits)
HR1681	MBIO:MBIO ModC Ch6 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1682				HR1681 (Lo-16bits)
HR1683	MBIO:MBIO ModC Ch7 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1684				HR1683 (Lo-16bits)
HR1685	MBIO:MBIO ModC Ch8 Invalid	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1686				HR1685 (Lo-16bits)
HR1687	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1688	LOGIC1:Logic out 1-32	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	NOC1:I> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1691	NOC1:I> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1692	NOC1:I> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1693	NOC1:I> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1694	NOC1:I> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1695	NOC1:I> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1696				HR1695 (Lo-16bits)
HR1697	NOC1:I> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1698				HR1697 (Lo-16bits)
HR1699	NOC1:I> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1700				HR1699 (Lo-16bits)
HR1701	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1702				HR1701 (Lo-16bits)
HR1703	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1704				HR1703 (Lo-16bits)
HR1705	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1706				HR1705 (Lo-16bits)
HR1707	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1708				HR1707 (Lo-16bits)
HR1709	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1710				HR1709 (Lo-16bits)
HR1711	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1712				HR1711 (Lo-16bits)
HR1713	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1714				HR1713 (Lo-16bits)
HR1715	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1716				HR1715 (Lo-16bits)
HR1717	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1718	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1719	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1720	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1721	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1722	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1723	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1724	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR1725	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1726				HR1725 (Lo-16bits)
HR1727	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1728				HR1727 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1729	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1730				HR1729 (Lo-16bits)
HR1731	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1732				HR1731 (Lo-16bits)
HR1733	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1734				HR1733 (Lo-16bits)
HR1735	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1736				HR1735 (Lo-16bits)
HR1737	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1738				HR1737 (Lo-16bits)
HR1739	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1740				HR1739 (Lo-16bits)
HR1741	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1742	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1743	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1744	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1745	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1746	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1747	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1748	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1749	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1750	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1751	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1752	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1753	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1754	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1755	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1756	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1757	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1758	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1759	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1760	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1761	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1762	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1763	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1764	NOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1765	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1766	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1767	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1768	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1769	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1770	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1771	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1772	NOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1773	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1775	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1776				HR1775 (Lo-16bits)
HR1777	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1778				HR1777 (Lo-16bits)
HR1779	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1780				HR1779 (Lo-16bits)
HR1781	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1784				HR1783 (Lo-16bits)
HR1785	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1786				HR1785 (Lo-16bits)
HR1787	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR1788				HR1787 (Lo-16bits)
HR1789	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1790				HR1789 (Lo-16bits)
HR1791	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1792				HR1791 (Lo-16bits)
HR1793	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1794				HR1793 (Lo-16bits)
HR1795	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1796				HR1795 (Lo-16bits)
HR1797	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1798				HR1797 (Lo-16bits)
HR1799	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1800				HR1799 (Lo-16bits)
HR1801	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1802				HR1801 (Lo-16bits)
HR1803	NOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1805	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1806				HR1805 (Lo-16bits)
HR1807	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1808				HR1807 (Lo-16bits)
HR1809	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1810				HR1809 (Lo-16bits)
HR1811	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)
HR1813	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1816				HR1815 (Lo-16bits)
HR1817	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1818				HR1817 (Lo-16bits)
HR1819	NOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1820				HR1819 (Lo-16bits)
HR1821	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1824				HR1823 (Lo-16bits)
HR1825	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1826				HR1825 (Lo-16bits)
HR1827	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1830				HR1829 (Lo-16bits)
HR1831	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1832				HR1831 (Lo-16bits)
HR1833	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1834				HR1833 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1835	NOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1838	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1839	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1840	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1841	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1842	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1843	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1844	NOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR1845	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1846				HR1845 (Lo-16bits)
HR1847	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1848				HR1847 (Lo-16bits)
HR1849	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1850				HR1849 (Lo-16bits)
HR1851	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1852				HR1851 (Lo-16bits)
HR1853	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1854				HR1853 (Lo-16bits)
HR1855	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1856				HR1855 (Lo-16bits)
HR1857	NOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1858				HR1857 (Lo-16bits)
HR1859	NOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1860				HR1859 (Lo-16bits)
HR1861	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1862	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1863	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1864	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1865	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1866	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1867	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1868	NOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR1869	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1870				HR1869 (Lo-16bits)
HR1871	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1872				HR1871 (Lo-16bits)
HR1873	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1874				HR1873 (Lo-16bits)
HR1875	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1876				HR1875 (Lo-16bits)
HR1877	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1878				HR1877 (Lo-16bits)
HR1879	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1880				HR1879 (Lo-16bits)
HR1881	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR1882				HR1881 (Lo-16bits)
HR1883	NOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1884				HR1883 (Lo-16bits)
HR1885	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1886	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1887	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1888	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1889	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1890	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1891	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1892	NOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR1893	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1894	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1895	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1896	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1897	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1898	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1899	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1900	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR1901	NOC1:Pick-up setting lset	RW	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1902				HR1901 (Lo-16bits)
HR1903	NOC1:Delay type	RW	1	0=DT 1=IDMT
HR1904	NOC1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR1905				HR1904 (Lo-16bits)
HR1906	NOC1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR1907	NOC1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1908	NOC1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1909	NOC1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR1910	NOC1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR1911				HR1910 (Lo-16bits)
HR1912	NOC1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR1913				HR1912 (Lo-16bits)
HR1914	NOC1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR1915				HR1914 (Lo-16bits)
HR1916	NOC1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR1917				HR1916 (Lo-16bits)
HR1918	NOC1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR1919	NOC1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR1920	NOC2: >> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR1921	NOC2: >> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1922	NOC2: >> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR1923	NOC2: >> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR1924	NOC2: >> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR1925	NOC2: >> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1926				HR1925 (Lo-16bits)
HR1927	NOC2:I>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	NOC2:I>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)
HR1939	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	NOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1948	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1949	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1950	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1951	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1952	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1953	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR1954	NOC2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR1955	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1958				HR1957 (Lo-16bits)
HR1959	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1962				HR1961 (Lo-16bits)
HR1963	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1972	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1973	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1974	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1975	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1976	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1977	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1978	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR1979	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR1980	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1981	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1982	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1983	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1984	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1985	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1986	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR1987	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1988	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1989	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1990	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1991	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1992	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1993	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1994	NOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR1995	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1996	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1997	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1998	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR1999	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2000	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2001	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2002	NOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2003	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)
HR2015	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2029	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	NOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2036				HR2035 (Lo-16bits)
HR2037	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2040				HR2039 (Lo-16bits)
HR2041	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2042				HR2041 (Lo-16bits)
HR2043	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2044				HR2043 (Lo-16bits)
HR2045	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2046				HR2045 (Lo-16bits)
HR2047	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2048				HR2047 (Lo-16bits)
HR2049	NOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2050				HR2049 (Lo-16bits)
HR2051	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2052				HR2051 (Lo-16bits)
HR2053	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2054				HR2053 (Lo-16bits)
HR2055	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2056				HR2055 (Lo-16bits)
HR2057	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2058				HR2057 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2059	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2060				HR2059 (Lo-16bits)
HR2061	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2062				HR2061 (Lo-16bits)
HR2063	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2064				HR2063 (Lo-16bits)
HR2065	NOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2066				HR2065 (Lo-16bits)
HR2067	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2068	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2069	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2070	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2071	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2072	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2073	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2074	NOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2075	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2078				HR2077 (Lo-16bits)
HR2079	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2080				HR2079 (Lo-16bits)
HR2081	NOC2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2083	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2090				HR2089 (Lo-16bits)
HR2091	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2092	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2093	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2094	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2095	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2096	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2097	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2098	NOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2099	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2102				HR2101 (Lo-16bits)
HR2103	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2104				HR2103 (Lo-16bits)
HR2105	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2106				HR2105 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2107	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2108				HR2107 (Lo-16bits)
HR2109	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2110				HR2109 (Lo-16bits)
HR2111	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2112				HR2111 (Lo-16bits)
HR2113	NOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2114				HR2113 (Lo-16bits)
HR2115	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2116	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2117	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2118	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2119	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2120	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2121	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2122	NOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2123	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2124	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2125	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2126	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2127	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2128	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2129	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2130	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2131	NOC2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2132				HR2131 (Lo-16bits)
HR2133	NOC2:Delay type	RW	1	0=DT 1=IDMT
HR2134	NOC2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2135				HR2134 (Lo-16bits)
HR2136	NOC2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2137	NOC2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2138	NOC2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2139	NOC2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2140	NOC2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2141				HR2140 (Lo-16bits)
HR2142	NOC2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2143				HR2142 (Lo-16bits)
HR2144	NOC2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2145				HR2144 (Lo-16bits)
HR2146	NOC2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2147				HR2146 (Lo-16bits)
HR2148	NOC2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2149	NOC2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2150	NOC3:I>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2151	NOC3:I>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR2152	NOC3:I>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2153	NOC3:I>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2154	NOC3:I>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2155	NOC3:I>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2156				HR2155 (Lo-16bits)
HR2157	NOC3:I>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2158				HR2157 (Lo-16bits)
HR2159	NOC3:I>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2160				HR2159 (Lo-16bits)
HR2161	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2162				HR2161 (Lo-16bits)
HR2163	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2164				HR2163 (Lo-16bits)
HR2165	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2166				HR2165 (Lo-16bits)
HR2167	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2168				HR2167 (Lo-16bits)
HR2169	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2170				HR2169 (Lo-16bits)
HR2171	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2172				HR2171 (Lo-16bits)
HR2173	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2174				HR2173 (Lo-16bits)
HR2175	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2176				HR2175 (Lo-16bits)
HR2177	NOC3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR2178	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2179	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2180	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2181	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2182	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2183	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2184	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2185	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2186				HR2185 (Lo-16bits)
HR2187	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2188				HR2187 (Lo-16bits)
HR2189	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2190				HR2189 (Lo-16bits)
HR2191	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2192				HR2191 (Lo-16bits)
HR2193	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2194				HR2193 (Lo-16bits)
HR2195	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2196				HR2195 (Lo-16bits)
HR2197	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2198				HR2197 (Lo-16bits)
HR2199	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2200				HR2199 (Lo-16bits)
HR2201	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2202	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2203	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2204	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2205	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2206	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2207	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2208	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2209	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2210	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2211	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2212	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2213	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2214	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2215	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2216	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2217	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2218	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2219	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2220	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2221	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2222	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2223	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2224	NOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2225	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2226	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2227	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2228	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2229	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2230	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2231	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2232	NOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2233	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2234				HR2233 (Lo-16bits)
HR2235	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2240				HR2239 (Lo-16bits)
HR2241	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2242				HR2241 (Lo-16bits)
HR2243	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2244				HR2243 (Lo-16bits)
HR2245	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2246				HR2245 (Lo-16bits)
HR2247	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2248				HR2247 (Lo-16bits)
HR2249	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2250				HR2249 (Lo-16bits)
HR2251	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2252				HR2251 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2253	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2254				HR2253 (Lo-16bits)
HR2255	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2256				HR2255 (Lo-16bits)
HR2257	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2262				HR2261 (Lo-16bits)
HR2263	NOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2264				HR2263 (Lo-16bits)
HR2265	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2266				HR2265 (Lo-16bits)
HR2267	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2268				HR2267 (Lo-16bits)
HR2269	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2270				HR2269 (Lo-16bits)
HR2271	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2272				HR2271 (Lo-16bits)
HR2273	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2274				HR2273 (Lo-16bits)
HR2275	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2276				HR2275 (Lo-16bits)
HR2277	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2278				HR2277 (Lo-16bits)
HR2279	NOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2283	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2284				HR2283 (Lo-16bits)
HR2285	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2286				HR2285 (Lo-16bits)
HR2287	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2288				HR2287 (Lo-16bits)
HR2289	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2290				HR2289 (Lo-16bits)
HR2291	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2292				HR2291 (Lo-16bits)
HR2293	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2294				HR2293 (Lo-16bits)
HR2295	NOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2296				HR2295 (Lo-16bits)
HR2297	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2298	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2299	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2300	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2301	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2302	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2303	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2304	NOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2305	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2306				HR2305 (Lo-16bits)
HR2307	NOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2308				HR2307 (Lo-16bits)
HR2309	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2310				HR2309 (Lo-16bits)
HR2311	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2312				HR2311 (Lo-16bits)
HR2313	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2314				HR2313 (Lo-16bits)
HR2315	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	NOC3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2322	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2323	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2324	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2325	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2326	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2327	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2328	NOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2329	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2330				HR2329 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2331	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2332				HR2331 (Lo-16bits)
HR2333	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2334				HR2333 (Lo-16bits)
HR2335	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2336				HR2335 (Lo-16bits)
HR2337	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2338				HR2337 (Lo-16bits)
HR2339	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2340				HR2339 (Lo-16bits)
HR2341	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2342				HR2341 (Lo-16bits)
HR2343	NOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2344				HR2343 (Lo-16bits)
HR2345	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2346	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2347	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2348	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2349	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2350	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2351	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2352	NOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2353	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2354	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2355	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2356	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2357	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2358	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2359	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2360	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2361	NOC3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2362				HR2361 (Lo-16bits)
HR2363	NOC3:Delay type	RW	1	0=DT 1=IDMT
HR2364	NOC3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	NOC3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2367	NOC3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2368	NOC3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2369	NOC3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2370	NOC3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	NOC3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2373				HR2372 (Lo-16bits)
HR2374	NOC3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	NOC3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2377				HR2376 (Lo-16bits)
HR2378	NOC3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2379	NOC3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2380	NOC4: >>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR2381	NOC4: >>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2382	NOC4: >>>> Phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Start ABC 11=Trip AB 12=Trip BC 13=Trip CA 14=Trip ABC
HR2383	NOC4: >>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2384	NOC4: >>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2385	NOC4: >>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2386				HR2385 (Lo-16bits)
HR2387	NOC4: >>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2388				HR2387 (Lo-16bits)
HR2389	NOC4: >>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2390				HR2389 (Lo-16bits)
HR2391	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2392				HR2391 (Lo-16bits)
HR2393	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2394				HR2393 (Lo-16bits)
HR2395	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2396				HR2395 (Lo-16bits)
HR2397	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2398				HR2397 (Lo-16bits)
HR2399	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2400				HR2399 (Lo-16bits)
HR2401	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2402				HR2401 (Lo-16bits)
HR2403	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2404				HR2403 (Lo-16bits)
HR2405	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2406				HR2405 (Lo-16bits)
HR2407	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2408	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2409	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2410	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2411	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2412	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2413	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2414	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2415	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2416				HR2415 (Lo-16bits)
HR2417	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2418				HR2417 (Lo-16bits)
HR2419	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2420				HR2419 (Lo-16bits)
HR2421	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2422				HR2421 (Lo-16bits)
HR2423	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2424				HR2423 (Lo-16bits)
HR2425	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2426				HR2425 (Lo-16bits)
HR2427	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2428				HR2427 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2429	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2430				HR2429 (Lo-16bits)
HR2431	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2432	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2433	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2434	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2435	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2436	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2437	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2438	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2439	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2440	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2441	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2442	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2443	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2444	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2445	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2446	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2447	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2448	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2449	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2450	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2451	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2452	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2453	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2454	NOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2455	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2456	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2457	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2458	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2459	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2460	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2461	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2462	NOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2463	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2464				HR2463 (Lo-16bits)
HR2465	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2466				HR2465 (Lo-16bits)
HR2467	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2468				HR2467 (Lo-16bits)
HR2469	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2470				HR2469 (Lo-16bits)
HR2471	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2472				HR2471 (Lo-16bits)
HR2473	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2474				HR2473 (Lo-16bits)
HR2475	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2476				HR2475 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2477	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2478				HR2477 (Lo-16bits)
HR2479	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2480				HR2479 (Lo-16bits)
HR2481	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2482				HR2481 (Lo-16bits)
HR2483	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2484				HR2483 (Lo-16bits)
HR2485	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2486				HR2485 (Lo-16bits)
HR2487	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2488				HR2487 (Lo-16bits)
HR2489	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2490				HR2489 (Lo-16bits)
HR2491	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2492				HR2491 (Lo-16bits)
HR2493	NOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2494				HR2493 (Lo-16bits)
HR2495	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2496				HR2495 (Lo-16bits)
HR2497	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2498				HR2497 (Lo-16bits)
HR2499	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2500				HR2499 (Lo-16bits)
HR2501	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2502				HR2501 (Lo-16bits)
HR2503	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2504				HR2503 (Lo-16bits)
HR2505	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2506				HR2505 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2507	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2508				HR2507 (Lo-16bits)
HR2509	NOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2510				HR2509 (Lo-16bits)
HR2511	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2512				HR2511 (Lo-16bits)
HR2513	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2514				HR2513 (Lo-16bits)
HR2515	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2516				HR2515 (Lo-16bits)
HR2517	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	NOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2528	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2529	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2530	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2531	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2532	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2533	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2534	NOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2535	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)
HR2537	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	NOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2552	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2553	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2554	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2555	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2556	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2557	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2558	NOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2559	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)
HR2565	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)
HR2567	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	NOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2576	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2577	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2578	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2579	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2580	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2581	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2582	NOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2583	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2584	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2585	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2586	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2587	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2588	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2589	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2590	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2591	NOC4:Pick-up setting lset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR2592				HR2591 (Lo-16bits)
HR2593	NOC4:Delay type	RW	1	0=DT 1=IDMT
HR2594	NOC4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR2595				HR2594 (Lo-16bits)
HR2596	NOC4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR2597	NOC4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2598	NOC4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2599	NOC4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR2600	NOC4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR2601				HR2600 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2602	NOC4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR2603				HR2602 (Lo-16bits)
HR2604	NOC4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR2605				HR2604 (Lo-16bits)
HR2606	NOC4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR2607				HR2606 (Lo-16bits)
HR2608	NOC4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2609	NOC4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)
HR2610	ROC1:ldrw> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2611	ROC1:ldrw> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2612	ROC2:ldrw>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2613	ROC2:ldrw>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2614	ROC3:ldrw>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2615	ROC3:ldrw>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2616	ROC4:ldrw>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2617	ROC4:ldrw>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2618	ROC5:ldrw>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2619	ROC5:ldrw>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2620	ROC6:ldrw>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2621	ROC6:ldrw>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2622	ROC7:ldrw>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2623	ROC7:ldrw>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse
HR2624	ROC8:ldrw>>>>>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2625	ROC8:ldrw>>>>>>>> Current direction	R	1	0=Undefined 1=Forward 2=Reverse

Holding Register	Parameter	Access	Length	Info
HR2626	ROV1:Urw>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2627	ROV2:Urw>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2628	ROV3:Urw>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2629	ROV4:Urw>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2630	COL1:ICOL> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2631	COL2:ICOL>> (49OL) mode	R(W)	1	0=Disabled 1=Activated
HR2632	CNU1:CNU> (50UB) mode	R(W)	1	0=Disabled 1=Activated
HR2633	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=Start On (Alarm) 3=Start On (Alarm & Trip) 4=Start On (Trip) 5=Alarm On 6=Start On 7=Start On Alarm On 8=Trip On 9=Alarm On Trip On 10=Multistatus On
HR2634	DOC1:ldir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2635	DOC1:ldir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2636	DOC1:ldir> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2637	DOC1:ldir> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2638				HR2637 (Lo-16bits)
HR2639	DOC1:ldir> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2640				HR2639 (Lo-16bits)
HR2641	DOC1:ldir> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2642				HR2641 (Lo-16bits)
HR2643	DOC1:ldir> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2644				HR2643 (Lo-16bits)
HR2645	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2646	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2647	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2648	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2649	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2650	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2651	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2652	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2653	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2654				HR2653 (Lo-16bits)
HR2655	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2656				HR2655 (Lo-16bits)
HR2657	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2658				HR2657 (Lo-16bits)
HR2659	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2660				HR2659 (Lo-16bits)
HR2661	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2670				HR2669 (Lo-16bits)
HR2671	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2672				HR2671 (Lo-16bits)
HR2673	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2688				HR2687 (Lo-16bits)
HR2689	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	DOC1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2699	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2700				HR2699 (Lo-16bits)
HR2701	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2702	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2703	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2704	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2705	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2706	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2707	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2708	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2709	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2725	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2726	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2727	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2728	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2729	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2730	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2731	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2732	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2733	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2734	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2735	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2736	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2737	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2738	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2739	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2740	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2741	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2742	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2743	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2744	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2745	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2746	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2747	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2748	DOC1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2749	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2750	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2751	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2752	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2753	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2754	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2755	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2756	DOC1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR2757	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)
HR2763	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2774				HR2773 (Lo-16bits)
HR2775	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2776				HR2775 (Lo-16bits)
HR2777	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2778				HR2777 (Lo-16bits)
HR2779	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2780				HR2779 (Lo-16bits)
HR2781	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2782				HR2781 (Lo-16bits)
HR2783	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2784				HR2783 (Lo-16bits)
HR2785	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2786				HR2785 (Lo-16bits)
HR2787	DOC1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2788				HR2787 (Lo-16bits)
HR2789	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	DOC1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2804				HR2803 (Lo-16bits)
HR2805	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2806				HR2805 (Lo-16bits)
HR2807	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2808				HR2807 (Lo-16bits)
HR2809	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2810				HR2809 (Lo-16bits)
HR2811	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2812				HR2811 (Lo-16bits)
HR2813	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	DOC1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2822	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2823	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2824	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2825	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2826	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2827	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2828	DOC1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2829	DOC1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2830				HR2829 (Lo-16bits)
HR2831	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2832				HR2831 (Lo-16bits)
HR2833	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2834				HR2833 (Lo-16bits)
HR2835	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2836				HR2835 (Lo-16bits)
HR2837	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2838				HR2837 (Lo-16bits)
HR2839	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2840				HR2839 (Lo-16bits)
HR2841	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2842				HR2841 (Lo-16bits)
HR2843	DOC1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2844				HR2843 (Lo-16bits)
HR2845	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2846	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2847	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2848	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2849	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2850	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2851	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR2852	DOC1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2853	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2854				HR2853 (Lo-16bits)
HR2855	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2856				HR2855 (Lo-16bits)
HR2857	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2858				HR2857 (Lo-16bits)
HR2859	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2860				HR2859 (Lo-16bits)
HR2861	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	DOC1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2870	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2871	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2872	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2873	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2874	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2875	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2876	DOC1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR2877	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2878	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2879	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2880	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2881	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2882	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2883	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2884	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2885	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2886	DOC2:ldir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR2887	DOC2:ldir>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2888	DOC2:ldir>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR2889				HR2888 (Lo-16bits)
HR2890	DOC2:ldir>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2891				HR2890 (Lo-16bits)
HR2892	DOC2:ldir>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2893				HR2892 (Lo-16bits)
HR2894	DOC2:ldir>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2895				HR2894 (Lo-16bits)
HR2896	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR2897	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2898	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2899	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2900	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2901	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2902	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2903	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR2904	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2905				HR2904 (Lo-16bits)
HR2906	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2907				HR2906 (Lo-16bits)
HR2908	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2909				HR2908 (Lo-16bits)
HR2910	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2911				HR2910 (Lo-16bits)
HR2912	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2913				HR2912 (Lo-16bits)
HR2914	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2915				HR2914 (Lo-16bits)
HR2916	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2917				HR2916 (Lo-16bits)
HR2918	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR2919				HR2918 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2920	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2921				HR2920 (Lo-16bits)
HR2922	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2923				HR2922 (Lo-16bits)
HR2924	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2925				HR2924 (Lo-16bits)
HR2926	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2927				HR2926 (Lo-16bits)
HR2928	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2929				HR2928 (Lo-16bits)
HR2930	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2931				HR2930 (Lo-16bits)
HR2932	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2933				HR2932 (Lo-16bits)
HR2934	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR2935				HR2934 (Lo-16bits)
HR2936	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2937				HR2936 (Lo-16bits)
HR2938	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2939				HR2938 (Lo-16bits)
HR2940	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2941				HR2940 (Lo-16bits)
HR2942	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2943				HR2942 (Lo-16bits)
HR2944	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2945				HR2944 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2946	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2947				HR2946 (Lo-16bits)
HR2948	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2949				HR2948 (Lo-16bits)
HR2950	DOC2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR2951				HR2950 (Lo-16bits)
HR2952	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2953	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2954	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2955	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2956	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2957	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2958	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2959	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2960	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2961				HR2960 (Lo-16bits)
HR2962	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2963				HR2962 (Lo-16bits)
HR2964	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2965				HR2964 (Lo-16bits)
HR2966	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2967				HR2966 (Lo-16bits)
HR2968	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2969				HR2968 (Lo-16bits)
HR2970	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2971				HR2970 (Lo-16bits)
HR2972	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2973				HR2972 (Lo-16bits)
HR2974	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2975				HR2974 (Lo-16bits)
HR2976	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2977	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2978	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2979	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2980	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2981	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2982	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2983	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2984	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2985	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2986	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2987	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2988	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2989	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2990	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2991	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2992	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2993	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2994	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2995	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2996	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2997	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2998	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2999	DOC2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3000	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3001	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3002	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3003	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3004	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3005	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3006	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3007	DOC2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3008	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3009				HR3008 (Lo-16bits)
HR3010	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3011				HR3010 (Lo-16bits)
HR3012	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3013				HR3012 (Lo-16bits)
HR3014	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3015				HR3014 (Lo-16bits)
HR3016	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3017				HR3016 (Lo-16bits)
HR3018	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3019				HR3018 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3020	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3021				HR3020 (Lo-16bits)
HR3022	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3023				HR3022 (Lo-16bits)
HR3024	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3025				HR3024 (Lo-16bits)
HR3026	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3027				HR3026 (Lo-16bits)
HR3028	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3029				HR3028 (Lo-16bits)
HR3030	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3031				HR3030 (Lo-16bits)
HR3032	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3033				HR3032 (Lo-16bits)
HR3034	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3035				HR3034 (Lo-16bits)
HR3036	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3037				HR3036 (Lo-16bits)
HR3038	DOC2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3039				HR3038 (Lo-16bits)
HR3040	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3041				HR3040 (Lo-16bits)
HR3042	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3043				HR3042 (Lo-16bits)
HR3044	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3045				HR3044 (Lo-16bits)
HR3046	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3047				HR3046 (Lo-16bits)
HR3048	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3049				HR3048 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3050	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3051				HR3050 (Lo-16bits)
HR3052	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3053				HR3052 (Lo-16bits)
HR3054	DOC2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3055				HR3054 (Lo-16bits)
HR3056	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3057				HR3056 (Lo-16bits)
HR3058	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3059				HR3058 (Lo-16bits)
HR3060	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3061				HR3060 (Lo-16bits)
HR3062	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3063				HR3062 (Lo-16bits)
HR3064	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3065				HR3064 (Lo-16bits)
HR3066	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3067				HR3066 (Lo-16bits)
HR3068	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3069				HR3068 (Lo-16bits)
HR3070	DOC2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3071				HR3070 (Lo-16bits)
HR3072	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3073	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3074	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3075	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3076	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3077	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3078	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3079	DOC2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3080	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3081				HR3080 (Lo-16bits)
HR3082	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3083				HR3082 (Lo-16bits)
HR3084	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3085				HR3084 (Lo-16bits)
HR3086	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3087				HR3086 (Lo-16bits)
HR3088	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3089				HR3088 (Lo-16bits)
HR3090	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3091				HR3090 (Lo-16bits)
HR3092	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3093				HR3092 (Lo-16bits)
HR3094	DOC2:2.nd Harmonic Block Limit (Iharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3095				HR3094 (Lo-16bits)
HR3096	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3097	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3098	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3099	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3100	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3101	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3102	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3103	DOC2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3104	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3105				HR3104 (Lo-16bits)
HR3106	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3107				HR3106 (Lo-16bits)
HR3108	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3109				HR3108 (Lo-16bits)
HR3110	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3111				HR3110 (Lo-16bits)
HR3112	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3113				HR3112 (Lo-16bits)
HR3114	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3115				HR3114 (Lo-16bits)
HR3116	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3117				HR3116 (Lo-16bits)
HR3118	DOC2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3119				HR3118 (Lo-16bits)
HR3120	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3121	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3122	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3123	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3124	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3125	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3126	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3127	DOC2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3128	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3129	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3130	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3131	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3132	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3133	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3134	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3135	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3136	DOC3:ldir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3137	DOC3:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3138	DOC3:ldir>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR3139	DOC3:ldir>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	DOC3:ldir>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3143	DOC3:Idir>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	DOC3:Idir>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)
HR3147	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3148	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3149	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3150	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3151	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3152	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3153	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3154	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3155	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3156				HR3155 (Lo-16bits)
HR3157	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3165	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3174				HR3173 (Lo-16bits)
HR3175	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3176				HR3175 (Lo-16bits)
HR3177	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3178				HR3177 (Lo-16bits)
HR3179	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3180				HR3179 (Lo-16bits)
HR3181	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3182				HR3181 (Lo-16bits)
HR3183	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3184				HR3183 (Lo-16bits)
HR3185	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3186				HR3185 (Lo-16bits)
HR3187	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3188				HR3187 (Lo-16bits)
HR3189	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3190				HR3189 (Lo-16bits)
HR3191	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3198				HR3197 (Lo-16bits)
HR3199	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3204	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3205	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3206	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3207	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3208	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3209	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3210	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3211	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3212				HR3211 (Lo-16bits)
HR3213	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3218				HR3217 (Lo-16bits)
HR3219	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)
HR3225	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3228	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3229	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3230	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3231	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3232	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3233	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3234	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3235	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3236	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3237	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3238	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3239	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3240	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3241	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3242	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3243	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3244	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3245	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3246	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3247	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3248	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3249	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3250	DOC3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3251	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3252	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3253	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3254	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3255	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3256	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3257	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3258	DOC3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3259	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3260				HR3259 (Lo-16bits)
HR3261	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3264				HR3263 (Lo-16bits)
HR3265	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3276				HR3275 (Lo-16bits)
HR3277	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	DOC3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3294				HR3293 (Lo-16bits)
HR3295	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	DOC3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3306				HR3305 (Lo-16bits)
HR3307	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)
HR3315	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	DOC3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3324	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3325	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3326	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3327	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3328	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3329	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3330	DOC3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3331	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3332				HR3331 (Lo-16bits)
HR3333	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	DOC3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3347	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3348	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3349	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3350	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3351	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3352	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3353	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3354	DOC3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3355	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3356				HR3355 (Lo-16bits)
HR3357	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3358				HR3357 (Lo-16bits)
HR3359	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	DOC3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3372	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3373	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3374	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3375	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3376	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3377	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3378	DOC3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3379	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3380	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3381	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3382	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3383	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3384	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3385	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3386	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3387	DOC4:ldir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3388	DOC4:ldir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3389	DOC4:ldir>>>> Last fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C

Holding Register	Parameter	Access	Length	Info
HR3390	DOC4:ldir>>>> Last fault angle	R	2	FLOAT [-360.00...360.00] (Hi-16bits)
HR3391				HR3390 (Lo-16bits)
HR3392	DOC4:ldir>>>> Last fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3393				HR3392 (Lo-16bits)
HR3394	DOC4:ldir>>>> Last fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3395				HR3394 (Lo-16bits)
HR3396	DOC4:ldir>>>> Last fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3397				HR3396 (Lo-16bits)
HR3398	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3399	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3400	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3401	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3402	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3403	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3404	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3405	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3406	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3407				HR3406 (Lo-16bits)
HR3408	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3409				HR3408 (Lo-16bits)
HR3410	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3411				HR3410 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3412	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3413				HR3412 (Lo-16bits)
HR3414	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3415				HR3414 (Lo-16bits)
HR3416	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3417				HR3416 (Lo-16bits)
HR3418	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3419				HR3418 (Lo-16bits)
HR3420	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3421				HR3420 (Lo-16bits)
HR3422	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3423				HR3422 (Lo-16bits)
HR3424	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3425				HR3424 (Lo-16bits)
HR3426	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3427				HR3426 (Lo-16bits)
HR3428	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3429				HR3428 (Lo-16bits)
HR3430	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3431				HR3430 (Lo-16bits)
HR3432	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3433				HR3432 (Lo-16bits)
HR3434	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3435				HR3434 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3436	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3437				HR3436 (Lo-16bits)
HR3438	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3439				HR3438 (Lo-16bits)
HR3440	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3441				HR3440 (Lo-16bits)
HR3442	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3443				HR3442 (Lo-16bits)
HR3444	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3445				HR3444 (Lo-16bits)
HR3446	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3447				HR3446 (Lo-16bits)
HR3448	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3449				HR3448 (Lo-16bits)
HR3450	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3451				HR3450 (Lo-16bits)
HR3452	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3453				HR3452 (Lo-16bits)
HR3454	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3455	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3456	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3457	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3458	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3459	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3460	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3461	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3462	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3463				HR3462 (Lo-16bits)
HR3464	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3465				HR3464 (Lo-16bits)
HR3466	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3467				HR3466 (Lo-16bits)
HR3468	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3469				HR3468 (Lo-16bits)
HR3470	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3471				HR3470 (Lo-16bits)
HR3472	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3473				HR3472 (Lo-16bits)
HR3474	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3475				HR3474 (Lo-16bits)
HR3476	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3477				HR3476 (Lo-16bits)
HR3478	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3479	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3480	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3481	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3482	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3483	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3484	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3485	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3486	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3487	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3488	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3489	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3490	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3491	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3492	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3493	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3494	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3495	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3496	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3497	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3498	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3499	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3500	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3501	DOC4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3502	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3503	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3504	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3505	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3506	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3507	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3508	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3509	DOC4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3510	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3511				HR3510 (Lo-16bits)
HR3512	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3513				HR3512 (Lo-16bits)
HR3514	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3515				HR3514 (Lo-16bits)
HR3516	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3517				HR3516 (Lo-16bits)
HR3518	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3519				HR3518 (Lo-16bits)
HR3520	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3521				HR3520 (Lo-16bits)
HR3522	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3523				HR3522 (Lo-16bits)
HR3524	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3525				HR3524 (Lo-16bits)
HR3526	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3527				HR3526 (Lo-16bits)
HR3528	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3529				HR3528 (Lo-16bits)
HR3530	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3531				HR3530 (Lo-16bits)
HR3532	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3533				HR3532 (Lo-16bits)
HR3534	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3535				HR3534 (Lo-16bits)
HR3536	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3537				HR3536 (Lo-16bits)
HR3538	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3539				HR3538 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3540	DOC4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3541				HR3540 (Lo-16bits)
HR3542	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3543				HR3542 (Lo-16bits)
HR3544	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3545				HR3544 (Lo-16bits)
HR3546	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3547				HR3546 (Lo-16bits)
HR3548	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3549				HR3548 (Lo-16bits)
HR3550	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3551				HR3550 (Lo-16bits)
HR3552	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3553				HR3552 (Lo-16bits)
HR3554	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3555				HR3554 (Lo-16bits)
HR3556	DOC4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3557				HR3556 (Lo-16bits)
HR3558	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3559				HR3558 (Lo-16bits)
HR3560	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3561				HR3560 (Lo-16bits)
HR3562	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3563				HR3562 (Lo-16bits)
HR3564	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3565				HR3564 (Lo-16bits)
HR3566	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3567				HR3566 (Lo-16bits)
HR3568	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3569				HR3568 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3570	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3571				HR3570 (Lo-16bits)
HR3572	DOC4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3573				HR3572 (Lo-16bits)
HR3574	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3575	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3576	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3577	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3578	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3579	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3580	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3581	DOC4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3582	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3583				HR3582 (Lo-16bits)
HR3584	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3585				HR3584 (Lo-16bits)
HR3586	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3587				HR3586 (Lo-16bits)
HR3588	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3589				HR3588 (Lo-16bits)
HR3590	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3591				HR3590 (Lo-16bits)
HR3592	DOC4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3593				HR3592 (Lo-16bits)
HR3594	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3595				HR3594 (Lo-16bits)
HR3596	DOC4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3597				HR3596 (Lo-16bits)
HR3598	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3599	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3600	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3601	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3602	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3603	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3604	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3605	DOC4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3606	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3607				HR3606 (Lo-16bits)
HR3608	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3609				HR3608 (Lo-16bits)
HR3610	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3611				HR3610 (Lo-16bits)
HR3612	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3613				HR3612 (Lo-16bits)
HR3614	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3615				HR3614 (Lo-16bits)
HR3616	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3617				HR3616 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3618	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3619				HR3618 (Lo-16bits)
HR3620	DOC4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3621				HR3620 (Lo-16bits)
HR3622	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3623	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3624	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3625	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3626	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3627	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3628	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3629	DOC4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3630	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3631	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3632	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3633	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3634	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3635	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3636	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3637	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3638	NEF1:I0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3639	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3640	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3641	NEF1:I0> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3642	NEF1:I0> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3643				HR3642 (Lo-16bits)
HR3644	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3645				HR3644 (Lo-16bits)
HR3646	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3647				HR3646 (Lo-16bits)
HR3648	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3649				HR3648 (Lo-16bits)
HR3650	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3651				HR3650 (Lo-16bits)
HR3652	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3653				HR3652 (Lo-16bits)
HR3654	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3655				HR3654 (Lo-16bits)
HR3656	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3657				HR3656 (Lo-16bits)
HR3658	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3659				HR3658 (Lo-16bits)
HR3660	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3661	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3662	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3663	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3664	NEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3665	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3666	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3667	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR3668	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3669				HR3668 (Lo-16bits)
HR3670	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3671				HR3670 (Lo-16bits)
HR3672	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3673				HR3672 (Lo-16bits)
HR3674	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3675				HR3674 (Lo-16bits)
HR3676	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3677				HR3676 (Lo-16bits)
HR3678	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3679				HR3678 (Lo-16bits)
HR3680	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3681				HR3680 (Lo-16bits)
HR3682	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3683				HR3682 (Lo-16bits)
HR3684	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3685	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3686	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3687	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3688	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3689	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3690	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3691	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3692	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3693	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3694	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3695	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3696	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3697	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3698	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3699	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3700	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3701	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3702	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3703	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3704	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3705	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3706	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3707	NEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3708	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3709	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3710	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3711	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3712	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3713	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3714	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3715	NEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3716	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3717				HR3716 (Lo-16bits)
HR3718	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3719				HR3718 (Lo-16bits)
HR3720	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3721				HR3720 (Lo-16bits)
HR3722	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3723				HR3722 (Lo-16bits)
HR3724	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3725				HR3724 (Lo-16bits)
HR3726	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3727				HR3726 (Lo-16bits)
HR3728	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3729				HR3728 (Lo-16bits)
HR3730	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3731				HR3730 (Lo-16bits)
HR3732	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3733				HR3732 (Lo-16bits)
HR3734	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3735				HR3734 (Lo-16bits)
HR3736	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3737				HR3736 (Lo-16bits)
HR3738	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3739				HR3738 (Lo-16bits)
HR3740	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3741				HR3740 (Lo-16bits)
HR3742	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3743				HR3742 (Lo-16bits)
HR3744	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3745				HR3744 (Lo-16bits)
HR3746	NEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3747				HR3746 (Lo-16bits)
HR3748	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3749				HR3748 (Lo-16bits)
HR3750	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3751				HR3750 (Lo-16bits)
HR3752	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3753				HR3752 (Lo-16bits)
HR3754	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3755				HR3754 (Lo-16bits)
HR3756	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3757				HR3756 (Lo-16bits)
HR3758	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3759				HR3758 (Lo-16bits)
HR3760	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3761				HR3760 (Lo-16bits)
HR3762	NEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3763				HR3762 (Lo-16bits)
HR3764	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3765				HR3764 (Lo-16bits)
HR3766	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3767				HR3766 (Lo-16bits)
HR3768	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3769				HR3768 (Lo-16bits)
HR3770	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3771				HR3770 (Lo-16bits)
HR3772	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3773				HR3772 (Lo-16bits)
HR3774	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3775				HR3774 (Lo-16bits)
HR3776	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3777				HR3776 (Lo-16bits)
HR3778	NEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3779				HR3778 (Lo-16bits)
HR3780	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3781	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3782	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3783	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3784	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3785	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3786	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3787	NEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3788	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3789				HR3788 (Lo-16bits)
HR3790	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3791				HR3790 (Lo-16bits)
HR3792	NEF1:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3793				HR3792 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3794	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3795				HR3794 (Lo-16bits)
HR3796	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3797				HR3796 (Lo-16bits)
HR3798	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3799				HR3798 (Lo-16bits)
HR3800	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3801				HR3800 (Lo-16bits)
HR3802	NEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3803				HR3802 (Lo-16bits)
HR3804	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3805	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3806	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3807	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3808	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3809	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3810	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3811	NEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR3812	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3813				HR3812 (Lo-16bits)
HR3814	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3815				HR3814 (Lo-16bits)
HR3816	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3817				HR3816 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3818	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3819				HR3818 (Lo-16bits)
HR3820	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3821				HR3820 (Lo-16bits)
HR3822	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3823				HR3822 (Lo-16bits)
HR3824	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3825				HR3824 (Lo-16bits)
HR3826	NEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3827				HR3826 (Lo-16bits)
HR3828	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3829	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3830	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3831	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3832	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3833	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3834	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3835	NEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR3836	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3837	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3838	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3839	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3840	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3841	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3842	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3843	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3844	NEF1:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR3845				HR3844 (Lo-16bits)
HR3846	NEF1:Delay type	RW	1	0=DT 1=IDMT
HR3847	NEF1:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	NEF1:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR3850	NEF1:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3851	NEF1:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3852	NEF1:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR3853	NEF1:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR3854				HR3853 (Lo-16bits)
HR3855	NEF1:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR3856				HR3855 (Lo-16bits)
HR3857	NEF1:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR3858				HR3857 (Lo-16bits)
HR3859	NEF1:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR3860				HR3859 (Lo-16bits)
HR3861	NEF1:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR3862	NEF1:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR3863	NEF2:I0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR3864	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR3865	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR3866	NEF2:I0>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR3867	NEF2:I0>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR3868				HR3867 (Lo-16bits)
HR3869	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3870				HR3869 (Lo-16bits)
HR3871	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3872				HR3871 (Lo-16bits)
HR3873	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3874				HR3873 (Lo-16bits)
HR3875	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3876				HR3875 (Lo-16bits)
HR3877	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3886	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3887	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3888	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3889	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3890	NEF2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3891	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3892	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR3893	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)
HR3899	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3910	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3911	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3912	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3913	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3914	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3915	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3916	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3917	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3918	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3919	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3920	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3921	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3922	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3923	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3924	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3925	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3926	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3927	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3928	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3929	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3930	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3931	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3932	NEF2:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR3933	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3934	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3935	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3936	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3937	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3938	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3939	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3940	NEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR3941	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3950				HR3949 (Lo-16bits)
HR3951	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3952				HR3951 (Lo-16bits)
HR3953	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3954				HR3953 (Lo-16bits)
HR3955	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3956				HR3955 (Lo-16bits)
HR3957	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3958				HR3957 (Lo-16bits)
HR3959	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3960				HR3959 (Lo-16bits)
HR3961	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3962				HR3961 (Lo-16bits)
HR3963	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3964				HR3963 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3965	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3966				HR3965 (Lo-16bits)
HR3967	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3968				HR3967 (Lo-16bits)
HR3969	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3970				HR3969 (Lo-16bits)
HR3971	NEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3972				HR3971 (Lo-16bits)
HR3973	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3974				HR3973 (Lo-16bits)
HR3975	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3976				HR3975 (Lo-16bits)
HR3977	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3978				HR3977 (Lo-16bits)
HR3979	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3980				HR3979 (Lo-16bits)
HR3981	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	NEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3995	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	NEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4006	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4007	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4008	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4009	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4010	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4011	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4012	NEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4013	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	NEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4019	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	NEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4030	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4031	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4032	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4033	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4034	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4035	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4036	NEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4037	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4043	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4046				HR4045 (Lo-16bits)
HR4047	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4048				HR4047 (Lo-16bits)
HR4049	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4050				HR4049 (Lo-16bits)
HR4051	NEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4052				HR4051 (Lo-16bits)
HR4053	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4054	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4055	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4056	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4057	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4058	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4059	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4060	NEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4061	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4062	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4063	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4064	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4065	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4066	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4067	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4068	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4069	NEF2:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4070				HR4069 (Lo-16bits)
HR4071	NEF2:Delay type	RW	1	0=DT 1=IDMT
HR4072	NEF2:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4073				HR4072 (Lo-16bits)
HR4074	NEF2:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4075	NEF2:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4076	NEF2:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4077	NEF2:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4078	NEF2:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4079				HR4078 (Lo-16bits)
HR4080	NEF2:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4081				HR4080 (Lo-16bits)
HR4082	NEF2:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4083				HR4082 (Lo-16bits)
HR4084	NEF2:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4085				HR4084 (Lo-16bits)
HR4086	NEF2:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4087	NEF2:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4088	NEF3:I0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4089	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4090	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4091	NEF3:I0>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4092	NEF3:I0>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4093				HR4092 (Lo-16bits)
HR4094	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4095				HR4094 (Lo-16bits)
HR4096	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4097				HR4096 (Lo-16bits)
HR4098	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4099				HR4098 (Lo-16bits)
HR4100	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4101				HR4100 (Lo-16bits)
HR4102	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4103				HR4102 (Lo-16bits)
HR4104	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4105				HR4104 (Lo-16bits)
HR4106	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4107				HR4106 (Lo-16bits)
HR4108	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4109				HR4108 (Lo-16bits)
HR4110	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4111	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4112	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4113	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4114	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4115	NEF3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4116	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4117	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4118	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4119				HR4118 (Lo-16bits)
HR4120	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4121				HR4120 (Lo-16bits)
HR4122	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4123				HR4122 (Lo-16bits)
HR4124	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4125				HR4124 (Lo-16bits)
HR4126	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4127				HR4126 (Lo-16bits)
HR4128	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4129				HR4128 (Lo-16bits)
HR4130	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4131				HR4130 (Lo-16bits)
HR4132	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4133				HR4132 (Lo-16bits)
HR4134	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4135	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4136	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4137	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4138	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4139	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4140	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4141	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4142	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4143	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4144	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4145	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4146	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4147	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4148	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4149	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4150	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4151	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4152	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4153	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4154	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4155	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4156	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4157	NEF3:Delay char. ANSI/ IEC	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEC MI 5=IEEC VI 6=IEEC EI 7=Param
HR4158	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4159	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4160	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4161	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4162	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4163	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4164	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4165	NEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4166	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4167				HR4166 (Lo-16bits)
HR4168	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4169				HR4168 (Lo-16bits)
HR4170	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4171				HR4170 (Lo-16bits)
HR4172	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4173				HR4172 (Lo-16bits)
HR4174	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4175				HR4174 (Lo-16bits)
HR4176	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4177				HR4176 (Lo-16bits)
HR4178	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4179				HR4178 (Lo-16bits)
HR4180	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4181				HR4180 (Lo-16bits)
HR4182	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4183				HR4182 (Lo-16bits)
HR4184	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4185				HR4184 (Lo-16bits)
HR4186	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4187				HR4186 (Lo-16bits)
HR4188	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4189				HR4188 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4190	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4191				HR4190 (Lo-16bits)
HR4192	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4193				HR4192 (Lo-16bits)
HR4194	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4195				HR4194 (Lo-16bits)
HR4196	NEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4197				HR4196 (Lo-16bits)
HR4198	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4199				HR4198 (Lo-16bits)
HR4200	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4201				HR4200 (Lo-16bits)
HR4202	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4203				HR4202 (Lo-16bits)
HR4204	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4205				HR4204 (Lo-16bits)
HR4206	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4207				HR4206 (Lo-16bits)
HR4208	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4209				HR4208 (Lo-16bits)
HR4210	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4211				HR4210 (Lo-16bits)
HR4212	NEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4213				HR4212 (Lo-16bits)
HR4214	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4215				HR4214 (Lo-16bits)
HR4216	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4217				HR4216 (Lo-16bits)
HR4218	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4219				HR4218 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4220	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4221				HR4220 (Lo-16bits)
HR4222	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4223				HR4222 (Lo-16bits)
HR4224	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4225				HR4224 (Lo-16bits)
HR4226	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4227				HR4226 (Lo-16bits)
HR4228	NEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4229				HR4228 (Lo-16bits)
HR4230	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4231	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4232	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4233	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4234	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4235	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4236	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4237	NEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4238	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4239				HR4238 (Lo-16bits)
HR4240	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4241				HR4240 (Lo-16bits)
HR4242	NEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4243				HR4242 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4244	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4245				HR4244 (Lo-16bits)
HR4246	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4247				HR4246 (Lo-16bits)
HR4248	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4249				HR4248 (Lo-16bits)
HR4250	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4251				HR4250 (Lo-16bits)
HR4252	NEF3:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4253				HR4252 (Lo-16bits)
HR4254	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4255	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4256	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4257	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4258	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4259	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4260	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4261	NEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4262	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4263				HR4262 (Lo-16bits)
HR4264	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4265				HR4264 (Lo-16bits)
HR4266	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4267				HR4266 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4268	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4269				HR4268 (Lo-16bits)
HR4270	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4271				HR4270 (Lo-16bits)
HR4272	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4273				HR4272 (Lo-16bits)
HR4274	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4275				HR4274 (Lo-16bits)
HR4276	NEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4277				HR4276 (Lo-16bits)
HR4278	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4279	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4280	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4281	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4282	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4283	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4284	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4285	NEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4286	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4287	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4288	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4289	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4290	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4291	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4292	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4293	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4294	NEF3:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4295				HR4294 (Lo-16bits)
HR4296	NEF3:Delay type	RW	1	0=DT 1=IDMT
HR4297	NEF3:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	NEF3:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4300	NEF3:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4301	NEF3:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4302	NEF3:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4303	NEF3:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	NEF3:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	NEF3:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	NEF3:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4310				HR4309 (Lo-16bits)
HR4311	NEF3:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4312	NEF3:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4313	NEF4:I0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR4314	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR4315	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR4316	NEF4:I0>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4317	NEF4:I0>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4332				HR4331 (Lo-16bits)
HR4333	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4334				HR4333 (Lo-16bits)
HR4335	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4336	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4337	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4338	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4339	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4340	NEF4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4341	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4342	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4343	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4344				HR4343 (Lo-16bits)
HR4345	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4346				HR4345 (Lo-16bits)
HR4347	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4348				HR4347 (Lo-16bits)
HR4349	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4354				HR4353 (Lo-16bits)
HR4355	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4360	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4361	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4362	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4363	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4364	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4365	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4366	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4367	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4368	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4369	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4370	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4371	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4372	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4373	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4374	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4375	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4376	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4377	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4378	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4379	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4380	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4381	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4382	NEF4:Delay char. ANSI/ IEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4383	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4384	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4385	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4386	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4387	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4388	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4389	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4390	NEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4391	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4392				HR4391 (Lo-16bits)
HR4393	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4394				HR4393 (Lo-16bits)
HR4395	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4396				HR4395 (Lo-16bits)
HR4397	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4398				HR4397 (Lo-16bits)
HR4399	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4400				HR4399 (Lo-16bits)
HR4401	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4402				HR4401 (Lo-16bits)
HR4403	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4404				HR4403 (Lo-16bits)
HR4405	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4406				HR4405 (Lo-16bits)
HR4407	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4408				HR4407 (Lo-16bits)
HR4409	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4410				HR4409 (Lo-16bits)
HR4411	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4412				HR4411 (Lo-16bits)
HR4413	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4414				HR4413 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4415	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4416				HR4415 (Lo-16bits)
HR4417	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4418				HR4417 (Lo-16bits)
HR4419	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4420				HR4419 (Lo-16bits)
HR4421	NEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)
HR4433	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	NEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4445	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4456	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4457	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4458	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4459	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4460	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4461	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4462	NEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4463	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4469	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF4:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4480	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4481	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4482	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4483	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4484	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4485	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4486	NEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4487	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4488				HR4487 (Lo-16bits)
HR4489	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4490				HR4489 (Lo-16bits)
HR4491	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4492				HR4491 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4493	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)
HR4503	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4504	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4505	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4506	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4507	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4508	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4509	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4510	NEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4511	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4512	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4513	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4514	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4515	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4516	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4517	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4518	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4519	NEF4:Pick-up setting Iset	RW	2	FLOAT [0.1...50] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF4:Delay type	RW	1	0=DT 1=IDMT
HR4522	NEF4:Definite (minimum) operating time delay	RW	2	FLOAT [0...1800] (Hi-16bits)
HR4523				HR4522 (Lo-16bits)
HR4524	NEF4:Delay curve series	RW	1	0=IEC 1=IEEE 2=Non standard
HR4525	NEF4:Delay characteristics IEC	RW	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4526	NEF4:Delay char. ANSI/IEEE	RW	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4527	NEF4:Non standard Delay char.	RW	1	0=RI-type 1=RD-type
HR4528	NEF4:Time dial setting k	RW	2	FLOAT [0.01...25] (Hi-16bits)
HR4529				HR4528 (Lo-16bits)
HR4530	NEF4:IDMT Const. A	RW	2	FLOAT [0...250] (Hi-16bits)
HR4531				HR4530 (Lo-16bits)
HR4532	NEF4:IDMT Const. B	RW	2	FLOAT [0...250] (Hi-16bits)
HR4533				HR4532 (Lo-16bits)
HR4534	NEF4:IDMT Const. C	RW	2	FLOAT [0...250] (Hi-16bits)
HR4535				HR4534 (Lo-16bits)
HR4536	NEF4:Select SG	RW	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR4537	NEF4:Write settings to selected SG	RW	1	0=- 1=Write (autoreset)

Holding Register	Parameter	Access	Length	Info
HR4538	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4539	DEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4540	DEF1:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4541	DEF1:I0dir> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4542	DEF1:I0dir> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4543				HR4542 (Lo-16bits)
HR4544	DEF1:I0dir> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4545				HR4544 (Lo-16bits)
HR4546	DEF1:I0dir> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4547				HR4546 (Lo-16bits)
HR4548	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4549	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4550	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4551	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4552	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4553	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4554	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4555	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4556	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4557				HR4556 (Lo-16bits)
HR4558	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4559				HR4558 (Lo-16bits)
HR4560	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4561				HR4560 (Lo-16bits)
HR4562	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4563				HR4562 (Lo-16bits)
HR4564	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4565				HR4564 (Lo-16bits)
HR4566	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4567				HR4566 (Lo-16bits)
HR4568	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4569				HR4568 (Lo-16bits)
HR4570	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4571				HR4570 (Lo-16bits)
HR4572	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4573				HR4572 (Lo-16bits)
HR4574	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4575				HR4574 (Lo-16bits)
HR4576	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4577				HR4576 (Lo-16bits)
HR4578	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4579				HR4578 (Lo-16bits)
HR4580	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4581				HR4580 (Lo-16bits)
HR4582	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4583				HR4582 (Lo-16bits)
HR4584	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4585				HR4584 (Lo-16bits)
HR4586	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4587				HR4586 (Lo-16bits)
HR4588	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4589				HR4588 (Lo-16bits)
HR4590	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4591				HR4590 (Lo-16bits)
HR4592	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4593				HR4592 (Lo-16bits)
HR4594	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4595				HR4594 (Lo-16bits)
HR4596	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4597				HR4596 (Lo-16bits)
HR4598	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4599				HR4598 (Lo-16bits)
HR4600	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4601				HR4600 (Lo-16bits)
HR4602	DEF1:Comp. Pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4603				HR4602 (Lo-16bits)
HR4604	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4605				HR4604 (Lo-16bits)
HR4606	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4607				HR4606 (Lo-16bits)
HR4608	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4609				HR4608 (Lo-16bits)
HR4610	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4611				HR4610 (Lo-16bits)
HR4612	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4613				HR4612 (Lo-16bits)
HR4614	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4615				HR4614 (Lo-16bits)
HR4616	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4617				HR4616 (Lo-16bits)
HR4618	DEF1:Comp. Pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4619				HR4618 (Lo-16bits)
HR4620	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4621				HR4620 (Lo-16bits)
HR4622	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4623				HR4622 (Lo-16bits)
HR4624	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4625				HR4624 (Lo-16bits)
HR4626	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4627				HR4626 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4628	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4629				HR4628 (Lo-16bits)
HR4630	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4631				HR4630 (Lo-16bits)
HR4632	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4633				HR4632 (Lo-16bits)
HR4634	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR4635				HR4634 (Lo-16bits)
HR4636	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4637				HR4636 (Lo-16bits)
HR4638	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4639				HR4638 (Lo-16bits)
HR4640	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4641				HR4640 (Lo-16bits)
HR4642	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4643				HR4642 (Lo-16bits)
HR4644	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4645				HR4644 (Lo-16bits)
HR4646	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4647				HR4646 (Lo-16bits)
HR4648	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4649				HR4648 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4650	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR4651				HR4650 (Lo-16bits)
HR4652	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4653	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4654	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4655	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4656	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4657	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4658	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4659	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR4660	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4661				HR4660 (Lo-16bits)
HR4662	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4663				HR4662 (Lo-16bits)
HR4664	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4665				HR4664 (Lo-16bits)
HR4666	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4667				HR4666 (Lo-16bits)
HR4668	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4669				HR4668 (Lo-16bits)
HR4670	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4671				HR4670 (Lo-16bits)
HR4672	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4673				HR4672 (Lo-16bits)
HR4674	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR4675				HR4674 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4676	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4677				HR4676 (Lo-16bits)
HR4678	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4679				HR4678 (Lo-16bits)
HR4680	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4681				HR4680 (Lo-16bits)
HR4682	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4683				HR4682 (Lo-16bits)
HR4684	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4685				HR4684 (Lo-16bits)
HR4686	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4687				HR4686 (Lo-16bits)
HR4688	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4689				HR4688 (Lo-16bits)
HR4690	DEF1:Unearth. Pick-up setting I0set (I0Sinf)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4691				HR4690 (Lo-16bits)
HR4692	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4693				HR4692 (Lo-16bits)
HR4694	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4695				HR4694 (Lo-16bits)
HR4696	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4697				HR4696 (Lo-16bits)
HR4698	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4699				HR4698 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4700	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4701				HR4700 (Lo-16bits)
HR4702	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4703				HR4702 (Lo-16bits)
HR4704	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4705				HR4704 (Lo-16bits)
HR4706	DEF1:Unearth. Pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4707				HR4706 (Lo-16bits)
HR4708	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4709				HR4708 (Lo-16bits)
HR4710	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4711				HR4710 (Lo-16bits)
HR4712	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4713				HR4712 (Lo-16bits)
HR4714	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4715				HR4714 (Lo-16bits)
HR4716	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4717				HR4716 (Lo-16bits)
HR4718	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4719				HR4718 (Lo-16bits)
HR4720	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4721				HR4720 (Lo-16bits)
HR4722	DEF1:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR4723				HR4722 (Lo-16bits)
HR4724	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4725				HR4724 (Lo-16bits)
HR4726	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4727				HR4726 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4728	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4729				HR4728 (Lo-16bits)
HR4730	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4731				HR4730 (Lo-16bits)
HR4732	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4733				HR4732 (Lo-16bits)
HR4734	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4735				HR4734 (Lo-16bits)
HR4736	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4737				HR4736 (Lo-16bits)
HR4738	DEF1:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR4739				HR4738 (Lo-16bits)
HR4740	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4741	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4742	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4743	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4744	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4745	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4746	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4747	DEF1:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR4748	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4749	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4750	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4751	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4752	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4753	DEF1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4754	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4755	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4756	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4757				HR4756 (Lo-16bits)
HR4758	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4759				HR4758 (Lo-16bits)
HR4760	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4761				HR4760 (Lo-16bits)
HR4762	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4763				HR4762 (Lo-16bits)
HR4764	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4765				HR4764 (Lo-16bits)
HR4766	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4767				HR4766 (Lo-16bits)
HR4768	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4769				HR4768 (Lo-16bits)
HR4770	DEF1:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4771				HR4770 (Lo-16bits)
HR4772	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4773				HR4772 (Lo-16bits)
HR4774	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4775				HR4774 (Lo-16bits)
HR4776	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4777				HR4776 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4778	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4779				HR4778 (Lo-16bits)
HR4780	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4781				HR4780 (Lo-16bits)
HR4782	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4783				HR4782 (Lo-16bits)
HR4784	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4785				HR4784 (Lo-16bits)
HR4786	DEF1:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4787				HR4786 (Lo-16bits)
HR4788	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4789				HR4788 (Lo-16bits)
HR4790	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4791				HR4790 (Lo-16bits)
HR4792	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4793				HR4792 (Lo-16bits)
HR4794	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4795				HR4794 (Lo-16bits)
HR4796	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4797				HR4796 (Lo-16bits)
HR4798	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4799				HR4798 (Lo-16bits)
HR4800	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4801				HR4800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4802	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4803				HR4802 (Lo-16bits)
HR4804	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4805	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4806	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4807	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4808	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4809	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4810	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4811	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4812	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4813	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4814	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4815	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4816	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4817	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4818	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4819	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4820	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4821	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4822	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4823	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4824	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4825	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4826	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4827	DEF1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4828	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4829	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4830	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4831	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4832	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4833	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4834	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4835	DEF1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR4836	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4837				HR4836 (Lo-16bits)
HR4838	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4839				HR4838 (Lo-16bits)
HR4840	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4841				HR4840 (Lo-16bits)
HR4842	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4843				HR4842 (Lo-16bits)
HR4844	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4845				HR4844 (Lo-16bits)
HR4846	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4847				HR4846 (Lo-16bits)
HR4848	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4849				HR4848 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4850	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4851				HR4850 (Lo-16bits)
HR4852	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4853				HR4852 (Lo-16bits)
HR4854	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4855				HR4854 (Lo-16bits)
HR4856	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4857				HR4856 (Lo-16bits)
HR4858	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4859				HR4858 (Lo-16bits)
HR4860	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4861				HR4860 (Lo-16bits)
HR4862	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4863				HR4862 (Lo-16bits)
HR4864	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4865				HR4864 (Lo-16bits)
HR4866	DEF1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4867				HR4866 (Lo-16bits)
HR4868	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4869				HR4868 (Lo-16bits)
HR4870	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4871				HR4870 (Lo-16bits)
HR4872	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4873				HR4872 (Lo-16bits)
HR4874	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4875				HR4874 (Lo-16bits)
HR4876	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4877				HR4876 (Lo-16bits)
HR4878	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4879				HR4878 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4880	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4881				HR4880 (Lo-16bits)
HR4882	DEF1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4883				HR4882 (Lo-16bits)
HR4884	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4885				HR4884 (Lo-16bits)
HR4886	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4887				HR4886 (Lo-16bits)
HR4888	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4889				HR4888 (Lo-16bits)
HR4890	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4891				HR4890 (Lo-16bits)
HR4892	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4893				HR4892 (Lo-16bits)
HR4894	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4895				HR4894 (Lo-16bits)
HR4896	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4897				HR4896 (Lo-16bits)
HR4898	DEF1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4899				HR4898 (Lo-16bits)
HR4900	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4901	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4902	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4903	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4904	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4905	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4906	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4907	DEF1:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4908	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4909				HR4908 (Lo-16bits)
HR4910	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4911				HR4910 (Lo-16bits)
HR4912	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4913				HR4912 (Lo-16bits)
HR4914	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4915				HR4914 (Lo-16bits)
HR4916	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4917				HR4916 (Lo-16bits)
HR4918	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4919				HR4918 (Lo-16bits)
HR4920	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4921				HR4920 (Lo-16bits)
HR4922	DEF1:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4923				HR4922 (Lo-16bits)
HR4924	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4925	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4926	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4927	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4928	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4929	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4930	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4931	DEF1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR4932	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4933				HR4932 (Lo-16bits)
HR4934	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4935				HR4934 (Lo-16bits)
HR4936	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4937				HR4936 (Lo-16bits)
HR4938	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4939				HR4938 (Lo-16bits)
HR4940	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4941				HR4940 (Lo-16bits)
HR4942	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4943				HR4942 (Lo-16bits)
HR4944	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4945				HR4944 (Lo-16bits)
HR4946	DEF1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4947				HR4946 (Lo-16bits)
HR4948	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4949	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4950	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4951	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4952	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4953	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4954	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4955	DEF1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR4956	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4957	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4958	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4959	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4960	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4961	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4962	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4963	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4964	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR4965	DEF2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR4966	DEF2:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR4967	DEF2:I0dir>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR4968	DEF2:I0dir>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR4969				HR4968 (Lo-16bits)
HR4970	DEF2:I0dir>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4971				HR4970 (Lo-16bits)
HR4972	DEF2:I0dir>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR4973				HR4972 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4974	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4975	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4976	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4977	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4978	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4979	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4980	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4981	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR4982	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4983				HR4982 (Lo-16bits)
HR4984	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4985				HR4984 (Lo-16bits)
HR4986	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4987				HR4986 (Lo-16bits)
HR4988	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4989				HR4988 (Lo-16bits)
HR4990	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4991				HR4990 (Lo-16bits)
HR4992	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4993				HR4992 (Lo-16bits)
HR4994	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4995				HR4994 (Lo-16bits)
HR4996	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR4997				HR4996 (Lo-16bits)
HR4998	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR4999				HR4998 (Lo-16bits)
HR5000	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5001				HR5000 (Lo-16bits)
HR5002	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5003				HR5002 (Lo-16bits)
HR5004	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5005				HR5004 (Lo-16bits)
HR5006	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5007				HR5006 (Lo-16bits)
HR5008	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5009				HR5008 (Lo-16bits)
HR5010	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5011				HR5010 (Lo-16bits)
HR5012	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5013				HR5012 (Lo-16bits)
HR5014	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5015				HR5014 (Lo-16bits)
HR5016	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5017				HR5016 (Lo-16bits)
HR5018	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5019				HR5018 (Lo-16bits)
HR5020	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5021				HR5020 (Lo-16bits)
HR5022	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5023				HR5022 (Lo-16bits)
HR5024	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5025				HR5024 (Lo-16bits)
HR5026	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5027				HR5026 (Lo-16bits)
HR5028	DEF2:Comp. Pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5029				HR5028 (Lo-16bits)
HR5030	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5031				HR5030 (Lo-16bits)
HR5032	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5033				HR5032 (Lo-16bits)
HR5034	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5035				HR5034 (Lo-16bits)
HR5036	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5037				HR5036 (Lo-16bits)
HR5038	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5039				HR5038 (Lo-16bits)
HR5040	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5041				HR5040 (Lo-16bits)
HR5042	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5043				HR5042 (Lo-16bits)
HR5044	DEF2:Comp. Pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5045				HR5044 (Lo-16bits)
HR5046	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5047				HR5046 (Lo-16bits)
HR5048	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5049				HR5048 (Lo-16bits)
HR5050	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5051				HR5050 (Lo-16bits)
HR5052	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5053				HR5052 (Lo-16bits)
HR5054	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5055				HR5054 (Lo-16bits)
HR5056	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5057				HR5056 (Lo-16bits)
HR5058	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5059				HR5058 (Lo-16bits)
HR5060	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5061				HR5060 (Lo-16bits)
HR5062	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5063				HR5062 (Lo-16bits)
HR5064	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5065				HR5064 (Lo-16bits)
HR5066	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5067				HR5066 (Lo-16bits)
HR5068	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5069				HR5068 (Lo-16bits)
HR5070	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5071				HR5070 (Lo-16bits)
HR5072	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5073				HR5072 (Lo-16bits)
HR5074	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5075				HR5074 (Lo-16bits)
HR5076	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5077				HR5076 (Lo-16bits)
HR5078	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5079	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5080	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5081	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5082	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5083	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5084	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5085	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5086	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5087				HR5086 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5088	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5089				HR5088 (Lo-16bits)
HR5090	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5091				HR5090 (Lo-16bits)
HR5092	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5093				HR5092 (Lo-16bits)
HR5094	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5095				HR5094 (Lo-16bits)
HR5096	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5097				HR5096 (Lo-16bits)
HR5098	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5099				HR5098 (Lo-16bits)
HR5100	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5101				HR5100 (Lo-16bits)
HR5102	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5103				HR5102 (Lo-16bits)
HR5104	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5105				HR5104 (Lo-16bits)
HR5106	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5107				HR5106 (Lo-16bits)
HR5108	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5109				HR5108 (Lo-16bits)
HR5110	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5111				HR5110 (Lo-16bits)
HR5112	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5113				HR5112 (Lo-16bits)
HR5114	DEF2:Unearth. Pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5115				HR5114 (Lo-16bits)
HR5116	DEF2:Unearth. Pick-up setting I0set(I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5117				HR5116 (Lo-16bits)
HR5118	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5119				HR5118 (Lo-16bits)
HR5120	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5121				HR5120 (Lo-16bits)
HR5122	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5123				HR5122 (Lo-16bits)
HR5124	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5125				HR5124 (Lo-16bits)
HR5126	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5127				HR5126 (Lo-16bits)
HR5128	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5129				HR5128 (Lo-16bits)
HR5130	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5131				HR5130 (Lo-16bits)
HR5132	DEF2:Unearth. Pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5133				HR5132 (Lo-16bits)
HR5134	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5135				HR5134 (Lo-16bits)
HR5136	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5137				HR5136 (Lo-16bits)
HR5138	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5139				HR5138 (Lo-16bits)
HR5140	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5141				HR5140 (Lo-16bits)
HR5142	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5143				HR5142 (Lo-16bits)
HR5144	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5145				HR5144 (Lo-16bits)
HR5146	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5147				HR5146 (Lo-16bits)
HR5148	DEF2:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5149				HR5148 (Lo-16bits)
HR5150	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5151				HR5150 (Lo-16bits)
HR5152	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5153				HR5152 (Lo-16bits)
HR5154	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5155				HR5154 (Lo-16bits)
HR5156	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5157				HR5156 (Lo-16bits)
HR5158	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5159				HR5158 (Lo-16bits)
HR5160	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5161				HR5160 (Lo-16bits)
HR5162	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5163				HR5162 (Lo-16bits)
HR5164	DEF2:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5165				HR5164 (Lo-16bits)
HR5166	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5167	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5168	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR5169	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5170	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5171	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5172	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5173	DEF2:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5174	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5175	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5176	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5177	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5178	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5179	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5180	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5181	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5182	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5183				HR5182 (Lo-16bits)
HR5184	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5185				HR5184 (Lo-16bits)
HR5186	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5187				HR5186 (Lo-16bits)
HR5188	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5189				HR5188 (Lo-16bits)
HR5190	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5191				HR5190 (Lo-16bits)
HR5192	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5193				HR5192 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5194	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5195				HR5194 (Lo-16bits)
HR5196	DEF2:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5197				HR5196 (Lo-16bits)
HR5198	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5199				HR5198 (Lo-16bits)
HR5200	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5201				HR5200 (Lo-16bits)
HR5202	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5203				HR5202 (Lo-16bits)
HR5204	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5205				HR5204 (Lo-16bits)
HR5206	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5207				HR5206 (Lo-16bits)
HR5208	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5209				HR5208 (Lo-16bits)
HR5210	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5211				HR5210 (Lo-16bits)
HR5212	DEF2:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5213				HR5212 (Lo-16bits)
HR5214	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5215				HR5214 (Lo-16bits)
HR5216	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5217				HR5216 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5218	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5219				HR5218 (Lo-16bits)
HR5220	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5221				HR5220 (Lo-16bits)
HR5222	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5223				HR5222 (Lo-16bits)
HR5224	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5225				HR5224 (Lo-16bits)
HR5226	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5227				HR5226 (Lo-16bits)
HR5228	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5229				HR5228 (Lo-16bits)
HR5230	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5231	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5232	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5233	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5234	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5235	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5236	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5237	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5238	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5239	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5240	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5241	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5242	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR5243	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5244	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5245	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5246	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5247	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5248	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5249	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5250	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5251	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5252	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5253	DEF2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5254	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5255	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5256	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5257	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5258	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5259	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5260	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5261	DEF2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5262	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5263				HR5262 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5264	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5265				HR5264 (Lo-16bits)
HR5266	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5267				HR5266 (Lo-16bits)
HR5268	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5269				HR5268 (Lo-16bits)
HR5270	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5271				HR5270 (Lo-16bits)
HR5272	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5273				HR5272 (Lo-16bits)
HR5274	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5275				HR5274 (Lo-16bits)
HR5276	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5277				HR5276 (Lo-16bits)
HR5278	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5279				HR5278 (Lo-16bits)
HR5280	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5281				HR5280 (Lo-16bits)
HR5282	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5283				HR5282 (Lo-16bits)
HR5284	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5285				HR5284 (Lo-16bits)
HR5286	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5287				HR5286 (Lo-16bits)
HR5288	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5289				HR5288 (Lo-16bits)
HR5290	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5291				HR5290 (Lo-16bits)
HR5292	DEF2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5293				HR5292 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5294	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5295				HR5294 (Lo-16bits)
HR5296	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5297				HR5296 (Lo-16bits)
HR5298	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5299				HR5298 (Lo-16bits)
HR5300	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5301				HR5300 (Lo-16bits)
HR5302	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5303				HR5302 (Lo-16bits)
HR5304	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5305				HR5304 (Lo-16bits)
HR5306	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5307				HR5306 (Lo-16bits)
HR5308	DEF2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5309				HR5308 (Lo-16bits)
HR5310	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5311				HR5310 (Lo-16bits)
HR5312	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5313				HR5312 (Lo-16bits)
HR5314	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5315				HR5314 (Lo-16bits)
HR5316	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5317				HR5316 (Lo-16bits)
HR5318	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5319				HR5318 (Lo-16bits)
HR5320	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5321				HR5320 (Lo-16bits)
HR5322	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5323				HR5322 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5324	DEF2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5325				HR5324 (Lo-16bits)
HR5326	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5327	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5328	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5329	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5330	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5331	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5332	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5333	DEF2:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5334	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5335				HR5334 (Lo-16bits)
HR5336	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5337				HR5336 (Lo-16bits)
HR5338	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5339				HR5338 (Lo-16bits)
HR5340	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5341				HR5340 (Lo-16bits)
HR5342	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5343				HR5342 (Lo-16bits)
HR5344	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5345				HR5344 (Lo-16bits)
HR5346	DEF2:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5347				HR5346 (Lo-16bits)
HR5348	DEF2:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5349				HR5348 (Lo-16bits)
HR5350	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5351	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5352	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5353	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5354	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5355	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5356	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5357	DEF2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5358	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5359				HR5358 (Lo-16bits)
HR5360	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5361				HR5360 (Lo-16bits)
HR5362	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5363				HR5362 (Lo-16bits)
HR5364	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5365				HR5364 (Lo-16bits)
HR5366	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5367				HR5366 (Lo-16bits)
HR5368	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5369				HR5368 (Lo-16bits)
HR5370	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5371				HR5370 (Lo-16bits)
HR5372	DEF2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5373				HR5372 (Lo-16bits)
HR5374	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5375	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5376	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5377	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5378	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5379	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5380	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5381	DEF2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5382	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5383	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5384	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5385	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5386	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5387	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5388	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5389	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5390	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Uearthed Start 5=Uearthed Trip 6=Compensated Start 7=Compensated Trip
HR5391	DEF3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5392	DEF3:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5393	DEF3:I0dir>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5394	DEF3:I0dir>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5395				HR5394 (Lo-16bits)
HR5396	DEF3:I0dir>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5397				HR5396 (Lo-16bits)
HR5398	DEF3:I0dir>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5399				HR5398 (Lo-16bits)
HR5400	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5401	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5402	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5403	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5404	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5405	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5406	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5407	DEF3:Grounding type	R(W)	1	0=Uearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5408	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5409				HR5408 (Lo-16bits)
HR5410	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5411				HR5410 (Lo-16bits)
HR5412	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5413				HR5412 (Lo-16bits)
HR5414	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5415				HR5414 (Lo-16bits)
HR5416	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5417				HR5416 (Lo-16bits)
HR5418	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5419				HR5418 (Lo-16bits)
HR5420	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5421				HR5420 (Lo-16bits)
HR5422	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5423				HR5422 (Lo-16bits)
HR5424	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5425				HR5424 (Lo-16bits)
HR5426	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5427				HR5426 (Lo-16bits)
HR5428	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5429				HR5428 (Lo-16bits)
HR5430	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5431				HR5430 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5432	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5433				HR5432 (Lo-16bits)
HR5434	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5435				HR5434 (Lo-16bits)
HR5436	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5437				HR5436 (Lo-16bits)
HR5438	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5439				HR5438 (Lo-16bits)
HR5440	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5441				HR5440 (Lo-16bits)
HR5442	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5443				HR5442 (Lo-16bits)
HR5444	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5445				HR5444 (Lo-16bits)
HR5446	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5447				HR5446 (Lo-16bits)
HR5448	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5449				HR5448 (Lo-16bits)
HR5450	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5451				HR5450 (Lo-16bits)
HR5452	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5453				HR5452 (Lo-16bits)
HR5454	DEF3:Comp. Pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5455				HR5454 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5456	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5457				HR5456 (Lo-16bits)
HR5458	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5459				HR5458 (Lo-16bits)
HR5460	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5461				HR5460 (Lo-16bits)
HR5462	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5463				HR5462 (Lo-16bits)
HR5464	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5465				HR5464 (Lo-16bits)
HR5466	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5467				HR5466 (Lo-16bits)
HR5468	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5469				HR5468 (Lo-16bits)
HR5470	DEF3:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5471				HR5470 (Lo-16bits)
HR5472	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5473				HR5472 (Lo-16bits)
HR5474	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5475				HR5474 (Lo-16bits)
HR5476	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5477				HR5476 (Lo-16bits)
HR5478	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5479				HR5478 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5480	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5481				HR5480 (Lo-16bits)
HR5482	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5483				HR5482 (Lo-16bits)
HR5484	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5485				HR5484 (Lo-16bits)
HR5486	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5487				HR5486 (Lo-16bits)
HR5488	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5489				HR5488 (Lo-16bits)
HR5490	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5491				HR5490 (Lo-16bits)
HR5492	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5493				HR5492 (Lo-16bits)
HR5494	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5495				HR5494 (Lo-16bits)
HR5496	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5497				HR5496 (Lo-16bits)
HR5498	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5499				HR5498 (Lo-16bits)
HR5500	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5501				HR5500 (Lo-16bits)
HR5502	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5503				HR5502 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5504	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5505	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5506	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5507	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5508	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5509	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5510	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5511	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5512	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5513				HR5512 (Lo-16bits)
HR5514	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5515				HR5514 (Lo-16bits)
HR5516	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5517				HR5516 (Lo-16bits)
HR5518	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5519				HR5518 (Lo-16bits)
HR5520	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5521				HR5520 (Lo-16bits)
HR5522	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5523				HR5522 (Lo-16bits)
HR5524	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5525				HR5524 (Lo-16bits)
HR5526	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5527				HR5526 (Lo-16bits)
HR5528	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5529				HR5528 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5530	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5531				HR5530 (Lo-16bits)
HR5532	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5533				HR5532 (Lo-16bits)
HR5534	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5535				HR5534 (Lo-16bits)
HR5536	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5537				HR5536 (Lo-16bits)
HR5538	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5539				HR5538 (Lo-16bits)
HR5540	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5541				HR5540 (Lo-16bits)
HR5542	DEF3:Unearth. Pick-up setting I0set (I0Sinf)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5543				HR5542 (Lo-16bits)
HR5544	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5545				HR5544 (Lo-16bits)
HR5546	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5547				HR5546 (Lo-16bits)
HR5548	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5549				HR5548 (Lo-16bits)
HR5550	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5551				HR5550 (Lo-16bits)
HR5552	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5553				HR5552 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5554	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5555				HR5554 (Lo-16bits)
HR5556	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5557				HR5556 (Lo-16bits)
HR5558	DEF3:Unearth. Pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5559				HR5558 (Lo-16bits)
HR5560	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5561				HR5560 (Lo-16bits)
HR5562	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5563				HR5562 (Lo-16bits)
HR5564	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5565				HR5564 (Lo-16bits)
HR5566	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5567				HR5566 (Lo-16bits)
HR5568	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5569				HR5568 (Lo-16bits)
HR5570	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5571				HR5570 (Lo-16bits)
HR5572	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5573				HR5572 (Lo-16bits)
HR5574	DEF3:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5575				HR5574 (Lo-16bits)
HR5576	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5577				HR5576 (Lo-16bits)
HR5578	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5579				HR5578 (Lo-16bits)
HR5580	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5581				HR5580 (Lo-16bits)
HR5582	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5583				HR5582 (Lo-16bits)
HR5584	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5585				HR5584 (Lo-16bits)
HR5586	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5587				HR5586 (Lo-16bits)
HR5588	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5589				HR5588 (Lo-16bits)
HR5590	DEF3:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5591				HR5590 (Lo-16bits)
HR5592	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5593	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5594	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5595	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5596	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5597	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5598	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5599	DEF3:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR5600	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5601	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5602	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5603	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5604	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5605	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5606	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5607	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR5608	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5609				HR5608 (Lo-16bits)
HR5610	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5611				HR5610 (Lo-16bits)
HR5612	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5613				HR5612 (Lo-16bits)
HR5614	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5615				HR5614 (Lo-16bits)
HR5616	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5617				HR5616 (Lo-16bits)
HR5618	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5619				HR5618 (Lo-16bits)
HR5620	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5621				HR5620 (Lo-16bits)
HR5622	DEF3:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5623				HR5622 (Lo-16bits)
HR5624	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5625				HR5624 (Lo-16bits)
HR5626	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5627				HR5626 (Lo-16bits)
HR5628	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5629				HR5628 (Lo-16bits)
HR5630	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5631				HR5630 (Lo-16bits)
HR5632	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5633				HR5632 (Lo-16bits)
HR5634	DEF3:Definite operating time delay(Uneearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5635				HR5634 (Lo-16bits)
HR5636	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5637				HR5636 (Lo-16bits)
HR5638	DEF3:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5639				HR5638 (Lo-16bits)
HR5640	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5641				HR5640 (Lo-16bits)
HR5642	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5643				HR5642 (Lo-16bits)
HR5644	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5645				HR5644 (Lo-16bits)
HR5646	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5647				HR5646 (Lo-16bits)
HR5648	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5649				HR5648 (Lo-16bits)
HR5650	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5651				HR5650 (Lo-16bits)
HR5652	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5653				HR5652 (Lo-16bits)
HR5654	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5655				HR5654 (Lo-16bits)
HR5656	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5657	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5658	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5659	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5660	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5661	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5662	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5663	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5664	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5665	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5666	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5667	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5668	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5669	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5670	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5671	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5672	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5673	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5674	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5675	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5676	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5677	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5678	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5679	DEF3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5680	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5681	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5682	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5683	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5684	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5685	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5686	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5687	DEF3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR5688	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5689				HR5688 (Lo-16bits)
HR5690	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5691				HR5690 (Lo-16bits)
HR5692	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5693				HR5692 (Lo-16bits)
HR5694	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5695				HR5694 (Lo-16bits)
HR5696	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5697				HR5696 (Lo-16bits)
HR5698	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5699				HR5698 (Lo-16bits)
HR5700	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5701				HR5700 (Lo-16bits)
HR5702	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5703				HR5702 (Lo-16bits)
HR5704	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5705				HR5704 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5706	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5707				HR5706 (Lo-16bits)
HR5708	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5709				HR5708 (Lo-16bits)
HR5710	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5711				HR5710 (Lo-16bits)
HR5712	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5713				HR5712 (Lo-16bits)
HR5714	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5715				HR5714 (Lo-16bits)
HR5716	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5717				HR5716 (Lo-16bits)
HR5718	DEF3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5719				HR5718 (Lo-16bits)
HR5720	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5721				HR5720 (Lo-16bits)
HR5722	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5723				HR5722 (Lo-16bits)
HR5724	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5725				HR5724 (Lo-16bits)
HR5726	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5727				HR5726 (Lo-16bits)
HR5728	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5729				HR5728 (Lo-16bits)
HR5730	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5731				HR5730 (Lo-16bits)
HR5732	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5733				HR5732 (Lo-16bits)
HR5734	DEF3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5735				HR5734 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5736	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5737				HR5736 (Lo-16bits)
HR5738	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5739				HR5738 (Lo-16bits)
HR5740	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5741				HR5740 (Lo-16bits)
HR5742	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5743				HR5742 (Lo-16bits)
HR5744	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5745				HR5744 (Lo-16bits)
HR5746	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5747				HR5746 (Lo-16bits)
HR5748	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5749				HR5748 (Lo-16bits)
HR5750	DEF3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5751				HR5750 (Lo-16bits)
HR5752	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5753	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5754	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5755	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5756	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5757	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5758	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5759	DEF3:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5760	DEF3:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5761				HR5760 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5762	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5763				HR5762 (Lo-16bits)
HR5764	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5765				HR5764 (Lo-16bits)
HR5766	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5767				HR5766 (Lo-16bits)
HR5768	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5769				HR5768 (Lo-16bits)
HR5770	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5771				HR5770 (Lo-16bits)
HR5772	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5773				HR5772 (Lo-16bits)
HR5774	DEF3:2.nd Harmonic Block Limit (lharm / lfund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5775				HR5774 (Lo-16bits)
HR5776	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5777	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5778	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5779	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5780	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5781	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5782	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5783	DEF3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR5784	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5785				HR5784 (Lo-16bits)
HR5786	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5787				HR5786 (Lo-16bits)
HR5788	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5789				HR5788 (Lo-16bits)
HR5790	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5791				HR5790 (Lo-16bits)
HR5792	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5793				HR5792 (Lo-16bits)
HR5794	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5795				HR5794 (Lo-16bits)
HR5796	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5797				HR5796 (Lo-16bits)
HR5798	DEF3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5799				HR5798 (Lo-16bits)
HR5800	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5801	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5802	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5803	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5804	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5805	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5806	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5807	DEF3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR5808	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5809	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5810	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5811	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5812	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5813	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5814	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5815	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5816	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR5817	DEF4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR5818	DEF4:I0 Direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR5819	DEF4:I0dir>>>> Last fault end with	R	1	0=No Faults 1=Start 2=Trip
HR5820	DEF4:I0dir>>>> Last fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR5821				HR5820 (Lo-16bits)
HR5822	DEF4:I0dir>>>> Last fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5823				HR5822 (Lo-16bits)
HR5824	DEF4:I0dir>>>> Last fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR5825				HR5824 (Lo-16bits)
HR5826	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5827	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5828	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5829	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5830	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5831	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5832	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5833	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5834	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5835				HR5834 (Lo-16bits)
HR5836	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5837				HR5836 (Lo-16bits)
HR5838	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5839				HR5838 (Lo-16bits)
HR5840	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5841				HR5840 (Lo-16bits)
HR5842	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5843				HR5842 (Lo-16bits)
HR5844	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5845				HR5844 (Lo-16bits)
HR5846	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5847				HR5846 (Lo-16bits)
HR5848	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5849				HR5848 (Lo-16bits)
HR5850	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5851				HR5850 (Lo-16bits)
HR5852	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5853				HR5852 (Lo-16bits)
HR5854	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5855				HR5854 (Lo-16bits)
HR5856	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5857				HR5856 (Lo-16bits)
HR5858	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5859				HR5858 (Lo-16bits)
HR5860	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5861				HR5860 (Lo-16bits)
HR5862	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5863				HR5862 (Lo-16bits)
HR5864	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5865				HR5864 (Lo-16bits)
HR5866	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5867				HR5866 (Lo-16bits)
HR5868	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5869				HR5868 (Lo-16bits)
HR5870	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5871				HR5870 (Lo-16bits)
HR5872	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5873				HR5872 (Lo-16bits)
HR5874	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5875				HR5874 (Lo-16bits)
HR5876	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5877				HR5876 (Lo-16bits)
HR5878	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5879				HR5878 (Lo-16bits)
HR5880	DEF4:Comp. Pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5881				HR5880 (Lo-16bits)
HR5882	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5883				HR5882 (Lo-16bits)
HR5884	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5885				HR5884 (Lo-16bits)
HR5886	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5887				HR5886 (Lo-16bits)
HR5888	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5889				HR5888 (Lo-16bits)
HR5890	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5891				HR5890 (Lo-16bits)
HR5892	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5893				HR5892 (Lo-16bits)
HR5894	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5895				HR5894 (Lo-16bits)
HR5896	DEF4:Comp. Pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5897				HR5896 (Lo-16bits)
HR5898	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5899				HR5898 (Lo-16bits)
HR5900	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5901				HR5900 (Lo-16bits)
HR5902	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5903				HR5902 (Lo-16bits)
HR5904	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5905				HR5904 (Lo-16bits)
HR5906	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5907				HR5906 (Lo-16bits)
HR5908	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5909				HR5908 (Lo-16bits)
HR5910	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5911				HR5910 (Lo-16bits)
HR5912	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5913				HR5912 (Lo-16bits)
HR5914	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5915				HR5914 (Lo-16bits)
HR5916	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5917				HR5916 (Lo-16bits)
HR5918	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5919				HR5918 (Lo-16bits)
HR5920	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5921				HR5920 (Lo-16bits)
HR5922	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5923				HR5922 (Lo-16bits)
HR5924	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5925				HR5924 (Lo-16bits)
HR5926	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5927				HR5926 (Lo-16bits)
HR5928	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5929				HR5928 (Lo-16bits)
HR5930	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5931	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5932	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5933	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5934	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5935	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5936	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5937	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5938	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5939				HR5938 (Lo-16bits)
HR5940	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5941				HR5940 (Lo-16bits)
HR5942	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5943				HR5942 (Lo-16bits)
HR5944	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5945				HR5944 (Lo-16bits)
HR5946	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5947				HR5946 (Lo-16bits)
HR5948	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5949				HR5948 (Lo-16bits)
HR5950	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5951				HR5950 (Lo-16bits)
HR5952	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5953				HR5952 (Lo-16bits)
HR5954	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5955				HR5954 (Lo-16bits)
HR5956	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5957				HR5956 (Lo-16bits)
HR5958	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5959				HR5958 (Lo-16bits)
HR5960	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5961				HR5960 (Lo-16bits)
HR5962	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5963				HR5962 (Lo-16bits)
HR5964	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5965				HR5964 (Lo-16bits)
HR5966	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5967				HR5966 (Lo-16bits)
HR5968	DEF4:Unearth. Pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5969				HR5968 (Lo-16bits)
HR5970	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5971				HR5970 (Lo-16bits)
HR5972	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5973				HR5972 (Lo-16bits)
HR5974	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5975				HR5974 (Lo-16bits)
HR5976	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5977				HR5976 (Lo-16bits)
HR5978	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5979				HR5978 (Lo-16bits)
HR5980	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5981				HR5980 (Lo-16bits)
HR5982	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5983				HR5982 (Lo-16bits)
HR5984	DEF4:Unearth. Pick-up setting U0set(I0Sinfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5985				HR5984 (Lo-16bits)
HR5986	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5987				HR5986 (Lo-16bits)
HR5988	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5989				HR5988 (Lo-16bits)
HR5990	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5991				HR5990 (Lo-16bits)
HR5992	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5993				HR5992 (Lo-16bits)
HR5994	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5995				HR5994 (Lo-16bits)
HR5996	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5997				HR5996 (Lo-16bits)
HR5998	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5999				HR5998 (Lo-16bits)
HR6000	DEF4:Trip area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6001				HR6000 (Lo-16bits)
HR6002	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6003				HR6002 (Lo-16bits)
HR6004	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6005				HR6004 (Lo-16bits)
HR6006	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6007				HR6006 (Lo-16bits)
HR6008	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6009				HR6008 (Lo-16bits)
HR6010	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6011				HR6010 (Lo-16bits)
HR6012	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6013				HR6012 (Lo-16bits)
HR6014	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6015				HR6014 (Lo-16bits)
HR6016	DEF4:Trip area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6017				HR6016 (Lo-16bits)
HR6018	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6019	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6020	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6021	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6022	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR6023	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6024	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6025	DEF4:Delay type of broadrange characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6026	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6027	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6028	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6029	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6030	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6031	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6032	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6033	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6034	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6035				HR6034 (Lo-16bits)
HR6036	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6037				HR6036 (Lo-16bits)
HR6038	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6039				HR6038 (Lo-16bits)
HR6040	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6041				HR6040 (Lo-16bits)
HR6042	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6043				HR6042 (Lo-16bits)
HR6044	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6045				HR6044 (Lo-16bits)
HR6046	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6047				HR6046 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6048	DEF4:Definite operating time delay(Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6049				HR6048 (Lo-16bits)
HR6050	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6051				HR6050 (Lo-16bits)
HR6052	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6053				HR6052 (Lo-16bits)
HR6054	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6055				HR6054 (Lo-16bits)
HR6056	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6057				HR6056 (Lo-16bits)
HR6058	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6059				HR6058 (Lo-16bits)
HR6060	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6061				HR6060 (Lo-16bits)
HR6062	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6063				HR6062 (Lo-16bits)
HR6064	DEF4:Definite operating time delay(Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6065				HR6064 (Lo-16bits)
HR6066	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6067				HR6066 (Lo-16bits)
HR6068	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6069				HR6068 (Lo-16bits)
HR6070	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6071				HR6070 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6072	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6073				HR6072 (Lo-16bits)
HR6074	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6075				HR6074 (Lo-16bits)
HR6076	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6077				HR6076 (Lo-16bits)
HR6078	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6079				HR6078 (Lo-16bits)
HR6080	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6081				HR6080 (Lo-16bits)
HR6082	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6083	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6084	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6085	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6086	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6087	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6088	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6089	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6090	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6091	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6092	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6093	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6094	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6095	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6096	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6097	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6098	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6099	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6100	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6101	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6102	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6103	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6104	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6105	DEF4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6106	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6107	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6108	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6109	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6110	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6111	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6112	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6113	DEF4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type
HR6114	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6115				HR6114 (Lo-16bits)
HR6116	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6117				HR6116 (Lo-16bits)
HR6118	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6119				HR6118 (Lo-16bits)
HR6120	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6121				HR6120 (Lo-16bits)
HR6122	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6123				HR6122 (Lo-16bits)
HR6124	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6125				HR6124 (Lo-16bits)
HR6126	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6127				HR6126 (Lo-16bits)
HR6128	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6129				HR6128 (Lo-16bits)
HR6130	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6131				HR6130 (Lo-16bits)
HR6132	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6133				HR6132 (Lo-16bits)
HR6134	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6135				HR6134 (Lo-16bits)
HR6136	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6137				HR6136 (Lo-16bits)
HR6138	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6139				HR6138 (Lo-16bits)
HR6140	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6141				HR6140 (Lo-16bits)
HR6142	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6143				HR6142 (Lo-16bits)
HR6144	DEF4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6145				HR6144 (Lo-16bits)
HR6146	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6147				HR6146 (Lo-16bits)
HR6148	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6149				HR6148 (Lo-16bits)
HR6150	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6151				HR6150 (Lo-16bits)
HR6152	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6153				HR6152 (Lo-16bits)
HR6154	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6155				HR6154 (Lo-16bits)
HR6156	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6157				HR6156 (Lo-16bits)
HR6158	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6159				HR6158 (Lo-16bits)
HR6160	DEF4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6161				HR6160 (Lo-16bits)
HR6162	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6163				HR6162 (Lo-16bits)
HR6164	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6165				HR6164 (Lo-16bits)
HR6166	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6167				HR6166 (Lo-16bits)
HR6168	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6169				HR6168 (Lo-16bits)
HR6170	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6171				HR6170 (Lo-16bits)
HR6172	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6173				HR6172 (Lo-16bits)
HR6174	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6175				HR6174 (Lo-16bits)
HR6176	DEF4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6177				HR6176 (Lo-16bits)
HR6178	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6179	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6180	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6181	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6182	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6183	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6184	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6185	DEF4:Inrush Harmonic Blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6186	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6187				HR6186 (Lo-16bits)
HR6188	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6189				HR6188 (Lo-16bits)
HR6190	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6191				HR6190 (Lo-16bits)
HR6192	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6193				HR6192 (Lo-16bits)
HR6194	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6195				HR6194 (Lo-16bits)
HR6196	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6197				HR6196 (Lo-16bits)
HR6198	DEF4:2.nd Harmonic Block Limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6199				HR6198 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6200	DEF4:2.nd Harmonic Block Limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6201				HR6200 (Lo-16bits)
HR6202	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6203	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6204	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6205	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6206	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6207	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6208	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6209	DEF4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6210	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6211				HR6210 (Lo-16bits)
HR6212	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6213				HR6212 (Lo-16bits)
HR6214	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6215				HR6214 (Lo-16bits)
HR6216	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6217				HR6216 (Lo-16bits)
HR6218	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6219				HR6218 (Lo-16bits)
HR6220	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6221				HR6220 (Lo-16bits)
HR6222	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6223				HR6222 (Lo-16bits)
HR6224	DEF4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6225				HR6224 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6226	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6227	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6228	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6229	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6230	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6231	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6232	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6233	DEF4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6234	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6235	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6236	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6237	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6238	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6239	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6240	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6241	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6242	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR6243	IEF1:U0> Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input

Holding Register	Parameter	Access	Length	Info
HR6244	CUB1:I2> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6245	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6246	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6247				HR6246 (Lo-16bits)
HR6248	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6249				HR6248 (Lo-16bits)
HR6250	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6251				HR6250 (Lo-16bits)
HR6252	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6253				HR6252 (Lo-16bits)
HR6254	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6255				HR6254 (Lo-16bits)
HR6256	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6257				HR6256 (Lo-16bits)
HR6258	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6259				HR6258 (Lo-16bits)
HR6260	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6261				HR6260 (Lo-16bits)
HR6262	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6263				HR6262 (Lo-16bits)
HR6264	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6265				HR6264 (Lo-16bits)
HR6266	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6267				HR6266 (Lo-16bits)
HR6268	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6269				HR6268 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6270	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6271				HR6270 (Lo-16bits)
HR6272	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6273				HR6272 (Lo-16bits)
HR6274	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6275				HR6274 (Lo-16bits)
HR6276	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6277				HR6276 (Lo-16bits)
HR6278	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6279				HR6278 (Lo-16bits)
HR6280	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6281				HR6280 (Lo-16bits)
HR6282	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6283				HR6282 (Lo-16bits)
HR6284	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6285				HR6284 (Lo-16bits)
HR6286	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6287				HR6286 (Lo-16bits)
HR6288	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6289				HR6288 (Lo-16bits)
HR6290	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6291				HR6290 (Lo-16bits)
HR6292	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6293				HR6292 (Lo-16bits)
HR6294	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6295	CUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6296	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6297	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6298	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6299	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6300	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6301	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6302	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6303				HR6302 (Lo-16bits)
HR6304	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6305				HR6304 (Lo-16bits)
HR6306	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6307				HR6306 (Lo-16bits)
HR6308	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6309				HR6308 (Lo-16bits)
HR6310	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6311				HR6310 (Lo-16bits)
HR6312	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6313				HR6312 (Lo-16bits)
HR6314	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6315				HR6314 (Lo-16bits)
HR6316	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6317				HR6316 (Lo-16bits)
HR6318	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6319	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6320	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6321	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6322	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6323	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6324	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6325	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6326	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6327	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6328	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6329	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6330	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6331	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6332	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6333	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6334	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6335	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6336	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6337	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6338	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6339	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6340	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6341	CUB1:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6342	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6343	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6344	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6345	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6346	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6347	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6348	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6349	CUB1:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6350	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6351				HR6350 (Lo-16bits)
HR6352	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6353				HR6352 (Lo-16bits)
HR6354	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6355				HR6354 (Lo-16bits)
HR6356	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6357				HR6356 (Lo-16bits)
HR6358	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6359				HR6358 (Lo-16bits)
HR6360	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6361				HR6360 (Lo-16bits)
HR6362	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6363				HR6362 (Lo-16bits)
HR6364	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6365				HR6364 (Lo-16bits)
HR6366	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6367				HR6366 (Lo-16bits)
HR6368	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6369				HR6368 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6370	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6371				HR6370 (Lo-16bits)
HR6372	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6373				HR6372 (Lo-16bits)
HR6374	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6375				HR6374 (Lo-16bits)
HR6376	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6377				HR6376 (Lo-16bits)
HR6378	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6379				HR6378 (Lo-16bits)
HR6380	CUB1:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6381				HR6380 (Lo-16bits)
HR6382	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6383				HR6382 (Lo-16bits)
HR6384	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6385				HR6384 (Lo-16bits)
HR6386	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6387				HR6386 (Lo-16bits)
HR6388	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6389				HR6388 (Lo-16bits)
HR6390	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6391				HR6390 (Lo-16bits)
HR6392	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6393				HR6392 (Lo-16bits)
HR6394	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6395				HR6394 (Lo-16bits)
HR6396	CUB1:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6397				HR6396 (Lo-16bits)
HR6398	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6399				HR6398 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6400	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6401				HR6400 (Lo-16bits)
HR6402	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6403				HR6402 (Lo-16bits)
HR6404	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6405				HR6404 (Lo-16bits)
HR6406	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6407				HR6406 (Lo-16bits)
HR6408	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6409				HR6408 (Lo-16bits)
HR6410	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6411				HR6410 (Lo-16bits)
HR6412	CUB1:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6413				HR6412 (Lo-16bits)
HR6414	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6415	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6416	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6417	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6418	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6419	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6420	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6421	CUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6422	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6423				HR6422 (Lo-16bits)
HR6424	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6425				HR6424 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6426	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6427				HR6426 (Lo-16bits)
HR6428	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6429				HR6428 (Lo-16bits)
HR6430	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6431				HR6430 (Lo-16bits)
HR6432	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6433				HR6432 (Lo-16bits)
HR6434	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6435				HR6434 (Lo-16bits)
HR6436	CUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6437				HR6436 (Lo-16bits)
HR6438	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6439	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6440	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6441	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6442	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6443	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6444	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6445	CUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6446	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6447	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6448	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6449	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6450	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6451	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6452	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6453	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6454	CUB2:I2>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6455	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6456	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6457				HR6456 (Lo-16bits)
HR6458	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6459				HR6458 (Lo-16bits)
HR6460	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6461				HR6460 (Lo-16bits)
HR6462	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6463				HR6462 (Lo-16bits)
HR6464	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6465				HR6464 (Lo-16bits)
HR6466	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6467				HR6466 (Lo-16bits)
HR6468	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6469				HR6468 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6470	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6471				HR6470 (Lo-16bits)
HR6472	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6473				HR6472 (Lo-16bits)
HR6474	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6475				HR6474 (Lo-16bits)
HR6476	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6477				HR6476 (Lo-16bits)
HR6478	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6479				HR6478 (Lo-16bits)
HR6480	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6481				HR6480 (Lo-16bits)
HR6482	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6483				HR6482 (Lo-16bits)
HR6484	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6485				HR6484 (Lo-16bits)
HR6486	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6487				HR6486 (Lo-16bits)
HR6488	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6489				HR6488 (Lo-16bits)
HR6490	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6491				HR6490 (Lo-16bits)
HR6492	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6493				HR6492 (Lo-16bits)
HR6494	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6495				HR6494 (Lo-16bits)
HR6496	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6497				HR6496 (Lo-16bits)
HR6498	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6499				HR6498 (Lo-16bits)
HR6500	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6501				HR6500 (Lo-16bits)
HR6502	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6503				HR6502 (Lo-16bits)
HR6504	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6505	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6506	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6507	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6508	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6509	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6510	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6511	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR6512	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6513				HR6512 (Lo-16bits)
HR6514	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6515				HR6514 (Lo-16bits)
HR6516	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6517				HR6516 (Lo-16bits)
HR6518	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6519				HR6518 (Lo-16bits)
HR6520	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6521				HR6520 (Lo-16bits)
HR6522	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6523				HR6522 (Lo-16bits)
HR6524	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6525				HR6524 (Lo-16bits)
HR6526	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6527				HR6526 (Lo-16bits)
HR6528	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6529	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6530	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6531	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6532	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6533	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6534	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6535	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6536	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6537	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6538	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6539	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6540	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6541	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6542	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6543	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6544	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6545	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6546	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6547	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6548	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6549	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6550	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6551	CUB2:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6552	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6553	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6554	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6555	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6556	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6557	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6558	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6559	CUB2:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6560	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6561				HR6560 (Lo-16bits)
HR6562	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6563				HR6562 (Lo-16bits)
HR6564	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6565				HR6564 (Lo-16bits)
HR6566	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6567				HR6566 (Lo-16bits)
HR6568	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6569				HR6568 (Lo-16bits)
HR6570	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6571				HR6570 (Lo-16bits)
HR6572	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6573				HR6572 (Lo-16bits)
HR6574	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6575				HR6574 (Lo-16bits)
HR6576	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6577				HR6576 (Lo-16bits)
HR6578	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6579				HR6578 (Lo-16bits)
HR6580	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6581				HR6580 (Lo-16bits)
HR6582	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6583				HR6582 (Lo-16bits)
HR6584	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6585				HR6584 (Lo-16bits)
HR6586	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6587				HR6586 (Lo-16bits)
HR6588	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6589				HR6588 (Lo-16bits)
HR6590	CUB2:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6591				HR6590 (Lo-16bits)
HR6592	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6593				HR6592 (Lo-16bits)
HR6594	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6595				HR6594 (Lo-16bits)
HR6596	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6597				HR6596 (Lo-16bits)
HR6598	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6599				HR6598 (Lo-16bits)
HR6600	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6601				HR6600 (Lo-16bits)
HR6602	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6603				HR6602 (Lo-16bits)
HR6604	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6605				HR6604 (Lo-16bits)
HR6606	CUB2:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6607				HR6606 (Lo-16bits)
HR6608	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6609				HR6608 (Lo-16bits)
HR6610	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6611				HR6610 (Lo-16bits)
HR6612	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6613				HR6612 (Lo-16bits)
HR6614	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6615				HR6614 (Lo-16bits)
HR6616	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6617				HR6616 (Lo-16bits)
HR6618	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6619				HR6618 (Lo-16bits)
HR6620	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6621				HR6620 (Lo-16bits)
HR6622	CUB2:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6623				HR6622 (Lo-16bits)
HR6624	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6625	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6626	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6627	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6628	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6629	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6630	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6631	CUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6632	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6633				HR6632 (Lo-16bits)
HR6634	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6635				HR6634 (Lo-16bits)
HR6636	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6637				HR6636 (Lo-16bits)
HR6638	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6639				HR6638 (Lo-16bits)
HR6640	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6641				HR6640 (Lo-16bits)
HR6642	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6643				HR6642 (Lo-16bits)
HR6644	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6645				HR6644 (Lo-16bits)
HR6646	CUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6647				HR6646 (Lo-16bits)
HR6648	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6649	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6650	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6651	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6652	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6653	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6654	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6655	CUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6656	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6657	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6658	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6659	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6660	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6661	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6662	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6663	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6664	CUB3:I2>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6665	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR6666	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6667				HR6666 (Lo-16bits)
HR6668	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6669				HR6668 (Lo-16bits)
HR6670	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6671				HR6670 (Lo-16bits)
HR6672	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6673				HR6672 (Lo-16bits)
HR6674	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6675				HR6674 (Lo-16bits)
HR6676	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6677				HR6676 (Lo-16bits)
HR6678	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6679				HR6678 (Lo-16bits)
HR6680	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6681				HR6680 (Lo-16bits)
HR6682	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6683				HR6682 (Lo-16bits)
HR6684	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6685				HR6684 (Lo-16bits)
HR6686	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6687				HR6686 (Lo-16bits)
HR6688	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6689				HR6688 (Lo-16bits)
HR6690	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6691				HR6690 (Lo-16bits)
HR6692	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6693				HR6692 (Lo-16bits)
HR6694	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6695				HR6694 (Lo-16bits)
HR6696	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6697				HR6696 (Lo-16bits)
HR6698	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6699				HR6698 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6700	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6701				HR6700 (Lo-16bits)
HR6702	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6703				HR6702 (Lo-16bits)
HR6704	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6705				HR6704 (Lo-16bits)
HR6706	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6707				HR6706 (Lo-16bits)
HR6708	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6709				HR6708 (Lo-16bits)
HR6710	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6711				HR6710 (Lo-16bits)
HR6712	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6713				HR6712 (Lo-16bits)
HR6714	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6715	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6716	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6717	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6718	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6719	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6720	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6721	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR6722	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6723				HR6722 (Lo-16bits)
HR6724	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6725				HR6724 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6726	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6727				HR6726 (Lo-16bits)
HR6728	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6729				HR6728 (Lo-16bits)
HR6730	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6731				HR6730 (Lo-16bits)
HR6732	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6733				HR6732 (Lo-16bits)
HR6734	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6735				HR6734 (Lo-16bits)
HR6736	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6737				HR6736 (Lo-16bits)
HR6738	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6739	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6740	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6741	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6742	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6743	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6744	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6745	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6746	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6747	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6748	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6749	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6750	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6751	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6752	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6753	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6754	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6755	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6756	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6757	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6758	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6759	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6760	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6761	CUB3:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6762	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6763	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6764	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6765	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6766	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6767	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6768	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6769	CUB3:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6770	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6771				HR6770 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6772	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6773				HR6772 (Lo-16bits)
HR6774	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6775				HR6774 (Lo-16bits)
HR6776	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6777				HR6776 (Lo-16bits)
HR6778	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6779				HR6778 (Lo-16bits)
HR6780	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6781				HR6780 (Lo-16bits)
HR6782	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6783				HR6782 (Lo-16bits)
HR6784	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6785				HR6784 (Lo-16bits)
HR6786	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6787				HR6786 (Lo-16bits)
HR6788	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6789				HR6788 (Lo-16bits)
HR6790	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6791				HR6790 (Lo-16bits)
HR6792	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6793				HR6792 (Lo-16bits)
HR6794	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6795				HR6794 (Lo-16bits)
HR6796	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6797				HR6796 (Lo-16bits)
HR6798	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6799				HR6798 (Lo-16bits)
HR6800	CUB3:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6801				HR6800 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6802	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6803				HR6802 (Lo-16bits)
HR6804	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6805				HR6804 (Lo-16bits)
HR6806	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6807				HR6806 (Lo-16bits)
HR6808	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6809				HR6808 (Lo-16bits)
HR6810	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6811				HR6810 (Lo-16bits)
HR6812	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6813				HR6812 (Lo-16bits)
HR6814	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6815				HR6814 (Lo-16bits)
HR6816	CUB3:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6817				HR6816 (Lo-16bits)
HR6818	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6819				HR6818 (Lo-16bits)
HR6820	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6821				HR6820 (Lo-16bits)
HR6822	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6823				HR6822 (Lo-16bits)
HR6824	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6825				HR6824 (Lo-16bits)
HR6826	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6827				HR6826 (Lo-16bits)
HR6828	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6829				HR6828 (Lo-16bits)
HR6830	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6831				HR6830 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6832	CUB3:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6833				HR6832 (Lo-16bits)
HR6834	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6835	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6836	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6837	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6838	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6839	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6840	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6841	CUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR6842	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6843				HR6842 (Lo-16bits)
HR6844	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6845				HR6844 (Lo-16bits)
HR6846	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6847				HR6846 (Lo-16bits)
HR6848	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6849				HR6848 (Lo-16bits)
HR6850	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6851				HR6850 (Lo-16bits)
HR6852	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6853				HR6852 (Lo-16bits)
HR6854	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6855				HR6854 (Lo-16bits)
HR6856	CUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6857				HR6856 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6858	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6859	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6860	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6861	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6862	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6863	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6864	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6865	CUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR6866	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6867	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6868	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6869	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6870	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6871	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6872	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6873	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6874	CUB4:I2>>>> [46] mode	R(W)	1	0=Disabled 1=Activated
HR6875	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR6876	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6877				HR6876 (Lo-16bits)
HR6878	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6879				HR6878 (Lo-16bits)
HR6880	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6881				HR6880 (Lo-16bits)
HR6882	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6883				HR6882 (Lo-16bits)
HR6884	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6885				HR6884 (Lo-16bits)
HR6886	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6887				HR6886 (Lo-16bits)
HR6888	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6889				HR6888 (Lo-16bits)
HR6890	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6891				HR6890 (Lo-16bits)
HR6892	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6893				HR6892 (Lo-16bits)
HR6894	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6895				HR6894 (Lo-16bits)
HR6896	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6897				HR6896 (Lo-16bits)
HR6898	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6899				HR6898 (Lo-16bits)
HR6900	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6901				HR6900 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6902	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6903				HR6902 (Lo-16bits)
HR6904	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6905				HR6904 (Lo-16bits)
HR6906	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6907				HR6906 (Lo-16bits)
HR6908	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6909				HR6908 (Lo-16bits)
HR6910	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6911				HR6910 (Lo-16bits)
HR6912	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6913				HR6912 (Lo-16bits)
HR6914	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6915				HR6914 (Lo-16bits)
HR6916	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6917				HR6916 (Lo-16bits)
HR6918	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6919				HR6918 (Lo-16bits)
HR6920	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6921				HR6920 (Lo-16bits)
HR6922	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6923				HR6922 (Lo-16bits)
HR6924	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6925	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6926	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6927	CUB4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6928	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6929	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6930	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6931	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR6932	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6933				HR6932 (Lo-16bits)
HR6934	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6935				HR6934 (Lo-16bits)
HR6936	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6937				HR6936 (Lo-16bits)
HR6938	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6939				HR6938 (Lo-16bits)
HR6940	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6941				HR6940 (Lo-16bits)
HR6942	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6943				HR6942 (Lo-16bits)
HR6944	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6945				HR6944 (Lo-16bits)
HR6946	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6947				HR6946 (Lo-16bits)
HR6948	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6949	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6950	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6951	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6952	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6953	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6954	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6955	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6956	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6957	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6958	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6959	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6960	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6961	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6962	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6963	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6964	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6965	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6966	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6967	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6968	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6969	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6970	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6971	CUB4:Delay char. ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6972	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6973	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6974	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6975	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6976	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6977	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6978	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6979	CUB4:Non standard Delay char.	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6980	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6981				HR6980 (Lo-16bits)
HR6982	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6983				HR6982 (Lo-16bits)
HR6984	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6985				HR6984 (Lo-16bits)
HR6986	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6987				HR6986 (Lo-16bits)
HR6988	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6989				HR6988 (Lo-16bits)
HR6990	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6991				HR6990 (Lo-16bits)
HR6992	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6993				HR6992 (Lo-16bits)
HR6994	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6995				HR6994 (Lo-16bits)
HR6996	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6997				HR6996 (Lo-16bits)
HR6998	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6999				HR6998 (Lo-16bits)
HR7000	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7001				HR7000 (Lo-16bits)
HR7002	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7003				HR7002 (Lo-16bits)
HR7004	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7005				HR7004 (Lo-16bits)
HR7006	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7007				HR7006 (Lo-16bits)
HR7008	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7009				HR7008 (Lo-16bits)
HR7010	CUB4:IDMT Const. A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7011				HR7010 (Lo-16bits)
HR7012	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7013				HR7012 (Lo-16bits)
HR7014	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7015				HR7014 (Lo-16bits)
HR7016	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7017				HR7016 (Lo-16bits)
HR7018	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7019				HR7018 (Lo-16bits)
HR7020	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7021				HR7020 (Lo-16bits)
HR7022	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7023				HR7022 (Lo-16bits)
HR7024	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7025				HR7024 (Lo-16bits)
HR7026	CUB4:IDMT Const. B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7027				HR7026 (Lo-16bits)
HR7028	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7029				HR7028 (Lo-16bits)
HR7030	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7031				HR7030 (Lo-16bits)
HR7032	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7033				HR7032 (Lo-16bits)
HR7034	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7035				HR7034 (Lo-16bits)
HR7036	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7037				HR7036 (Lo-16bits)
HR7038	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7039				HR7038 (Lo-16bits)
HR7040	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7041				HR7040 (Lo-16bits)
HR7042	CUB4:IDMT Const. C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7043				HR7042 (Lo-16bits)
HR7044	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7045	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7046	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7047	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7048	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7049	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7050	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7051	CUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7052	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7053				HR7052 (Lo-16bits)
HR7054	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7055				HR7054 (Lo-16bits)
HR7056	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7057				HR7056 (Lo-16bits)
HR7058	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7059				HR7058 (Lo-16bits)
HR7060	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7061				HR7060 (Lo-16bits)
HR7062	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7063				HR7062 (Lo-16bits)
HR7064	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7065				HR7064 (Lo-16bits)
HR7066	CUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7067				HR7066 (Lo-16bits)
HR7068	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7069	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7070	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7071	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7072	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7073	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7074	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7075	CUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7076	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7077	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7078	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7079	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7080	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7081	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7082	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7083	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7084	HOC1:Ih> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7085	HOC1:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)
HR7087	HOC1:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)
HR7089	HOC1:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	HOC1:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	HOC1:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7094				HR7093 (Lo-16bits)
HR7095	HOC1:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7096				HR7095 (Lo-16bits)
HR7097	HOC1:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7098				HR7097 (Lo-16bits)
HR7099	HOC1:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7100				HR7099 (Lo-16bits)
HR7101	HOC1:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7102				HR7101 (Lo-16bits)
HR7103	HOC1:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7104				HR7103 (Lo-16bits)
HR7105	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7106	HOC2:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7107				HR7106 (Lo-16bits)
HR7108	HOC2:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7109				HR7108 (Lo-16bits)
HR7110	HOC2:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7111				HR7110 (Lo-16bits)
HR7112	HOC2:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7113				HR7112 (Lo-16bits)
HR7114	HOC2:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7115				HR7114 (Lo-16bits)
HR7116	HOC2:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7117				HR7116 (Lo-16bits)
HR7118	HOC2:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7119				HR7118 (Lo-16bits)
HR7120	HOC2:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7121				HR7120 (Lo-16bits)
HR7122	HOC2:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7123				HR7122 (Lo-16bits)
HR7124	HOC2:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7125				HR7124 (Lo-16bits)
HR7126	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7127	HOC3:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	HOC3:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	HOC3:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	HOC3:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	HOC3:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7136				HR7135 (Lo-16bits)
HR7137	HOC3:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	HOC3:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	HOC3:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	HOC3:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	HOC3:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7148	HOC4:L1 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7149				HR7148 (Lo-16bits)
HR7150	HOC4:L1 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7151				HR7150 (Lo-16bits)
HR7152	HOC4:L2 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7153				HR7152 (Lo-16bits)
HR7154	HOC4:L2 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7155				HR7154 (Lo-16bits)
HR7156	HOC4:L3 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7157				HR7156 (Lo-16bits)
HR7158	HOC4:L3 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7159				HR7158 (Lo-16bits)
HR7160	HOC4:I01 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7161				HR7160 (Lo-16bits)
HR7162	HOC4:I01 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7163				HR7162 (Lo-16bits)
HR7164	HOC4:I02 Harm p.u.	R	2	FLOAT [0...100000] (Hi-16bits)
HR7165				HR7164 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7166	HOC4:I02 Harm per	R	2	FLOAT [0...100000] (Hi-16bits)
HR7167				HR7166 (Lo-16bits)
HR7168	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR7169	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked
HR7170	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR7171	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR7172	DIF1:Id< condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR7173				HR7172 (Lo-16bits)
HR7174	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR7175				HR7174 (Lo-16bits)
HR7176	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)
HR7177				HR7176 (Lo-16bits)
HR7178	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7179	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7180	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR7181	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR7182	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7183	TOLT1:TT> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7184	TOLT1:TT> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7185	TOLT1:TT> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7186	TOLT1:TT> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7187	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7188	TOLT1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7189	TOLT1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7190	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR7191	TOLT1:TT> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7192				HR7191 (Lo-16bits)
HR7193	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7194	TOLF1:TF> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7195	TOLF1:TF> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7196	TOLF1:TF> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7197	TOLF1:TF> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7198	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR7199	TOLF1:Temp C or F deg	RW	1	0=C 1=F
HR7200	TOLF1:Ambient Temp Sel	RW	1	0=Manual Set 1=RTD Meas
HR7201	TOLF1:RTD Sensor	R	1	0=Invalid 1=OK
HR7202	TOLF1:Set or Estimate tau (t const)	RW	1	0=Set 1=Estimate
HR7203	TOLF1:TF> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR7206	TOLM1:TM> Setting alarm	R	1	0=SF setting ok 1=Service factor set fault. Override to 1.0
HR7207	TOLM1:TM> Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault. Override to 1.0
HR7208	TOLM1:TM> Setting alarm	R	1	0=Nominal current calc ok 1=Nominal current set fault. Override to 1.0
HR7209	TOLM1:TM> Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR7210	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR7211	TOLM1:Temp C or F deg	RW	1	0=C 1=F

Holding Register	Parameter	Access	Length	Info
HR7212	TOLM1:NPS Biasing in use	RW	1	0=No NPS biasing 1=NPS Biasing in use
HR7213	TOLM1:Time constants	RW	1	0=Single 1=Multiple
HR7214	TOLM1:Estimate Short TC and timings	RW	1	0=Set manually 1=Estimate (on-line)
HR7215	TOLM1:T const dyn. balancing	RW	1	0=Fixed 1=Dynamic
HR7216	TOLM1:Dev.Temp (tmax)	RW	1	0=A 1=B 2=F 3=H 4=Manual set
HR7217	TOLM1:Amb. T Selection	RW	1	0=Manual set 1=RTD
HR7218	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR7219	TOLM1:TM> Thermal cap used	R	2	FLOAT [0...500] (Hi-16bits)
HR7220				HR7219 (Lo-16bits)
HR7221	LCR1:ISt> condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR7222	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked
HR7223	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7224	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7225	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7226	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7227	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7228	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7229	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7230	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7231	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7232	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7233	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7234	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7235	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7240				HR7239 (Lo-16bits)
HR7241	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)
HR7249	OV1:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7252	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7253	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7254	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7255	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7256	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7257	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7258	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7259	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7268				HR7267 (Lo-16bits)
HR7269	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7278				HR7277 (Lo-16bits)
HR7279	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7280				HR7279 (Lo-16bits)
HR7281	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7282				HR7281 (Lo-16bits)
HR7283	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7284				HR7283 (Lo-16bits)
HR7285	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7297	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7302				HR7301 (Lo-16bits)
HR7303	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7304				HR7303 (Lo-16bits)
HR7305	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7306				HR7305 (Lo-16bits)
HR7307	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7308				HR7307 (Lo-16bits)
HR7309	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7310				HR7309 (Lo-16bits)
HR7311	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7312				HR7311 (Lo-16bits)
HR7313	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7314				HR7313 (Lo-16bits)
HR7315	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7316				HR7315 (Lo-16bits)
HR7317	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7318				HR7317 (Lo-16bits)
HR7319	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7320				HR7319 (Lo-16bits)
HR7321	OV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7322				HR7321 (Lo-16bits)
HR7323	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7324	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7325	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7326	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7327	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7328	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7329	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7330	OV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7331	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7332	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7333	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7334	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7335	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7336	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7337	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7338	OV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7339	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7340	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7341	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7342	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7343	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7344	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7345	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7346	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7347	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7348	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7349	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7350	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7351	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7352	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7353	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7354	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7355	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7356	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7357	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7371	OV2:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7374	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7375	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7376	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7377	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7378	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7379	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7380	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7381	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)
HR7383	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)
HR7387	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7397	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7398				HR7397 (Lo-16bits)
HR7399	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7400				HR7399 (Lo-16bits)
HR7401	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7402				HR7401 (Lo-16bits)
HR7403	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7404				HR7403 (Lo-16bits)
HR7405	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)
HR7417	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7422				HR7421 (Lo-16bits)
HR7423	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7424				HR7423 (Lo-16bits)
HR7425	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7426				HR7425 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7427	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7428				HR7427 (Lo-16bits)
HR7429	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7430				HR7429 (Lo-16bits)
HR7431	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7432				HR7431 (Lo-16bits)
HR7433	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7434				HR7433 (Lo-16bits)
HR7435	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7436				HR7435 (Lo-16bits)
HR7437	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)
HR7439	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	OV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7446	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7447	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7448	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7449	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7450	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7451	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7452	OV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7453	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7454	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7455	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7456	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7457	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7458	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7459	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7460	OV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7461	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7462	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7463	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7464	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7465	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7466	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7467	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7468	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7469	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7470	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR7471	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7472	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7473	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7474	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7475	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7476	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7477	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7478	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7479	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7486				HR7485 (Lo-16bits)
HR7487	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7488				HR7487 (Lo-16bits)
HR7489	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7490				HR7489 (Lo-16bits)
HR7491	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7492				HR7491 (Lo-16bits)
HR7493	OV3:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7496	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7497	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7498	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7499	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7500	OV3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7501	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7502	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7503	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7510				HR7509 (Lo-16bits)
HR7511	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7512				HR7511 (Lo-16bits)
HR7513	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7514				HR7513 (Lo-16bits)
HR7515	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7516				HR7515 (Lo-16bits)
HR7517	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7518				HR7517 (Lo-16bits)
HR7519	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7520				HR7519 (Lo-16bits)
HR7521	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7522				HR7521 (Lo-16bits)
HR7523	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7524				HR7523 (Lo-16bits)
HR7525	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7526				HR7525 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7527	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7528				HR7527 (Lo-16bits)
HR7529	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7530				HR7529 (Lo-16bits)
HR7531	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7532				HR7531 (Lo-16bits)
HR7533	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7534				HR7533 (Lo-16bits)
HR7535	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7536				HR7535 (Lo-16bits)
HR7537	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7538				HR7537 (Lo-16bits)
HR7539	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7540				HR7539 (Lo-16bits)
HR7541	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)
HR7547	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7557	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	OV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7568	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7569	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7570	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7571	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7572	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7573	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7574	OV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7575	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7576	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7577	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7578	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7579	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7580	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7581	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7582	OV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7583	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7584	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7585	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7586	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7587	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7588	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7589	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7590	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7591	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR7592	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR7593	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7594	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7595	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7596	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7597	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7598	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7599	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7600	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7601	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7606				HR7605 (Lo-16bits)
HR7607	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7608				HR7607 (Lo-16bits)
HR7609	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7610				HR7609 (Lo-16bits)
HR7611	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7612				HR7611 (Lo-16bits)
HR7613	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	OV4:Pick-up setting Uset	R(W)	2	FLOAT [50...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7618	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7619	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7620	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7621	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7622	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7623	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7624	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR7625	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7630				HR7629 (Lo-16bits)
HR7631	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7632				HR7631 (Lo-16bits)
HR7633	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7634				HR7633 (Lo-16bits)
HR7635	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7636				HR7635 (Lo-16bits)
HR7637	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7638				HR7637 (Lo-16bits)
HR7639	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7640				HR7639 (Lo-16bits)
HR7641	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7642				HR7641 (Lo-16bits)
HR7643	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7644				HR7643 (Lo-16bits)
HR7645	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7646				HR7645 (Lo-16bits)
HR7647	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7648				HR7647 (Lo-16bits)
HR7649	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7650				HR7649 (Lo-16bits)
HR7651	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7652				HR7651 (Lo-16bits)
HR7653	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7658				HR7657 (Lo-16bits)
HR7659	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7670				HR7669 (Lo-16bits)
HR7671	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7672				HR7671 (Lo-16bits)
HR7673	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7674				HR7673 (Lo-16bits)
HR7675	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7676				HR7675 (Lo-16bits)
HR7677	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7688				HR7687 (Lo-16bits)
HR7689	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7690	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7691	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7692	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7693	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7694	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7695	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7696	OV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7697	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7698	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7699	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7700	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7701	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7702	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7703	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7704	OV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7705	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7706	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7707	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7708	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7709	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7710	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7711	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7712	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7713	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7714	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7715	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7716	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7717	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7718	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7719	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7720	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7721	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7722	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7723	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7731	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7742				HR7741 (Lo-16bits)
HR7743	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7744				HR7743 (Lo-16bits)
HR7745	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7746				HR7745 (Lo-16bits)
HR7747	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7748				HR7747 (Lo-16bits)
HR7749	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7750				HR7749 (Lo-16bits)
HR7751	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7752				HR7751 (Lo-16bits)
HR7753	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7754				HR7753 (Lo-16bits)
HR7755	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7756	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7757	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7758	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7759	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7760	UV1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7761	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7762	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7763	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7764				HR7763 (Lo-16bits)
HR7765	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7766				HR7765 (Lo-16bits)
HR7767	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7768				HR7767 (Lo-16bits)
HR7769	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7770				HR7769 (Lo-16bits)
HR7771	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7772				HR7771 (Lo-16bits)
HR7773	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7787	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7790				HR7789 (Lo-16bits)
HR7791	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7792				HR7791 (Lo-16bits)
HR7793	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7794				HR7793 (Lo-16bits)
HR7795	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7796				HR7795 (Lo-16bits)
HR7797	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)
HR7801	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7817	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	UV1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7828	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7829	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7830	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7831	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7832	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7833	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7834	UV1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7835	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7836	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7837	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7838	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7839	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7840	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7841	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7842	UV1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7843	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7844	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7845	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7846	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7847	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7848	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7849	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7850	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7851	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR7852	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7853	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7854	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7855	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7856	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7857	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7858	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7859	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7860	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7861	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7862				HR7861 (Lo-16bits)
HR7863	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7864				HR7863 (Lo-16bits)
HR7865	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7866				HR7865 (Lo-16bits)
HR7867	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7868				HR7867 (Lo-16bits)
HR7869	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7870				HR7869 (Lo-16bits)
HR7871	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7872				HR7871 (Lo-16bits)
HR7873	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7874				HR7873 (Lo-16bits)
HR7875	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR7876				HR7875 (Lo-16bits)
HR7877	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7878				HR7877 (Lo-16bits)
HR7879	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7880				HR7879 (Lo-16bits)
HR7881	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7882				HR7881 (Lo-16bits)
HR7883	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7884				HR7883 (Lo-16bits)
HR7885	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7886				HR7885 (Lo-16bits)
HR7887	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7888				HR7887 (Lo-16bits)
HR7889	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7890				HR7889 (Lo-16bits)
HR7891	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR7892				HR7891 (Lo-16bits)
HR7893	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7894	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7895	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7896	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7897	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7898	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7899	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7900	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7901	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7910				HR7909 (Lo-16bits)
HR7911	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7912				HR7911 (Lo-16bits)
HR7913	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7914				HR7913 (Lo-16bits)
HR7915	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7916				HR7915 (Lo-16bits)
HR7917	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7944				HR7943 (Lo-16bits)
HR7945	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7946				HR7945 (Lo-16bits)
HR7947	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	UV2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7966	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7967	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7968	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7969	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7970	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7971	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7972	UV2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR7973	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7974	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7975	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7976	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7977	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7978	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7979	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7980	UV2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR7981	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7982	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7983	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7984	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7985	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7986	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7987	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7988	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7989	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR7990	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR7991	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7992	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7993	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7994	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7995	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7996	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7997	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7998	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7999	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8000				HR7999 (Lo-16bits)
HR8001	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8002				HR8001 (Lo-16bits)
HR8003	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8004				HR8003 (Lo-16bits)
HR8005	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8006				HR8005 (Lo-16bits)
HR8007	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8008				HR8007 (Lo-16bits)
HR8009	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8010				HR8009 (Lo-16bits)
HR8011	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8012				HR8011 (Lo-16bits)
HR8013	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8016				HR8015 (Lo-16bits)
HR8017	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8026				HR8025 (Lo-16bits)
HR8027	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8030				HR8029 (Lo-16bits)
HR8031	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8032	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8033	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8034	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8035	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8036	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8037	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8038	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8039	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8045	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8056				HR8055 (Lo-16bits)
HR8057	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8073	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8086				HR8085 (Lo-16bits)
HR8087	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	UV3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8102				HR8101 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8103	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8104	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8105	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8106	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8107	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8108	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8109	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8110	UV3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8111	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8112	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8113	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8114	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8115	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8116	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8117	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8118	UV3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8119	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8120	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8121	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8122	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8123	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8124	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8125	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8126	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8127	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR8128	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR8129	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8130	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8131	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8132	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8133	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8134	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8135	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8136	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8137	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)
HR8139	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8145	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8166				HR8165 (Lo-16bits)
HR8167	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8168				HR8167 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8169	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8170	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8171	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8172	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8173	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8174	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8175	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8176	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8177	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8195	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)
HR8199	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8225	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)
HR8229	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8238				HR8237 (Lo-16bits)
HR8239	UV4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8240				HR8239 (Lo-16bits)
HR8241	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8242	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8243	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8244	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8245	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8246	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8247	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8248	UV4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8249	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8250	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8251	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8252	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8253	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8254	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8255	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8256	UV4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8257	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8258	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8259	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8260	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8261	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8262	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8263	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8264	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8265	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8266	NOV1:U0> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8267	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8268	NOV2:U0>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8269	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8270	NOV3:U0>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8271	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8272	NOV4:U0>>>> Measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR8273	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8274	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8275	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8276	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8277	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8278	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8279	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8280	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8281	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8282	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8283	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8296				HR8295 (Lo-16bits)
HR8297	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8302				HR8301 (Lo-16bits)
HR8303	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8304				HR8303 (Lo-16bits)
HR8305	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8306				HR8305 (Lo-16bits)
HR8307	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8308				HR8307 (Lo-16bits)
HR8309	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)
HR8311	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8316	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8317	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8318	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8319	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8320	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8321	VUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8322	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8323	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)
HR8341	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8348				HR8347 (Lo-16bits)
HR8349	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)
HR8371	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8374				HR8373 (Lo-16bits)
HR8375	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8376				HR8375 (Lo-16bits)
HR8377	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8378				HR8377 (Lo-16bits)
HR8379	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8380				HR8379 (Lo-16bits)
HR8381	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8382				HR8381 (Lo-16bits)
HR8383	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8384				HR8383 (Lo-16bits)
HR8385	VUB1:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8386				HR8385 (Lo-16bits)
HR8387	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8388	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8389	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8390	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8391	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8392	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8393	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8394	VUB1:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8395	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8396	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8397	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8398	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8399	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8400	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8401	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8402	VUB1:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8403	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8404	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8405	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8406	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8407	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8408	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8409	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8410	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8411	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8412	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8413	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8414	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8415	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8416	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8417	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8418	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8419	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8420	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8421	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8422				HR8421 (Lo-16bits)
HR8423	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8438				HR8437 (Lo-16bits)
HR8439	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8440				HR8439 (Lo-16bits)
HR8441	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8442				HR8441 (Lo-16bits)
HR8443	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8444				HR8443 (Lo-16bits)
HR8445	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8449	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)
HR8453	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8454	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8455	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8456	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8457	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8458	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8459	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8460	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8461	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8475	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)
HR8483	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8505	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8510				HR8509 (Lo-16bits)
HR8511	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8512				HR8511 (Lo-16bits)
HR8513	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8514				HR8513 (Lo-16bits)
HR8515	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8516				HR8515 (Lo-16bits)
HR8517	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8518				HR8517 (Lo-16bits)
HR8519	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8520				HR8519 (Lo-16bits)
HR8521	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8522				HR8521 (Lo-16bits)
HR8523	VUB2:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8524				HR8523 (Lo-16bits)
HR8525	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8526	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8527	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8528	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8529	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8530	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8531	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8532	VUB2:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8533	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8534	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8535	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8536	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8537	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8538	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8539	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8540	VUB2:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8541	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8542	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8543	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8544	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8545	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8546	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8547	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8548	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8549	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8550	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8551	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8552	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8553	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8554	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8555	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8556	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8557	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8558	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8559	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)
HR8565	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8574				HR8573 (Lo-16bits)
HR8575	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8576				HR8575 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8577	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8578				HR8577 (Lo-16bits)
HR8579	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8580				HR8579 (Lo-16bits)
HR8581	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8592	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8593	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8594	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8595	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8596	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8597	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8598	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8599	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8603	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)
HR8625	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8630				HR8629 (Lo-16bits)
HR8631	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8646				HR8645 (Lo-16bits)
HR8647	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8648				HR8647 (Lo-16bits)
HR8649	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8650				HR8649 (Lo-16bits)
HR8651	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8652				HR8651 (Lo-16bits)
HR8653	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8654				HR8653 (Lo-16bits)
HR8655	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8656				HR8655 (Lo-16bits)
HR8657	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8658				HR8657 (Lo-16bits)
HR8659	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8660				HR8659 (Lo-16bits)
HR8661	VUB3:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8662				HR8661 (Lo-16bits)
HR8663	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8664	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8665	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8666	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8667	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8668	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8669	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8670	VUB3:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8671	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8672	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8673	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8674	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8675	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8676	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8677	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8678	VUB3:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8679	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8680	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8681	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8682	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8683	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8684	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8685	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8686	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8687	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR8688	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8689	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8690	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8691	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8692	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8693	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8694	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8695	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8696	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8697	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8702				HR8701 (Lo-16bits)
HR8703	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)
HR8707	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8710				HR8709 (Lo-16bits)
HR8711	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8712				HR8711 (Lo-16bits)
HR8713	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8714				HR8713 (Lo-16bits)
HR8715	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8716				HR8715 (Lo-16bits)
HR8717	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8728				HR8727 (Lo-16bits)
HR8729	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8730	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8731	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8732	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8733	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8734	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8735	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8736	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR8737	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8754				HR8753 (Lo-16bits)
HR8755	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)
HR8767	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8782				HR8781 (Lo-16bits)
HR8783	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8784				HR8783 (Lo-16bits)
HR8785	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8786				HR8785 (Lo-16bits)
HR8787	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8788				HR8787 (Lo-16bits)
HR8789	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8790				HR8789 (Lo-16bits)
HR8791	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8792				HR8791 (Lo-16bits)
HR8793	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8794				HR8793 (Lo-16bits)
HR8795	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8796				HR8795 (Lo-16bits)
HR8797	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8798				HR8797 (Lo-16bits)
HR8799	VUB4:Release Time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8800				HR8799 (Lo-16bits)
HR8801	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8802	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8803	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8804	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8805	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8806	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8807	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8808	VUB4:Delayed Pick-up release	R(W)	1	0=No 1=Yes
HR8809	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8810	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8811	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8812	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8813	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8814	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8815	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8816	VUB4:Op.Time calc reset after release time	R(W)	1	0=No 1=Yes
HR8817	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8818	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8819	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8820	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8821	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8822	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8823	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8824	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8825	PGS1:PS1 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8826	PGS1:PS2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8827	PGS1:PS3 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8828	PGS1:PS4 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR8829	PGS1:PS5 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8830	PGS1:PS6 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8831	PGS1:PS7 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8832	PGS1:PS8 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8833	PGS1:PS9 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8834	PGS1:PS10 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8835	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8836	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8837	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8838	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8839	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8840	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8841	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8842	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8843	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR8844	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR8845	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8846	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes
HR8847	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8848	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR8849	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8850	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR8851	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8852	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR8853	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8854	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR8855	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8856	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8857	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8858	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR8859	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR8860	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8861	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8862	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8863	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8864	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8865	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8866	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8867	FRQV1:f> Used in SG	R(W)	1	0=No 1=Yes
HR8868	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8869				HR8868 (Lo-16bits)
HR8870	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8871				HR8870 (Lo-16bits)
HR8872	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8873				HR8872 (Lo-16bits)
HR8874	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8875				HR8874 (Lo-16bits)
HR8876	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8877				HR8876 (Lo-16bits)
HR8878	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8879				HR8878 (Lo-16bits)
HR8880	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8881				HR8880 (Lo-16bits)
HR8882	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8883				HR8882 (Lo-16bits)
HR8884	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8885				HR8884 (Lo-16bits)
HR8886	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8887				HR8886 (Lo-16bits)
HR8888	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8889				HR8888 (Lo-16bits)
HR8890	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8891				HR8890 (Lo-16bits)
HR8892	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8893				HR8892 (Lo-16bits)
HR8894	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8895				HR8894 (Lo-16bits)
HR8896	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8897				HR8896 (Lo-16bits)
HR8898	FRQV1:f> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8899				HR8898 (Lo-16bits)
HR8900	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8901	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8902	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8903	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8904	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8905	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8906	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8907	FRQV1:f>> Used in SG	R(W)	1	0=No 1=Yes
HR8908	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8909				HR8908 (Lo-16bits)
HR8910	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8911				HR8910 (Lo-16bits)
HR8912	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8913				HR8912 (Lo-16bits)
HR8914	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8915				HR8914 (Lo-16bits)
HR8916	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8917				HR8916 (Lo-16bits)
HR8918	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8919				HR8918 (Lo-16bits)
HR8920	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8921				HR8920 (Lo-16bits)
HR8922	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8923				HR8922 (Lo-16bits)
HR8924	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8925				HR8924 (Lo-16bits)
HR8926	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8927				HR8926 (Lo-16bits)
HR8928	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8929				HR8928 (Lo-16bits)
HR8930	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8931				HR8930 (Lo-16bits)
HR8932	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8933				HR8932 (Lo-16bits)
HR8934	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8935				HR8934 (Lo-16bits)
HR8936	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8937				HR8936 (Lo-16bits)
HR8938	FRQV1:f>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8939				HR8938 (Lo-16bits)
HR8940	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8941	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8942	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8943	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8944	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8945	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8946	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8947	FRQV1:f>>> Used in SG	R(W)	1	0=No 1=Yes
HR8948	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8949				HR8948 (Lo-16bits)
HR8950	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8951				HR8950 (Lo-16bits)
HR8952	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8953				HR8952 (Lo-16bits)
HR8954	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8955				HR8954 (Lo-16bits)
HR8956	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8957				HR8956 (Lo-16bits)
HR8958	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8959				HR8958 (Lo-16bits)
HR8960	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8961				HR8960 (Lo-16bits)
HR8962	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8963				HR8962 (Lo-16bits)
HR8964	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8965				HR8964 (Lo-16bits)
HR8966	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8967				HR8966 (Lo-16bits)
HR8968	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8969				HR8968 (Lo-16bits)
HR8970	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8971				HR8970 (Lo-16bits)
HR8972	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8973				HR8972 (Lo-16bits)
HR8974	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8975				HR8974 (Lo-16bits)
HR8976	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8977				HR8976 (Lo-16bits)
HR8978	FRQV1:f>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8979				HR8978 (Lo-16bits)
HR8980	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8981	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8982	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8983	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8984	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8985	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8986	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8987	FRQV1:f>>>> Used in SG	R(W)	1	0=No 1=Yes
HR8988	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8989				HR8988 (Lo-16bits)
HR8990	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8991				HR8990 (Lo-16bits)
HR8992	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8993				HR8992 (Lo-16bits)
HR8994	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8995				HR8994 (Lo-16bits)
HR8996	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8997				HR8996 (Lo-16bits)
HR8998	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR8999				HR8998 (Lo-16bits)
HR9000	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9001				HR9000 (Lo-16bits)
HR9002	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9003				HR9002 (Lo-16bits)
HR9004	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9005				HR9004 (Lo-16bits)
HR9006	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9007				HR9006 (Lo-16bits)
HR9008	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9009				HR9008 (Lo-16bits)
HR9010	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9011				HR9010 (Lo-16bits)
HR9012	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9013				HR9012 (Lo-16bits)
HR9014	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9015				HR9014 (Lo-16bits)
HR9016	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9017				HR9016 (Lo-16bits)
HR9018	FRQV1:f>>>> Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9019				HR9018 (Lo-16bits)
HR9020	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9021	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9022	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9023	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9024	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9025	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9026	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9027	FRQV1:f< Used in SG	R(W)	1	0=No 1=Yes
HR9028	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9029				HR9028 (Lo-16bits)
HR9030	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9031				HR9030 (Lo-16bits)
HR9032	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9033				HR9032 (Lo-16bits)
HR9034	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9035				HR9034 (Lo-16bits)
HR9036	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9037				HR9036 (Lo-16bits)
HR9038	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9039				HR9038 (Lo-16bits)
HR9040	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9041				HR9040 (Lo-16bits)
HR9042	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9043				HR9042 (Lo-16bits)
HR9044	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9045				HR9044 (Lo-16bits)
HR9046	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9047				HR9046 (Lo-16bits)
HR9048	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9049				HR9048 (Lo-16bits)
HR9050	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9051				HR9050 (Lo-16bits)
HR9052	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9053				HR9052 (Lo-16bits)
HR9054	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9055				HR9054 (Lo-16bits)
HR9056	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9057				HR9056 (Lo-16bits)
HR9058	FRQV1:f< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9059				HR9058 (Lo-16bits)
HR9060	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9061	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9062	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9063	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9064	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9065	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9066	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9067	FRQV1:f<< Used in SG	R(W)	1	0=No 1=Yes
HR9068	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9069				HR9068 (Lo-16bits)
HR9070	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9071				HR9070 (Lo-16bits)
HR9072	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9073				HR9072 (Lo-16bits)
HR9074	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9075				HR9074 (Lo-16bits)
HR9076	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9077				HR9076 (Lo-16bits)
HR9078	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9079				HR9078 (Lo-16bits)
HR9080	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9081				HR9080 (Lo-16bits)
HR9082	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9083				HR9082 (Lo-16bits)
HR9084	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9085				HR9084 (Lo-16bits)
HR9086	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9087				HR9086 (Lo-16bits)
HR9088	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9089				HR9088 (Lo-16bits)
HR9090	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9091				HR9090 (Lo-16bits)
HR9092	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9093				HR9092 (Lo-16bits)
HR9094	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9095				HR9094 (Lo-16bits)
HR9096	FRQV1:f<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9097				HR9096 (Lo-16bits)
HR9098	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9099				HR9098 (Lo-16bits)
HR9100	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9101	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9102	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9103	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9104	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9105	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9106	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9107	FRQV1:f<<< Used in SG	R(W)	1	0=No 1=Yes
HR9108	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9109				HR9108 (Lo-16bits)
HR9110	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9111				HR9110 (Lo-16bits)
HR9112	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9113				HR9112 (Lo-16bits)
HR9114	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9115				HR9114 (Lo-16bits)
HR9116	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9117				HR9116 (Lo-16bits)
HR9118	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9119				HR9118 (Lo-16bits)
HR9120	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9121				HR9120 (Lo-16bits)
HR9122	FRQV1:f<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9123				HR9122 (Lo-16bits)
HR9124	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9125				HR9124 (Lo-16bits)
HR9126	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9127				HR9126 (Lo-16bits)
HR9128	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9129				HR9128 (Lo-16bits)
HR9130	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9131				HR9130 (Lo-16bits)
HR9132	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9133				HR9132 (Lo-16bits)
HR9134	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9135				HR9134 (Lo-16bits)
HR9136	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9137				HR9136 (Lo-16bits)
HR9138	FRQV1:f<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9139				HR9138 (Lo-16bits)
HR9140	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9141	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9142	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9143	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9144	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9145	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9146	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9147	FRQV1:f<<<< Used in SG	R(W)	1	0=No 1=Yes
HR9148	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9149				HR9148 (Lo-16bits)
HR9150	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9151				HR9150 (Lo-16bits)
HR9152	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9153				HR9152 (Lo-16bits)
HR9154	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9155				HR9154 (Lo-16bits)
HR9156	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9157				HR9156 (Lo-16bits)
HR9158	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9159				HR9158 (Lo-16bits)
HR9160	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9161				HR9160 (Lo-16bits)
HR9162	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9163				HR9162 (Lo-16bits)
HR9164	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9165				HR9164 (Lo-16bits)
HR9166	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9167				HR9166 (Lo-16bits)
HR9168	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9169				HR9168 (Lo-16bits)
HR9170	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9171				HR9170 (Lo-16bits)
HR9172	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9173				HR9172 (Lo-16bits)
HR9174	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9175				HR9174 (Lo-16bits)
HR9176	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9177				HR9176 (Lo-16bits)
HR9178	FRQV1:f<<<< Op.time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9179				HR9178 (Lo-16bits)
HR9180	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9181	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9182	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9183	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9184	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9185	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9186	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9187	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9188	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR9189	RHC1:Running hours	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9190				HR9189 (Lo-16bits)
HR9191	RHC1:Start count	RW	2	UINT32 [0...4294967295] (Hi-16bits)
HR9192				HR9191 (Lo-16bits)
HR9193	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR9194	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9195	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9196	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9197	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9198	OPW1:P> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9199	UPW1:P< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9200	RPW1:Prev> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9201	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9202	SEF1:U03rd< Measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR9203	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9204	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9205	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9206	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR9207	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR9208	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=VHz(1) Blocked 2=VHz(1) Start 3=VHz(1) Alarm 4=VHz(1) Trip
HR9209	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=VHz(2) Start 2=VHz(2) Alarm 3=VHz(2) Trip 4=VHz(2) Blocked
HR9210	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR9211	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9212				HR9211 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9213	RTDIN:S1 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9214				HR9213 (Lo-16bits)
HR9215	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9216	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9217				HR9216 (Lo-16bits)
HR9218	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9219				HR9218 (Lo-16bits)
HR9220	RTDIN:S2 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9221				HR9220 (Lo-16bits)
HR9222	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9223	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9224				HR9223 (Lo-16bits)
HR9225	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9226				HR9225 (Lo-16bits)
HR9227	RTDIN:S3 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9228				HR9227 (Lo-16bits)
HR9229	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9230	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9231				HR9230 (Lo-16bits)
HR9232	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9233				HR9232 (Lo-16bits)
HR9234	RTDIN:S4 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9235				HR9234 (Lo-16bits)
HR9236	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9237	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9238				HR9237 (Lo-16bits)
HR9239	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	RTDIN:S5 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9244	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9245				HR9244 (Lo-16bits)
HR9246	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9247				HR9246 (Lo-16bits)
HR9248	RTDIN:S6 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9249				HR9248 (Lo-16bits)
HR9250	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9251	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	RTDIN:S7 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	RTDIN:S7 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9260	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9261				HR9260 (Lo-16bits)
HR9262	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9263				HR9262 (Lo-16bits)
HR9264	RTDIN:S8 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9265				HR9264 (Lo-16bits)
HR9266	RTDIN:S8 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9267				HR9266 (Lo-16bits)
HR9268	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9269	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	RTDIN:S9 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)
HR9275	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9276	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9277				HR9276 (Lo-16bits)
HR9278	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9279				HR9278 (Lo-16bits)
HR9280	RTDIN:S10 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9281				HR9280 (Lo-16bits)
HR9282	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9283	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	RTDIN:S11 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9288				HR9287 (Lo-16bits)
HR9289	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9290	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9291				HR9290 (Lo-16bits)
HR9292	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9293				HR9292 (Lo-16bits)
HR9294	RTDIN:S12 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9295				HR9294 (Lo-16bits)
HR9296	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9297	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	RTDIN:S13 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9302				HR9301 (Lo-16bits)
HR9303	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9304	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9305				HR9304 (Lo-16bits)
HR9306	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9307				HR9306 (Lo-16bits)
HR9308	RTDIN:S14 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9309				HR9308 (Lo-16bits)
HR9310	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9311	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9313	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	RTDIN:S15 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)
HR9317	RTDIN:S15 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9320	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9321				HR9320 (Lo-16bits)
HR9322	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9323				HR9322 (Lo-16bits)
HR9324	RTDIN:S16 Resistance	R	2	FLOAT [0...4000.000] (Hi-16bits)
HR9325				HR9324 (Lo-16bits)
HR9326	RTDIN:S16 Measurement mA	R	2	FLOAT [0.000...20.000] (Hi-16bits)
HR9327				HR9326 (Lo-16bits)
HR9328	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9329	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200.0...2000.0] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)
HR9335	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9337	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9342				HR9341 (Lo-16bits)
HR9343	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9344				HR9343 (Lo-16bits)
HR9345	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9346				HR9345 (Lo-16bits)
HR9347	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9348				HR9347 (Lo-16bits)
HR9349	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9361	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	RTD1:S1 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)
HR9365	RTD1:S1 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9366				HR9365 (Lo-16bits)
HR9367	RTD1:S1 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	RTD1:S2 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	RTD1:S2 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	RTD1:S2 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	RTD1:S3 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	RTD1:S3 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	RTD1:S3 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	RTD1:S4 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9382				HR9381 (Lo-16bits)
HR9383	RTD1:S4 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9384				HR9383 (Lo-16bits)
HR9385	RTD1:S4 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9386				HR9385 (Lo-16bits)
HR9387	RTD1:S5 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9388				HR9387 (Lo-16bits)
HR9389	RTD1:S5 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9391	RTD1:S5 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	RTD1:S6 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)
HR9395	RTD1:S6 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	RTD1:S6 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	RTD1:S7 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	RTD1:S7 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	RTD1:S7 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	RTD1:S8 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	RTD1:S8 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	RTD1:S8 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	RTD1:S9 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	RTD1:S9 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	RTD1:S9 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	RTD1:S10 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	RTD1:S10 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9421	RTD1:S10 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9422				HR9421 (Lo-16bits)
HR9423	RTD1:S11 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9424				HR9423 (Lo-16bits)
HR9425	RTD1:S11 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9426				HR9425 (Lo-16bits)
HR9427	RTD1:S11 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9428				HR9427 (Lo-16bits)
HR9429	RTD1:S12 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	RTD1:S12 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	RTD1:S12 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	RTD1:S13 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	RTD1:S13 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	RTD1:S13 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	RTD1:S14 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	RTD1:S14 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	RTD1:S14 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	RTD1:S15 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	RTD1:S15 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9451	RTD1:S15 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	RTD1:S16 ALARM1 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)
HR9455	RTD1:S16 ALARM2 ON	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	RTD1:S16 MEAS INVALID	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	ARC1:I/O Arc> condition	R	1	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked
HR9460	ARC1:I/O Arc> S1 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9461				HR9460 (Lo-16bits)
HR9462	ARC1:I/O Arc> S2 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9463				HR9462 (Lo-16bits)
HR9464	ARC1:I/O Arc> S3 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9465				HR9464 (Lo-16bits)
HR9466	ARC1:I/O Arc> S4 Sensor fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9467				HR9466 (Lo-16bits)
HR9468	ARC1:I/O Arc> IO unit fault	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9469				HR9468 (Lo-16bits)
HR9470	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9471	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR9472	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTBusfail 3=VTLinefail 4=Blocked
HR9473	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR9474	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR9475	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9476	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR9477	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR9478	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR9479	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)
HR9485	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	SYN1:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	SYN1:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	SYN1:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9501	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9502				HR9501 (Lo-16bits)
HR9503	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9504				HR9503 (Lo-16bits)
HR9505	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9506				HR9505 (Lo-16bits)
HR9507	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9508				HR9507 (Lo-16bits)
HR9509	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360.000] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360.000...360.000] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	VJP1:<DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR9514	VJP1:Voltage meas selected	R	1	0=Selection Ok 1=Selection not available
HR9515	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9516	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9517	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9518	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9519	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9520	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9521	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9522	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR9523	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9524	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9525	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9526	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9527	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9528	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9529	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9530	AR1:Auto-recloser [79] Mode	RW	1	0=Disabled 1=Activated
HR9531	AR1:AR Status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR9534	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR9535				HR9534 (Lo-16bits)
HR9536	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR9537				HR9536 (Lo-16bits)
HR9538	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR9539	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9540				HR9539 (Lo-16bits)
HR9541	VRG1:Voltage difference to set target	R	2	FLOAT [-140...140] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	VRG1:Voltage meas condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok
HR9544	VRG1:Tap location now (-0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR9545				HR9544 (Lo-16bits)
HR9546	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR9547				HR9546 (Lo-16bits)
HR9548	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR9549	VRG1:Remote tap control input	RW	1	0=Stop 1=Lower 2=Higher
HR9550	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9551				HR9550 (Lo-16bits)
HR9552	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9553				HR9552 (Lo-16bits)
HR9554	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9555				HR9554 (Lo-16bits)
HR9556	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9557				HR9556 (Lo-16bits)
HR9558	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9559				HR9558 (Lo-16bits)
HR9560	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9561				HR9560 (Lo-16bits)
HR9562	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9563				HR9562 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9564	VRG1:Target Voltage (UTGT)	RW	2	FLOAT [70...140] (Hi-16bits)
HR9565				HR9564 (Lo-16bits)
HR9566	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9567				HR9566 (Lo-16bits)
HR9568	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9569				HR9568 (Lo-16bits)
HR9570	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9571				HR9570 (Lo-16bits)
HR9572	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9573				HR9572 (Lo-16bits)
HR9574	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9575				HR9574 (Lo-16bits)
HR9576	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9577				HR9576 (Lo-16bits)
HR9578	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9579				HR9578 (Lo-16bits)
HR9580	VRG1:U> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9587				HR9586 (Lo-16bits)
HR9588	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9594	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	VRG1:U< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9599				HR9598 (Lo-16bits)
HR9600	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9605				HR9604 (Lo-16bits)
HR9606	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	VRG1:U>> setting (+UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9617				HR9616 (Lo-16bits)
HR9618	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9619				HR9618 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9620	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9621				HR9620 (Lo-16bits)
HR9622	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9623				HR9622 (Lo-16bits)
HR9624	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9625				HR9624 (Lo-16bits)
HR9626	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9627				HR9626 (Lo-16bits)
HR9628	VRG1:U<< setting (-UTGT)	RW	2	FLOAT [0.1...30] (Hi-16bits)
HR9629				HR9628 (Lo-16bits)
HR9630	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLT detected
HR9631	EMON1:I0> (50N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9632	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLT detected
HR9633	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9634	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal 1=FLT FW 2=FLT REV
HR9635	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=No voltage
HR9636	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLT detected
HR9637	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLT detected
HR9638	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR9639				HR9638 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9640	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR9641				HR9640 (Lo-16bits)
HR9642	GSYN:Synchronizer mode	RW	1	0=Disabled 1=Activated
HR9643	GSYN:Synchronizing by	RW	1	0=Autosynchronizer 1=Manual commands
HR9644	GSYN:Manual switching mode	RW	1	0=Assisted synchroswitch 1=I know what I am doing
HR9645	GSYN:Magnitude side A	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9646				HR9645 (Lo-16bits)
HR9647	GSYN:Magnitude side B	R	2	FLOAT [0.000...200.000] (Hi-16bits)
HR9648				HR9647 (Lo-16bits)
HR9649	GSYN:Frequency side A	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	GSYN:Frequency side B	R	2	FLOAT [0.000...75.000] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR9660				HR9659 (Lo-16bits)
HR9661	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9665	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	GSYN:Magnitude difference fine tune	RW	2	FLOAT [-200...200] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	GSYN:Frequency difference fine tune	RW	2	FLOAT [-100...100] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	GSYN:Angle difference fine tune	RW	2	FLOAT [-360...360] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	GSYN:Start Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	GSYN:Stop Synchronizer	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	GSYN:Block and Interrupt Synchronizing	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9684				HR9683 (Lo-16bits)
HR9685	GSYN:Breaker Closed	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	GSYN:Manual increase voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	GSYN:Manual decrease voltage	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9690				HR9689 (Lo-16bits)
HR9691	GSYN:Manual increase frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	GSYN:Manual decrease frequency	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	GSYN:Manual Close Breaker	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)
HR9697	GSYN:GSYN Voltage Mag.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	GSYN:GSYN Voltage Freq.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	GSYN:GSYN Voltage Ang.Difference Ok	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	GSYN:GSYN Blocked	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	GSYN:GSYN Running	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	GSYN:GSYN Increase Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9708				HR9707 (Lo-16bits)
HR9709	GSYN:GSYN Decrease Voltage	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	GSYN:GSYN Increase Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	GSYN:GSYN Decrease Frequency	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9715	GSYN:GSYN Breaker Close Pulse	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	GSYN:GSYN Long Sync Time	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	GSYN:GSYN Nets Standstill	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	GSYN:GSYN Nets Departing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	GSYN:GSYN Nets Enclosing	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	VREC:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9726	VREC:Scale current values to primary	RW	1	0=No 1=Yes
HR9727	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	RW	1	0=No 1=Yes
HR9728	VREC:Trigger register to memory	RW	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR9729				HR9728 (Lo-16bits)
HR9730	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR9731				HR9730 (Lo-16bits)
HR9732	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR9733				HR9732 (Lo-16bits)
HR9734	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR9735				HR9734 (Lo-16bits)
HR9736	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR9737				HR9736 (Lo-16bits)
HR9738	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR9739				HR9738 (Lo-16bits)
HR9740	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9741				HR9740 (Lo-16bits)
HR9742	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR9743				HR9742 (Lo-16bits)
HR9744	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR9745				HR9744 (Lo-16bits)
HR9746	RTB:Measurement value recorder mode	RW	1	0=Disabled 1=Activated
HR9747	RTB:Scale current values to primary	RW	1	0=No 1=Yes
HR9748	RTB:RTS to bus Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9749				HR9748 (Lo-16bits)
HR9750	RTB:RTS to bus Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9751				HR9750 (Lo-16bits)
HR9752	RTB:RTS to bus Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9753				HR9752 (Lo-16bits)
HR9754	RTB:RTS to bus Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9755				HR9754 (Lo-16bits)
HR9756	RTB:RTS to bus Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9757				HR9756 (Lo-16bits)
HR9758	RTB:RTS to bus Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9759				HR9758 (Lo-16bits)
HR9760	RTB:RTS to bus Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9761				HR9760 (Lo-16bits)
HR9762	RTB:RTS to bus Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9763				HR9762 (Lo-16bits)
HR9764	RTB:RTS to bus Magnitude 9	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9765				HR9764 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9766	RTB:RTS to bus Magnitude 10	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9767				HR9766 (Lo-16bits)
HR9768	AOUT:mA In Channel 1	R	2	FLOAT [0...24] (Hi-16bits)
HR9769				HR9768 (Lo-16bits)
HR9770	AOUT:mA In Channel 2	R	2	FLOAT [0...24] (Hi-16bits)
HR9771				HR9770 (Lo-16bits)
HR9772	AOUT:mA Out Channel 1 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9773				HR9772 (Lo-16bits)
HR9774	AOUT:mA Out Channel 1 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9775				HR9774 (Lo-16bits)
HR9776	AOUT:mA Out Channel 2 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9777				HR9776 (Lo-16bits)
HR9778	AOUT:mA Out Channel 2 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9779				HR9778 (Lo-16bits)
HR9780	AOUT:mA Out Channel 3 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9781				HR9780 (Lo-16bits)
HR9782	AOUT:mA Out Channel 3 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9783				HR9782 (Lo-16bits)
HR9784	AOUT:mA Out Channel 4 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9785				HR9784 (Lo-16bits)
HR9786	AOUT:mA Out Channel 4 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9787				HR9786 (Lo-16bits)
HR9788	AOUT:mA Out Channel 5 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9789				HR9788 (Lo-16bits)
HR9790	AOUT:mA Out Channel 5 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9791				HR9790 (Lo-16bits)
HR9792	AOUT:mA Out Channel 6 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9793				HR9792 (Lo-16bits)
HR9794	AOUT:mA Out Channel 6 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9795				HR9794 (Lo-16bits)
HR9796	AOUT:mA Out Channel 7 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9797				HR9796 (Lo-16bits)
HR9798	AOUT:mA Out Channel 7 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9799				HR9798 (Lo-16bits)
HR9800	AOUT:mA Out Channel 8 Input Magnitude now	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR9801				HR9800 (Lo-16bits)
HR9802	AOUT:mA Out Channel 8 Outputs now	R	2	FLOAT [0...24] (Hi-16bits)
HR9803				HR9802 (Lo-16bits)
HR9906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

2 AQ-25x frame Modbus map

The following table lists Modbus holding registers for AQ-25x frame units with firmware v2.5.7.1-306-AP2. For AQ-21x frame Modbus map refer to chapter *AQ-21x frame Modbus map*.



NOTICE!

Best way to always get the latest Modbus map is to load the aqs configuration file from the unit and in the AQtivate setting tool go to *Tools → Communication → Modbus map*.

Table. 2 - 6. AQ-25x frame Modbus map

Holding Register	Parameter	Access	Length	Info
HR1	Modbus requests	R/W=0	1	Diagnostic request counter
HR2	Modbus quality	R	1	Diagnostic quality
HR3	Modbus errors	R/W=0	1	Diagnostic error counter
HR4	Modbus Ctrl OK	R/W=0	1	Diagnostic Control OK counter
HR5	Modbus Ctrl Errors	R/W=0	1	Diagnostic Control Error counter
HR6	Object1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR7	Object2 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR8	Object3 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR9	Object4 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR10	Object5 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR11	Previous Event register	R	6	Event code
HR12	Previous Event register	R	6	Event timestamp (ms)

Holding Register	Parameter	Access	Length	Info
HR13	Previous Event register	R	6	Event timestamp (sec Hi-16bits)
HR14	Previous Event register	R	6	Event timestamp (sec Lo-16bits)
HR15	Previous Event register	R	6	Event sequential index
HR16	Previous Event register	R	6	RESERVED
HR17	Event register	R	6	Event code
HR18	Event register	R	6	Event timestamp (ms)
HR19	Event register	R	6	Event timestamp (sec Hi-16bits)
HR20	Event register	R	6	Event timestamp (sec Lo-16bits)
HR21	Event register	R	6	Event sequential index
HR22	Event register	R	6	RESERVED
HR23	System time	RW	3	System time (ms)
HR24	System time	RW	3	System time (sec Hi-16bits)
HR25	System time	RW	3	System time (sec Lo-16bits)
HR26	MEAS1: Pri.Pha.curr.IL1	R	2	FLOAT [0...100000] (Hi-16bits)
HR27				HR26 (Lo-16bits)
HR28	MEAS1: Pri.Pha.curr.IL2	R	2	FLOAT [0...100000] (Hi-16bits)
HR29				HR28 (Lo-16bits)
HR30	MEAS1: Pri.Pha.curr.IL3	R	2	FLOAT [0...100000] (Hi-16bits)
HR31				HR30 (Lo-16bits)
HR32	MEAS1: Pha.angle IL1	R	2	FLOAT [-360...360] (Hi-16bits)
HR33				HR32 (Lo-16bits)
HR34	MEAS1: Pha.angle IL2	R	2	FLOAT [-360...360] (Hi-16bits)
HR35				HR34 (Lo-16bits)
HR36	MEAS1: Pha.angle IL3	R	2	FLOAT [-360...360] (Hi-16bits)
HR37				HR36 (Lo-16bits)
HR38	MEAS1: Pri.Res.curr.I01	R	2	FLOAT [0...100000] (Hi-16bits)
HR39				HR38 (Lo-16bits)
HR40	MEAS1: Res.curr.angle I01	R	2	FLOAT [-360...360] (Hi-16bits)
HR41				HR40 (Lo-16bits)
HR42	MEAS1: Pri.Res.curr.I02	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR43				HR42 (Lo-16bits)
HR44	MEAS1:Res.curr.angle I02	R	2	FLOAT [-360...360] (Hi-16bits)
HR45				HR44 (Lo-16bits)
HR46	MEAS1:Pri.calc.I0	R	2	FLOAT [0...100000] (Hi-16bits)
HR47				HR46 (Lo-16bits)
HR48	MEAS1:Calculated I0 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR49				HR48 (Lo-16bits)
HR50	MEAS1:Pha.curr.IL1 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR51				HR50 (Lo-16bits)
HR52	MEAS1:Pha.curr.IL2 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR53				HR52 (Lo-16bits)
HR54	MEAS1:Pha.curr.IL3 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR55				HR54 (Lo-16bits)
HR56	MEAS1:Pri.Positive sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR57				HR56 (Lo-16bits)
HR58	MEAS1:Pri.Negative sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR59				HR58 (Lo-16bits)
HR60	MEAS1:Pri.Zero sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR61				HR60 (Lo-16bits)
HR62	MEAS1:Positive sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR63				HR62 (Lo-16bits)
HR64	MEAS1:Negative sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR65				HR64 (Lo-16bits)
HR66	MEAS1:Zero sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR67				HR66 (Lo-16bits)
HR68	MEAS1:Res.curr.I01 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR69				HR68 (Lo-16bits)
HR70	MEAS1:Res.curr.I02 TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR71				HR70 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR72	MEAS1:Pha.L1 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR73				HR72 (Lo-16bits)
HR74	MEAS1:Pha.L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR75				HR74 (Lo-16bits)
HR76	MEAS1:Pha.L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR77				HR76 (Lo-16bits)
HR78	MEAS1:Pha.L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR79				HR78 (Lo-16bits)
HR80	MEAS1:Pha.L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR81				HR80 (Lo-16bits)
HR82	MEAS1:Pha.L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR83				HR82 (Lo-16bits)
HR84	MEAS1:Res.I01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR85				HR84 (Lo-16bits)
HR86	MEAS1:Res.I01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR87				HR86 (Lo-16bits)
HR88	MEAS1:Res.I02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR89				HR88 (Lo-16bits)
HR90	MEAS1:Res.I02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR91				HR90 (Lo-16bits)
HR92	MEAS1:P-P curr.IL1	R	2	FLOAT [0...500] (Hi-16bits)
HR93				HR92 (Lo-16bits)
HR94	MEAS1:P-P curr.IL2	R	2	FLOAT [0...500] (Hi-16bits)
HR95				HR94 (Lo-16bits)
HR96	MEAS1:P-P curr.IL3	R	2	FLOAT [0...500] (Hi-16bits)
HR97				HR96 (Lo-16bits)
HR98	MEAS1:P-P curr.I01	R	2	FLOAT [0...500] (Hi-16bits)
HR99				HR98 (Lo-16bits)
HR100	MEAS1:P-P curr.I02	R	2	FLOAT [0...500] (Hi-16bits)
HR101				HR100 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR102	MEAS1:IL1 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR103				HR102 (Lo-16bits)
HR104	MEAS1:IL1 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR105				HR104 (Lo-16bits)
HR106	MEAS1:IL1 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR107				HR106 (Lo-16bits)
HR108	MEAS1:IL1 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR109				HR108 (Lo-16bits)
HR110	MEAS1:IL1 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR111				HR110 (Lo-16bits)
HR112	MEAS1:IL1 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR113				HR112 (Lo-16bits)
HR114	MEAS1:IL1 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR115				HR114 (Lo-16bits)
HR116	MEAS1:IL1 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR117				HR116 (Lo-16bits)
HR118	MEAS1:IL1 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR119				HR118 (Lo-16bits)
HR120	MEAS1:IL1 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR121				HR120 (Lo-16bits)
HR122	MEAS1:IL1 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR123				HR122 (Lo-16bits)
HR124	MEAS1:IL1 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR125				HR124 (Lo-16bits)
HR126	MEAS1:IL1 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR127				HR126 (Lo-16bits)
HR128	MEAS1:IL1 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR129				HR128 (Lo-16bits)
HR130	MEAS1:IL1 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR131				HR130 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR132	MEAS1:IL1 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR133				HR132 (Lo-16bits)
HR134	MEAS1:IL1 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR135				HR134 (Lo-16bits)
HR136	MEAS1:IL1 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR137				HR136 (Lo-16bits)
HR138	MEAS1:IL1 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR139				HR138 (Lo-16bits)
HR140	MEAS1:IL1 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR141				HR140 (Lo-16bits)
HR142	MEAS1:IL1 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR143				HR142 (Lo-16bits)
HR144	MEAS1:IL1 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR145				HR144 (Lo-16bits)
HR146	MEAS1:IL1 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR147				HR146 (Lo-16bits)
HR148	MEAS1:IL1 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR149				HR148 (Lo-16bits)
HR150	MEAS1:IL1 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR151				HR150 (Lo-16bits)
HR152	MEAS1:IL1 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR153				HR152 (Lo-16bits)
HR154	MEAS1:IL1 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR155				HR154 (Lo-16bits)
HR156	MEAS1:IL1 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR157				HR156 (Lo-16bits)
HR158	MEAS1:IL1 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR159				HR158 (Lo-16bits)
HR160	MEAS1:IL1 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR161				HR160 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR162	MEAS1:IL1 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR163				HR162 (Lo-16bits)
HR164	MEAS1:IL2 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR165				HR164 (Lo-16bits)
HR166	MEAS1:IL2 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR167				HR166 (Lo-16bits)
HR168	MEAS1:IL2 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR169				HR168 (Lo-16bits)
HR170	MEAS1:IL2 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR171				HR170 (Lo-16bits)
HR172	MEAS1:IL2 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR173				HR172 (Lo-16bits)
HR174	MEAS1:IL2 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR175				HR174 (Lo-16bits)
HR176	MEAS1:IL2 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR177				HR176 (Lo-16bits)
HR178	MEAS1:IL2 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR179				HR178 (Lo-16bits)
HR180	MEAS1:IL2 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR181				HR180 (Lo-16bits)
HR182	MEAS1:IL2 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR183				HR182 (Lo-16bits)
HR184	MEAS1:IL2 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR185				HR184 (Lo-16bits)
HR186	MEAS1:IL2 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR187				HR186 (Lo-16bits)
HR188	MEAS1:IL2 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR189				HR188 (Lo-16bits)
HR190	MEAS1:IL2 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR191				HR190 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR192	MEAS1:IL2 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR193				HR192 (Lo-16bits)
HR194	MEAS1:IL2 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR195				HR194 (Lo-16bits)
HR196	MEAS1:IL2 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR197				HR196 (Lo-16bits)
HR198	MEAS1:IL2 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR199				HR198 (Lo-16bits)
HR200	MEAS1:IL2 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR201				HR200 (Lo-16bits)
HR202	MEAS1:IL2 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR203				HR202 (Lo-16bits)
HR204	MEAS1:IL2 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR205				HR204 (Lo-16bits)
HR206	MEAS1:IL2 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR207				HR206 (Lo-16bits)
HR208	MEAS1:IL2 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR209				HR208 (Lo-16bits)
HR210	MEAS1:IL2 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR211				HR210 (Lo-16bits)
HR212	MEAS1:IL2 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR213				HR212 (Lo-16bits)
HR214	MEAS1:IL2 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR215				HR214 (Lo-16bits)
HR216	MEAS1:IL2 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR217				HR216 (Lo-16bits)
HR218	MEAS1:IL2 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR219				HR218 (Lo-16bits)
HR220	MEAS1:IL2 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR221				HR220 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR222	MEAS1:IL2 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR223				HR222 (Lo-16bits)
HR224	MEAS1:IL2 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR225				HR224 (Lo-16bits)
HR226	MEAS1:IL3 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR227				HR226 (Lo-16bits)
HR228	MEAS1:IL3 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR229				HR228 (Lo-16bits)
HR230	MEAS1:IL3 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR231				HR230 (Lo-16bits)
HR232	MEAS1:IL3 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR233				HR232 (Lo-16bits)
HR234	MEAS1:IL3 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR235				HR234 (Lo-16bits)
HR236	MEAS1:IL3 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR237				HR236 (Lo-16bits)
HR238	MEAS1:IL3 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR239				HR238 (Lo-16bits)
HR240	MEAS1:IL3 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR241				HR240 (Lo-16bits)
HR242	MEAS1:IL3 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR243				HR242 (Lo-16bits)
HR244	MEAS1:IL3 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR245				HR244 (Lo-16bits)
HR246	MEAS1:IL3 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR247				HR246 (Lo-16bits)
HR248	MEAS1:IL3 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR249				HR248 (Lo-16bits)
HR250	MEAS1:IL3 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR251				HR250 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR252	MEAS1:IL3 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR253				HR252 (Lo-16bits)
HR254	MEAS1:IL3 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR255				HR254 (Lo-16bits)
HR256	MEAS1:IL3 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR257				HR256 (Lo-16bits)
HR258	MEAS1:IL3 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR259				HR258 (Lo-16bits)
HR260	MEAS1:IL3 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR261				HR260 (Lo-16bits)
HR262	MEAS1:IL3 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR263				HR262 (Lo-16bits)
HR264	MEAS1:IL3 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR265				HR264 (Lo-16bits)
HR266	MEAS1:IL3 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR267				HR266 (Lo-16bits)
HR268	MEAS1:IL3 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR269				HR268 (Lo-16bits)
HR270	MEAS1:IL3 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR271				HR270 (Lo-16bits)
HR272	MEAS1:IL3 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR273				HR272 (Lo-16bits)
HR274	MEAS1:IL3 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR275				HR274 (Lo-16bits)
HR276	MEAS1:IL3 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR277				HR276 (Lo-16bits)
HR278	MEAS1:IL3 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR279				HR278 (Lo-16bits)
HR280	MEAS1:IL3 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR281				HR280 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR282	MEAS1:IL3 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR283				HR282 (Lo-16bits)
HR284	MEAS1:IL3 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR285				HR284 (Lo-16bits)
HR286	MEAS1:IL3 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR287				HR286 (Lo-16bits)
HR288	MEAS1:I01 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR289				HR288 (Lo-16bits)
HR290	MEAS1:I01 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR291				HR290 (Lo-16bits)
HR292	MEAS1:I01 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR293				HR292 (Lo-16bits)
HR294	MEAS1:I01 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR295				HR294 (Lo-16bits)
HR296	MEAS1:I01 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR297				HR296 (Lo-16bits)
HR298	MEAS1:I01 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR299				HR298 (Lo-16bits)
HR300	MEAS1:I01 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR301				HR300 (Lo-16bits)
HR302	MEAS1:I01 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR303				HR302 (Lo-16bits)
HR304	MEAS1:I01 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR305				HR304 (Lo-16bits)
HR306	MEAS1:I01 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR307				HR306 (Lo-16bits)
HR308	MEAS1:I01 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR309				HR308 (Lo-16bits)
HR310	MEAS1:I01 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR311				HR310 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR312	MEAS1:I01 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR313				HR312 (Lo-16bits)
HR314	MEAS1:I01 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR315				HR314 (Lo-16bits)
HR316	MEAS1:I01 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR317				HR316 (Lo-16bits)
HR318	MEAS1:I01 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR319				HR318 (Lo-16bits)
HR320	MEAS1:I01 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR321				HR320 (Lo-16bits)
HR322	MEAS1:I01 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR323				HR322 (Lo-16bits)
HR324	MEAS1:I01 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR325				HR324 (Lo-16bits)
HR326	MEAS1:I01 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR327				HR326 (Lo-16bits)
HR328	MEAS1:I01 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR329				HR328 (Lo-16bits)
HR330	MEAS1:I01 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR331				HR330 (Lo-16bits)
HR332	MEAS1:I01 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR333				HR332 (Lo-16bits)
HR334	MEAS1:I01 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR335				HR334 (Lo-16bits)
HR336	MEAS1:I01 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR337				HR336 (Lo-16bits)
HR338	MEAS1:I01 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR339				HR338 (Lo-16bits)
HR340	MEAS1:I01 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR341				HR340 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR342	MEAS1:I01 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR343				HR342 (Lo-16bits)
HR344	MEAS1:I01 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR345				HR344 (Lo-16bits)
HR346	MEAS1:I01 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR347				HR346 (Lo-16bits)
HR348	MEAS1:I01 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR349				HR348 (Lo-16bits)
HR350	MEAS1:I02 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR351				HR350 (Lo-16bits)
HR352	MEAS1:I02 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR353				HR352 (Lo-16bits)
HR354	MEAS1:I02 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR355				HR354 (Lo-16bits)
HR356	MEAS1:I02 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR357				HR356 (Lo-16bits)
HR358	MEAS1:I02 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR359				HR358 (Lo-16bits)
HR360	MEAS1:I02 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR361				HR360 (Lo-16bits)
HR362	MEAS1:I02 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR363				HR362 (Lo-16bits)
HR364	MEAS1:I02 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR365				HR364 (Lo-16bits)
HR366	MEAS1:I02 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR367				HR366 (Lo-16bits)
HR368	MEAS1:I02 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR369				HR368 (Lo-16bits)
HR370	MEAS1:I02 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR371				HR370 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR372	MEAS1:I02 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR373				HR372 (Lo-16bits)
HR374	MEAS1:I02 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR375				HR374 (Lo-16bits)
HR376	MEAS1:I02 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR377				HR376 (Lo-16bits)
HR378	MEAS1:I02 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR379				HR378 (Lo-16bits)
HR380	MEAS1:I02 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR381				HR380 (Lo-16bits)
HR382	MEAS1:I02 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR383				HR382 (Lo-16bits)
HR384	MEAS1:I02 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR385				HR384 (Lo-16bits)
HR386	MEAS1:I02 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR387				HR386 (Lo-16bits)
HR388	MEAS1:I02 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR389				HR388 (Lo-16bits)
HR390	MEAS1:I02 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR391				HR390 (Lo-16bits)
HR392	MEAS1:I02 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR393				HR392 (Lo-16bits)
HR394	MEAS1:I02 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR395				HR394 (Lo-16bits)
HR396	MEAS1:I02 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR397				HR396 (Lo-16bits)
HR398	MEAS1:I02 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR399				HR398 (Lo-16bits)
HR400	MEAS1:I02 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR401				HR400 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR402	MEAS1:I02 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR403				HR402 (Lo-16bits)
HR404	MEAS1:I02 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR405				HR404 (Lo-16bits)
HR406	MEAS1:I02 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR407				HR406 (Lo-16bits)
HR408	MEAS1:I02 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR409				HR408 (Lo-16bits)
HR410	MEAS1:I02 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR411				HR410 (Lo-16bits)
HR412	MEAS2:Pri.Pha.curr.I"L1	R	2	FLOAT [0...100000] (Hi-16bits)
HR413				HR412 (Lo-16bits)
HR414	MEAS2:Pri.Pha.curr.I"L2	R	2	FLOAT [0...100000] (Hi-16bits)
HR415				HR414 (Lo-16bits)
HR416	MEAS2:Pri.Pha.curr.I"L3	R	2	FLOAT [0...100000] (Hi-16bits)
HR417				HR416 (Lo-16bits)
HR418	MEAS2:Pha.angle I"L1	R	2	FLOAT [-360...360] (Hi-16bits)
HR419				HR418 (Lo-16bits)
HR420	MEAS2:Pha.angle I"L2	R	2	FLOAT [-360...360] (Hi-16bits)
HR421				HR420 (Lo-16bits)
HR422	MEAS2:Pha.angle I"L3	R	2	FLOAT [-360...360] (Hi-16bits)
HR423				HR422 (Lo-16bits)
HR424	MEAS2:Pri.Res.curr.I"01	R	2	FLOAT [0...100000] (Hi-16bits)
HR425				HR424 (Lo-16bits)
HR426	MEAS2:Res.curr.angle I"01	R	2	FLOAT [-360...360] (Hi-16bits)
HR427				HR426 (Lo-16bits)
HR428	MEAS2:Pri.Res.curr.I"02	R	2	FLOAT [0...100000] (Hi-16bits)
HR429				HR428 (Lo-16bits)
HR430	MEAS2:Res.curr.angle I"02	R	2	FLOAT [-360...360] (Hi-16bits)
HR431				HR430 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR432	MEAS2: Pri.calc.I ⁰	R	2	FLOAT [0...100000] (Hi-16bits)
HR433				HR432 (Lo-16bits)
HR434	MEAS2: Calculated I ⁰ angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR435				HR434 (Lo-16bits)
HR436	MEAS2: Pha.curr.I ^{L1} TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR437				HR436 (Lo-16bits)
HR438	MEAS2: Pha.curr.I ^{L2} TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR439				HR438 (Lo-16bits)
HR440	MEAS2: Pha.curr.I ^{L3} TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR441				HR440 (Lo-16bits)
HR442	MEAS2: I ⁺ Pri.Positive sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR443				HR442 (Lo-16bits)
HR444	MEAS2: I ⁻ Pri.Negative sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR445				HR444 (Lo-16bits)
HR446	MEAS2: I ⁰ Pri.Zero sequence curr.	R	2	FLOAT [0...100000] (Hi-16bits)
HR447				HR446 (Lo-16bits)
HR448	MEAS2: I ⁺ Positive sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR449				HR448 (Lo-16bits)
HR450	MEAS2: I ⁻ Negative sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR451				HR450 (Lo-16bits)
HR452	MEAS2: I ⁰ Zero sequence curr.angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR453				HR452 (Lo-16bits)
HR454	MEAS2: Res.curr.I ⁰¹ TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR455				HR454 (Lo-16bits)
HR456	MEAS2: Res.curr.I ⁰² TRMS Pri	R	2	FLOAT [0...100000] (Hi-16bits)
HR457				HR456 (Lo-16bits)
HR458	MEAS2: Pha.I ^{L1} ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR459				HR458 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR460	MEAS2:Pha.I"L1 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR461				HR460 (Lo-16bits)
HR462	MEAS2:Pha.I"L2 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR463				HR462 (Lo-16bits)
HR464	MEAS2:Pha.I"L2 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR465				HR464 (Lo-16bits)
HR466	MEAS2:Pha.I"L3 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR467				HR466 (Lo-16bits)
HR468	MEAS2:Pha.I"L3 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR469				HR468 (Lo-16bits)
HR470	MEAS2:Res.I"01 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR471				HR470 (Lo-16bits)
HR472	MEAS2:Res.I"01 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR473				HR472 (Lo-16bits)
HR474	MEAS2:Res.I"02 ampl. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR475				HR474 (Lo-16bits)
HR476	MEAS2:Res.I"02 pow. THD	R	2	FLOAT [0...100] (Hi-16bits)
HR477				HR476 (Lo-16bits)
HR478	MEAS2:P-P curr.I"L1	R	2	FLOAT [0...500] (Hi-16bits)
HR479				HR478 (Lo-16bits)
HR480	MEAS2:P-P curr.I"L2	R	2	FLOAT [0...500] (Hi-16bits)
HR481				HR480 (Lo-16bits)
HR482	MEAS2:P-P curr.I"L3	R	2	FLOAT [0...500] (Hi-16bits)
HR483				HR482 (Lo-16bits)
HR484	MEAS2:P-P curr.I"01	R	2	FLOAT [0...500] (Hi-16bits)
HR485				HR484 (Lo-16bits)
HR486	MEAS2:P-P curr.I"02	R	2	FLOAT [0...500] (Hi-16bits)
HR487				HR486 (Lo-16bits)
HR488	MEAS2:I"L1 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR489				HR488 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR490	MEAS2:I ^L L1 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR491				HR490 (Lo-16bits)
HR492	MEAS2:I ^L L1 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR493				HR492 (Lo-16bits)
HR494	MEAS2:I ^L L1 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR495				HR494 (Lo-16bits)
HR496	MEAS2:I ^L L1 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR497				HR496 (Lo-16bits)
HR498	MEAS2:I ^L L1 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR499				HR498 (Lo-16bits)
HR500	MEAS2:I ^L L1 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR501				HR500 (Lo-16bits)
HR502	MEAS2:I ^L L1 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR503				HR502 (Lo-16bits)
HR504	MEAS2:I ^L L1 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR505				HR504 (Lo-16bits)
HR506	MEAS2:I ^L L1 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR507				HR506 (Lo-16bits)
HR508	MEAS2:I ^L L1 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR509				HR508 (Lo-16bits)
HR510	MEAS2:I ^L L1 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR511				HR510 (Lo-16bits)
HR512	MEAS2:I ^L L1 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR513				HR512 (Lo-16bits)
HR514	MEAS2:I ^L L1 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR515				HR514 (Lo-16bits)
HR516	MEAS2:I ^L L1 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR517				HR516 (Lo-16bits)
HR518	MEAS2:I ^L L1 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR519				HR518 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR520	MEAS2:IL1 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR521				HR520 (Lo-16bits)
HR522	MEAS2:IL1 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR523				HR522 (Lo-16bits)
HR524	MEAS2:IL1 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR525				HR524 (Lo-16bits)
HR526	MEAS2:IL1 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR527				HR526 (Lo-16bits)
HR528	MEAS2:IL1 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR529				HR528 (Lo-16bits)
HR530	MEAS2:IL1 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR531				HR530 (Lo-16bits)
HR532	MEAS2:IL1 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR533				HR532 (Lo-16bits)
HR534	MEAS2:IL1 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR535				HR534 (Lo-16bits)
HR536	MEAS2:IL1 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR537				HR536 (Lo-16bits)
HR538	MEAS2:IL1 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR539				HR538 (Lo-16bits)
HR540	MEAS2:IL1 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR541				HR540 (Lo-16bits)
HR542	MEAS2:IL1 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR543				HR542 (Lo-16bits)
HR544	MEAS2:IL1 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR545				HR544 (Lo-16bits)
HR546	MEAS2:IL1 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR547				HR546 (Lo-16bits)
HR548	MEAS2:IL1 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR549				HR548 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR550	MEAS2:I ^L L2 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR551				HR550 (Lo-16bits)
HR552	MEAS2:I ^L L2 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR553				HR552 (Lo-16bits)
HR554	MEAS2:I ^L L2 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR555				HR554 (Lo-16bits)
HR556	MEAS2:I ^L L2 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR557				HR556 (Lo-16bits)
HR558	MEAS2:I ^L L2 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR559				HR558 (Lo-16bits)
HR560	MEAS2:I ^L L2 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR561				HR560 (Lo-16bits)
HR562	MEAS2:I ^L L2 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR563				HR562 (Lo-16bits)
HR564	MEAS2:I ^L L2 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR565				HR564 (Lo-16bits)
HR566	MEAS2:I ^L L2 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR567				HR566 (Lo-16bits)
HR568	MEAS2:I ^L L2 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR569				HR568 (Lo-16bits)
HR570	MEAS2:I ^L L2 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR571				HR570 (Lo-16bits)
HR572	MEAS2:I ^L L2 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR573				HR572 (Lo-16bits)
HR574	MEAS2:I ^L L2 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR575				HR574 (Lo-16bits)
HR576	MEAS2:I ^L L2 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR577				HR576 (Lo-16bits)
HR578	MEAS2:I ^L L2 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR579				HR578 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR580	MEAS2:I ^L L2 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR581				HR580 (Lo-16bits)
HR582	MEAS2:I ^L L2 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR583				HR582 (Lo-16bits)
HR584	MEAS2:I ^L L2 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR585				HR584 (Lo-16bits)
HR586	MEAS2:I ^L L2 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR587				HR586 (Lo-16bits)
HR588	MEAS2:I ^L L2 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR589				HR588 (Lo-16bits)
HR590	MEAS2:I ^L L2 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR591				HR590 (Lo-16bits)
HR592	MEAS2:I ^L L2 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR593				HR592 (Lo-16bits)
HR594	MEAS2:I ^L L2 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR595				HR594 (Lo-16bits)
HR596	MEAS2:I ^L L2 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR597				HR596 (Lo-16bits)
HR598	MEAS2:I ^L L2 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR599				HR598 (Lo-16bits)
HR600	MEAS2:I ^L L2 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR601				HR600 (Lo-16bits)
HR602	MEAS2:I ^L L2 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR603				HR602 (Lo-16bits)
HR604	MEAS2:I ^L L2 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR605				HR604 (Lo-16bits)
HR606	MEAS2:I ^L L2 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR607				HR606 (Lo-16bits)
HR608	MEAS2:I ^L L2 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR609				HR608 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR610	MEAS2:I ^L L2 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR611				HR610 (Lo-16bits)
HR612	MEAS2:I ^L L3 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR613				HR612 (Lo-16bits)
HR614	MEAS2:I ^L L3 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR615				HR614 (Lo-16bits)
HR616	MEAS2:I ^L L3 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR617				HR616 (Lo-16bits)
HR618	MEAS2:I ^L L3 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR619				HR618 (Lo-16bits)
HR620	MEAS2:I ^L L3 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR621				HR620 (Lo-16bits)
HR622	MEAS2:I ^L L3 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR623				HR622 (Lo-16bits)
HR624	MEAS2:I ^L L3 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR625				HR624 (Lo-16bits)
HR626	MEAS2:I ^L L3 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR627				HR626 (Lo-16bits)
HR628	MEAS2:I ^L L3 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR629				HR628 (Lo-16bits)
HR630	MEAS2:I ^L L3 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR631				HR630 (Lo-16bits)
HR632	MEAS2:I ^L L3 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR633				HR632 (Lo-16bits)
HR634	MEAS2:I ^L L3 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR635				HR634 (Lo-16bits)
HR636	MEAS2:I ^L L3 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR637				HR636 (Lo-16bits)
HR638	MEAS2:I ^L L3 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR639				HR638 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR640	MEAS2:I ³ L3 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR641				HR640 (Lo-16bits)
HR642	MEAS2:I ³ L3 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR643				HR642 (Lo-16bits)
HR644	MEAS2:I ³ L3 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR645				HR644 (Lo-16bits)
HR646	MEAS2:I ³ L3 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR647				HR646 (Lo-16bits)
HR648	MEAS2:I ³ L3 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR649				HR648 (Lo-16bits)
HR650	MEAS2:I ³ L3 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR651				HR650 (Lo-16bits)
HR652	MEAS2:I ³ L3 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR653				HR652 (Lo-16bits)
HR654	MEAS2:I ³ L3 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR655				HR654 (Lo-16bits)
HR656	MEAS2:I ³ L3 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR657				HR656 (Lo-16bits)
HR658	MEAS2:I ³ L3 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR659				HR658 (Lo-16bits)
HR660	MEAS2:I ³ L3 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR661				HR660 (Lo-16bits)
HR662	MEAS2:I ³ L3 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR663				HR662 (Lo-16bits)
HR664	MEAS2:I ³ L3 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR665				HR664 (Lo-16bits)
HR666	MEAS2:I ³ L3 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR667				HR666 (Lo-16bits)
HR668	MEAS2:I ³ L3 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR669				HR668 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR670	MEAS2:I"L3 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR671				HR670 (Lo-16bits)
HR672	MEAS2:I"L3 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR673				HR672 (Lo-16bits)
HR674	MEAS2:I"01 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR675				HR674 (Lo-16bits)
HR676	MEAS2:I"01 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR677				HR676 (Lo-16bits)
HR678	MEAS2:I"01 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR679				HR678 (Lo-16bits)
HR680	MEAS2:I"01 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR681				HR680 (Lo-16bits)
HR682	MEAS2:I"01 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR683				HR682 (Lo-16bits)
HR684	MEAS2:I"01 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR685				HR684 (Lo-16bits)
HR686	MEAS2:I"01 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR687				HR686 (Lo-16bits)
HR688	MEAS2:I"01 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR689				HR688 (Lo-16bits)
HR690	MEAS2:I"01 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR691				HR690 (Lo-16bits)
HR692	MEAS2:I"01 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR693				HR692 (Lo-16bits)
HR694	MEAS2:I"01 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR695				HR694 (Lo-16bits)
HR696	MEAS2:I"01 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR697				HR696 (Lo-16bits)
HR698	MEAS2:I"01 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR699				HR698 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR700	MEAS2:I"01 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR701				HR700 (Lo-16bits)
HR702	MEAS2:I"01 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR703				HR702 (Lo-16bits)
HR704	MEAS2:I"01 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR705				HR704 (Lo-16bits)
HR706	MEAS2:I"01 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR707				HR706 (Lo-16bits)
HR708	MEAS2:I"01 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR709				HR708 (Lo-16bits)
HR710	MEAS2:I"01 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR711				HR710 (Lo-16bits)
HR712	MEAS2:I"01 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR713				HR712 (Lo-16bits)
HR714	MEAS2:I"01 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR715				HR714 (Lo-16bits)
HR716	MEAS2:I"01 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR717				HR716 (Lo-16bits)
HR718	MEAS2:I"01 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR719				HR718 (Lo-16bits)
HR720	MEAS2:I"01 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR721				HR720 (Lo-16bits)
HR722	MEAS2:I"01 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR723				HR722 (Lo-16bits)
HR724	MEAS2:I"01 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR725				HR724 (Lo-16bits)
HR726	MEAS2:I"01 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR727				HR726 (Lo-16bits)
HR728	MEAS2:I"01 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR729				HR728 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR730	MEAS2:I"01 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR731				HR730 (Lo-16bits)
HR732	MEAS2:I"01 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR733				HR732 (Lo-16bits)
HR734	MEAS2:I"01 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR735				HR734 (Lo-16bits)
HR736	MEAS2:I"02 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR737				HR736 (Lo-16bits)
HR738	MEAS2:I"02 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR739				HR738 (Lo-16bits)
HR740	MEAS2:I"02 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR741				HR740 (Lo-16bits)
HR742	MEAS2:I"02 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR743				HR742 (Lo-16bits)
HR744	MEAS2:I"02 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR745				HR744 (Lo-16bits)
HR746	MEAS2:I"02 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR747				HR746 (Lo-16bits)
HR748	MEAS2:I"02 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR749				HR748 (Lo-16bits)
HR750	MEAS2:I"02 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR751				HR750 (Lo-16bits)
HR752	MEAS2:I"02 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR753				HR752 (Lo-16bits)
HR754	MEAS2:I"02 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR755				HR754 (Lo-16bits)
HR756	MEAS2:I"02 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR757				HR756 (Lo-16bits)
HR758	MEAS2:I"02 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR759				HR758 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR760	MEAS2:I"02 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR761				HR760 (Lo-16bits)
HR762	MEAS2:I"02 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR763				HR762 (Lo-16bits)
HR764	MEAS2:I"02 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR765				HR764 (Lo-16bits)
HR766	MEAS2:I"02 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR767				HR766 (Lo-16bits)
HR768	MEAS2:I"02 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR769				HR768 (Lo-16bits)
HR770	MEAS2:I"02 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR771				HR770 (Lo-16bits)
HR772	MEAS2:I"02 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR773				HR772 (Lo-16bits)
HR774	MEAS2:I"02 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR775				HR774 (Lo-16bits)
HR776	MEAS2:I"02 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR777				HR776 (Lo-16bits)
HR778	MEAS2:I"02 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR779				HR778 (Lo-16bits)
HR780	MEAS2:I"02 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR781				HR780 (Lo-16bits)
HR782	MEAS2:I"02 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR783				HR782 (Lo-16bits)
HR784	MEAS2:I"02 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR785				HR784 (Lo-16bits)
HR786	MEAS2:I"02 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR787				HR786 (Lo-16bits)
HR788	MEAS2:I"02 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR789				HR788 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR790	MEAS2:I"02 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR791				HR790 (Lo-16bits)
HR792	MEAS2:I"02 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR793				HR792 (Lo-16bits)
HR794	MEAS2:I"02 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR795				HR794 (Lo-16bits)
HR796	MEAS2:I"02 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR797				HR796 (Lo-16bits)
HR798	MEAS3:U1 Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR799				HR798 (Lo-16bits)
HR800	MEAS3:U1 Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR801				HR800 (Lo-16bits)
HR802	MEAS3:U2 Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR803				HR802 (Lo-16bits)
HR804	MEAS3:U2 Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR805				HR804 (Lo-16bits)
HR806	MEAS3:U3 Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR807				HR806 (Lo-16bits)
HR808	MEAS3:U3 Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR809				HR808 (Lo-16bits)
HR810	MEAS3:U4 Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR811				HR810 (Lo-16bits)
HR812	MEAS3:U4 Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR813				HR812 (Lo-16bits)
HR814	MEAS3:U1 Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR815				HR814 (Lo-16bits)
HR816	MEAS3:U1 Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR817				HR816 (Lo-16bits)
HR818	MEAS3:U2 Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR819				HR818 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR820	MEAS3:U2 Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR821				HR820 (Lo-16bits)
HR822	MEAS3:U3 Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR823				HR822 (Lo-16bits)
HR824	MEAS3:U3 Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR825				HR824 (Lo-16bits)
HR826	MEAS3:U4 Volt TRMS pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR827				HR826 (Lo-16bits)
HR828	MEAS3:U4 Volt TRMS sec	R	2	FLOAT [0...500] (Hi-16bits)
HR829				HR828 (Lo-16bits)
HR830	MEAS3:Positive sequence volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR831				HR830 (Lo-16bits)
HR832	MEAS3:Positive sequence volt.sec	R	2	FLOAT [0...480] (Hi-16bits)
HR833				HR832 (Lo-16bits)
HR834	MEAS3:Negative sequence volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR835				HR834 (Lo-16bits)
HR836	MEAS3:Negative sequence volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR837				HR836 (Lo-16bits)
HR838	MEAS3:Zero sequence Volt.pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR839				HR838 (Lo-16bits)
HR840	MEAS3:Zero sequence Volt.sec	R	2	FLOAT [0...500] (Hi-16bits)
HR841				HR840 (Lo-16bits)
HR842	MEAS3:U0 Calc.Volt pri	R	2	FLOAT [0...1000000] (Hi-16bits)
HR843				HR842 (Lo-16bits)
HR844	MEAS3:U0 Calc.Volt sec	R	2	FLOAT [0...500] (Hi-16bits)
HR845				HR844 (Lo-16bits)
HR846	MEAS3:U1 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR847				HR846 (Lo-16bits)
HR848	MEAS3:U2 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR849				HR848 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR850	MEAS3:U3 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR851				HR850 (Lo-16bits)
HR852	MEAS3:U4 Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR853				HR852 (Lo-16bits)
HR854	MEAS3:Pos.Seq.volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR855				HR854 (Lo-16bits)
HR856	MEAS3:Negative sequence volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR857				HR856 (Lo-16bits)
HR858	MEAS3:Zero sequence volt.Angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR859				HR858 (Lo-16bits)
HR860	MEAS3:System volt UL12 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR861				HR860 (Lo-16bits)
HR862	MEAS3:System volt UL12 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR863				HR862 (Lo-16bits)
HR864	MEAS3:System volt UL12 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR865				HR864 (Lo-16bits)
HR866	MEAS3:System volt UL23 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR867				HR866 (Lo-16bits)
HR868	MEAS3:System volt UL23 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR869				HR868 (Lo-16bits)
HR870	MEAS3:System volt UL23 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR871				HR870 (Lo-16bits)
HR872	MEAS3:System volt UL31 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR873				HR872 (Lo-16bits)
HR874	MEAS3:System volt UL31 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR875				HR874 (Lo-16bits)
HR876	MEAS3:System volt UL31 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR877				HR876 (Lo-16bits)
HR878	MEAS3:System volt UL1 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR879				HR878 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR880	MEAS3:System volt UL1 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR881				HR880 (Lo-16bits)
HR882	MEAS3:System volt UL1 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR883				HR882 (Lo-16bits)
HR884	MEAS3:System volt UL2 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR885				HR884 (Lo-16bits)
HR886	MEAS3:System volt UL2 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR887				HR886 (Lo-16bits)
HR888	MEAS3:System volt UL2 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR889				HR888 (Lo-16bits)
HR890	MEAS3:System volt UL3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR891				HR890 (Lo-16bits)
HR892	MEAS3:System volt UL3 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR893				HR892 (Lo-16bits)
HR894	MEAS3:System volt UL3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR895				HR894 (Lo-16bits)
HR896	MEAS3:System volt U0 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR897				HR896 (Lo-16bits)
HR898	MEAS3:System volt U0 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR899				HR898 (Lo-16bits)
HR900	MEAS3:System volt U0 mag(%)	R	2	FLOAT [0...120] (Hi-16bits)
HR901				HR900 (Lo-16bits)
HR902	MEAS3:System volt U0 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR903				HR902 (Lo-16bits)
HR904	MEAS3:System volt U3 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR905				HR904 (Lo-16bits)
HR906	MEAS3:System volt U3 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR907				HR906 (Lo-16bits)
HR908	MEAS3:System volt U3 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR909				HR908 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR910	MEAS3:System volt U4 mag	R	2	FLOAT [0...1000000] (Hi-16bits)
HR911				HR910 (Lo-16bits)
HR912	MEAS3:System volt U4 mag (kV)	R	2	FLOAT [0...1000] (Hi-16bits)
HR913				HR912 (Lo-16bits)
HR914	MEAS3:System volt U4 ang	R	2	FLOAT [-360...360] (Hi-16bits)
HR915				HR914 (Lo-16bits)
HR916	MEAS3:System volt UL1 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR917				HR916 (Lo-16bits)
HR918	MEAS3:System volt UL2 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR919				HR918 (Lo-16bits)
HR920	MEAS3:System volt UL3 ang Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR921				HR920 (Lo-16bits)
HR922	MEAS3:U1 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR923				HR922 (Lo-16bits)
HR924	MEAS3:U1 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR925				HR924 (Lo-16bits)
HR926	MEAS3:U1 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR927				HR926 (Lo-16bits)
HR928	MEAS3:U1 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR929				HR928 (Lo-16bits)
HR930	MEAS3:U1 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR931				HR930 (Lo-16bits)
HR932	MEAS3:U1 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR933				HR932 (Lo-16bits)
HR934	MEAS3:U1 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR935				HR934 (Lo-16bits)
HR936	MEAS3:U1 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR937				HR936 (Lo-16bits)
HR938	MEAS3:U1 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR939				HR938 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR940	MEAS3:U1 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR941				HR940 (Lo-16bits)
HR942	MEAS3:U1 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR943				HR942 (Lo-16bits)
HR944	MEAS3:U1 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR945				HR944 (Lo-16bits)
HR946	MEAS3:U1 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR947				HR946 (Lo-16bits)
HR948	MEAS3:U1 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR949				HR948 (Lo-16bits)
HR950	MEAS3:U1 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR951				HR950 (Lo-16bits)
HR952	MEAS3:U1 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR953				HR952 (Lo-16bits)
HR954	MEAS3:U1 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR955				HR954 (Lo-16bits)
HR956	MEAS3:U1 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR957				HR956 (Lo-16bits)
HR958	MEAS3:U1 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR959				HR958 (Lo-16bits)
HR960	MEAS3:U1 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR961				HR960 (Lo-16bits)
HR962	MEAS3:U1 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR963				HR962 (Lo-16bits)
HR964	MEAS3:U1 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR965				HR964 (Lo-16bits)
HR966	MEAS3:U1 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR967				HR966 (Lo-16bits)
HR968	MEAS3:U1 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR969				HR968 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR970	MEAS3:U1 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR971				HR970 (Lo-16bits)
HR972	MEAS3:U1 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR973				HR972 (Lo-16bits)
HR974	MEAS3:U1 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR975				HR974 (Lo-16bits)
HR976	MEAS3:U1 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR977				HR976 (Lo-16bits)
HR978	MEAS3:U1 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR979				HR978 (Lo-16bits)
HR980	MEAS3:U1 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR981				HR980 (Lo-16bits)
HR982	MEAS3:U1 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR983				HR982 (Lo-16bits)
HR984	MEAS3:U2 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR985				HR984 (Lo-16bits)
HR986	MEAS3:U2 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR987				HR986 (Lo-16bits)
HR988	MEAS3:U2 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR989				HR988 (Lo-16bits)
HR990	MEAS3:U2 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR991				HR990 (Lo-16bits)
HR992	MEAS3:U2 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR993				HR992 (Lo-16bits)
HR994	MEAS3:U2 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR995				HR994 (Lo-16bits)
HR996	MEAS3:U2 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR997				HR996 (Lo-16bits)
HR998	MEAS3:U2 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR999				HR998 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1000	MEAS3:U2 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1001				HR1000 (Lo-16bits)
HR1002	MEAS3:U2 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1003				HR1002 (Lo-16bits)
HR1004	MEAS3:U2 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1005				HR1004 (Lo-16bits)
HR1006	MEAS3:U2 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1007				HR1006 (Lo-16bits)
HR1008	MEAS3:U2 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1009				HR1008 (Lo-16bits)
HR1010	MEAS3:U2 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1011				HR1010 (Lo-16bits)
HR1012	MEAS3:U2 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1013				HR1012 (Lo-16bits)
HR1014	MEAS3:U2 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1015				HR1014 (Lo-16bits)
HR1016	MEAS3:U2 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1017				HR1016 (Lo-16bits)
HR1018	MEAS3:U2 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1019				HR1018 (Lo-16bits)
HR1020	MEAS3:U2 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1021				HR1020 (Lo-16bits)
HR1022	MEAS3:U2 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1023				HR1022 (Lo-16bits)
HR1024	MEAS3:U2 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1025				HR1024 (Lo-16bits)
HR1026	MEAS3:U2 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1027				HR1026 (Lo-16bits)
HR1028	MEAS3:U2 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1029				HR1028 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1030	MEAS3:U2 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1031				HR1030 (Lo-16bits)
HR1032	MEAS3:U2 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1033				HR1032 (Lo-16bits)
HR1034	MEAS3:U2 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1035				HR1034 (Lo-16bits)
HR1036	MEAS3:U2 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1037				HR1036 (Lo-16bits)
HR1038	MEAS3:U2 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1039				HR1038 (Lo-16bits)
HR1040	MEAS3:U2 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1041				HR1040 (Lo-16bits)
HR1042	MEAS3:U2 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1043				HR1042 (Lo-16bits)
HR1044	MEAS3:U2 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1045				HR1044 (Lo-16bits)
HR1046	MEAS3:U3 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR1047				HR1046 (Lo-16bits)
HR1048	MEAS3:U3 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1049				HR1048 (Lo-16bits)
HR1050	MEAS3:U3 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1051				HR1050 (Lo-16bits)
HR1052	MEAS3:U3 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1053				HR1052 (Lo-16bits)
HR1054	MEAS3:U3 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1055				HR1054 (Lo-16bits)
HR1056	MEAS3:U3 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1057				HR1056 (Lo-16bits)
HR1058	MEAS3:U3 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1059				HR1058 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1060	MEAS3:U3 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1061				HR1060 (Lo-16bits)
HR1062	MEAS3:U3 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1063				HR1062 (Lo-16bits)
HR1064	MEAS3:U3 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1065				HR1064 (Lo-16bits)
HR1066	MEAS3:U3 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1067				HR1066 (Lo-16bits)
HR1068	MEAS3:U3 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1069				HR1068 (Lo-16bits)
HR1070	MEAS3:U3 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1071				HR1070 (Lo-16bits)
HR1072	MEAS3:U3 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1073				HR1072 (Lo-16bits)
HR1074	MEAS3:U3 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1075				HR1074 (Lo-16bits)
HR1076	MEAS3:U3 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1077				HR1076 (Lo-16bits)
HR1078	MEAS3:U3 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1079				HR1078 (Lo-16bits)
HR1080	MEAS3:U3 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1081				HR1080 (Lo-16bits)
HR1082	MEAS3:U3 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1083				HR1082 (Lo-16bits)
HR1084	MEAS3:U3 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1085				HR1084 (Lo-16bits)
HR1086	MEAS3:U3 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1087				HR1086 (Lo-16bits)
HR1088	MEAS3:U3 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1089				HR1088 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1090	MEAS3:U3 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1091				HR1090 (Lo-16bits)
HR1092	MEAS3:U3 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1093				HR1092 (Lo-16bits)
HR1094	MEAS3:U3 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1095				HR1094 (Lo-16bits)
HR1096	MEAS3:U3 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1097				HR1096 (Lo-16bits)
HR1098	MEAS3:U3 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1099				HR1098 (Lo-16bits)
HR1100	MEAS3:U3 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1101				HR1100 (Lo-16bits)
HR1102	MEAS3:U3 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1103				HR1102 (Lo-16bits)
HR1104	MEAS3:U3 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1105				HR1104 (Lo-16bits)
HR1106	MEAS3:U3 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1107				HR1106 (Lo-16bits)
HR1108	MEAS3:U4 fundamental	R	2	FLOAT [0...100000] (Hi-16bits)
HR1109				HR1108 (Lo-16bits)
HR1110	MEAS3:U4 2nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1111				HR1110 (Lo-16bits)
HR1112	MEAS3:U4 3rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1113				HR1112 (Lo-16bits)
HR1114	MEAS3:U4 4th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1115				HR1114 (Lo-16bits)
HR1116	MEAS3:U4 5th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1117				HR1116 (Lo-16bits)
HR1118	MEAS3:U4 6th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1119				HR1118 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1120	MEAS3:U4 7th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1121				HR1120 (Lo-16bits)
HR1122	MEAS3:U4 8th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1123				HR1122 (Lo-16bits)
HR1124	MEAS3:U4 9th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1125				HR1124 (Lo-16bits)
HR1126	MEAS3:U4 10th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1127				HR1126 (Lo-16bits)
HR1128	MEAS3:U4 11th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1129				HR1128 (Lo-16bits)
HR1130	MEAS3:U4 12th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1131				HR1130 (Lo-16bits)
HR1132	MEAS3:U4 13th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1133				HR1132 (Lo-16bits)
HR1134	MEAS3:U4 14th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1135				HR1134 (Lo-16bits)
HR1136	MEAS3:U4 15th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1137				HR1136 (Lo-16bits)
HR1138	MEAS3:U4 16th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1139				HR1138 (Lo-16bits)
HR1140	MEAS3:U4 17th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1141				HR1140 (Lo-16bits)
HR1142	MEAS3:U4 18th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1143				HR1142 (Lo-16bits)
HR1144	MEAS3:U4 19th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1145				HR1144 (Lo-16bits)
HR1146	MEAS3:U4 20th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1147				HR1146 (Lo-16bits)
HR1148	MEAS3:U4 21st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1149				HR1148 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1150	MEAS3:U4 22nd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1151				HR1150 (Lo-16bits)
HR1152	MEAS3:U4 23rd harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1153				HR1152 (Lo-16bits)
HR1154	MEAS3:U4 24th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1155				HR1154 (Lo-16bits)
HR1156	MEAS3:U4 25th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1157				HR1156 (Lo-16bits)
HR1158	MEAS3:U4 26th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1159				HR1158 (Lo-16bits)
HR1160	MEAS3:U4 27th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1161				HR1160 (Lo-16bits)
HR1162	MEAS3:U4 28th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1163				HR1162 (Lo-16bits)
HR1164	MEAS3:U4 29th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1165				HR1164 (Lo-16bits)
HR1166	MEAS3:U4 30th harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1167				HR1166 (Lo-16bits)
HR1168	MEAS3:U4 31st harmonic	R	2	FLOAT [0...100000] (Hi-16bits)
HR1169				HR1168 (Lo-16bits)
HR1170	POW1:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1171				HR1170 (Lo-16bits)
HR1172	POW1:Export Active Energy (P) kWh	R(W)	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1173				HR1172 (Lo-16bits)
HR1174	POW1:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1175				HR1174 (Lo-16bits)
HR1176	POW1:Import Active Energy (P) kWh	R(W)	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1177				HR1176 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1178	POW1:Active Energy (P) Export/ Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1179				HR1178 (Lo-16bits)
HR1180	POW1:Active Energy (P) Export/ Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1181				HR1180 (Lo-16bits)
HR1182	POW1:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1183				HR1182 (Lo-16bits)
HR1184	POW1:Imported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1185				HR1184 (Lo-16bits)
HR1186	POW1:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1187				HR1186 (Lo-16bits)
HR1188	POW1:Imported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1189				HR1188 (Lo-16bits)
HR1190	POW1:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1191				HR1190 (Lo-16bits)
HR1192	POW1:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1193				HR1192 (Lo-16bits)
HR1194	POW1:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1195				HR1194 (Lo-16bits)
HR1196	POW1:Exported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1197				HR1196 (Lo-16bits)
HR1198	POW1:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1199				HR1198 (Lo-16bits)
HR1200	POW1:Exported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1201				HR1200 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1202	POW1:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1203				HR1202 (Lo-16bits)
HR1204	POW1:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR1205				HR1204 (Lo-16bits)
HR1206	POW1:Reset 3ph energies	R(W)	1	0=- 1=Reset
HR1207	POW1:POW1 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1208				HR1207 (Lo-16bits)
HR1209	POW1:POW1 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1210				HR1209 (Lo-16bits)
HR1211	POW1:POW1 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1212				HR1211 (Lo-16bits)
HR1213	POW1:POW1 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1214				HR1213 (Lo-16bits)
HR1215	POW1:POW1 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1216				HR1215 (Lo-16bits)
HR1217	POW1:POW1 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1218				HR1217 (Lo-16bits)
HR1219	POW1:POW1 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1220				HR1219 (Lo-16bits)
HR1221	POW1:POW1 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1222				HR1221 (Lo-16bits)
HR1223	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1224				HR1223 (Lo-16bits)
HR1225	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1226				HR1225 (Lo-16bits)
HR1227	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1228				HR1227 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1229	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1230				HR1229 (Lo-16bits)
HR1231	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1232				HR1231 (Lo-16bits)
HR1233	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1234				HR1233 (Lo-16bits)
HR1235	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1236				HR1235 (Lo-16bits)
HR1237	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1238				HR1237 (Lo-16bits)
HR1239	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1240				HR1239 (Lo-16bits)
HR1241	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1242				HR1241 (Lo-16bits)
HR1243	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1244				HR1243 (Lo-16bits)
HR1245	FLTREG:Recorded fault value	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1246				HR1245 (Lo-16bits)
HR1247	FREQS:f atm. Protections (when not measurable returns to nominal)	R	2	FLOAT [0...75] (Hi-16bits)
HR1248				HR1247 (Lo-16bits)
HR1249	DIGI:Dis	R	2	0b=DI1 1b=DI2 2b=DI3 3b=DI4 4b=DI5 5b=DI6 6b=DI7 7b=DI8 8b=DI9 9b=DI10 10b=DI11 11b=DI12 12b=DI13 13b=DI14 14b=DI15 15b=DI16 16b=DI17 17b=DI18 18b=DI19 19b=DI20 20b=DI21 21b=DI22 22b=DI23 23b=DI24 24b=DI25 25b=DI26 26b=DI27 27b=DI28 28b=DI29 29b=DI30 30b=DI31 31b=DI32 (Hi-16bits)
HR1250				HR1249 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1251	DIGI:DIs	R	2	0b=DI33 1b=DI34 2b=DI35 3b=DI36 4b=DI37 5b=DI38 6b=DI39 7b=DI40 8b=DI41 9b=DI42 10b=DI43 11b=DI44 12b=DI45 13b=DI46 14b=DI47 15b=DI48 16b=DI49 17b=DI50 18b=DI51 19b=DI52 20b=DI53 21b=DI54 22b=DI55 23b=DI56 24b=DI57 25b=DI58 26b=DI59 27b=DI60 28b=DI61 29b=DI62 30b=DI63 31b=DI64 (Hi-16bits)
HR1252				HR1251 (Lo-16bits)
HR1253	RELE:Outputs status	R	2	0b=O1 1b=O2 2b=O3 3b=O4 4b=O5 5b=O6 6b=O7 7b=O8 8b=O9 9b=O10 10b=O11 11b=O12 12b=O13 13b=O14 14b=O15 15b=O16 16b=O17 17b=O18 18b=O19 19b=O20 20b=O21 21b=O22 22b=O23 23b=O24 24b=O25 25b=O26 26b=O27 27b=O28 28b=O29 29b=O30 30b=O31 31b=O32 (Hi-16bits)
HR1254				HR1253 (Lo-16bits)
HR1255	SGS:Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR1256	SGS:Force setting group	R(W)	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1257	SGS:Force setting group change	R(W)	1	0=Disabled 1=Enabled
HR1258	SGS:Used setting groups	R(W)	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR1259	SGS:Remote setting group change	R(W)	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR1260	LOGIC:Reset Latches	RW	1	0=- 1=Reset
HR1261	NOC1:l> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1262	NOC2:l>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1263	NOC3:l>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1264	NOC4:l>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1265	DOC1:ldir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1266	DOC2:ldir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1267	DOC3:ldir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1268	DOC4:ldir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR1269	NEF1:I0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1270	NEF1:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR1271	NEF2:I0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1272	NEF2:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR1273	NEF3:I0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1274	NEF3:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR1275	NEF4:I0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1276	NEF4:Detected fault type	R	1	0=- 1=A-G-R 2=B-G-F 3=C-G-R 4=A-G-F 5=B-G-R 6=C-G-F
HR1277	DEF1:I0dir> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR1278	DEF1:U0> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1279	DEF1:I0 direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR1280	DEF2:I0dir>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR1281	DEF2:U0>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1282	DEF2:I0 direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR1283	DEF3:I0dir>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR1284	DEF3:U0>>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1285	DEF3:I0 direction now	R	1	0=Reverse 1=Forward 2=Undefined
HR1286	DEF4:I0dir>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Unearthed Start 5=Unearthed Trip 6=Compensated Start 7=Compensated Trip
HR1287	DEF4:U0>>>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1288	DEF4:I0 direction now	R	1	0=Reverse 1=Forward 2=Undefined

Holding Register	Parameter	Access	Length	Info
HR1289	IEF1:I0Int> condition	R	1	0=Normal 1=StartFWD 2=StartREV 3=Trip 4=Blocked
HR1290	IEF1:U0> measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR1291	CUB1:I2> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1292	CUB2:I2>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1293	CUB3:I2>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1294	CUB4:I2>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1295	HOC1:Ih> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1296	HOC1:L1 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1297				HR1296 (Lo-16bits)
HR1298	HOC1:L1 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1299				HR1298 (Lo-16bits)
HR1300	HOC1:L2 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1301				HR1300 (Lo-16bits)
HR1302	HOC1:L2 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1303				HR1302 (Lo-16bits)
HR1304	HOC1:L3 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1305				HR1304 (Lo-16bits)
HR1306	HOC1:L3 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1307				HR1306 (Lo-16bits)
HR1308	HOC1:I01 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1309				HR1308 (Lo-16bits)
HR1310	HOC1:I01 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1311				HR1310 (Lo-16bits)
HR1312	HOC1:I02 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1313				HR1312 (Lo-16bits)
HR1314	HOC1:I02 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1315				HR1314 (Lo-16bits)
HR1316	HOC2:Ih>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1317	HOC2:L1 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1318				HR1317 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1319	HOC2:L1 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1320				HR1319 (Lo-16bits)
HR1321	HOC2:L2 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1322				HR1321 (Lo-16bits)
HR1323	HOC2:L2 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1324				HR1323 (Lo-16bits)
HR1325	HOC2:L3 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1326				HR1325 (Lo-16bits)
HR1327	HOC2:L3 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1328				HR1327 (Lo-16bits)
HR1329	HOC2:I01 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1330				HR1329 (Lo-16bits)
HR1331	HOC2:I01 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1332				HR1331 (Lo-16bits)
HR1333	HOC2:I02 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1334				HR1333 (Lo-16bits)
HR1335	HOC2:I02 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1336				HR1335 (Lo-16bits)
HR1337	HOC3:Ih>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1338	HOC3:L1 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1339				HR1338 (Lo-16bits)
HR1340	HOC3:L1 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1341				HR1340 (Lo-16bits)
HR1342	HOC3:L2 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1343				HR1342 (Lo-16bits)
HR1344	HOC3:L2 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1345				HR1344 (Lo-16bits)
HR1346	HOC3:L3 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1347				HR1346 (Lo-16bits)
HR1348	HOC3:L3 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1349				HR1348 (Lo-16bits)
HR1350	HOC3:I01 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1351				HR1350 (Lo-16bits)
HR1352	HOC3:I01 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1353				HR1352 (Lo-16bits)
HR1354	HOC3:I02 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1355				HR1354 (Lo-16bits)
HR1356	HOC3:I02 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1357				HR1356 (Lo-16bits)
HR1358	HOC4:Ih>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1359	HOC4:L1 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1360				HR1359 (Lo-16bits)
HR1361	HOC4:L1 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1362				HR1361 (Lo-16bits)
HR1363	HOC4:L2 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1364				HR1363 (Lo-16bits)
HR1365	HOC4:L2 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1366				HR1365 (Lo-16bits)
HR1367	HOC4:L3 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1368				HR1367 (Lo-16bits)
HR1369	HOC4:L3 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1370				HR1369 (Lo-16bits)
HR1371	HOC4:I01 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1372				HR1371 (Lo-16bits)
HR1373	HOC4:I01 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1374				HR1373 (Lo-16bits)
HR1375	HOC4:I02 harmonic (p.u.)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1376				HR1375 (Lo-16bits)
HR1377	HOC4:I02 harmonic (%)	R	2	FLOAT [0...100000] (Hi-16bits)
HR1378				HR1377 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1379	CLP1:CLP condition	R	1	0=Normal 1=Curr low 2=Overcurrent On 3=CLPU On 4=CLPU blocked
HR1380	CBF1:CBFP condition	R	1	0=Normal 1=Start 2=ReTrip 3=CBFP On 4=Blocked
HR1381	TRF1:Transformer is now	R	1	0=No/LightLoad 1=Energizing 2=Normal load 3=Overloaded 4=On high overload
HR1382	DIF1:Idx> condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR1383				HR1382 (Lo-16bits)
HR1384	DIF1:L1 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR1385	DIF1:L2 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR1386	DIF1:L3 Condition	R	1	0=No Diff 1=Magnitude diff 2=Angle diff 3=Mag.and angle diff
HR1387	REF1:I0d> condition	R	1	0=Normal 1=Trip 2=Blocked
HR1388	TOLT1:TT> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR1389	TOLT1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR1390	TOLT1:Temp C or F deg	R(W)	1	0=C 1=F
HR1391	TOLT1:Ambient Temp Sel	R(W)	1	0=Manual Set 1=RTD Meas
HR1392	TOLT1:RTD Sensor	R	1	0=Invalid 1=OK
HR1393	TOLF1:TF> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked
HR1394	TOLF1:Thermal status	R	1	0=Light / No load 1=High overload 2=Overloading 3=Load normal
HR1395	TOLF1:Temp C or F deg	R(W)	1	0=C 1=F
HR1396	TOLF1:Ambient Temp Sel	R(W)	1	0=Manual Set 1=RTD Meas
HR1397	TOLF1:RTD Sensor	R(W)	1	0=Invalid 1=OK
HR1398	TOLF1:Set or Estimate tau (t const)	R(W)	1	0=Set 1=Estimate
HR1399	MST1:Motor is now	R	1	0=Stopped 1=Starting 2=Running 3=Stalled
HR1400	TOLM1:TM> condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On 3=Inhibit On 4=Trip On 5=Blocked

Holding Register	Parameter	Access	Length	Info
HR1401	TOLM1:Motor status	R	1	0=Stopped 1=Stalled 2=JustStopped 3=Overloading 4=Running normal
HR1402	TOLM1:Temp C or F deg	R(W)	1	0=C 1=F
HR1403	TOLM1:NPS Biasing in use	R(W)	1	0=No NPS biasing 1=NPS Biasing in use
HR1404	TOLM1:Time constants	R(W)	1	0=Single 1=Multiple
HR1405	TOLM1:Estimate Short TC and timings	R(W)	1	0=Set manually 1=Estimate (on-line)
HR1406	TOLM1:T const dyn. balancing	R(W)	1	0=Fixed 1=Dynamic
HR1407	TOLM1:Dev.Temp (tmax)	R(W)	1	0=A 1=B 2=F 3=H 4=Manual set
HR1408	TOLM1:Amb. T Selection	R(W)	1	0=Manual set 1=RTD
HR1409	TOLM1:RTD Sensor	R	1	0=Invalid 1=OK
HR1410	LCR1:ISt> condition	R	1	0=Normal 1=Start 2=Set time Trip 3=M cap Trip 4=Blocked
HR1411	FSP1:N> condition	R	1	0=Normal 1=Alarm On 2=Inhibits 3=Blocked
HR1412	NUC1:I< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1413	MJP1:Im> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1414	OV1:U> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1415	OV2:U>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1416	OV3:U>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1417	OV4:U>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1418	UV1:U< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On 5=Block input active
HR1419	UV2:U<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On 5=Block input active
HR1420	UV3:U<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On 5=Block input active
HR1421	UV4:U<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On 5=Block input active
HR1422	NOV1:U0> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1423	NOV1:U0> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1424	NOV2:U0>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR1425	NOV2:U0>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1426	NOV3:U0>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1427	NOV3:U0>>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1428	NOV4:U0>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1429	NOV4:U0>>>> measuring now	R	1	0=No U0 avail! 1=U0Calc 2=U3 Input 3=U4 Input
HR1430	VUB1:U1/2 >/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1431	VUB2:U1/2 >>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1432	VUB3:U1/2 >>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1433	VUB4:U1/2 >>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1434	FRQC1:f> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1435	FRQC1:f>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1436	FRQC1:f>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1437	FRQC1:f>>>> [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1438	FRQC1:f< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1439	FRQC1:f<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1440	FRQC1:f<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1441	FRQC1:f<<<< [Curr] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1442	FRQV1:f> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1443	FRQV1:f>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1444	FRQV1:f>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1445	FRQV1:f>>>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1446	FRQV1:f< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1447	FRQV1:f<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1448	FRQV1:f<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active
HR1449	FRQV1:f<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Block input active

Holding Register	Parameter	Access	Length	Info
HR1450	DFT1:df/dt >/< (1) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1451	DFT1:df/dt >/< (2) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1452	DFT1:df/dt >/< (3) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1453	DFT1:df/dt >/< (4) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1454	DFT1:df/dt >/< (5) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1455	DFT1:df/dt >/< (6) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1456	DFT1:df/dt >/< (7) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1457	DFT1:df/dt >/< (8) condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1458	GST1:Generator is now	R	1	0=No/LightLoad 1=Normal load 2=Overloaded 3=On high overload
HR1459	OPW1:P> condition (OBSOLETE)	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1460	UPW1:P< condition (OBSOLETE)	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1461	RPW1:Prev> condition (OBSOLETE)	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1462	SEF1:U03rd< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1463	SEF1:U03rd< measuring now	R	1	0=No U0 avail! 1=U3 Input 2=U4 Input
HR1464	UEX1:Q< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1465	UIM1:Z< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1466	VOC1:lv> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1467	VOC1:Voltage measurement	R	1	0=Invalid U1 not avail. 1=Ok
HR1468	VHZ1:V/Hz(1) > condition	R	1	0=Normal 1=Start 2=Alarm 3=Trip 4=Blocked
HR1469	UPF1:PF< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Alarm Start 5=Alarm
HR1470	ARC1:I/I0 Arc> condition	R	2	0b=Z1 Trip 1b=Z1 Blocked 2b=Z2 Trip 3b=Z2 Blocked 4b=Z3 Trip 5b=Z3 Blocked 6b=Z4 Trip 7b=Z4 Blocked (Hi-16bits)
HR1471				HR1470 (Lo-16bits)
HR1472	CTS1:CTS1 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR1473	CTS2:CTS2 condition	R	1	0=Normal 1=Alarm 2=Blocked
HR1474	VTS1:VTS condition	R	1	0=Normal 1=Start 2=VTLinefail 3=VTBusfail 4=Blocked

Holding Register	Parameter	Access	Length	Info
HR1475	VTS1:Bus voltages	R	1	0=Bus dead 1=Bus Live VTS Ok SEQ Ok 2=Bus Live VTS Ok SEQ Rev 3=Bus Live VTS Ok SEQ Undef 4=Bus Live VTS problem
HR1476	FLX1:Fault locator condition	R	1	0=Idle 1=Triggered 2=Calc ready
HR1477	THD1:THD condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked
HR1478	CBW1:CBW condition	R	1	0=Normal 1=Alarm1 On 2=Alarm2 On
HR1479	NJK1:U0> RECL condition	R	1	0=Normal 1=Requested 2=Blocked 3=Active 4=BK clo cmd 5=Bk Closed.U0> blocked
HR1480	SOF1:SOTF condition	R	1	0=Normal 1=Init 2=Active 3=Trip 4=Blocked
HR1481	SYN1:SYN1 Condition	R	2	0b=SYN1 Blocked 1b=SYN1 Ok 2b=SYN1 Bypass 3b=SYN1 Vcond Ok 4b=SYN1 Vdiff Ok 5b=SYN1 Adiff Ok 6b=SYN1 fdiff Ok 7b=SYN1 block input active (Hi-16bits)
HR1482				HR1481 (Lo-16bits)
HR1483	SYN1:SYN2 Condition	R	2	0b=SYN2 Blocked 1b=SYN2 Ok 2b=SYN2 Bypass 3b=SYN2 Vcond Ok 4b=SYN2 Vdiff Ok 5b=SYN2 Adiff Ok 6b=SYN2 fdiff Ok 7b=SYN2 block input active (Hi-16bits)
HR1484				HR1483 (Lo-16bits)
HR1485	SYN1:SYN3 Condition	R	2	0b=SYN3 Blocked 1b=SYN3 Ok 2b=SYN3 Bypass 3b=SYN3 Vcond Ok 4b=SYN3 Vdiff Ok 5b=SYN3 Adiff Ok 6b=SYN3 fdiff Ok 7b=SYN3 block input active (Hi-16bits)
HR1486				HR1485 (Lo-16bits)
HR1487	OBJ1:Object1 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1488	OBJ2:Object2 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1489	OBJ3:Object3 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1490	OBJ4:Object4 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1491	OBJ5:Object5 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1492	CIN1:Ind.1 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1493	CIN2:Ind.2 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1494	CIN3:Ind.3 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1495	CIN4:Ind.4 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1496	CIN5:Ind.5 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR1497	AR1:AR status	R	2	0b=AR is inhibit 1b=AR is ready 2b=AR is locked 3b=AR is running 4b=AR is not running 5b=Lock out delay is running 6b=Reclaim time counting 7b=Start time counting 8b=Dead time counting 9b=Arcing or discr time counting 10b=Reclaim time counting 11b=AR1 Requested 12b=AR2 Requested 13b=AR3 Requested 14b=AR4 Requested 15b=AR5 Requested 16b=Executing Shot1 17b=Executing Shot2 18b=Executing Shot3 19b=Executing Shot4 20b=Executing Shot5 21b=Shot Clear (Hi-16bits)
HR1498				HR1497 (Lo-16bits)
HR1499	AR1:Timer active	R	1	0=- 1=AR Lockout 2=AR Reset Reclaim 3=AR Start Delay 4=AR Dead Time 5=AR Discrimination 6=AR Shot Reclaim
HR1500	VRG1:Vreg settings condition	R	2	0b=U>> set too low 1b=U< set too high 2b=U> set higher than U>> 3b=U< set lower than U<< 4=U>>> set too low 5=U<<< set too high 6=VT selection not ok (Hi-16bits)
HR1501				HR1500 (Lo-16bits)
HR1502	VRG1:Vreg condition	R	2	0b=Raise command on 1b=Lower command on 2b=Operation blocked 3b=Output control blocked 4b=U<<< Block on 5b=I> block on 6b=Tap on highlimit 7b=Tap on lowlimit 8b=Operation blocked 9b=U>/< pickup on 10b=U>>/<< pickup on 11b=Control wait time on 12b=Manual control on (Hi-16bits)
HR1503				HR1502 (Lo-16bits)
HR1504	VRG1:Vreg timer active	R	1	0=- 1=Fine tune Decrease 2=Fine tune Increase 3=Low set Decrease 4=High set Decrease 5=Instant Decrease 6=Low set Increase 7=High set Increase
HR1505	VRG1:Voltage measurement condition	R	1	0=Not configured 1=System U12 2=System U23 3=System U31 4=VT4 meas U12 5=VT4 meas U23 6=VT4 meas U31 7=VT4 conf not ok
HR1506	EMON1:I> (50) Monitor condition	R	1	0=Normal 1=FLTdetected
HR1507	EMON1:I0> (50N) Monitor condition	R	1	0=Normal 1=FLTdetected
HR1508	EMON1:Idir> (67) Monitor condition	R	1	0=Normal 1=FLTdetected
HR1509	EMON1:I0dir> (67N) Monitor condition	R	1	0=Normal 1=FLTdetected

Holding Register	Parameter	Access	Length	Info
HR1510	EMON1:I0Int> (67NT) Monitor condition	R	1	0=Normal 1=FLTFW 2=FLTREV
HR1511	EMON1:U< (27) Monitor condition	R	1	0=Normal 1=Undervoltage 2=Novoltage
HR1512	EMON1:U0> (59N) Monitor condition	R	1	0=Normal 1=FLTdetected
HR1513	EMON1:FL (21FL) Monitor condition	R	1	0=Normal 1=FLTdetected
HR1514	MEAS1:Meas1 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR1515				HR1514 (Lo-16bits)
HR1516	DIF1:REFHV> condition	R	2	0b=HV REF Blocked 1b=HV REF Trip (Hi-16bits)
HR1517				HR1516 (Lo-16bits)
HR1518	DIF1:REFLV> condition	R	2	0b=LV REF Blocked 1b=LV REF Trip (Hi-16bits)
HR1519				HR1518 (Lo-16bits)
HR1520	ALARM:Alarms 1-32	R	2	0b=A1 1b=A2 2b=A3 3b=A4 4b=A5 5b=A6 6b=A7 7b=A8 8b=A9 9b=A10 10b=A11 11b=A12 12b=A13 13b=A14 14b=A15 15b=A16 16b=A17 17b=A18 18b=A19 19b=A20 20b=A21 21b=A22 22b=A23 23b=A24 24b=A25 25b=A26 26b=A27 27b=A28 28b=A29 29b=A30 30b=A31 31b=A32 (Hi-16bits)
HR1521				HR1520 (Lo-16bits)
HR1522	ALARM:Alarms 33-64	R	2	0b=A33 1b=A34 2b=A35 3b=A36 4b=A37 5b=A38 6b=A39 7b=A40 8b=A41 9b=A42 10b=A43 11b=A44 12b=A45 13b=A46 14b=A47 15b=A48 16b=A49 17b=A50 18b=A51 19b=A52 20b=A53 21b=A54 22b=A55 23b=A56 24b=A57 25b=A58 26b=A59 27b=A60 28b=A61 29b=A62 30b=A63 31b=A64 (Hi-16bits)
HR1523				HR1522 (Lo-16bits)
HR1524	DIAG:Configuration changed	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR1525				HR1524 (Lo-16bits)
HR1526	RTDIN:S1 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1527				HR1526 (Lo-16bits)
HR1528	RTDIN:S2 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1529				HR1528 (Lo-16bits)
HR1530	RTDIN:S3 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1531				HR1530 (Lo-16bits)
HR1532	RTDIN:S4 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1533				HR1532 (Lo-16bits)
HR1534	RTDIN:S5 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1535				HR1534 (Lo-16bits)
HR1536	RTDIN:S6 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1537				HR1536 (Lo-16bits)
HR1538	RTDIN:S7 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1539				HR1538 (Lo-16bits)
HR1540	RTDIN:S7 Measurement mA	R	2	FLOAT [0...20] (Hi-16bits)
HR1541				HR1540 (Lo-16bits)
HR1542	RTDIN:S8 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1543				HR1542 (Lo-16bits)
HR1544	RTDIN:S8 Measurement mA	R	2	FLOAT [0...20] (Hi-16bits)
HR1545				HR1544 (Lo-16bits)
HR1546	RTDIN:S9 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1547				HR1546 (Lo-16bits)
HR1548	RTDIN:S10 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1549				HR1548 (Lo-16bits)
HR1550	RTDIN:S11 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1551				HR1550 (Lo-16bits)
HR1552	RTDIN:S12 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1553				HR1552 (Lo-16bits)
HR1554	RTDIN:S13 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1555				HR1554 (Lo-16bits)
HR1556	RTDIN:S14 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1557				HR1556 (Lo-16bits)
HR1558	RTDIN:S15 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1559				HR1558 (Lo-16bits)
HR1560	RTDIN:S15 Measurement mA	R	2	FLOAT [0...20] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1561				HR1560 (Lo-16bits)
HR1562	RTDIN:S16 Measurement(Raw)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1563				HR1562 (Lo-16bits)
HR1564	RTDIN:S16 Measurement mA	R	2	FLOAT [0...20] (Hi-16bits)
HR1565				HR1564 (Lo-16bits)
HR1566	RTD1:S1 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1567				HR1566 (Lo-16bits)
HR1568	RTD1:S2 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1569				HR1568 (Lo-16bits)
HR1570	RTD1:S3 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1571				HR1570 (Lo-16bits)
HR1572	RTD1:S4 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1573				HR1572 (Lo-16bits)
HR1574	RTD1:S5 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1575				HR1574 (Lo-16bits)
HR1576	RTD1:S6 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1577				HR1576 (Lo-16bits)
HR1578	RTD1:S7 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1579				HR1578 (Lo-16bits)
HR1580	RTD1:S8 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1581				HR1580 (Lo-16bits)
HR1582	RTD1:S9 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1583				HR1582 (Lo-16bits)
HR1584	RTD1:S10 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1585				HR1584 (Lo-16bits)
HR1586	RTD1:S11 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1587				HR1586 (Lo-16bits)
HR1588	RTD1:S12 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1589				HR1588 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1590	RTD1:S1 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1591				HR1590 (Lo-16bits)
HR1592	RTD1:S1 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1593				HR1592 (Lo-16bits)
HR1594	RTD1:S2 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1595				HR1594 (Lo-16bits)
HR1596	RTD1:S2 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1597				HR1596 (Lo-16bits)
HR1598	RTD1:S3 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1599				HR1598 (Lo-16bits)
HR1600	RTD1:S3 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1601				HR1600 (Lo-16bits)
HR1602	RTD1:S4 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1603				HR1602 (Lo-16bits)
HR1604	RTD1:S4 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1605				HR1604 (Lo-16bits)
HR1606	RTD1:S5 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1607				HR1606 (Lo-16bits)
HR1608	RTD1:S5 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1609				HR1608 (Lo-16bits)
HR1610	RTD1:S6 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1611				HR1610 (Lo-16bits)
HR1612	RTD1:S6 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1613				HR1612 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1614	RTD1:S7 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1615				HR1614 (Lo-16bits)
HR1616	RTD1:S7 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1617				HR1616 (Lo-16bits)
HR1618	RTD1:S8 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1619				HR1618 (Lo-16bits)
HR1620	RTD1:S8 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1621				HR1620 (Lo-16bits)
HR1622	RTD1:S9 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1623				HR1622 (Lo-16bits)
HR1624	RTD1:S9 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1625				HR1624 (Lo-16bits)
HR1626	RTD1:S10 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1627				HR1626 (Lo-16bits)
HR1628	RTD1:S10 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1629				HR1628 (Lo-16bits)
HR1630	RTD1:S11 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1631				HR1630 (Lo-16bits)
HR1632	RTD1:S11 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1633				HR1632 (Lo-16bits)
HR1634	RTD1:S12 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1635				HR1634 (Lo-16bits)
HR1636	VHZ1:V/Hz(2) > condition	R	1	0=Normal 1=Start 2=Alarm 3=Trip 4=Blocked
HR1637	RTD1:S12 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1638				HR1637 (Lo-16bits)
HR1639	UIM2:Z<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1640	RTD1:S1 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1641				HR1640 (Lo-16bits)
HR1642	RTD1:S2 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1643				HR1642 (Lo-16bits)
HR1644	RTD1:S3 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1645				HR1644 (Lo-16bits)
HR1646	RTD1:S4 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1647				HR1646 (Lo-16bits)
HR1648	RTD1:S5 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1649				HR1648 (Lo-16bits)
HR1650	RTD1:S6 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1651				HR1650 (Lo-16bits)
HR1652	RTD1:S7 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1653				HR1652 (Lo-16bits)
HR1654	RTD1:S8 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1655				HR1654 (Lo-16bits)
HR1656	RTD1:S9 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1657				HR1656 (Lo-16bits)
HR1658	RTD1:S10 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1659				HR1658 (Lo-16bits)
HR1660	RTD1:S11 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1661				HR1660 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1662	RTD1:S12 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1663				HR1662 (Lo-16bits)
HR1664	MBIO:MBIO ModA Ch1 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1665				HR1664 (Lo-16bits)
HR1666	MBIO:MBIO ModA Ch2 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1667				HR1666 (Lo-16bits)
HR1668	MBIO:MBIO ModA Ch3 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1669				HR1668 (Lo-16bits)
HR1670	MBIO:MBIO ModA Ch4 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1671				HR1670 (Lo-16bits)
HR1672	MBIO:MBIO ModA Ch5 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1673				HR1672 (Lo-16bits)
HR1674	MBIO:MBIO ModA Ch6 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1675				HR1674 (Lo-16bits)
HR1676	MBIO:MBIO ModA Ch7 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1677				HR1676 (Lo-16bits)
HR1678	MBIO:MBIO ModA Ch8 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1679				HR1678 (Lo-16bits)
HR1680	MBIO:MBIO ModB Ch1 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1681				HR1680 (Lo-16bits)
HR1682	MBIO:MBIO ModB Ch2 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1683				HR1682 (Lo-16bits)
HR1684	MBIO:MBIO ModB Ch3 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1685				HR1684 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1686	MBIO:MBIO ModB Ch4 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1687				HR1686 (Lo-16bits)
HR1688	MBIO:MBIO ModB Ch5 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1689				HR1688 (Lo-16bits)
HR1690	MBIO:MBIO ModB Ch6 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1691				HR1690 (Lo-16bits)
HR1692	MBIO:MBIO ModB Ch7 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1693				HR1692 (Lo-16bits)
HR1694	MBIO:MBIO ModB Ch8 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1695				HR1694 (Lo-16bits)
HR1696	MBIO:MBIO ModC Ch1 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1697				HR1696 (Lo-16bits)
HR1698	MBIO:MBIO ModC Ch2 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1699				HR1698 (Lo-16bits)
HR1700	MBIO:MBIO ModC Ch3 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1701				HR1700 (Lo-16bits)
HR1702	MBIO:MBIO ModC Ch4 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1703				HR1702 (Lo-16bits)
HR1704	MBIO:MBIO ModC Ch5 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1705				HR1704 (Lo-16bits)
HR1706	MBIO:MBIO ModC Ch6 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1707				HR1706 (Lo-16bits)
HR1708	MBIO:MBIO ModC Ch7 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1709				HR1708 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1710	MBIO:MBIO ModC Ch8 Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1711				HR1710 (Lo-16bits)
HR1712	OBJ6:Object6 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1713	OBJ7:Object7 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1714	OBJ8:Object8 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1715	OBJ9:Object9 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1716	OBJ10:Object10 Breaker Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR1717	VRG1:Voltage now	R	2	FLOAT [0...140] (Hi-16bits)
HR1718				HR1717 (Lo-16bits)
HR1719	VRG1:Voltage difference to set target	R	2	FLOAT [-100...140] (Hi-16bits)
HR1720				HR1719 (Lo-16bits)
HR1721	VRG1:Tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR1722				HR1721 (Lo-16bits)
HR1723	VRG1:Absolute tap location	R	2	FLOAT [0...50] (Hi-16bits)
HR1724				HR1723 (Lo-16bits)
HR1725	VRG1:Control mode	R	1	0=Auto 1=Manual (loc&rem)
HR1726	VRG1:Remote tap control input	R(W)	1	0=Stop 1=Lower 2=Higher
HR1727	VJP1:Vector jump <DELTA><alpha> > condition	R	1	0=Normal 1=Blocked 2=Trip 3=Alarm
HR1728	LOGIC1:Logic out 1-32	R(W)	2	UINT32 [0...4294967295] (Hi-16bits)
HR1729				HR1728 (Lo-16bits)
HR1730	PGS1:PS1 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1731	VJP1:Selected voltage measurement	R	1	0=Selection Ok 1=Selection not available
HR1732	PGS1:PS2 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1733	PGS1:PS3 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1734	PGS1:PS4 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1735	PGS1:PS5 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1736	PGS1:PS6 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1737	PGS1:PS7 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR1738	PGS1:PS8 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1739	PGS1:PS9 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1740	PGS1:PS10 >/< Condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1741	ASC1:Curve1 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1742				HR1741 (Lo-16bits)
HR1743	ASC1:Curve1 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1744				HR1743 (Lo-16bits)
HR1745	ASC1:Curve2 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1746				HR1745 (Lo-16bits)
HR1747	ASC1:Curve2 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1748				HR1747 (Lo-16bits)
HR1749	ASC1:Curve3 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1750				HR1749 (Lo-16bits)
HR1751	ASC1:Curve3 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1752				HR1751 (Lo-16bits)
HR1753	ASC1:Curve4 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1754				HR1753 (Lo-16bits)
HR1755	ASC1:Curve4 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR1756				HR1755 (Lo-16bits)
HR1757	GMAG:Command AVR (remote)	R(W)	1	0=Off 1=On
HR1758	GMAG:Command FCR (remote)	R(W)	1	0=Off 1=On
HR1759	GMAG:Command MVAR (remote)	R(W)	1	0=Off 1=On
HR1760	GMAG:Command PF (remote)	R(W)	1	0=Off 1=On
HR1761	GMAG:Unload Q (remote)	R(W)	1	0=Off 1=On
HR1762	GMAG:Reset (remote)	R(W)	1	0=Off 1=On
HR1763	GMAG:AVR Ref execute (remote)	R(W)	1	0=Off 1=On
HR1764	GMAG:AVR Ref value (remote)	R(W)	2	FLOAT [0...450000] (Hi-16bits)
HR1765				HR1764 (Lo-16bits)
HR1766	GMAG:FCR Ref execute (remote)	R(W)	1	0=Off 1=On
HR1767	GMAG:FCR Ref value (remote)	R(W)	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1768				HR1767 (Lo-16bits)
HR1769	GMAG:MVar Ref execute (remote)	R(W)	1	0=Off 1=On
HR1770	GMAG:MVar Ref value (MVar) (remote)	R(W)	2	FLOAT [-1000...1000] (Hi-16bits)
HR1771				HR1770 (Lo-16bits)
HR1772	GMAG:PF Ref execute (remote)	R(W)	1	0=Off 1=On
HR1773	GMAG:PF Ref value (remote)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR1774				HR1773 (Lo-16bits)
HR1775	GMAG:Open FB command (remote)	R(W)	1	0=Off 1=On
HR1776	GMAG:Close FB command (remote)	R(W)	1	0=Off 1=On
HR1777	GMAG:AVR speed 90 (remote)	R(W)	1	0=Off 1=On
HR1778	GMAG:Ind GB closed (remote)	R(W)	1	0=Off 1=On
HR1779	GMAG:Island mode (remote)	R(W)	1	0=Off 1=On
HR1780	GMAG:Command voltage matching (remote)	R(W)	1	0=Off 1=On
HR1781	GMAG:Grid voltage (remote)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR1782				HR1781 (Lo-16bits)
HR1783	GMAG:AVR Ref start (remote)	R(W)	1	0=Off 1=On
HR1784	GMAG:Field current (remote)	R	2	FLOAT [-500...500] (Hi-16bits)
HR1785				HR1784 (Lo-16bits)
HR1786	GMAG:AVR Ref actual (remote)	R	2	FLOAT [-500...500] (Hi-16bits)
HR1787				HR1786 (Lo-16bits)
HR1788	GMAG:FCR Ref actual (remote)	R	2	FLOAT [-500...500] (Hi-16bits)
HR1789				HR1788 (Lo-16bits)
HR1790	GMAG:MVAR Ref actual (remote)	R	2	FLOAT [-500...500] (Hi-16bits)
HR1791				HR1790 (Lo-16bits)
HR1792	GMAG:PF Ref actual (remote)	R	2	FLOAT [-500...500] (Hi-16bits)
HR1793				HR1792 (Lo-16bits)
HR1794	GMAG:Mode MVAR (remote)	R	1	0=Off 1=On
HR1795	GMAG:Mode PF (remote)	R	1	0=Off 1=On

Holding Register	Parameter	Access	Length	Info
HR1796	DIGI:DIs	R	2	0b=DI65 1b=DI66 2b=DI67 3b=DI68 4b=DI69 5b=DI70 6b=DI71 7b=DI72 8b=DI73 9b=DI74 10b=DI75 11b=DI76 12b=DI77 13b=DI78 14b=DI79 15b=DI80 16b=DI81 17b=DI82 18b=DI83 19b=DI84 20b=DI85 21b=DI86 22b=DI87 23b=DI88 24b=DI89 25b=DI90 26b=DI91 27b=DI92 28b=DI93 29b=DI94 30b=DI95 31b=DI96 (Hi-16bits)
HR1797				HR1796 (Lo-16bits)
HR1798	DIGI:DIs	R	2	0b=DI97 1b=DI98 2b=DI99 3b=DI100 4b=DI101 5b=DI102 6b=DI103 7b=DI104 8b=DI105 9b=DI106 10b=DI107 11b=DI108 12b=DI109 13b=DI110 14b=DI111 15b=DI112 16b=DI113 17b=DI114 18b=DI115 19b=DI116 20b=DI117 21b=DI118 22b=DI119 23b=DI120 24b=DI121 25b=DI122 26b=DI123 27b=DI124 28b=DI125 29b=DI126 30b=DI127 31b=DI128 (Hi-16bits)
HR1799				HR1798 (Lo-16bits)
HR1800	RELE:Outputs status	R	2	0b=O33 1b=O34 2b=O35 (Hi-16bits)
HR1801				HR1800 (Lo-16bits)
HR1802	GMAG:Command Excitation to Remote control	R(W)	1	0=Off 1=On
HR1803	FREQS:f atm. Display (when not measurable is 0Hz)	R	2	FLOAT [0...75] (Hi-16bits)
HR1804				HR1803 (Lo-16bits)
HR1805	GMAG:Command Excitation to Remote control Received	R	1	0=No 1=Yes
HR1806	GMAG:Command AVR (remote) Received	R	1	0=No 1=Yes
HR1807	GMAG:Command FCR (remote) Received	R	1	0=No 1=Yes
HR1808	GMAG:Command MVAR (remote) Received	R	1	0=No 1=Yes
HR1809	GMAG:Command PF (remote) Received	R	1	0=No 1=Yes
HR1810	GMAG:Reset remote Received indications	R(W)	1	0=- 1=Reset
HR1811	GMAG:GMAG FCR Mode	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1812				HR1811 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1813	GMAG:GMAG AVR Mode	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1814				HR1813 (Lo-16bits)
HR1815	GMAG:Unload Q (remote) Received	R	1	0=No 1=Yes
HR1816	GMAG:Reset (remote) Received	R	1	0=No 1=Yes
HR1817	GMAG:AVR Ref execute (remote) Received	R	1	0=No 1=Yes
HR1818	GMAG:AVR Ref value (remote) Received	R	2	FLOAT [0...450000] (Hi-16bits)
HR1819				HR1818 (Lo-16bits)
HR1820	GMAG:FCR Ref execute (remote) Received	R	1	0=No 1=Yes
HR1821	GMAG:FCR Ref value (remote) Received	R	2	FLOAT [0...100] (Hi-16bits)
HR1822				HR1821 (Lo-16bits)
HR1823	GMAG:MVar Ref execute (remote) Received	R	1	0=No 1=Yes
HR1824	GMAG:MVar Ref value (remote) Received	R	2	FLOAT [-1000...1000] (Hi-16bits)
HR1825				HR1824 (Lo-16bits)
HR1826	GMAG:PF Ref execute (remote) Received	R	1	0=No 1=Yes
HR1827	GMAG:PF Ref value (remote) Received	R	2	FLOAT [-1...1] (Hi-16bits)
HR1828				HR1827 (Lo-16bits)
HR1829	GMAG:Open FB command (remote) Received	R	1	0=No 1=Yes
HR1830	GMAG:Close FB command (remote) Received	R	1	0=No 1=Yes
HR1831	GMAG:AVR speed 90 (remote) Received	R	1	0=No 1=Yes
HR1832	GMAG:Ind GB closed (remote) Received	R	1	0=No 1=Yes
HR1833	GMAG:Island mode (remote) Received	R	1	0=No 1=Yes
HR1834	GMAG:Command voltage matching (remote) Received	R	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR1835	GMAG:Grid voltage (remote) Received	R	2	FLOAT [0...500] (Hi-16bits)
HR1836				HR1835 (Lo-16bits)
HR1837	GMAG:AVR Ref start (remote) Received	R	1	0=No 1=Yes
HR1838	IAE1:I>U< I.A.E. condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR1839	GSYN:Synchronizer mode	R(W)	1	0=Disabled 1=Activated
HR1840	GSYN:Magnitude difference	R	2	FLOAT [-200...200] (Hi-16bits)
HR1841				HR1840 (Lo-16bits)
HR1842	GSYN:Frequency difference	R	2	FLOAT [-100...100] (Hi-16bits)
HR1843				HR1842 (Lo-16bits)
HR1844	GSYN:Angle difference	R	2	FLOAT [-360...360] (Hi-16bits)
HR1845				HR1844 (Lo-16bits)
HR1846	GSYN:Start Synchronizer	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1847				HR1846 (Lo-16bits)
HR1848	GSYN:Stop Synchronizer	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1849				HR1848 (Lo-16bits)
HR1850	GSYN:Block and Interrupt Synchronizing	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1851				HR1850 (Lo-16bits)
HR1852	GSYN:Breaker Closed	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1853				HR1852 (Lo-16bits)
HR1854	GSYN:GSYN Running	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1855				HR1854 (Lo-16bits)
HR1856	GSYN:GSYN Increase Voltage	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1857				HR1856 (Lo-16bits)
HR1858	GSYN:GSYN Decrease Voltage	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1859				HR1858 (Lo-16bits)
HR1860	GSYN:GSYN Increase Frequency	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1861				HR1860 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1862	GSYN:GSYN Decrease Frequency	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1863				HR1862 (Lo-16bits)
HR1864	GSYN:GSYN Breaker Close Pulse	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1865				HR1864 (Lo-16bits)
HR1866	GSYN:GSYN Long Sync Time	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1867				HR1866 (Lo-16bits)
HR1868	GSYN:Magnitude difference fine tune	R(W)	2	FLOAT [-200...200] (Hi-16bits)
HR1869				HR1868 (Lo-16bits)
HR1870	GSYN:Frequency difference fine tune	R(W)	2	FLOAT [-100...100] (Hi-16bits)
HR1871				HR1870 (Lo-16bits)
HR1872	GSYN:Angle difference fine tune	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR1873				HR1872 (Lo-16bits)
HR1874	GSYN:GSYN Blocked	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1875				HR1874 (Lo-16bits)
HR1876	GSYN:GSYN Voltage Mag.Difference Ok	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1877				HR1876 (Lo-16bits)
HR1878	GSYN:GSYN Voltage Freq.Difference Ok	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1879				HR1878 (Lo-16bits)
HR1880	GSYN:GSYN Voltage Ang.Difference Ok	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1881				HR1880 (Lo-16bits)
HR1882	GSYN:Magnitude difference on closing BRK	R	2	FLOAT [-200...200] (Hi-16bits)
HR1883				HR1882 (Lo-16bits)
HR1884	GSYN:Frequency difference on closing BRK	R	2	FLOAT [-100...100] (Hi-16bits)
HR1885				HR1884 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1886	GSYN:Angle difference on closing BRK	R	2	FLOAT [-360...360] (Hi-16bits)
HR1887				HR1886 (Lo-16bits)
HR1888	GSYN:Synchronizing time left	R	2	FLOAT [0...1800] (Hi-16bits)
HR1889				HR1888 (Lo-16bits)
HR1890	VREC:Fault register mode	R(W)	1	0=Disabled 1=Activated
HR1891	VREC:Scale current values to primary	R(W)	1	0=No 1=Yes
HR1892	VREC:Fault register trigger	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR1893				HR1892 (Lo-16bits)
HR1894	VREC:Record fault types (Overcurrent & Voltage Over/Under) faults	R(W)	1	0=No 1=Yes
HR1895	VREC:VREC Magnitude 1	R	2	FLOAT [0...1800] (Hi-16bits)
HR1896				HR1895 (Lo-16bits)
HR1897	VREC:VREC Magnitude 2	R	2	FLOAT [0...1800] (Hi-16bits)
HR1898				HR1897 (Lo-16bits)
HR1899	VREC:VREC Magnitude 3	R	2	FLOAT [0...1800] (Hi-16bits)
HR1900				HR1899 (Lo-16bits)
HR1901	VREC:VREC Magnitude 4	R	2	FLOAT [0...1800] (Hi-16bits)
HR1902				HR1901 (Lo-16bits)
HR1903	VREC:VREC Magnitude 5	R	2	FLOAT [0...1800] (Hi-16bits)
HR1904				HR1903 (Lo-16bits)
HR1905	VREC:VREC Magnitude 6	R	2	FLOAT [0...1800] (Hi-16bits)
HR1906				HR1905 (Lo-16bits)
HR1907	VREC:VREC Magnitude 7	R	2	FLOAT [0...1800] (Hi-16bits)
HR1908				HR1907 (Lo-16bits)
HR1909	VREC:VREC Magnitude 8	R	2	FLOAT [0...1800] (Hi-16bits)
HR1910				HR1909 (Lo-16bits)
HR1911	RTB:Real time bus measurements	R(W)	1	0=Disabled 1=Activated
HR1912	RTB:Scale current values to primary	R(W)	1	0=No 1=Yes
HR1913	RTB:Magnitude 1	R	2	FLOAT [-10000000...10000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR1914				HR1913 (Lo-16bits)
HR1915	RTB:Magnitude 2	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1916				HR1915 (Lo-16bits)
HR1917	RTB:Magnitude 3	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1918				HR1917 (Lo-16bits)
HR1919	RTB:Magnitude 4	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1920				HR1919 (Lo-16bits)
HR1921	RTB:Magnitude 5	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1922				HR1921 (Lo-16bits)
HR1923	RTB:Magnitude 6	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1924				HR1923 (Lo-16bits)
HR1925	RTB:Magnitude 7	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1926				HR1925 (Lo-16bits)
HR1927	RTB:Magnitude 8	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1928				HR1927 (Lo-16bits)
HR1929	AOUT:mA In (I option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR1930				HR1929 (Lo-16bits)
HR1931	AOUT:mA In (I option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR1932				HR1931 (Lo-16bits)
HR1933	AOUT:mA Out Channel 1 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1934				HR1933 (Lo-16bits)
HR1935	AOUT:mA Out Channel 1 Outputs now (I option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR1936				HR1935 (Lo-16bits)
HR1937	AOUT:mA Out Channel 2 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1938				HR1937 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1939	AOUT:mA Out Channel 2 Outputs now (I option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR1940				HR1939 (Lo-16bits)
HR1941	AOUT:mA Out Channel 3 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1942				HR1941 (Lo-16bits)
HR1943	AOUT:mA Out Channel 3 Outputs now (I option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR1944				HR1943 (Lo-16bits)
HR1945	AOUT:mA Out Channel 4 Input Magnitude now (I option 1)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1946				HR1945 (Lo-16bits)
HR1947	AOUT:mA Out Channel 4 Outputs now (I option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR1948				HR1947 (Lo-16bits)
HR1949	AOUT:mA Out Channel 5 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1950				HR1949 (Lo-16bits)
HR1951	AOUT:mA Out Channel 5 Outputs now (I option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR1952				HR1951 (Lo-16bits)
HR1953	AOUT:mA Out Channel 6 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1954				HR1953 (Lo-16bits)
HR1955	AOUT:mA Out Channel 6 Outputs now (I option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR1956				HR1955 (Lo-16bits)
HR1957	AOUT:mA Out Channel 7 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1958				HR1957 (Lo-16bits)
HR1959	AOUT:mA Out Channel 7 Outputs now (I option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR1960				HR1959 (Lo-16bits)
HR1961	AOUT:mA Out Channel 8 Input Magnitude now (I option 2)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR1962				HR1961 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1963	AOUT:mA Out Channel 8 Outputs now (I option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR1964				HR1963 (Lo-16bits)
HR1965	RTD1:S13 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1966				HR1965 (Lo-16bits)
HR1967	RTD1:S14 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1968				HR1967 (Lo-16bits)
HR1969	RTD1:S15 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1970				HR1969 (Lo-16bits)
HR1971	RTD1:S16 Measurement (Protection)	R	2	FLOAT [-200...2000] (Hi-16bits)
HR1972				HR1971 (Lo-16bits)
HR1973	RTD1:S13 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1974				HR1973 (Lo-16bits)
HR1975	RTD1:S13 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1976				HR1975 (Lo-16bits)
HR1977	RTD1:S13 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1978				HR1977 (Lo-16bits)
HR1979	RTD1:S14 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1980				HR1979 (Lo-16bits)
HR1981	RTD1:S14 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1982				HR1981 (Lo-16bits)
HR1983	RTD1:S14 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1984				HR1983 (Lo-16bits)
HR1985	RTD1:S15 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1986				HR1985 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR1987	RTD1:S15 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1988				HR1987 (Lo-16bits)
HR1989	RTD1:S15 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1990				HR1989 (Lo-16bits)
HR1991	RTD1:S16 Alarm1 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1992				HR1991 (Lo-16bits)
HR1993	RTD1:S16 Alarm2 On	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1994				HR1993 (Lo-16bits)
HR1995	RTD1:S16 Meas Invalid	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR1996				HR1995 (Lo-16bits)
HR1997	RTDIN:S1 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR1998				HR1997 (Lo-16bits)
HR1999	RTDIN:S2 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2000				HR1999 (Lo-16bits)
HR2001	RTDIN:S3 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2002				HR2001 (Lo-16bits)
HR2003	RTDIN:S4 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2004				HR2003 (Lo-16bits)
HR2005	RTDIN:S5 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2006				HR2005 (Lo-16bits)
HR2007	RTDIN:S6 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2008				HR2007 (Lo-16bits)
HR2009	RTDIN:S7 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2010				HR2009 (Lo-16bits)
HR2011	RTDIN:S8 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2012				HR2011 (Lo-16bits)
HR2013	RTDIN:S9 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2014				HR2013 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2015	RTDIN:S10 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2016				HR2015 (Lo-16bits)
HR2017	RTDIN:S11 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2018				HR2017 (Lo-16bits)
HR2019	RTDIN:S12 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2020				HR2019 (Lo-16bits)
HR2021	RTDIN:S13 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2022				HR2021 (Lo-16bits)
HR2023	RTDIN:S14 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2024				HR2023 (Lo-16bits)
HR2025	RTDIN:S15 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2026				HR2025 (Lo-16bits)
HR2027	RTDIN:S16 Resistance	R	2	FLOAT [0...4000] (Hi-16bits)
HR2028				HR2027 (Lo-16bits)
HR2029	GSYN:Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR2030				HR2029 (Lo-16bits)
HR2031	GSYN:Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR2032				HR2031 (Lo-16bits)
HR2033	GSYN:Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR2034				HR2033 (Lo-16bits)
HR2035	GMAG:Read MVar Ref from	R(W)	1	0=Value from comm Bus 1=Value from AI
HR2036	GMAG:MVar Ref selection	R(W)	1	0=Scaled input 1 1=Scaled input 2 2=Scaled input 3 3=Scaled input 4
HR2037	GMAG:MVar Ref change threshold (AI)	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR2038				HR2037 (Lo-16bits)
HR2039	GMAG:Update MVar Ref automatically when threshold exceeds	R(W)	1	0=No 1=Yes
HR2040	PWR1:PQS>/< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2041	PWR2:PQS>>/<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2042	PWR3:PQS>>>/<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR2043	PWR4:PQS>>>>/<<<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR2044	CIN6:Ind.6 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2045	CIN7:Ind.7 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2046	CIN8:Ind.8 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2047	CIN9:Ind.9 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2048	CIN10:Ind.10 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2049	CIN11:Ind.11 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2050	CIN12:Ind.12 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2051	CIN13:Ind.13 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2052	CIN14:Ind.14 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2053	CIN15:Ind.15 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2054	CIN16:Ind.16 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2055	CIN17:Ind.17 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2056	CIN18:Ind.18 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2057	CIN19:Ind.19 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2058	CIN20:Ind.20 Object Status	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR2059	RHC1:Counter is now	R	1	0=Idle 1=Counting
HR2060	RHC1:Running hours	R(W)	2	UINT32 [0...4294967295] (Hi-16bits)
HR2061				HR2060 (Lo-16bits)
HR2062	GST1:Generating time (Running hours)	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR2063				HR2062 (Lo-16bits)
HR2064	NOC1:l> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2065	NOC1:l> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2066	NOC1:l> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2067				HR2066 (Lo-16bits)
HR2068	NOC1:l> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2069				HR2068 (Lo-16bits)
HR2070	NOC1:l> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2071				HR2070 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2072	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2073				HR2072 (Lo-16bits)
HR2074	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2075	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2076				HR2075 (Lo-16bits)
HR2077	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2078	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2079	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2080	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2081	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2082				HR2081 (Lo-16bits)
HR2083	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2084				HR2083 (Lo-16bits)
HR2085	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2086				HR2085 (Lo-16bits)
HR2087	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2088				HR2087 (Lo-16bits)
HR2089	NOC1:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2090	NOC1:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2091	NOC2:I>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2092	NOC2:I>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2093	NOC2:I>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2094				HR2093 (Lo-16bits)
HR2095	NOC2:I>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2096				HR2095 (Lo-16bits)
HR2097	NOC2:I>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2098				HR2097 (Lo-16bits)
HR2099	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2100				HR2099 (Lo-16bits)
HR2101	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2102	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2103				HR2102 (Lo-16bits)
HR2104	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2105	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2106	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2107	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2108	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2109				HR2108 (Lo-16bits)
HR2110	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2111				HR2110 (Lo-16bits)
HR2112	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2113				HR2112 (Lo-16bits)
HR2114	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2115				HR2114 (Lo-16bits)
HR2116	NOC2:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2117	NOC2:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2118	NOC3: >>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2119	NOC3: >>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2120	NOC3: >>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2121				HR2120 (Lo-16bits)
HR2122	NOC3: >>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2123				HR2122 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2124	NOC3:I>>>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2125				HR2124 (Lo-16bits)
HR2126	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2127				HR2126 (Lo-16bits)
HR2128	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR2129	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2130				HR2129 (Lo-16bits)
HR2131	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2132	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2133	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2134	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2135	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2136				HR2135 (Lo-16bits)
HR2137	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2138				HR2137 (Lo-16bits)
HR2139	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2140				HR2139 (Lo-16bits)
HR2141	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2142				HR2141 (Lo-16bits)
HR2143	NOC3:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2144	NOC3:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2145	NOC4:I>>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2146	NOC4:I>>>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2147	NOC4:I>>>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2148				HR2147 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2149	NOC4:Idir>>>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2150				HR2149 (Lo-16bits)
HR2151	NOC4:Idir>>>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2152				HR2151 (Lo-16bits)
HR2153	NOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2154				HR2153 (Lo-16bits)
HR2155	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR2156	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2157				HR2156 (Lo-16bits)
HR2158	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2159	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2160	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2161	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2162	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2163				HR2162 (Lo-16bits)
HR2164	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2165				HR2164 (Lo-16bits)
HR2166	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2167				HR2166 (Lo-16bits)
HR2168	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2169				HR2168 (Lo-16bits)
HR2170	NOC4:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2171	NOC4:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2172	DOC1:Idir> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2173	DOC1:Idir> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2174	DOC1:Idir> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2175				HR2174 (Lo-16bits)
HR2176	DOC1:ldir> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2177				HR2176 (Lo-16bits)
HR2178	DOC1:ldir> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2179				HR2178 (Lo-16bits)
HR2180	DOC1:ldir> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2181				HR2180 (Lo-16bits)
HR2182	DOC2:ldir>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2183	DOC2:ldir>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2184	DOC2:ldir>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2185				HR2184 (Lo-16bits)
HR2186	DOC2:ldir>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2187				HR2186 (Lo-16bits)
HR2188	DOC2:ldir>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2189				HR2188 (Lo-16bits)
HR2190	DOC2:ldir>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2191				HR2190 (Lo-16bits)
HR2192	DOC3:ldir>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2193	DOC3:ldir>>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2194	DOC3:ldir>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2195				HR2194 (Lo-16bits)
HR2196	DOC3:ldir>>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2197				HR2196 (Lo-16bits)
HR2198	DOC3:ldir>>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2199				HR2198 (Lo-16bits)
HR2200	DOC3:ldir>>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2201				HR2200 (Lo-16bits)
HR2202	DOC4:ldir>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2203	DOC4:ldir>>> Latest fault type	R	1	0=- 1=A-G 2=B-G 3=A-B 4=C-G 5=A-C 6=B-C 7=A-B-C
HR2204	DOC4:ldir>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2205				HR2204 (Lo-16bits)
HR2206	DOC4:ldir>>> Latest fault IA current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2207				HR2206 (Lo-16bits)
HR2208	DOC4:ldir>>> Latest fault IB current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2209				HR2208 (Lo-16bits)
HR2210	DOC4:ldir>>> Latest fault IC current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2211				HR2210 (Lo-16bits)
HR2212	NEF1:I0> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2213	NEF1:I0> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2214				HR2213 (Lo-16bits)
HR2215	NEF1:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2216				HR2215 (Lo-16bits)
HR2217	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR2218	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2219				HR2218 (Lo-16bits)
HR2220	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2221	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2222	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2223	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2224	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2225				HR2224 (Lo-16bits)
HR2226	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2227				HR2226 (Lo-16bits)
HR2228	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2229				HR2228 (Lo-16bits)
HR2230	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2231				HR2230 (Lo-16bits)
HR2232	NEF1:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2233	NEF1:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2234	NEF2:I0>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2235	NEF2:I0>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2236				HR2235 (Lo-16bits)
HR2237	NEF2:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2238				HR2237 (Lo-16bits)
HR2239	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR2240	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2241				HR2240 (Lo-16bits)
HR2242	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2243	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2244	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2245	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2246	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2247				HR2246 (Lo-16bits)
HR2248	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2249				HR2248 (Lo-16bits)
HR2250	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2251				HR2250 (Lo-16bits)
HR2252	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2253				HR2252 (Lo-16bits)
HR2254	NEF2:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2255	NEF2:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2256	NEF3:I0>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2257	NEF3:I0>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2258				HR2257 (Lo-16bits)
HR2259	NEF3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2260				HR2259 (Lo-16bits)
HR2261	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR2262	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2263				HR2262 (Lo-16bits)
HR2264	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2265	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2266	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2267	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2268	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2269				HR2268 (Lo-16bits)
HR2270	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2271				HR2270 (Lo-16bits)
HR2272	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2273				HR2272 (Lo-16bits)
HR2274	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2275				HR2274 (Lo-16bits)
HR2276	NEF3:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2277	NEF3:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2278	NEF4:I0>>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip

Holding Register	Parameter	Access	Length	Info
HR2279	NEF4:I0>>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2280				HR2279 (Lo-16bits)
HR2281	NEF4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2282				HR2281 (Lo-16bits)
HR2283	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR2284	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2285				HR2284 (Lo-16bits)
HR2286	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2287	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2288	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2289	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2290	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2291				HR2290 (Lo-16bits)
HR2292	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2293				HR2292 (Lo-16bits)
HR2294	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2295				HR2294 (Lo-16bits)
HR2296	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2297				HR2296 (Lo-16bits)
HR2298	NEF4:Select setting group	R(W)	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8 8=Edit Active SG
HR2299	NEF4:Write settings to selected SG	R(W)	1	0=- 1=Write (autoreset)
HR2300	DEF1:I0dir> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2301	DEF1:I0dir> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2302				HR2301 (Lo-16bits)
HR2303	DEF1:I0dir> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2304				HR2303 (Lo-16bits)
HR2305	DEF1:I0dir> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2306				HR2305 (Lo-16bits)
HR2307	DEF2:I0dir>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2308	DEF2:I0dir>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2309				HR2308 (Lo-16bits)
HR2310	DEF2:I0dir>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2311				HR2310 (Lo-16bits)
HR2312	DEF2:I0dir>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2313				HR2312 (Lo-16bits)
HR2314	DEF3:I0dir>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2315	DEF3:I0dir>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2316				HR2315 (Lo-16bits)
HR2317	DEF3:I0dir>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2318				HR2317 (Lo-16bits)
HR2319	DEF3:I0dir>>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2320				HR2319 (Lo-16bits)
HR2321	DEF4:I0dir>>>> Latest fault ended with	R	1	0=No Faults 1=Start 2=Trip
HR2322	DEF4:I0dir>>>> Latest fault angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR2323				HR2322 (Lo-16bits)
HR2324	DEF4:I0dir>>>> Latest fault I0 current (pri)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2325				HR2324 (Lo-16bits)
HR2326	DEF4:I0dir>>>> Latest fault U0(%)	R	2	FLOAT [0...1000000] (Hi-16bits)
HR2327				HR2326 (Lo-16bits)
HR2328	GMAG:MVar Ref value (kVar) (remote)	R(W)	2	FLOAT [-1000...1000] (Hi-16bits)
HR2329				HR2328 (Lo-16bits)
HR2330	GMAG:Mode FCR (remote)	R	1	0=Off 1=On
HR2331	GMAG:Mode AVR (remote)	R	1	0=Off 1=On

Holding Register	Parameter	Access	Length	Info
HR2332	ALARM:Alarms 65-96	R	2	0b=A65 1b=A66 2b=A67 3b=A68 4b=A69 5b=A70 6b=A71 7b=A72 8b=A73 9b=A74 10b=A75 11b=A76 12b=A77 13b=A78 14b=A79 15b=A80 16b=A81 17b=A82 18b=A83 19b=A84 20b=A85 21b=A86 22b=A87 23b=A88 24b=A89 25b=A90 26b=A91 27b=A92 28b=A93 29b=A94 30b=A95 31b=A96 (Hi-16bits)
HR2333				HR2332 (Lo-16bits)
HR2334	ALARM:Alarms 97-128	R	2	0b=A97 1b=A98 2b=A99 3b=A100 4b=A101 5b=A102 6b=A103 7b=A104 8b=A105 9b=A106 10b=A107 11b=A108 12b=A109 13b=A110 14b=A111 15b=A112 16b=A113 17b=A114 18b=A115 19b=A116 20b=A117 21b=A118 22b=A119 23b=A120 24b=A121 25b=A122 26b=A123 27b=A124 28b=A125 29b=A126 30b=A127 31b=A128 (Hi-16bits)
HR2335				HR2334 (Lo-16bits)
HR2336	VUB1:U1/2 >/< Measurement condition	R	1	0=Seq Volt available 1=No Seq volts available
HR2337	VUB2:U1/2 >>/<< Measurement condition	R	1	0=Seq Volt available 1=No Seq volts available
HR2338	VUB3:U1/2 >>>/<<< Measurement condition	R	1	0=Seq Volt available 1=No Seq volts available
HR2339	VUB4:U1/2 >>>>/<<<< Measurement condition	R	1	0=Seq Volt available 1=No Seq volts available
HR2340	VREC:Reset last fault values from memory	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR2341				HR2340 (Lo-16bits)
HR2342	NOC1:l> phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Trip AB 11=Trip BC 12=Trip CA 13=Start ABC 14=Trip ABC
HR2343	NOC2:l>> phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Trip AB 11=Trip BC 12=Trip CA 13=Start ABC 14=Trip ABC
HR2344	NOC3:l>>> phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Trip AB 11=Trip BC 12=Trip CA 13=Start ABC 14=Trip ABC

Holding Register	Parameter	Access	Length	Info
HR2345	NOC4:I>>>> phases condition	R	1	0=Normal 1=Start A 2=Start B 3=Start C 4=Trip A 5=Trip B 6=Trip C 7=Start AB 8=Start BC 9=Start CA 10=Trip AB 11=Trip BC 12=Trip CA 13=Start ABC 14=Trip ABC
HR2346	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2347				HR2346 (Lo-16bits)
HR2348	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2349				HR2348 (Lo-16bits)
HR2350	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2351				HR2350 (Lo-16bits)
HR2352	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2353				HR2352 (Lo-16bits)
HR2354	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2355				HR2354 (Lo-16bits)
HR2356	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2357				HR2356 (Lo-16bits)
HR2358	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2359				HR2358 (Lo-16bits)
HR2360	VRG1:Target voltage (UTGT)	R(W)	2	FLOAT [70...140] (Hi-16bits)
HR2361				HR2360 (Lo-16bits)
HR2362	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2363				HR2362 (Lo-16bits)
HR2364	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2365				HR2364 (Lo-16bits)
HR2366	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2367				HR2366 (Lo-16bits)
HR2368	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2369				HR2368 (Lo-16bits)
HR2370	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2371				HR2370 (Lo-16bits)
HR2372	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2373				HR2372 (Lo-16bits)
HR2374	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2375				HR2374 (Lo-16bits)
HR2376	VRG1:U> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2377				HR2376 (Lo-16bits)
HR2378	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2379				HR2378 (Lo-16bits)
HR2380	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2381				HR2380 (Lo-16bits)
HR2382	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2383				HR2382 (Lo-16bits)
HR2384	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2385				HR2384 (Lo-16bits)
HR2386	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2387				HR2386 (Lo-16bits)
HR2388	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2389				HR2388 (Lo-16bits)
HR2390	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2391				HR2390 (Lo-16bits)
HR2392	VRG1:U< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2393				HR2392 (Lo-16bits)
HR2394	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2395				HR2394 (Lo-16bits)
HR2396	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2397				HR2396 (Lo-16bits)
HR2398	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2399				HR2398 (Lo-16bits)
HR2400	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2401				HR2400 (Lo-16bits)
HR2402	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2403				HR2402 (Lo-16bits)
HR2404	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2405				HR2404 (Lo-16bits)
HR2406	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2407				HR2406 (Lo-16bits)
HR2408	VRG1:U>/< T1 time delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2409				HR2408 (Lo-16bits)
HR2410	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2411				HR2410 (Lo-16bits)
HR2412	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2413				HR2412 (Lo-16bits)
HR2414	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2415				HR2414 (Lo-16bits)
HR2416	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2417				HR2416 (Lo-16bits)
HR2418	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2419				HR2418 (Lo-16bits)
HR2420	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2421				HR2420 (Lo-16bits)
HR2422	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2423				HR2422 (Lo-16bits)
HR2424	VRG1:U>> setting (+UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2425				HR2424 (Lo-16bits)
HR2426	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2427				HR2426 (Lo-16bits)
HR2428	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2429				HR2428 (Lo-16bits)
HR2430	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2431				HR2430 (Lo-16bits)
HR2432	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2433				HR2432 (Lo-16bits)
HR2434	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2435				HR2434 (Lo-16bits)
HR2436	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2437				HR2436 (Lo-16bits)
HR2438	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2439				HR2438 (Lo-16bits)
HR2440	VRG1:U<< setting (-UTGT)	R(W)	2	FLOAT [0.1...30] (Hi-16bits)
HR2441				HR2440 (Lo-16bits)
HR2442	MEAS2:MEAS2 IL ang. Ref	R	2	FLOAT [-360...360] (Hi-16bits)
HR2443				HR2442 (Lo-16bits)
HR2444	IMERX:IL1 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2445				HR2444 (Lo-16bits)
HR2446	IMERX:IL1 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2447				HR2446 (Lo-16bits)
HR2448	IMERX:IL2 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2449				HR2448 (Lo-16bits)
HR2450	IMERX:IL2 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2451				HR2450 (Lo-16bits)
HR2452	IMERX:IL3 Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2453				HR2452 (Lo-16bits)
HR2454	IMERX:IL3 Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2455				HR2454 (Lo-16bits)
HR2456	IMERX:Positive sequence Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2457				HR2456 (Lo-16bits)
HR2458	IMERX:Positive sequence Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2459				HR2458 (Lo-16bits)
HR2460	IMERX:I01 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2461				HR2460 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2462	IMERX:I01 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2463				HR2462 (Lo-16bits)
HR2464	IMERX:I02 Residual Resistive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2465				HR2464 (Lo-16bits)
HR2466	IMERX:I02 Residual Reactive Current Pri.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2467				HR2466 (Lo-16bits)
HR2468	IMERX:I01 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2469				HR2468 (Lo-16bits)
HR2470	IMERX:I01 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2471				HR2470 (Lo-16bits)
HR2472	IMERX:I02 Residual Resistive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2473				HR2472 (Lo-16bits)
HR2474	IMERX:I02 Residual Reactive Current Sec.	R	2	FLOAT [-100000...100000] (Hi-16bits)
HR2475				HR2474 (Lo-16bits)
HR2476	POW1:3PH Power factor	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2477				HR2476 (Lo-16bits)
HR2478	POW2:Export Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2479				HR2478 (Lo-16bits)
HR2480	POW2:Export Active Energy (P) kWh	R(W)	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2481				HR2480 (Lo-16bits)
HR2482	POW2:Import Active Energy (P) MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2483				HR2482 (Lo-16bits)
HR2484	POW2:Import Active Energy (P) kWh	R(W)	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2485				HR2484 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2486	POW2:Active Energy (P) Export/ Import balance MWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2487				HR2486 (Lo-16bits)
HR2488	POW2:Active Energy (P) Export/ Import balance kWh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2489				HR2488 (Lo-16bits)
HR2490	POW2:Imported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2491				HR2490 (Lo-16bits)
HR2492	POW2:Imported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2493				HR2492 (Lo-16bits)
HR2494	POW2:Imported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2495				HR2494 (Lo-16bits)
HR2496	POW2:Imported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2497				HR2496 (Lo-16bits)
HR2498	POW2:Reactive Energy (Q) balance while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2499				HR2498 (Lo-16bits)
HR2500	POW2:Reactive Energy (Q) balance while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2501				HR2500 (Lo-16bits)
HR2502	POW2:Exported (Q) while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2503				HR2502 (Lo-16bits)
HR2504	POW2:Exported (Q) while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2505				HR2504 (Lo-16bits)
HR2506	POW2:Exported (Q) while Import (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2507				HR2506 (Lo-16bits)
HR2508	POW2:Exported (Q) while Import (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2509				HR2508 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2510	POW2:Reactive Energy (Q) balance while Export (P) MVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2511				HR2510 (Lo-16bits)
HR2512	POW2:Reactive Energy (Q) balance while Export (P) kVarh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR2513				HR2512 (Lo-16bits)
HR2514	POW2:Reset 3ph energies	R(W)	1	0=- 1=Reset
HR2515	POW2:POW2 3PH Apparent power (S)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2516				HR2515 (Lo-16bits)
HR2517	POW2:POW2 3PH Apparent power (S MVA)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2518				HR2517 (Lo-16bits)
HR2519	POW2:POW2 3PH Active power (P)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2520				HR2519 (Lo-16bits)
HR2521	POW2:POW2 3PH Active power (P MW)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2522				HR2521 (Lo-16bits)
HR2523	POW2:POW2 3PH Reactive power (Q)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2524				HR2523 (Lo-16bits)
HR2525	POW2:POW2 3PH Reactive power (QMVar)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2526				HR2525 (Lo-16bits)
HR2527	POW2:POW2 3PH Tan(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2528				HR2527 (Lo-16bits)
HR2529	POW2:POW2 3PH Cos(phi)	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2530				HR2529 (Lo-16bits)
HR2531	POW2:3PH Power factor	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR2532				HR2531 (Lo-16bits)
HR2533	LOGIC1:Logic out 33-64	R(W)	2	UINT32 [0...4294967295] (Hi-16bits)
HR2534				HR2533 (Lo-16bits)
HR2535	RTDIN:S1 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2536				HR2535 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2537	RTDIN:S2 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2538				HR2537 (Lo-16bits)
HR2539	RTDIN:S3 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2540				HR2539 (Lo-16bits)
HR2541	RTDIN:S4 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2542				HR2541 (Lo-16bits)
HR2543	RTDIN:S5 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2544				HR2543 (Lo-16bits)
HR2545	RTDIN:S6 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2546				HR2545 (Lo-16bits)
HR2547	RTDIN:S7 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2548				HR2547 (Lo-16bits)
HR2549	RTDIN:S8 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2550				HR2549 (Lo-16bits)
HR2551	RTDIN:S9 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2552				HR2551 (Lo-16bits)
HR2553	RTDIN:S10 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2554				HR2553 (Lo-16bits)
HR2555	RTDIN:S11 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2556				HR2555 (Lo-16bits)
HR2557	RTDIN:S12 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2558				HR2557 (Lo-16bits)
HR2559	RTDIN:S13 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2560				HR2559 (Lo-16bits)
HR2561	RTDIN:S14 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2562				HR2561 (Lo-16bits)
HR2563	RTDIN:S15 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2564				HR2563 (Lo-16bits)
HR2565	RTDIN:S16 Filtered measurement	R	2	FLOAT [-200...2000] (Hi-16bits)
HR2566				HR2565 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2567	SYN1:SYN1 Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR2568				HR2567 (Lo-16bits)
HR2569	SYN1:SYN1 Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR2570				HR2569 (Lo-16bits)
HR2571	SYN1:SYN1 Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR2572				HR2571 (Lo-16bits)
HR2573	GSYN:GSYN Nets Standstill	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2574				HR2573 (Lo-16bits)
HR2575	GSYN:GSYN Nets Departing	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2576				HR2575 (Lo-16bits)
HR2577	GSYN:GSYN Nets Enclosing	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2578				HR2577 (Lo-16bits)
HR2579	PCS1:PCS1 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2580				HR2579 (Lo-16bits)
HR2581	PCS2:PCS2 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2582				HR2581 (Lo-16bits)
HR2583	PCS3:PCS3 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2584				HR2583 (Lo-16bits)
HR2585	PCS4:PCS4 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2586				HR2585 (Lo-16bits)
HR2587	PCS5:PCS5 Switch Status	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR2588				HR2587 (Lo-16bits)
HR2589	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2590				HR2589 (Lo-16bits)
HR2591	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2592				HR2591 (Lo-16bits)
HR2593	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2594				HR2593 (Lo-16bits)
HR2595	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2596				HR2595 (Lo-16bits)
HR2597	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2598				HR2597 (Lo-16bits)
HR2599	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2600				HR2599 (Lo-16bits)
HR2601	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2602				HR2601 (Lo-16bits)
HR2603	NOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2604				HR2603 (Lo-16bits)
HR2605	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2606	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2607	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2608	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2609	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2610	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2611	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2612	NOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR2613	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2614				HR2613 (Lo-16bits)
HR2615	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2616				HR2615 (Lo-16bits)
HR2617	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2618				HR2617 (Lo-16bits)
HR2619	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2620				HR2619 (Lo-16bits)
HR2621	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2622				HR2621 (Lo-16bits)
HR2623	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2624				HR2623 (Lo-16bits)
HR2625	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2626				HR2625 (Lo-16bits)
HR2627	NOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2628				HR2627 (Lo-16bits)
HR2629	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2630	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2631	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2632	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2633	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2634	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2635	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2636	NOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2637	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2638	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2639	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2640	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2641	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2642	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2643	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2644	NOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2645	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2646	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR2647	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2648	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2649	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2650	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2651	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2652	NOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2653	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2654	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2655	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2656	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2657	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2658	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2659	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2660	NOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2661	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2662				HR2661 (Lo-16bits)
HR2663	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2664				HR2663 (Lo-16bits)
HR2665	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2666				HR2665 (Lo-16bits)
HR2667	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2668				HR2667 (Lo-16bits)
HR2669	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2670				HR2669 (Lo-16bits)
HR2671	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2672				HR2671 (Lo-16bits)
HR2673	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2674				HR2673 (Lo-16bits)
HR2675	NOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2676				HR2675 (Lo-16bits)
HR2677	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2678				HR2677 (Lo-16bits)
HR2679	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2680				HR2679 (Lo-16bits)
HR2681	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2682				HR2681 (Lo-16bits)
HR2683	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2684				HR2683 (Lo-16bits)
HR2685	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2686				HR2685 (Lo-16bits)
HR2687	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2688				HR2687 (Lo-16bits)
HR2689	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2690				HR2689 (Lo-16bits)
HR2691	NOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2692				HR2691 (Lo-16bits)
HR2693	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2694				HR2693 (Lo-16bits)
HR2695	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2696				HR2695 (Lo-16bits)
HR2697	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2698				HR2697 (Lo-16bits)
HR2699	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2700				HR2699 (Lo-16bits)
HR2701	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2702				HR2701 (Lo-16bits)
HR2703	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2704				HR2703 (Lo-16bits)
HR2705	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2706				HR2705 (Lo-16bits)
HR2707	NOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2708				HR2707 (Lo-16bits)
HR2709	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2710				HR2709 (Lo-16bits)
HR2711	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2712				HR2711 (Lo-16bits)
HR2713	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2714				HR2713 (Lo-16bits)
HR2715	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2716				HR2715 (Lo-16bits)
HR2717	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2718				HR2717 (Lo-16bits)
HR2719	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2720				HR2719 (Lo-16bits)
HR2721	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2722				HR2721 (Lo-16bits)
HR2723	NOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2724				HR2723 (Lo-16bits)
HR2725	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2726	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2727	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2728	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2729	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2730	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2731	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2732	NOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2733	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2734				HR2733 (Lo-16bits)
HR2735	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2736				HR2735 (Lo-16bits)
HR2737	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2738				HR2737 (Lo-16bits)
HR2739	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2740				HR2739 (Lo-16bits)
HR2741	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2742				HR2741 (Lo-16bits)
HR2743	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2744				HR2743 (Lo-16bits)
HR2745	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2746				HR2745 (Lo-16bits)
HR2747	NOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2748				HR2747 (Lo-16bits)
HR2749	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2750	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2751	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2752	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2753	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2754	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2755	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2756	NOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2757	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2758				HR2757 (Lo-16bits)
HR2759	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2760				HR2759 (Lo-16bits)
HR2761	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2762				HR2761 (Lo-16bits)
HR2763	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2764				HR2763 (Lo-16bits)
HR2765	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2766				HR2765 (Lo-16bits)
HR2767	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2768				HR2767 (Lo-16bits)
HR2769	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2770				HR2769 (Lo-16bits)
HR2771	NOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2772				HR2771 (Lo-16bits)
HR2773	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2774	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2775	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2776	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2777	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2778	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2779	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2780	NOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2781	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2782	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2783	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2784	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2785	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2786	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2787	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2788	NOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2789	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2790				HR2789 (Lo-16bits)
HR2791	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2792				HR2791 (Lo-16bits)
HR2793	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2794				HR2793 (Lo-16bits)
HR2795	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2796				HR2795 (Lo-16bits)
HR2797	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2798				HR2797 (Lo-16bits)
HR2799	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2800				HR2799 (Lo-16bits)
HR2801	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2802				HR2801 (Lo-16bits)
HR2803	NOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2804				HR2803 (Lo-16bits)
HR2805	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2806	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2807	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2808	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2809	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2810	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2811	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2812	NOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR2813	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2814				HR2813 (Lo-16bits)
HR2815	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2816				HR2815 (Lo-16bits)
HR2817	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2818				HR2817 (Lo-16bits)
HR2819	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2820				HR2819 (Lo-16bits)
HR2821	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2822				HR2821 (Lo-16bits)
HR2823	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2824				HR2823 (Lo-16bits)
HR2825	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2826				HR2825 (Lo-16bits)
HR2827	NOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR2828				HR2827 (Lo-16bits)
HR2829	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR2830	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2831	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2832	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2833	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2834	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2835	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2836	NOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR2837	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2838	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2839	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2840	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2841	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2842	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2843	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2844	NOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR2845	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2846	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2847	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2848	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2849	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2850	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2851	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2852	NOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR2853	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2854	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR2855	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2856	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2857	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2858	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2859	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2860	NOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR2861	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2862				HR2861 (Lo-16bits)
HR2863	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2864				HR2863 (Lo-16bits)
HR2865	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2866				HR2865 (Lo-16bits)
HR2867	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2868				HR2867 (Lo-16bits)
HR2869	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2870				HR2869 (Lo-16bits)
HR2871	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2872				HR2871 (Lo-16bits)
HR2873	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2874				HR2873 (Lo-16bits)
HR2875	NOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR2876				HR2875 (Lo-16bits)
HR2877	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2878				HR2877 (Lo-16bits)
HR2879	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2880				HR2879 (Lo-16bits)
HR2881	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2882				HR2881 (Lo-16bits)
HR2883	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2884				HR2883 (Lo-16bits)
HR2885	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2886				HR2885 (Lo-16bits)
HR2887	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2888				HR2887 (Lo-16bits)
HR2889	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2890				HR2889 (Lo-16bits)
HR2891	NOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2892				HR2891 (Lo-16bits)
HR2893	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2894				HR2893 (Lo-16bits)
HR2895	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2896				HR2895 (Lo-16bits)
HR2897	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2898				HR2897 (Lo-16bits)
HR2899	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2900				HR2899 (Lo-16bits)
HR2901	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2902				HR2901 (Lo-16bits)
HR2903	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2904				HR2903 (Lo-16bits)
HR2905	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2906				HR2905 (Lo-16bits)
HR2907	NOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR2908				HR2907 (Lo-16bits)
HR2909	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2910				HR2909 (Lo-16bits)
HR2911	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2912				HR2911 (Lo-16bits)
HR2913	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2914				HR2913 (Lo-16bits)
HR2915	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2916				HR2915 (Lo-16bits)
HR2917	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2918				HR2917 (Lo-16bits)
HR2919	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2920				HR2919 (Lo-16bits)
HR2921	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2922				HR2921 (Lo-16bits)
HR2923	NOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR2924				HR2923 (Lo-16bits)
HR2925	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2926	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2927	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2928	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2929	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2930	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2931	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2932	NOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR2933	NOC2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2934				HR2933 (Lo-16bits)
HR2935	NOC2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2936				HR2935 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR2937	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2938				HR2937 (Lo-16bits)
HR2939	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2940				HR2939 (Lo-16bits)
HR2941	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2942				HR2941 (Lo-16bits)
HR2943	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2944				HR2943 (Lo-16bits)
HR2945	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2946				HR2945 (Lo-16bits)
HR2947	NOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2948				HR2947 (Lo-16bits)
HR2949	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2950	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2951	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2952	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2953	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2954	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2955	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2956	NOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR2957	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2958				HR2957 (Lo-16bits)
HR2959	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2960				HR2959 (Lo-16bits)
HR2961	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2962				HR2961 (Lo-16bits)
HR2963	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR2964				HR2963 (Lo-16bits)
HR2965	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2966				HR2965 (Lo-16bits)
HR2967	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2968				HR2967 (Lo-16bits)
HR2969	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2970				HR2969 (Lo-16bits)
HR2971	NOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR2972				HR2971 (Lo-16bits)
HR2973	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2974	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2975	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2976	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2977	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2978	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2979	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2980	NOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR2981	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2982	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2983	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2984	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2985	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2986	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR2987	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2988	NOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR2989	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2990				HR2989 (Lo-16bits)
HR2991	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2992				HR2991 (Lo-16bits)
HR2993	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2994				HR2993 (Lo-16bits)
HR2995	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2996				HR2995 (Lo-16bits)
HR2997	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR2998				HR2997 (Lo-16bits)
HR2999	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3000				HR2999 (Lo-16bits)
HR3001	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3002				HR3001 (Lo-16bits)
HR3003	NOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3004				HR3003 (Lo-16bits)
HR3005	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3006	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3007	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3008	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3009	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3010	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3011	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3012	NOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3013	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3014				HR3013 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3015	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3016				HR3015 (Lo-16bits)
HR3017	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3018				HR3017 (Lo-16bits)
HR3019	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3020				HR3019 (Lo-16bits)
HR3021	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3022				HR3021 (Lo-16bits)
HR3023	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3024				HR3023 (Lo-16bits)
HR3025	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3026				HR3025 (Lo-16bits)
HR3027	NOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3028				HR3027 (Lo-16bits)
HR3029	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3030	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3031	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3032	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3033	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3034	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3035	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3036	NOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3037	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3038	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3039	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3040	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3041	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR3042	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3043	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3044	NOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3045	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3046	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3047	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3048	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3049	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3050	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3051	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3052	NOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3053	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3054	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3055	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3056	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3057	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3058	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3059	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3060	NOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3061	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3062				HR3061 (Lo-16bits)
HR3063	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3064				HR3063 (Lo-16bits)
HR3065	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3066				HR3065 (Lo-16bits)
HR3067	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3068				HR3067 (Lo-16bits)
HR3069	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3070				HR3069 (Lo-16bits)
HR3071	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3072				HR3071 (Lo-16bits)
HR3073	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3074				HR3073 (Lo-16bits)
HR3075	NOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3076				HR3075 (Lo-16bits)
HR3077	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3078				HR3077 (Lo-16bits)
HR3079	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3080				HR3079 (Lo-16bits)
HR3081	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3082				HR3081 (Lo-16bits)
HR3083	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3084				HR3083 (Lo-16bits)
HR3085	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3086				HR3085 (Lo-16bits)
HR3087	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3088				HR3087 (Lo-16bits)
HR3089	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3090				HR3089 (Lo-16bits)
HR3091	NOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3092				HR3091 (Lo-16bits)
HR3093	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3094				HR3093 (Lo-16bits)
HR3095	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3096				HR3095 (Lo-16bits)
HR3097	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3098				HR3097 (Lo-16bits)
HR3099	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3100				HR3099 (Lo-16bits)
HR3101	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3102				HR3101 (Lo-16bits)
HR3103	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3104				HR3103 (Lo-16bits)
HR3105	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3106				HR3105 (Lo-16bits)
HR3107	NOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3108				HR3107 (Lo-16bits)
HR3109	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3110				HR3109 (Lo-16bits)
HR3111	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3112				HR3111 (Lo-16bits)
HR3113	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3114				HR3113 (Lo-16bits)
HR3115	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3116				HR3115 (Lo-16bits)
HR3117	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3118				HR3117 (Lo-16bits)
HR3119	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3120				HR3119 (Lo-16bits)
HR3121	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3122				HR3121 (Lo-16bits)
HR3123	NOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3124				HR3123 (Lo-16bits)
HR3125	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3126	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3127	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3128	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3129	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3130	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3131	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3132	NOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3133	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3134				HR3133 (Lo-16bits)
HR3135	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3136				HR3135 (Lo-16bits)
HR3137	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3138				HR3137 (Lo-16bits)
HR3139	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3140				HR3139 (Lo-16bits)
HR3141	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3142				HR3141 (Lo-16bits)
HR3143	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3144				HR3143 (Lo-16bits)
HR3145	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3146				HR3145 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3147	NOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3148				HR3147 (Lo-16bits)
HR3149	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3150	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3151	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3152	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3153	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3154	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3155	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3156	NOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3157	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3158				HR3157 (Lo-16bits)
HR3159	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3160				HR3159 (Lo-16bits)
HR3161	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3162				HR3161 (Lo-16bits)
HR3163	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3164				HR3163 (Lo-16bits)
HR3165	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3166				HR3165 (Lo-16bits)
HR3167	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3168				HR3167 (Lo-16bits)
HR3169	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3170				HR3169 (Lo-16bits)
HR3171	NOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3172				HR3171 (Lo-16bits)
HR3173	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3174	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3175	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3176	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3177	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3178	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3179	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3180	NOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3181	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3182	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3183	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3184	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3185	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3186	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3187	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3188	NOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3189	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3190				HR3189 (Lo-16bits)
HR3191	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3192				HR3191 (Lo-16bits)
HR3193	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3194				HR3193 (Lo-16bits)
HR3195	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3196				HR3195 (Lo-16bits)
HR3197	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3198				HR3197 (Lo-16bits)
HR3199	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3200				HR3199 (Lo-16bits)
HR3201	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3202				HR3201 (Lo-16bits)
HR3203	NOC4:Pick-up setting lset	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3204				HR3203 (Lo-16bits)
HR3205	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3206	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3207	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3208	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3209	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3210	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3211	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3212	NOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR3213	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3214				HR3213 (Lo-16bits)
HR3215	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3216				HR3215 (Lo-16bits)
HR3217	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3218				HR3217 (Lo-16bits)
HR3219	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3220				HR3219 (Lo-16bits)
HR3221	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3222				HR3221 (Lo-16bits)
HR3223	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3224				HR3223 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3225	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3226				HR3225 (Lo-16bits)
HR3227	NOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3228				HR3227 (Lo-16bits)
HR3229	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3230	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3231	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3232	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3233	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3234	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3235	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3236	NOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3237	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3238	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3239	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3240	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3241	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3242	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3243	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3244	NOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3245	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3246	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3247	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3248	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3249	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3250	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3251	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3252	NOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3253	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3254	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3255	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3256	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3257	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3258	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3259	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3260	NOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3261	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3262				HR3261 (Lo-16bits)
HR3263	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3264				HR3263 (Lo-16bits)
HR3265	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3266				HR3265 (Lo-16bits)
HR3267	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3268				HR3267 (Lo-16bits)
HR3269	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3270				HR3269 (Lo-16bits)
HR3271	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3272				HR3271 (Lo-16bits)
HR3273	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3274				HR3273 (Lo-16bits)
HR3275	NOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3276				HR3275 (Lo-16bits)
HR3277	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3278				HR3277 (Lo-16bits)
HR3279	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3280				HR3279 (Lo-16bits)
HR3281	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3282				HR3281 (Lo-16bits)
HR3283	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3284				HR3283 (Lo-16bits)
HR3285	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3286				HR3285 (Lo-16bits)
HR3287	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3288				HR3287 (Lo-16bits)
HR3289	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3290				HR3289 (Lo-16bits)
HR3291	NOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3292				HR3291 (Lo-16bits)
HR3293	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3294				HR3293 (Lo-16bits)
HR3295	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3296				HR3295 (Lo-16bits)
HR3297	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3298				HR3297 (Lo-16bits)
HR3299	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3300				HR3299 (Lo-16bits)
HR3301	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3302				HR3301 (Lo-16bits)
HR3303	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3304				HR3303 (Lo-16bits)
HR3305	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3306				HR3305 (Lo-16bits)
HR3307	NOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3308				HR3307 (Lo-16bits)
HR3309	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3310				HR3309 (Lo-16bits)
HR3311	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3312				HR3311 (Lo-16bits)
HR3313	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3314				HR3313 (Lo-16bits)
HR3315	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3316				HR3315 (Lo-16bits)
HR3317	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3318				HR3317 (Lo-16bits)
HR3319	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3320				HR3319 (Lo-16bits)
HR3321	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3322				HR3321 (Lo-16bits)
HR3323	NOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3324				HR3323 (Lo-16bits)
HR3325	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3326	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3327	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3328	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3329	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3330	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3331	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3332	NOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3333	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3334				HR3333 (Lo-16bits)
HR3335	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3336				HR3335 (Lo-16bits)
HR3337	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3338				HR3337 (Lo-16bits)
HR3339	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3340				HR3339 (Lo-16bits)
HR3341	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3342				HR3341 (Lo-16bits)
HR3343	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3344				HR3343 (Lo-16bits)
HR3345	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3346				HR3345 (Lo-16bits)
HR3347	NOC4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3348				HR3347 (Lo-16bits)
HR3349	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3350	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3351	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3352	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3353	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3354	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3355	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3356	NOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3357	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3358				HR3357 (Lo-16bits)
HR3359	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3360				HR3359 (Lo-16bits)
HR3361	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3362				HR3361 (Lo-16bits)
HR3363	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3364				HR3363 (Lo-16bits)
HR3365	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3366				HR3365 (Lo-16bits)
HR3367	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3368				HR3367 (Lo-16bits)
HR3369	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3370				HR3369 (Lo-16bits)
HR3371	NOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3372				HR3371 (Lo-16bits)
HR3373	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3374	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3375	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3376	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3377	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3378	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3379	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3380	NOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3381	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3382	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3383	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3384	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3385	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3386	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3387	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3388	NOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3389	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3390	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3391	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3392	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3393	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3394	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3395	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3396	DOC1:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3397	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3398				HR3397 (Lo-16bits)
HR3399	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3400				HR3399 (Lo-16bits)
HR3401	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3402				HR3401 (Lo-16bits)
HR3403	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3404				HR3403 (Lo-16bits)
HR3405	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3406				HR3405 (Lo-16bits)
HR3407	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3408				HR3407 (Lo-16bits)
HR3409	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3410				HR3409 (Lo-16bits)
HR3411	DOC1:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3412				HR3411 (Lo-16bits)
HR3413	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3414				HR3413 (Lo-16bits)
HR3415	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3416				HR3415 (Lo-16bits)
HR3417	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3418				HR3417 (Lo-16bits)
HR3419	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3420				HR3419 (Lo-16bits)
HR3421	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3422				HR3421 (Lo-16bits)
HR3423	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3424				HR3423 (Lo-16bits)
HR3425	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3426				HR3425 (Lo-16bits)
HR3427	DOC1:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3428				HR3427 (Lo-16bits)
HR3429	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3430				HR3429 (Lo-16bits)
HR3431	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3432				HR3431 (Lo-16bits)
HR3433	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3434				HR3433 (Lo-16bits)
HR3435	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3436				HR3435 (Lo-16bits)
HR3437	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3438				HR3437 (Lo-16bits)
HR3439	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3440				HR3439 (Lo-16bits)
HR3441	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3442				HR3441 (Lo-16bits)
HR3443	DOC1:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3444				HR3443 (Lo-16bits)
HR3445	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3446	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3447	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3448	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3449	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3450	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3451	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3452	DOC1:Delay type	R(W)	1	0=DT 1=IDMT
HR3453	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3454				HR3453 (Lo-16bits)
HR3455	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3456				HR3455 (Lo-16bits)
HR3457	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3458				HR3457 (Lo-16bits)
HR3459	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3460				HR3459 (Lo-16bits)
HR3461	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3462				HR3461 (Lo-16bits)
HR3463	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3464				HR3463 (Lo-16bits)
HR3465	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3466				HR3465 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3467	DOC1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3468				HR3467 (Lo-16bits)
HR3469	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3470	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3471	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3472	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3473	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3474	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3475	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3476	DOC1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3477	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3478	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3479	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3480	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3481	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3482	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3483	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3484	DOC1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3485	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3486	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3487	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3488	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3489	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3490	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3491	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3492	DOC1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR3493	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3494	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3495	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3496	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3497	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3498	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3499	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3500	DOC1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3501	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3502				HR3501 (Lo-16bits)
HR3503	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3504				HR3503 (Lo-16bits)
HR3505	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3506				HR3505 (Lo-16bits)
HR3507	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3508				HR3507 (Lo-16bits)
HR3509	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3510				HR3509 (Lo-16bits)
HR3511	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3512				HR3511 (Lo-16bits)
HR3513	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3514				HR3513 (Lo-16bits)
HR3515	DOC1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3516				HR3515 (Lo-16bits)
HR3517	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3518				HR3517 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3519	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3520				HR3519 (Lo-16bits)
HR3521	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3522				HR3521 (Lo-16bits)
HR3523	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3524				HR3523 (Lo-16bits)
HR3525	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3526				HR3525 (Lo-16bits)
HR3527	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3528				HR3527 (Lo-16bits)
HR3529	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3530				HR3529 (Lo-16bits)
HR3531	DOC1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3532				HR3531 (Lo-16bits)
HR3533	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3534				HR3533 (Lo-16bits)
HR3535	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3536				HR3535 (Lo-16bits)
HR3537	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3538				HR3537 (Lo-16bits)
HR3539	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3540				HR3539 (Lo-16bits)
HR3541	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3542				HR3541 (Lo-16bits)
HR3543	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3544				HR3543 (Lo-16bits)
HR3545	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3546				HR3545 (Lo-16bits)
HR3547	DOC1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3548				HR3547 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3549	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3550				HR3549 (Lo-16bits)
HR3551	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3552				HR3551 (Lo-16bits)
HR3553	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3554				HR3553 (Lo-16bits)
HR3555	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3556				HR3555 (Lo-16bits)
HR3557	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3558				HR3557 (Lo-16bits)
HR3559	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3560				HR3559 (Lo-16bits)
HR3561	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3562				HR3561 (Lo-16bits)
HR3563	DOC1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3564				HR3563 (Lo-16bits)
HR3565	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3566	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3567	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3568	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3569	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3570	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3571	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3572	DOC1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3573	DOC1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3574				HR3573 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3575	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3576				HR3575 (Lo-16bits)
HR3577	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3578				HR3577 (Lo-16bits)
HR3579	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3580				HR3579 (Lo-16bits)
HR3581	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3582				HR3581 (Lo-16bits)
HR3583	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3584				HR3583 (Lo-16bits)
HR3585	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3586				HR3585 (Lo-16bits)
HR3587	DOC1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3588				HR3587 (Lo-16bits)
HR3589	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3590	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3591	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3592	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3593	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3594	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3595	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3596	DOC1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3597	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3598				HR3597 (Lo-16bits)
HR3599	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3600				HR3599 (Lo-16bits)
HR3601	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3602				HR3601 (Lo-16bits)
HR3603	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3604				HR3603 (Lo-16bits)
HR3605	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3606				HR3605 (Lo-16bits)
HR3607	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3608				HR3607 (Lo-16bits)
HR3609	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3610				HR3609 (Lo-16bits)
HR3611	DOC1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3612				HR3611 (Lo-16bits)
HR3613	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3614	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3615	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3616	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3617	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3618	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3619	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3620	DOC1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3621	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3622	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3623	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3624	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3625	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3626	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3627	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3628	DOC1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3629	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3630	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3631	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3632	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3633	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3634	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3635	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3636	DOC2:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3637	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3638				HR3637 (Lo-16bits)
HR3639	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3640				HR3639 (Lo-16bits)
HR3641	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3642				HR3641 (Lo-16bits)
HR3643	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3644				HR3643 (Lo-16bits)
HR3645	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3646				HR3645 (Lo-16bits)
HR3647	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3648				HR3647 (Lo-16bits)
HR3649	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3650				HR3649 (Lo-16bits)
HR3651	DOC2:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3652				HR3651 (Lo-16bits)
HR3653	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3654				HR3653 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3655	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3656				HR3655 (Lo-16bits)
HR3657	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3658				HR3657 (Lo-16bits)
HR3659	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3660				HR3659 (Lo-16bits)
HR3661	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3662				HR3661 (Lo-16bits)
HR3663	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3664				HR3663 (Lo-16bits)
HR3665	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3666				HR3665 (Lo-16bits)
HR3667	DOC2:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3668				HR3667 (Lo-16bits)
HR3669	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3670				HR3669 (Lo-16bits)
HR3671	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3672				HR3671 (Lo-16bits)
HR3673	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3674				HR3673 (Lo-16bits)
HR3675	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3676				HR3675 (Lo-16bits)
HR3677	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3678				HR3677 (Lo-16bits)
HR3679	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3680				HR3679 (Lo-16bits)
HR3681	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3682				HR3681 (Lo-16bits)
HR3683	DOC2:Pick-up setting lset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3684				HR3683 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3685	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3686	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3687	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3688	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3689	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3690	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3691	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3692	DOC2:Delay type	R(W)	1	0=DT 1=IDMT
HR3693	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3694				HR3693 (Lo-16bits)
HR3695	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3696				HR3695 (Lo-16bits)
HR3697	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3698				HR3697 (Lo-16bits)
HR3699	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3700				HR3699 (Lo-16bits)
HR3701	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3702				HR3701 (Lo-16bits)
HR3703	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3704				HR3703 (Lo-16bits)
HR3705	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3706				HR3705 (Lo-16bits)
HR3707	DOC2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3708				HR3707 (Lo-16bits)
HR3709	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3710	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3711	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3712	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3713	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3714	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3715	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3716	DOC2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3717	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3718	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3719	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3720	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3721	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3722	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3723	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3724	DOC2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3725	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3726	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3727	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3728	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3729	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3730	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3731	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3732	DOC2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3733	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3734	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3735	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3736	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3737	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3738	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3739	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3740	DOC2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3741	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3742				HR3741 (Lo-16bits)
HR3743	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3744				HR3743 (Lo-16bits)
HR3745	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3746				HR3745 (Lo-16bits)
HR3747	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3748				HR3747 (Lo-16bits)
HR3749	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3750				HR3749 (Lo-16bits)
HR3751	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3752				HR3751 (Lo-16bits)
HR3753	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3754				HR3753 (Lo-16bits)
HR3755	DOC2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3756				HR3755 (Lo-16bits)
HR3757	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3758				HR3757 (Lo-16bits)
HR3759	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3760				HR3759 (Lo-16bits)
HR3761	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3762				HR3761 (Lo-16bits)
HR3763	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3764				HR3763 (Lo-16bits)
HR3765	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3766				HR3765 (Lo-16bits)
HR3767	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3768				HR3767 (Lo-16bits)
HR3769	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3770				HR3769 (Lo-16bits)
HR3771	DOC2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3772				HR3771 (Lo-16bits)
HR3773	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3774				HR3773 (Lo-16bits)
HR3775	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3776				HR3775 (Lo-16bits)
HR3777	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3778				HR3777 (Lo-16bits)
HR3779	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3780				HR3779 (Lo-16bits)
HR3781	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3782				HR3781 (Lo-16bits)
HR3783	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3784				HR3783 (Lo-16bits)
HR3785	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3786				HR3785 (Lo-16bits)
HR3787	DOC2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR3788				HR3787 (Lo-16bits)
HR3789	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3790				HR3789 (Lo-16bits)
HR3791	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3792				HR3791 (Lo-16bits)
HR3793	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR3794				HR3793 (Lo-16bits)
HR3795	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3796				HR3795 (Lo-16bits)
HR3797	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3798				HR3797 (Lo-16bits)
HR3799	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3800				HR3799 (Lo-16bits)
HR3801	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3802				HR3801 (Lo-16bits)
HR3803	DOC2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3804				HR3803 (Lo-16bits)
HR3805	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3806	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3807	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3808	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3809	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3810	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3811	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3812	DOC2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR3813	DOC2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3814				HR3813 (Lo-16bits)
HR3815	DOC2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3816				HR3815 (Lo-16bits)
HR3817	DOC2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3818				HR3817 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3819	DOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3820				HR3819 (Lo-16bits)
HR3821	DOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3822				HR3821 (Lo-16bits)
HR3823	DOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3824				HR3823 (Lo-16bits)
HR3825	DOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3826				HR3825 (Lo-16bits)
HR3827	DOC2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR3828				HR3827 (Lo-16bits)
HR3829	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3830	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3831	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3832	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3833	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3834	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3835	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3836	DOC2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR3837	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3838				HR3837 (Lo-16bits)
HR3839	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3840				HR3839 (Lo-16bits)
HR3841	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3842				HR3841 (Lo-16bits)
HR3843	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3844				HR3843 (Lo-16bits)
HR3845	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3846				HR3845 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3847	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3848				HR3847 (Lo-16bits)
HR3849	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3850				HR3849 (Lo-16bits)
HR3851	DOC2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR3852				HR3851 (Lo-16bits)
HR3853	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3854	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3855	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3856	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3857	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3858	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3859	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3860	DOC2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR3861	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3862	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3863	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3864	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3865	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3866	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3867	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR3868	DOC2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR3869	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3870	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3871	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3872	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3873	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3874	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3875	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3876	DOC3:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR3877	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3878				HR3877 (Lo-16bits)
HR3879	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3880				HR3879 (Lo-16bits)
HR3881	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3882				HR3881 (Lo-16bits)
HR3883	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3884				HR3883 (Lo-16bits)
HR3885	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3886				HR3885 (Lo-16bits)
HR3887	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3888				HR3887 (Lo-16bits)
HR3889	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3890				HR3889 (Lo-16bits)
HR3891	DOC3:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR3892				HR3891 (Lo-16bits)
HR3893	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3894				HR3893 (Lo-16bits)
HR3895	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3896				HR3895 (Lo-16bits)
HR3897	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3898				HR3897 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR3899	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3900				HR3899 (Lo-16bits)
HR3901	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3902				HR3901 (Lo-16bits)
HR3903	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3904				HR3903 (Lo-16bits)
HR3905	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3906				HR3905 (Lo-16bits)
HR3907	DOC3:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR3908				HR3907 (Lo-16bits)
HR3909	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3910				HR3909 (Lo-16bits)
HR3911	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3912				HR3911 (Lo-16bits)
HR3913	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3914				HR3913 (Lo-16bits)
HR3915	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3916				HR3915 (Lo-16bits)
HR3917	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3918				HR3917 (Lo-16bits)
HR3919	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3920				HR3919 (Lo-16bits)
HR3921	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3922				HR3921 (Lo-16bits)
HR3923	DOC3:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR3924				HR3923 (Lo-16bits)
HR3925	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3926	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3927	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3928	DOC3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR3929	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3930	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3931	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3932	DOC3:Delay type	R(W)	1	0=DT 1=IDMT
HR3933	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3934				HR3933 (Lo-16bits)
HR3935	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3936				HR3935 (Lo-16bits)
HR3937	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3938				HR3937 (Lo-16bits)
HR3939	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3940				HR3939 (Lo-16bits)
HR3941	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3942				HR3941 (Lo-16bits)
HR3943	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3944				HR3943 (Lo-16bits)
HR3945	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3946				HR3945 (Lo-16bits)
HR3947	DOC3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR3948				HR3947 (Lo-16bits)
HR3949	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3950	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3951	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3952	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3953	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3954	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR3955	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3956	DOC3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR3957	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3958	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3959	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3960	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3961	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3962	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3963	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3964	DOC3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR3965	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3966	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3967	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3968	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3969	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3970	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3971	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3972	DOC3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR3973	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3974	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3975	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3976	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3977	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR3978	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3979	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3980	DOC3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR3981	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3982				HR3981 (Lo-16bits)
HR3983	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3984				HR3983 (Lo-16bits)
HR3985	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3986				HR3985 (Lo-16bits)
HR3987	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3988				HR3987 (Lo-16bits)
HR3989	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3990				HR3989 (Lo-16bits)
HR3991	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3992				HR3991 (Lo-16bits)
HR3993	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3994				HR3993 (Lo-16bits)
HR3995	DOC3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR3996				HR3995 (Lo-16bits)
HR3997	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR3998				HR3997 (Lo-16bits)
HR3999	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4000				HR3999 (Lo-16bits)
HR4001	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4002				HR4001 (Lo-16bits)
HR4003	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4004				HR4003 (Lo-16bits)
HR4005	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4006				HR4005 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4007	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4008				HR4007 (Lo-16bits)
HR4009	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4010				HR4009 (Lo-16bits)
HR4011	DOC3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4012				HR4011 (Lo-16bits)
HR4013	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4014				HR4013 (Lo-16bits)
HR4015	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4016				HR4015 (Lo-16bits)
HR4017	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4018				HR4017 (Lo-16bits)
HR4019	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4020				HR4019 (Lo-16bits)
HR4021	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4022				HR4021 (Lo-16bits)
HR4023	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4024				HR4023 (Lo-16bits)
HR4025	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4026				HR4025 (Lo-16bits)
HR4027	DOC3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4028				HR4027 (Lo-16bits)
HR4029	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4030				HR4029 (Lo-16bits)
HR4031	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4032				HR4031 (Lo-16bits)
HR4033	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4034				HR4033 (Lo-16bits)
HR4035	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4036				HR4035 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4037	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4038				HR4037 (Lo-16bits)
HR4039	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4040				HR4039 (Lo-16bits)
HR4041	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4042				HR4041 (Lo-16bits)
HR4043	DOC3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4044				HR4043 (Lo-16bits)
HR4045	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4046	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4047	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4048	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4049	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4050	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4051	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4052	DOC3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4053	DOC3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4054				HR4053 (Lo-16bits)
HR4055	DOC3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4056				HR4055 (Lo-16bits)
HR4057	DOC3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4058				HR4057 (Lo-16bits)
HR4059	DOC3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4060				HR4059 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4061	DOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4062				HR4061 (Lo-16bits)
HR4063	DOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4064				HR4063 (Lo-16bits)
HR4065	DOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4066				HR4065 (Lo-16bits)
HR4067	DOC3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4068				HR4067 (Lo-16bits)
HR4069	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4070	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4071	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4072	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4073	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4074	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4075	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4076	DOC3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4077	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4078				HR4077 (Lo-16bits)
HR4079	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4080				HR4079 (Lo-16bits)
HR4081	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4082				HR4081 (Lo-16bits)
HR4083	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4084				HR4083 (Lo-16bits)
HR4085	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4086				HR4085 (Lo-16bits)
HR4087	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4088				HR4087 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4089	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4090				HR4089 (Lo-16bits)
HR4091	DOC3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4092				HR4091 (Lo-16bits)
HR4093	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4094	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4095	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4096	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4097	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4098	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4099	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4100	DOC3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4101	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4102	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4103	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4104	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4105	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4106	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4107	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4108	DOC3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4109	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4110	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir

Holding Register	Parameter	Access	Length	Info
HR4111	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4112	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4113	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4114	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4115	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4116	DOC4:Characteristic direction	R(W)	1	0=Directional 1=NonDir
HR4117	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4118				HR4117 (Lo-16bits)
HR4119	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4120				HR4119 (Lo-16bits)
HR4121	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4122				HR4121 (Lo-16bits)
HR4123	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4124				HR4123 (Lo-16bits)
HR4125	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4126				HR4125 (Lo-16bits)
HR4127	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4128				HR4127 (Lo-16bits)
HR4129	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4130				HR4129 (Lo-16bits)
HR4131	DOC4:Operating sector size (+/-)	R(W)	2	FLOAT [1...170] (Hi-16bits)
HR4132				HR4131 (Lo-16bits)
HR4133	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4134				HR4133 (Lo-16bits)
HR4135	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4136				HR4135 (Lo-16bits)
HR4137	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4138				HR4137 (Lo-16bits)
HR4139	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4140				HR4139 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4141	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4142				HR4141 (Lo-16bits)
HR4143	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4144				HR4143 (Lo-16bits)
HR4145	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4146				HR4145 (Lo-16bits)
HR4147	DOC4:Operating sector center	R(W)	2	FLOAT [-180...180] (Hi-16bits)
HR4148				HR4147 (Lo-16bits)
HR4149	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4150				HR4149 (Lo-16bits)
HR4151	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4152				HR4151 (Lo-16bits)
HR4153	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4154				HR4153 (Lo-16bits)
HR4155	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4156				HR4155 (Lo-16bits)
HR4157	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4158				HR4157 (Lo-16bits)
HR4159	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4160				HR4159 (Lo-16bits)
HR4161	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4162				HR4161 (Lo-16bits)
HR4163	DOC4:Pick-up setting Iset	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR4164				HR4163 (Lo-16bits)
HR4165	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4166	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4167	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4168	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4169	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4170	DOC4:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4171	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4172	DOC4:Delay type	R(W)	1	0=DT 1=IDMT
HR4173	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4174				HR4173 (Lo-16bits)
HR4175	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4176				HR4175 (Lo-16bits)
HR4177	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4178				HR4177 (Lo-16bits)
HR4179	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4180				HR4179 (Lo-16bits)
HR4181	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4182				HR4181 (Lo-16bits)
HR4183	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4184				HR4183 (Lo-16bits)
HR4185	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4186				HR4185 (Lo-16bits)
HR4187	DOC4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4188				HR4187 (Lo-16bits)
HR4189	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4190	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4191	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4192	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4193	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4194	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4195	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4196	DOC4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4197	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4198	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4199	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4200	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4201	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4202	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4203	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4204	DOC4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4205	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4206	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4207	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4208	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4209	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4210	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4211	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4212	DOC4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4213	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4214	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4215	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4216	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4217	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4218	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4219	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4220	DOC4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4221	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4222				HR4221 (Lo-16bits)
HR4223	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4224				HR4223 (Lo-16bits)
HR4225	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4226				HR4225 (Lo-16bits)
HR4227	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4228				HR4227 (Lo-16bits)
HR4229	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4230				HR4229 (Lo-16bits)
HR4231	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4232				HR4231 (Lo-16bits)
HR4233	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4234				HR4233 (Lo-16bits)
HR4235	DOC4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4236				HR4235 (Lo-16bits)
HR4237	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4238				HR4237 (Lo-16bits)
HR4239	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4240				HR4239 (Lo-16bits)
HR4241	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4242				HR4241 (Lo-16bits)
HR4243	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4244				HR4243 (Lo-16bits)
HR4245	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4246				HR4245 (Lo-16bits)
HR4247	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4248				HR4247 (Lo-16bits)
HR4249	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4250				HR4249 (Lo-16bits)
HR4251	DOC4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4252				HR4251 (Lo-16bits)
HR4253	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4254				HR4253 (Lo-16bits)
HR4255	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4256				HR4255 (Lo-16bits)
HR4257	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4258				HR4257 (Lo-16bits)
HR4259	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4260				HR4259 (Lo-16bits)
HR4261	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4262				HR4261 (Lo-16bits)
HR4263	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4264				HR4263 (Lo-16bits)
HR4265	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4266				HR4265 (Lo-16bits)
HR4267	DOC4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4268				HR4267 (Lo-16bits)
HR4269	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4270				HR4269 (Lo-16bits)
HR4271	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4272				HR4271 (Lo-16bits)
HR4273	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4274				HR4273 (Lo-16bits)
HR4275	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4276				HR4275 (Lo-16bits)
HR4277	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4278				HR4277 (Lo-16bits)
HR4279	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4280				HR4279 (Lo-16bits)
HR4281	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4282				HR4281 (Lo-16bits)
HR4283	DOC4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4284				HR4283 (Lo-16bits)
HR4285	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4286	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4287	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4288	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4289	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4290	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4291	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4292	DOC4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4293	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4294				HR4293 (Lo-16bits)
HR4295	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4296				HR4295 (Lo-16bits)
HR4297	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4298				HR4297 (Lo-16bits)
HR4299	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4300				HR4299 (Lo-16bits)
HR4301	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4302				HR4301 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4303	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4304				HR4303 (Lo-16bits)
HR4305	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4306				HR4305 (Lo-16bits)
HR4307	DOC4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4308				HR4307 (Lo-16bits)
HR4309	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4310	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4311	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4312	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4313	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4314	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4315	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4316	DOC4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4317	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4318				HR4317 (Lo-16bits)
HR4319	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4320				HR4319 (Lo-16bits)
HR4321	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4322				HR4321 (Lo-16bits)
HR4323	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4324				HR4323 (Lo-16bits)
HR4325	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4326				HR4325 (Lo-16bits)
HR4327	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4328				HR4327 (Lo-16bits)
HR4329	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4330				HR4329 (Lo-16bits)
HR4331	DOC4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4332				HR4331 (Lo-16bits)
HR4333	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4334	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4335	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4336	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4337	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4338	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4339	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4340	DOC4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4341	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4342	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4343	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4344	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4345	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4346	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4347	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4348	DOC4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4349	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4350				HR4349 (Lo-16bits)
HR4351	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4352				HR4351 (Lo-16bits)
HR4353	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4354				HR4353 (Lo-16bits)
HR4355	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4356				HR4355 (Lo-16bits)
HR4357	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4358				HR4357 (Lo-16bits)
HR4359	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4360				HR4359 (Lo-16bits)
HR4361	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4362				HR4361 (Lo-16bits)
HR4363	NEF1:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4364				HR4363 (Lo-16bits)
HR4365	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4366	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4367	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4368	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4369	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4370	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4371	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4372	NEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR4373	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4374				HR4373 (Lo-16bits)
HR4375	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4376				HR4375 (Lo-16bits)
HR4377	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4378				HR4377 (Lo-16bits)
HR4379	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4380				HR4379 (Lo-16bits)
HR4381	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4382				HR4381 (Lo-16bits)
HR4383	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4384				HR4383 (Lo-16bits)
HR4385	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4386				HR4385 (Lo-16bits)
HR4387	NEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4388				HR4387 (Lo-16bits)
HR4389	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4390	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4391	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4392	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4393	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4394	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4395	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4396	NEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4397	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4398	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4399	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4400	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4401	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4402	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4403	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4404	NEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4405	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4406	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4407	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4408	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR4409	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4410	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4411	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4412	NEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4413	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4414	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4415	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4416	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4417	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4418	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4419	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4420	NEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4421	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4422				HR4421 (Lo-16bits)
HR4423	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4424				HR4423 (Lo-16bits)
HR4425	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4426				HR4425 (Lo-16bits)
HR4427	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4428				HR4427 (Lo-16bits)
HR4429	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4430				HR4429 (Lo-16bits)
HR4431	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4432				HR4431 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4433	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4434				HR4433 (Lo-16bits)
HR4435	NEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4436				HR4435 (Lo-16bits)
HR4437	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4438				HR4437 (Lo-16bits)
HR4439	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4440				HR4439 (Lo-16bits)
HR4441	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4442				HR4441 (Lo-16bits)
HR4443	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4444				HR4443 (Lo-16bits)
HR4445	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4446				HR4445 (Lo-16bits)
HR4447	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4448				HR4447 (Lo-16bits)
HR4449	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4450				HR4449 (Lo-16bits)
HR4451	NEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4452				HR4451 (Lo-16bits)
HR4453	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4454				HR4453 (Lo-16bits)
HR4455	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4456				HR4455 (Lo-16bits)
HR4457	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4458				HR4457 (Lo-16bits)
HR4459	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4460				HR4459 (Lo-16bits)
HR4461	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4462				HR4461 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4463	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4464				HR4463 (Lo-16bits)
HR4465	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4466				HR4465 (Lo-16bits)
HR4467	NEF1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4468				HR4467 (Lo-16bits)
HR4469	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4470				HR4469 (Lo-16bits)
HR4471	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4472				HR4471 (Lo-16bits)
HR4473	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4474				HR4473 (Lo-16bits)
HR4475	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4476				HR4475 (Lo-16bits)
HR4477	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4478				HR4477 (Lo-16bits)
HR4479	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4480				HR4479 (Lo-16bits)
HR4481	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4482				HR4481 (Lo-16bits)
HR4483	NEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4484				HR4483 (Lo-16bits)
HR4485	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4486	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4487	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4488	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4489	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4490	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4491	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4492	NEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4493	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4494				HR4493 (Lo-16bits)
HR4495	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4496				HR4495 (Lo-16bits)
HR4497	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4498				HR4497 (Lo-16bits)
HR4499	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4500				HR4499 (Lo-16bits)
HR4501	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4502				HR4501 (Lo-16bits)
HR4503	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4504				HR4503 (Lo-16bits)
HR4505	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4506				HR4505 (Lo-16bits)
HR4507	NEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4508				HR4507 (Lo-16bits)
HR4509	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4510	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4511	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4512	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4513	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4514	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4515	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4516	NEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4517	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4518				HR4517 (Lo-16bits)
HR4519	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4520				HR4519 (Lo-16bits)
HR4521	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4522				HR4521 (Lo-16bits)
HR4523	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4524				HR4523 (Lo-16bits)
HR4525	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4526				HR4525 (Lo-16bits)
HR4527	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4528				HR4527 (Lo-16bits)
HR4529	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4530				HR4529 (Lo-16bits)
HR4531	NEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4532				HR4531 (Lo-16bits)
HR4533	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4534	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4535	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4536	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4537	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4538	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4539	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4540	NEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4541	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4542	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4543	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4544	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4545	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4546	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4547	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4548	NEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4549	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4550				HR4549 (Lo-16bits)
HR4551	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4552				HR4551 (Lo-16bits)
HR4553	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4554				HR4553 (Lo-16bits)
HR4555	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4556				HR4555 (Lo-16bits)
HR4557	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4558				HR4557 (Lo-16bits)
HR4559	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4560				HR4559 (Lo-16bits)
HR4561	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4562				HR4561 (Lo-16bits)
HR4563	NEF2:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4564				HR4563 (Lo-16bits)
HR4565	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4566	NEF2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR4567	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4568	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4569	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4570	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4571	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4572	NEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR4573	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4574				HR4573 (Lo-16bits)
HR4575	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4576				HR4575 (Lo-16bits)
HR4577	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4578				HR4577 (Lo-16bits)
HR4579	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4580				HR4579 (Lo-16bits)
HR4581	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4582				HR4581 (Lo-16bits)
HR4583	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4584				HR4583 (Lo-16bits)
HR4585	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4586				HR4585 (Lo-16bits)
HR4587	NEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4588				HR4587 (Lo-16bits)
HR4589	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4590	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4591	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4592	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR4593	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4594	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4595	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4596	NEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4597	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4598	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4599	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4600	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4601	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4602	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4603	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4604	NEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4605	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4606	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4607	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4608	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4609	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4610	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4611	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4612	NEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4613	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4614	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4615	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4616	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type

Holding Register	Parameter	Access	Length	Info
HR4617	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4618	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4619	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4620	NEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4621	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4622				HR4621 (Lo-16bits)
HR4623	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4624				HR4623 (Lo-16bits)
HR4625	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4626				HR4625 (Lo-16bits)
HR4627	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4628				HR4627 (Lo-16bits)
HR4629	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4630				HR4629 (Lo-16bits)
HR4631	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4632				HR4631 (Lo-16bits)
HR4633	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4634				HR4633 (Lo-16bits)
HR4635	NEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4636				HR4635 (Lo-16bits)
HR4637	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4638				HR4637 (Lo-16bits)
HR4639	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4640				HR4639 (Lo-16bits)
HR4641	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4642				HR4641 (Lo-16bits)
HR4643	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4644				HR4643 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4645	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4646				HR4645 (Lo-16bits)
HR4647	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4648				HR4647 (Lo-16bits)
HR4649	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4650				HR4649 (Lo-16bits)
HR4651	NEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4652				HR4651 (Lo-16bits)
HR4653	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4654				HR4653 (Lo-16bits)
HR4655	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4656				HR4655 (Lo-16bits)
HR4657	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4658				HR4657 (Lo-16bits)
HR4659	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4660				HR4659 (Lo-16bits)
HR4661	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4662				HR4661 (Lo-16bits)
HR4663	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4664				HR4663 (Lo-16bits)
HR4665	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4666				HR4665 (Lo-16bits)
HR4667	NEF2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4668				HR4667 (Lo-16bits)
HR4669	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4670				HR4669 (Lo-16bits)
HR4671	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4672				HR4671 (Lo-16bits)
HR4673	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4674				HR4673 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4675	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4676				HR4675 (Lo-16bits)
HR4677	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4678				HR4677 (Lo-16bits)
HR4679	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4680				HR4679 (Lo-16bits)
HR4681	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4682				HR4681 (Lo-16bits)
HR4683	NEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4684				HR4683 (Lo-16bits)
HR4685	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4686	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4687	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4688	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4689	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4690	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4691	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4692	NEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4693	NEF2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4694				HR4693 (Lo-16bits)
HR4695	NEF2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4696				HR4695 (Lo-16bits)
HR4697	NEF2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4698				HR4697 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4699	NEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4700				HR4699 (Lo-16bits)
HR4701	NEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4702				HR4701 (Lo-16bits)
HR4703	NEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4704				HR4703 (Lo-16bits)
HR4705	NEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4706				HR4705 (Lo-16bits)
HR4707	NEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4708				HR4707 (Lo-16bits)
HR4709	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4710	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4711	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4712	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4713	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4714	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4715	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4716	NEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4717	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4718				HR4717 (Lo-16bits)
HR4719	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4720				HR4719 (Lo-16bits)
HR4721	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4722				HR4721 (Lo-16bits)
HR4723	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4724				HR4723 (Lo-16bits)
HR4725	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4726				HR4725 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4727	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4728				HR4727 (Lo-16bits)
HR4729	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4730				HR4729 (Lo-16bits)
HR4731	NEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4732				HR4731 (Lo-16bits)
HR4733	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4734	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4735	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4736	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4737	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4738	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4739	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4740	NEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4741	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4742	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4743	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4744	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4745	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4746	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4747	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4748	NEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4749	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4750				HR4749 (Lo-16bits)
HR4751	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4752				HR4751 (Lo-16bits)
HR4753	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4754				HR4753 (Lo-16bits)
HR4755	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4756				HR4755 (Lo-16bits)
HR4757	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4758				HR4757 (Lo-16bits)
HR4759	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4760				HR4759 (Lo-16bits)
HR4761	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4762				HR4761 (Lo-16bits)
HR4763	NEF3:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4764				HR4763 (Lo-16bits)
HR4765	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4766	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4767	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4768	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4769	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4770	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4771	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4772	NEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR4773	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4774				HR4773 (Lo-16bits)
HR4775	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4776				HR4775 (Lo-16bits)
HR4777	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR4778				HR4777 (Lo-16bits)
HR4779	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4780				HR4779 (Lo-16bits)
HR4781	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4782				HR4781 (Lo-16bits)
HR4783	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4784				HR4783 (Lo-16bits)
HR4785	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4786				HR4785 (Lo-16bits)
HR4787	NEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4788				HR4787 (Lo-16bits)
HR4789	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4790	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4791	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4792	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4793	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4794	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4795	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4796	NEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4797	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4798	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4799	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4800	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4801	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4802	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4803	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4804	NEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR4805	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4806	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4807	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4808	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4809	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4810	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4811	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4812	NEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR4813	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4814	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4815	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4816	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4817	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4818	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4819	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4820	NEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR4821	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4822				HR4821 (Lo-16bits)
HR4823	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4824				HR4823 (Lo-16bits)
HR4825	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4826				HR4825 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4827	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4828				HR4827 (Lo-16bits)
HR4829	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4830				HR4829 (Lo-16bits)
HR4831	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4832				HR4831 (Lo-16bits)
HR4833	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4834				HR4833 (Lo-16bits)
HR4835	NEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR4836				HR4835 (Lo-16bits)
HR4837	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4838				HR4837 (Lo-16bits)
HR4839	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4840				HR4839 (Lo-16bits)
HR4841	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4842				HR4841 (Lo-16bits)
HR4843	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4844				HR4843 (Lo-16bits)
HR4845	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4846				HR4845 (Lo-16bits)
HR4847	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4848				HR4847 (Lo-16bits)
HR4849	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4850				HR4849 (Lo-16bits)
HR4851	NEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4852				HR4851 (Lo-16bits)
HR4853	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4854				HR4853 (Lo-16bits)
HR4855	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4856				HR4855 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4857	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4858				HR4857 (Lo-16bits)
HR4859	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4860				HR4859 (Lo-16bits)
HR4861	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4862				HR4861 (Lo-16bits)
HR4863	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4864				HR4863 (Lo-16bits)
HR4865	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4866				HR4865 (Lo-16bits)
HR4867	NEF3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR4868				HR4867 (Lo-16bits)
HR4869	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4870				HR4869 (Lo-16bits)
HR4871	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4872				HR4871 (Lo-16bits)
HR4873	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4874				HR4873 (Lo-16bits)
HR4875	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4876				HR4875 (Lo-16bits)
HR4877	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4878				HR4877 (Lo-16bits)
HR4879	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4880				HR4879 (Lo-16bits)
HR4881	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4882				HR4881 (Lo-16bits)
HR4883	NEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR4884				HR4883 (Lo-16bits)
HR4885	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4886	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4887	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4888	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4889	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4890	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4891	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4892	NEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR4893	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4894				HR4893 (Lo-16bits)
HR4895	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4896				HR4895 (Lo-16bits)
HR4897	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4898				HR4897 (Lo-16bits)
HR4899	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4900				HR4899 (Lo-16bits)
HR4901	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4902				HR4901 (Lo-16bits)
HR4903	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4904				HR4903 (Lo-16bits)
HR4905	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4906				HR4905 (Lo-16bits)
HR4907	NEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR4908				HR4907 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4909	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4910	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4911	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4912	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4913	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4914	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4915	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4916	NEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR4917	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4918				HR4917 (Lo-16bits)
HR4919	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4920				HR4919 (Lo-16bits)
HR4921	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4922				HR4921 (Lo-16bits)
HR4923	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4924				HR4923 (Lo-16bits)
HR4925	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4926				HR4925 (Lo-16bits)
HR4927	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4928				HR4927 (Lo-16bits)
HR4929	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4930				HR4929 (Lo-16bits)
HR4931	NEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR4932				HR4931 (Lo-16bits)
HR4933	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4934	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4935	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4936	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR4937	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4938	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4939	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4940	NEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR4941	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4942	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4943	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4944	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4945	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4946	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4947	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4948	NEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR4949	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4950				HR4949 (Lo-16bits)
HR4951	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4952				HR4951 (Lo-16bits)
HR4953	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4954				HR4953 (Lo-16bits)
HR4955	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4956				HR4955 (Lo-16bits)
HR4957	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4958				HR4957 (Lo-16bits)
HR4959	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4960				HR4959 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4961	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4962				HR4961 (Lo-16bits)
HR4963	NEF4:Pick-up setting I0set	R(W)	2	FLOAT [0.0001...40] (Hi-16bits)
HR4964				HR4963 (Lo-16bits)
HR4965	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4966	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4967	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4968	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4969	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4970	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4971	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4972	NEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR4973	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4974				HR4973 (Lo-16bits)
HR4975	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4976				HR4975 (Lo-16bits)
HR4977	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4978				HR4977 (Lo-16bits)
HR4979	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4980				HR4979 (Lo-16bits)
HR4981	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4982				HR4981 (Lo-16bits)
HR4983	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4984				HR4983 (Lo-16bits)
HR4985	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4986				HR4985 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR4987	NEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR4988				HR4987 (Lo-16bits)
HR4989	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4990	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4991	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4992	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4993	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4994	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4995	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4996	NEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR4997	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4998	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR4999	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5000	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5001	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5002	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5003	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5004	NEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5005	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5006	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5007	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5008	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5009	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5010	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5011	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5012	NEF4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5013	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5014	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5015	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5016	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5017	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5018	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5019	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5020	NEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5021	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5022				HR5021 (Lo-16bits)
HR5023	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5024				HR5023 (Lo-16bits)
HR5025	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5026				HR5025 (Lo-16bits)
HR5027	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5028				HR5027 (Lo-16bits)
HR5029	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5030				HR5029 (Lo-16bits)
HR5031	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5032				HR5031 (Lo-16bits)
HR5033	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5034				HR5033 (Lo-16bits)
HR5035	NEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5036				HR5035 (Lo-16bits)
HR5037	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5038				HR5037 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5039	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5040				HR5039 (Lo-16bits)
HR5041	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5042				HR5041 (Lo-16bits)
HR5043	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5044				HR5043 (Lo-16bits)
HR5045	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5046				HR5045 (Lo-16bits)
HR5047	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5048				HR5047 (Lo-16bits)
HR5049	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5050				HR5049 (Lo-16bits)
HR5051	NEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5052				HR5051 (Lo-16bits)
HR5053	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5054				HR5053 (Lo-16bits)
HR5055	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5056				HR5055 (Lo-16bits)
HR5057	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5058				HR5057 (Lo-16bits)
HR5059	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5060				HR5059 (Lo-16bits)
HR5061	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5062				HR5061 (Lo-16bits)
HR5063	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5064				HR5063 (Lo-16bits)
HR5065	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5066				HR5065 (Lo-16bits)
HR5067	NEF4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR5068				HR5067 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5069	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5070				HR5069 (Lo-16bits)
HR5071	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5072				HR5071 (Lo-16bits)
HR5073	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5074				HR5073 (Lo-16bits)
HR5075	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5076				HR5075 (Lo-16bits)
HR5077	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5078				HR5077 (Lo-16bits)
HR5079	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5080				HR5079 (Lo-16bits)
HR5081	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5082				HR5081 (Lo-16bits)
HR5083	NEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5084				HR5083 (Lo-16bits)
HR5085	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5086	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5087	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5088	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5089	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5090	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5091	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5092	NEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5093	NEF4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5094				HR5093 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5095	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5096				HR5095 (Lo-16bits)
HR5097	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5098				HR5097 (Lo-16bits)
HR5099	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5100				HR5099 (Lo-16bits)
HR5101	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5102				HR5101 (Lo-16bits)
HR5103	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5104				HR5103 (Lo-16bits)
HR5105	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5106				HR5105 (Lo-16bits)
HR5107	NEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5108				HR5107 (Lo-16bits)
HR5109	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5110	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5111	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5112	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5113	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5114	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5115	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5116	NEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5117	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5118				HR5117 (Lo-16bits)
HR5119	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5120				HR5119 (Lo-16bits)
HR5121	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5122				HR5121 (Lo-16bits)
HR5123	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5124				HR5123 (Lo-16bits)
HR5125	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5126				HR5125 (Lo-16bits)
HR5127	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5128				HR5127 (Lo-16bits)
HR5129	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5130				HR5129 (Lo-16bits)
HR5131	NEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5132				HR5131 (Lo-16bits)
HR5133	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5134	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5135	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5136	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5137	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5138	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5139	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5140	NEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5141	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5142	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5143	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5144	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5145	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5146	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5147	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5148	NEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5149	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5150	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5151	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5152	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5153	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5154	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5155	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5156	DEF1:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5157	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5158				HR5157 (Lo-16bits)
HR5159	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5160				HR5159 (Lo-16bits)
HR5161	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5162				HR5161 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5163	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5164				HR5163 (Lo-16bits)
HR5165	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5166				HR5165 (Lo-16bits)
HR5167	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5168				HR5167 (Lo-16bits)
HR5169	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5170				HR5169 (Lo-16bits)
HR5171	DEF1:Pick-up setting I0set>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5172				HR5171 (Lo-16bits)
HR5173	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5174				HR5173 (Lo-16bits)
HR5175	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5176				HR5175 (Lo-16bits)
HR5177	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5178				HR5177 (Lo-16bits)
HR5179	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5180				HR5179 (Lo-16bits)
HR5181	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5182				HR5181 (Lo-16bits)
HR5183	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5184				HR5183 (Lo-16bits)
HR5185	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5186				HR5185 (Lo-16bits)
HR5187	DEF1:Pick-up setting U0set>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5188				HR5187 (Lo-16bits)
HR5189	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5190				HR5189 (Lo-16bits)
HR5191	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5192				HR5191 (Lo-16bits)
HR5193	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5194				HR5193 (Lo-16bits)
HR5195	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5196				HR5195 (Lo-16bits)
HR5197	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5198				HR5197 (Lo-16bits)
HR5199	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5200				HR5199 (Lo-16bits)
HR5201	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5202				HR5201 (Lo-16bits)
HR5203	DEF1:Comp. pick-up setting I0set (I0Cosfi)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5204				HR5203 (Lo-16bits)
HR5205	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5206				HR5205 (Lo-16bits)
HR5207	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5208				HR5207 (Lo-16bits)
HR5209	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5210				HR5209 (Lo-16bits)
HR5211	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5212				HR5211 (Lo-16bits)
HR5213	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5214				HR5213 (Lo-16bits)
HR5215	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5216				HR5215 (Lo-16bits)
HR5217	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5218				HR5217 (Lo-16bits)
HR5219	DEF1:Comp. pick-up setting U0set(I0Cosfi)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5220				HR5219 (Lo-16bits)
HR5221	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5222				HR5221 (Lo-16bits)
HR5223	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5224				HR5223 (Lo-16bits)
HR5225	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5226				HR5225 (Lo-16bits)
HR5227	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5228				HR5227 (Lo-16bits)
HR5229	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5230				HR5229 (Lo-16bits)
HR5231	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5232				HR5231 (Lo-16bits)
HR5233	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5234				HR5233 (Lo-16bits)
HR5235	DEF1:Unearthed / Compensated border angle (0 = not in use)	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5236				HR5235 (Lo-16bits)
HR5237	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5238				HR5237 (Lo-16bits)
HR5239	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5240				HR5239 (Lo-16bits)
HR5241	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5242				HR5241 (Lo-16bits)
HR5243	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5244				HR5243 (Lo-16bits)
HR5245	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5246				HR5245 (Lo-16bits)
HR5247	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5248				HR5247 (Lo-16bits)
HR5249	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5250				HR5249 (Lo-16bits)
HR5251	DEF1:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5252				HR5251 (Lo-16bits)
HR5253	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5254	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5255	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5256	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5257	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5258	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5259	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5260	DEF1:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5261	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5262				HR5261 (Lo-16bits)
HR5263	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5264				HR5263 (Lo-16bits)
HR5265	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5266				HR5265 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5267	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5268				HR5267 (Lo-16bits)
HR5269	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5270				HR5269 (Lo-16bits)
HR5271	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5272				HR5271 (Lo-16bits)
HR5273	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5274				HR5273 (Lo-16bits)
HR5275	DEF1:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5276				HR5275 (Lo-16bits)
HR5277	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5278				HR5277 (Lo-16bits)
HR5279	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5280				HR5279 (Lo-16bits)
HR5281	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5282				HR5281 (Lo-16bits)
HR5283	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5284				HR5283 (Lo-16bits)
HR5285	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5286				HR5285 (Lo-16bits)
HR5287	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5288				HR5287 (Lo-16bits)
HR5289	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5290				HR5289 (Lo-16bits)
HR5291	DEF1:Unearth. pick-up setting I0set (I0Sinfj)>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5292				HR5291 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5293	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5294				HR5293 (Lo-16bits)
HR5295	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5296				HR5295 (Lo-16bits)
HR5297	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5298				HR5297 (Lo-16bits)
HR5299	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5300				HR5299 (Lo-16bits)
HR5301	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5302				HR5301 (Lo-16bits)
HR5303	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5304				HR5303 (Lo-16bits)
HR5305	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5306				HR5305 (Lo-16bits)
HR5307	DEF1:Unearth. pick-up setting U0set(I0Sinf)>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5308				HR5307 (Lo-16bits)
HR5309	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5310				HR5309 (Lo-16bits)
HR5311	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5312				HR5311 (Lo-16bits)
HR5313	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5314				HR5313 (Lo-16bits)
HR5315	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5316				HR5315 (Lo-16bits)
HR5317	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5318				HR5317 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5319	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5320				HR5319 (Lo-16bits)
HR5321	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5322				HR5321 (Lo-16bits)
HR5323	DEF1:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5324				HR5323 (Lo-16bits)
HR5325	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5326				HR5325 (Lo-16bits)
HR5327	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5328				HR5327 (Lo-16bits)
HR5329	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5330				HR5329 (Lo-16bits)
HR5331	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5332				HR5331 (Lo-16bits)
HR5333	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5334				HR5333 (Lo-16bits)
HR5335	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5336				HR5335 (Lo-16bits)
HR5337	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5338				HR5337 (Lo-16bits)
HR5339	DEF1:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5340				HR5339 (Lo-16bits)
HR5341	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5342	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5343	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5344	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5345	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi

Holding Register	Parameter	Access	Length	Info
HR5346	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5347	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5348	DEF1:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5349	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5350	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5351	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5352	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5353	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5354	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5355	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5356	DEF1:Delay type	R(W)	1	0=DT 1=IDMT
HR5357	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5358				HR5357 (Lo-16bits)
HR5359	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5360				HR5359 (Lo-16bits)
HR5361	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5362				HR5361 (Lo-16bits)
HR5363	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5364				HR5363 (Lo-16bits)
HR5365	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5366				HR5365 (Lo-16bits)
HR5367	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5368				HR5367 (Lo-16bits)
HR5369	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5370				HR5369 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5371	DEF1:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5372				HR5371 (Lo-16bits)
HR5373	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5374				HR5373 (Lo-16bits)
HR5375	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5376				HR5375 (Lo-16bits)
HR5377	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5378				HR5377 (Lo-16bits)
HR5379	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5380				HR5379 (Lo-16bits)
HR5381	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5382				HR5381 (Lo-16bits)
HR5383	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5384				HR5383 (Lo-16bits)
HR5385	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5386				HR5385 (Lo-16bits)
HR5387	DEF1:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5388				HR5387 (Lo-16bits)
HR5389	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5390				HR5389 (Lo-16bits)
HR5391	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5392				HR5391 (Lo-16bits)
HR5393	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5394				HR5393 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5395	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5396				HR5395 (Lo-16bits)
HR5397	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5398				HR5397 (Lo-16bits)
HR5399	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5400				HR5399 (Lo-16bits)
HR5401	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5402				HR5401 (Lo-16bits)
HR5403	DEF1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5404				HR5403 (Lo-16bits)
HR5405	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5406	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5407	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5408	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5409	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5410	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5411	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5412	DEF1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5413	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5414	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5415	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5416	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5417	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5418	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5419	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5420	DEF1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5421	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5422	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5423	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5424	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5425	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5426	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5427	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5428	DEF1:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5429	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5430	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5431	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5432	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5433	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5434	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5435	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5436	DEF1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5437	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5438				HR5437 (Lo-16bits)
HR5439	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5440				HR5439 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5441	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5442				HR5441 (Lo-16bits)
HR5443	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5444				HR5443 (Lo-16bits)
HR5445	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5446				HR5445 (Lo-16bits)
HR5447	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5448				HR5447 (Lo-16bits)
HR5449	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5450				HR5449 (Lo-16bits)
HR5451	DEF1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5452				HR5451 (Lo-16bits)
HR5453	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5454				HR5453 (Lo-16bits)
HR5455	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5456				HR5455 (Lo-16bits)
HR5457	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5458				HR5457 (Lo-16bits)
HR5459	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5460				HR5459 (Lo-16bits)
HR5461	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5462				HR5461 (Lo-16bits)
HR5463	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5464				HR5463 (Lo-16bits)
HR5465	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5466				HR5465 (Lo-16bits)
HR5467	DEF1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5468				HR5467 (Lo-16bits)
HR5469	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5470				HR5469 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5471	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5472				HR5471 (Lo-16bits)
HR5473	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5474				HR5473 (Lo-16bits)
HR5475	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5476				HR5475 (Lo-16bits)
HR5477	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5478				HR5477 (Lo-16bits)
HR5479	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5480				HR5479 (Lo-16bits)
HR5481	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5482				HR5481 (Lo-16bits)
HR5483	DEF1:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5484				HR5483 (Lo-16bits)
HR5485	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5486				HR5485 (Lo-16bits)
HR5487	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5488				HR5487 (Lo-16bits)
HR5489	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5490				HR5489 (Lo-16bits)
HR5491	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5492				HR5491 (Lo-16bits)
HR5493	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5494				HR5493 (Lo-16bits)
HR5495	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5496				HR5495 (Lo-16bits)
HR5497	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5498				HR5497 (Lo-16bits)
HR5499	DEF1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5500				HR5499 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5501	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5502	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5503	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5504	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5505	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5506	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5507	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5508	DEF1:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5509	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5510				HR5509 (Lo-16bits)
HR5511	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5512				HR5511 (Lo-16bits)
HR5513	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5514				HR5513 (Lo-16bits)
HR5515	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5516				HR5515 (Lo-16bits)
HR5517	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5518				HR5517 (Lo-16bits)
HR5519	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5520				HR5519 (Lo-16bits)
HR5521	DEF1:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5522				HR5521 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5523	DEF1:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5524				HR5523 (Lo-16bits)
HR5525	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5526	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5527	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5528	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5529	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5530	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5531	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5532	DEF1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5533	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5534				HR5533 (Lo-16bits)
HR5535	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5536				HR5535 (Lo-16bits)
HR5537	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5538				HR5537 (Lo-16bits)
HR5539	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5540				HR5539 (Lo-16bits)
HR5541	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5542				HR5541 (Lo-16bits)
HR5543	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5544				HR5543 (Lo-16bits)
HR5545	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5546				HR5545 (Lo-16bits)
HR5547	DEF1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5548				HR5547 (Lo-16bits)
HR5549	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5550	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5551	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5552	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5553	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5554	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5555	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5556	DEF1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5557	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5558	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5559	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5560	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5561	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5562	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5563	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5564	DEF1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5565	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5566	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5567	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR5568	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5569	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5570	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5571	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5572	DEF2:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5573	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5574				HR5573 (Lo-16bits)
HR5575	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5576				HR5575 (Lo-16bits)
HR5577	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5578				HR5577 (Lo-16bits)
HR5579	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5580				HR5579 (Lo-16bits)
HR5581	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5582				HR5581 (Lo-16bits)
HR5583	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5584				HR5583 (Lo-16bits)
HR5585	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5586				HR5585 (Lo-16bits)
HR5587	DEF2:Pick-up setting I0set>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5588				HR5587 (Lo-16bits)
HR5589	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5590				HR5589 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5591	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5592				HR5591 (Lo-16bits)
HR5593	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5594				HR5593 (Lo-16bits)
HR5595	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5596				HR5595 (Lo-16bits)
HR5597	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5598				HR5597 (Lo-16bits)
HR5599	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5600				HR5599 (Lo-16bits)
HR5601	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5602				HR5601 (Lo-16bits)
HR5603	DEF2:Pick-up setting U0set>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5604				HR5603 (Lo-16bits)
HR5605	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5606				HR5605 (Lo-16bits)
HR5607	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5608				HR5607 (Lo-16bits)
HR5609	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5610				HR5609 (Lo-16bits)
HR5611	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5612				HR5611 (Lo-16bits)
HR5613	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5614				HR5613 (Lo-16bits)
HR5615	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5616				HR5615 (Lo-16bits)
HR5617	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5618				HR5617 (Lo-16bits)
HR5619	DEF2:Comp. pick-up setting I0set (I0Cosfi)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5620				HR5619 (Lo-16bits)
HR5621	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5622				HR5621 (Lo-16bits)
HR5623	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5624				HR5623 (Lo-16bits)
HR5625	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5626				HR5625 (Lo-16bits)
HR5627	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5628				HR5627 (Lo-16bits)
HR5629	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5630				HR5629 (Lo-16bits)
HR5631	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5632				HR5631 (Lo-16bits)
HR5633	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5634				HR5633 (Lo-16bits)
HR5635	DEF2:Comp. pick-up setting U0set(I0Cosfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5636				HR5635 (Lo-16bits)
HR5637	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5638				HR5637 (Lo-16bits)
HR5639	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5640				HR5639 (Lo-16bits)
HR5641	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5642				HR5641 (Lo-16bits)
HR5643	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5644				HR5643 (Lo-16bits)
HR5645	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5646				HR5645 (Lo-16bits)
HR5647	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5648				HR5647 (Lo-16bits)
HR5649	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5650				HR5649 (Lo-16bits)
HR5651	DEF2:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR5652				HR5651 (Lo-16bits)
HR5653	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5654				HR5653 (Lo-16bits)
HR5655	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5656				HR5655 (Lo-16bits)
HR5657	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5658				HR5657 (Lo-16bits)
HR5659	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5660				HR5659 (Lo-16bits)
HR5661	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5662				HR5661 (Lo-16bits)
HR5663	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5664				HR5663 (Lo-16bits)
HR5665	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5666				HR5665 (Lo-16bits)
HR5667	DEF2:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR5668				HR5667 (Lo-16bits)
HR5669	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5670	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5671	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5672	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5673	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5674	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5675	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5676	DEF2:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR5677	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5678				HR5677 (Lo-16bits)
HR5679	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5680				HR5679 (Lo-16bits)
HR5681	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5682				HR5681 (Lo-16bits)
HR5683	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5684				HR5683 (Lo-16bits)
HR5685	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5686				HR5685 (Lo-16bits)
HR5687	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5688				HR5687 (Lo-16bits)
HR5689	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5690				HR5689 (Lo-16bits)
HR5691	DEF2:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR5692				HR5691 (Lo-16bits)
HR5693	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5694				HR5693 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5695	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5696				HR5695 (Lo-16bits)
HR5697	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5698				HR5697 (Lo-16bits)
HR5699	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5700				HR5699 (Lo-16bits)
HR5701	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5702				HR5701 (Lo-16bits)
HR5703	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5704				HR5703 (Lo-16bits)
HR5705	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5706				HR5705 (Lo-16bits)
HR5707	DEF2:Unearth. pick-up setting I0set (I0Sinf)>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5708				HR5707 (Lo-16bits)
HR5709	DEF2:Unearth. pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5710				HR5709 (Lo-16bits)
HR5711	DEF2:Unearth. pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5712				HR5711 (Lo-16bits)
HR5713	DEF2:Unearth. pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5714				HR5713 (Lo-16bits)
HR5715	DEF2:Unearth. pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5716				HR5715 (Lo-16bits)
HR5717	DEF2:Unearth. pick-up setting U0set(I0Sinf)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5718				HR5717 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5719	DEF2:Unearth. pick-up setting U0set(I0SInfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5720				HR5719 (Lo-16bits)
HR5721	DEF2:Unearth. pick-up setting U0set(I0SInfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5722				HR5721 (Lo-16bits)
HR5723	DEF2:Unearth. pick-up setting U0set(I0SInfi)>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR5724				HR5723 (Lo-16bits)
HR5725	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5726				HR5725 (Lo-16bits)
HR5727	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5728				HR5727 (Lo-16bits)
HR5729	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5730				HR5729 (Lo-16bits)
HR5731	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5732				HR5731 (Lo-16bits)
HR5733	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5734				HR5733 (Lo-16bits)
HR5735	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5736				HR5735 (Lo-16bits)
HR5737	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5738				HR5737 (Lo-16bits)
HR5739	DEF2:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR5740				HR5739 (Lo-16bits)
HR5741	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5742				HR5741 (Lo-16bits)
HR5743	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5744				HR5743 (Lo-16bits)
HR5745	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5746				HR5745 (Lo-16bits)
HR5747	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5748				HR5747 (Lo-16bits)
HR5749	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5750				HR5749 (Lo-16bits)
HR5751	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5752				HR5751 (Lo-16bits)
HR5753	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5754				HR5753 (Lo-16bits)
HR5755	DEF2:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR5756				HR5755 (Lo-16bits)
HR5757	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5758	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5759	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5760	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5761	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5762	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5763	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5764	DEF2:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR5765	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5766	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5767	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5768	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5769	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5770	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5771	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5772	DEF2:Delay type	R(W)	1	0=DT 1=IDMT
HR5773	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5774				HR5773 (Lo-16bits)
HR5775	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5776				HR5775 (Lo-16bits)
HR5777	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5778				HR5777 (Lo-16bits)
HR5779	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5780				HR5779 (Lo-16bits)
HR5781	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5782				HR5781 (Lo-16bits)
HR5783	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5784				HR5783 (Lo-16bits)
HR5785	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5786				HR5785 (Lo-16bits)
HR5787	DEF2:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5788				HR5787 (Lo-16bits)
HR5789	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5790				HR5789 (Lo-16bits)
HR5791	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5792				HR5791 (Lo-16bits)
HR5793	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5794				HR5793 (Lo-16bits)
HR5795	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5796				HR5795 (Lo-16bits)
HR5797	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5798				HR5797 (Lo-16bits)
HR5799	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5800				HR5799 (Lo-16bits)
HR5801	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5802				HR5801 (Lo-16bits)
HR5803	DEF2:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5804				HR5803 (Lo-16bits)
HR5805	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5806				HR5805 (Lo-16bits)
HR5807	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5808				HR5807 (Lo-16bits)
HR5809	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5810				HR5809 (Lo-16bits)
HR5811	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5812				HR5811 (Lo-16bits)
HR5813	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5814				HR5813 (Lo-16bits)
HR5815	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5816				HR5815 (Lo-16bits)
HR5817	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5818				HR5817 (Lo-16bits)
HR5819	DEF2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR5820				HR5819 (Lo-16bits)
HR5821	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5822	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR5823	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5824	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5825	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5826	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5827	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5828	DEF2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR5829	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5830	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5831	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5832	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5833	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5834	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5835	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5836	DEF2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR5837	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5838	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5839	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5840	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5841	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5842	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5843	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR5844	DEF2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR5845	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5846	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5847	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5848	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5849	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5850	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5851	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5852	DEF2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR5853	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5854				HR5853 (Lo-16bits)
HR5855	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5856				HR5855 (Lo-16bits)
HR5857	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5858				HR5857 (Lo-16bits)
HR5859	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5860				HR5859 (Lo-16bits)
HR5861	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5862				HR5861 (Lo-16bits)
HR5863	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5864				HR5863 (Lo-16bits)
HR5865	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5866				HR5865 (Lo-16bits)
HR5867	DEF2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR5868				HR5867 (Lo-16bits)
HR5869	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5870				HR5869 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5871	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5872				HR5871 (Lo-16bits)
HR5873	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5874				HR5873 (Lo-16bits)
HR5875	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5876				HR5875 (Lo-16bits)
HR5877	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5878				HR5877 (Lo-16bits)
HR5879	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5880				HR5879 (Lo-16bits)
HR5881	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5882				HR5881 (Lo-16bits)
HR5883	DEF2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5884				HR5883 (Lo-16bits)
HR5885	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5886				HR5885 (Lo-16bits)
HR5887	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5888				HR5887 (Lo-16bits)
HR5889	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5890				HR5889 (Lo-16bits)
HR5891	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5892				HR5891 (Lo-16bits)
HR5893	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5894				HR5893 (Lo-16bits)
HR5895	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5896				HR5895 (Lo-16bits)
HR5897	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5898				HR5897 (Lo-16bits)
HR5899	DEF2:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5900				HR5899 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5901	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5902				HR5901 (Lo-16bits)
HR5903	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5904				HR5903 (Lo-16bits)
HR5905	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5906				HR5905 (Lo-16bits)
HR5907	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5908				HR5907 (Lo-16bits)
HR5909	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5910				HR5909 (Lo-16bits)
HR5911	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5912				HR5911 (Lo-16bits)
HR5913	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5914				HR5913 (Lo-16bits)
HR5915	DEF2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR5916				HR5915 (Lo-16bits)
HR5917	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5918	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5919	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5920	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5921	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5922	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5923	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5924	DEF2:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR5925	DEF2:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5926				HR5925 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5927	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5928				HR5927 (Lo-16bits)
HR5929	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5930				HR5929 (Lo-16bits)
HR5931	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5932				HR5931 (Lo-16bits)
HR5933	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5934				HR5933 (Lo-16bits)
HR5935	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5936				HR5935 (Lo-16bits)
HR5937	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5938				HR5937 (Lo-16bits)
HR5939	DEF2:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR5940				HR5939 (Lo-16bits)
HR5941	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5942	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5943	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5944	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5945	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5946	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5947	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5948	DEF2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR5949	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5950				HR5949 (Lo-16bits)
HR5951	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5952				HR5951 (Lo-16bits)
HR5953	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR5954				HR5953 (Lo-16bits)
HR5955	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5956				HR5955 (Lo-16bits)
HR5957	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5958				HR5957 (Lo-16bits)
HR5959	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5960				HR5959 (Lo-16bits)
HR5961	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5962				HR5961 (Lo-16bits)
HR5963	DEF2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR5964				HR5963 (Lo-16bits)
HR5965	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5966	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5967	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5968	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5969	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5970	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5971	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5972	DEF2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR5973	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5974	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5975	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5976	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5977	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR5978	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5979	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5980	DEF2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR5981	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5982	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5983	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5984	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5985	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5986	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5987	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5988	DEF3:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR5989	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5990				HR5989 (Lo-16bits)
HR5991	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5992				HR5991 (Lo-16bits)
HR5993	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5994				HR5993 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR5995	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5996				HR5995 (Lo-16bits)
HR5997	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR5998				HR5997 (Lo-16bits)
HR5999	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6000				HR5999 (Lo-16bits)
HR6001	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6002				HR6001 (Lo-16bits)
HR6003	DEF3:Pick-up setting I0set>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6004				HR6003 (Lo-16bits)
HR6005	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6006				HR6005 (Lo-16bits)
HR6007	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6008				HR6007 (Lo-16bits)
HR6009	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6010				HR6009 (Lo-16bits)
HR6011	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6012				HR6011 (Lo-16bits)
HR6013	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6014				HR6013 (Lo-16bits)
HR6015	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6016				HR6015 (Lo-16bits)
HR6017	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6018				HR6017 (Lo-16bits)
HR6019	DEF3:Pick-up setting U0set>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6020				HR6019 (Lo-16bits)
HR6021	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6022				HR6021 (Lo-16bits)
HR6023	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6024				HR6023 (Lo-16bits)
HR6025	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6026				HR6025 (Lo-16bits)
HR6027	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6028				HR6027 (Lo-16bits)
HR6029	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6030				HR6029 (Lo-16bits)
HR6031	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6032				HR6031 (Lo-16bits)
HR6033	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6034				HR6033 (Lo-16bits)
HR6035	DEF3:Comp. pick-up setting I0set (I0Cosfi)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6036				HR6035 (Lo-16bits)
HR6037	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6038				HR6037 (Lo-16bits)
HR6039	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6040				HR6039 (Lo-16bits)
HR6041	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6042				HR6041 (Lo-16bits)
HR6043	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6044				HR6043 (Lo-16bits)
HR6045	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6046				HR6045 (Lo-16bits)
HR6047	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6048				HR6047 (Lo-16bits)
HR6049	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6050				HR6049 (Lo-16bits)
HR6051	DEF3:Comp. pick-up setting U0set(I0Cosfi)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6052				HR6051 (Lo-16bits)
HR6053	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6054				HR6053 (Lo-16bits)
HR6055	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6056				HR6055 (Lo-16bits)
HR6057	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6058				HR6057 (Lo-16bits)
HR6059	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6060				HR6059 (Lo-16bits)
HR6061	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6062				HR6061 (Lo-16bits)
HR6063	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6064				HR6063 (Lo-16bits)
HR6065	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6066				HR6065 (Lo-16bits)
HR6067	DEF3:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6068				HR6067 (Lo-16bits)
HR6069	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6070				HR6069 (Lo-16bits)
HR6071	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6072				HR6071 (Lo-16bits)
HR6073	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6074				HR6073 (Lo-16bits)
HR6075	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6076				HR6075 (Lo-16bits)
HR6077	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6078				HR6077 (Lo-16bits)
HR6079	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6080				HR6079 (Lo-16bits)
HR6081	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6082				HR6081 (Lo-16bits)
HR6083	DEF3:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6084				HR6083 (Lo-16bits)
HR6085	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6086	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6087	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6088	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6089	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6090	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6091	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6092	DEF3:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6093	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6094				HR6093 (Lo-16bits)
HR6095	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6096				HR6095 (Lo-16bits)
HR6097	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6098				HR6097 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6099	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6100				HR6099 (Lo-16bits)
HR6101	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6102				HR6101 (Lo-16bits)
HR6103	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6104				HR6103 (Lo-16bits)
HR6105	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6106				HR6105 (Lo-16bits)
HR6107	DEF3:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6108				HR6107 (Lo-16bits)
HR6109	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6110				HR6109 (Lo-16bits)
HR6111	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6112				HR6111 (Lo-16bits)
HR6113	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6114				HR6113 (Lo-16bits)
HR6115	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6116				HR6115 (Lo-16bits)
HR6117	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6118				HR6117 (Lo-16bits)
HR6119	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6120				HR6119 (Lo-16bits)
HR6121	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6122				HR6121 (Lo-16bits)
HR6123	DEF3:Unearth. pick-up setting I0set (I0Sinfj)>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6124				HR6123 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6125	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6126				HR6125 (Lo-16bits)
HR6127	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6128				HR6127 (Lo-16bits)
HR6129	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6130				HR6129 (Lo-16bits)
HR6131	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6132				HR6131 (Lo-16bits)
HR6133	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6134				HR6133 (Lo-16bits)
HR6135	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6136				HR6135 (Lo-16bits)
HR6137	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6138				HR6137 (Lo-16bits)
HR6139	DEF3:Unearth. pick-up setting U0set(I0Sinf)>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6140				HR6139 (Lo-16bits)
HR6141	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6142				HR6141 (Lo-16bits)
HR6143	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6144				HR6143 (Lo-16bits)
HR6145	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6146				HR6145 (Lo-16bits)
HR6147	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6148				HR6147 (Lo-16bits)
HR6149	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6150				HR6149 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6151	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6152				HR6151 (Lo-16bits)
HR6153	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6154				HR6153 (Lo-16bits)
HR6155	DEF3:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6156				HR6155 (Lo-16bits)
HR6157	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6158				HR6157 (Lo-16bits)
HR6159	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6160				HR6159 (Lo-16bits)
HR6161	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6162				HR6161 (Lo-16bits)
HR6163	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6164				HR6163 (Lo-16bits)
HR6165	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6166				HR6165 (Lo-16bits)
HR6167	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6168				HR6167 (Lo-16bits)
HR6169	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6170				HR6169 (Lo-16bits)
HR6171	DEF3:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6172				HR6171 (Lo-16bits)
HR6173	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6174	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6175	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6176	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi
HR6177	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for IOCosfi and IOSinfi

Holding Register	Parameter	Access	Length	Info
HR6178	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6179	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6180	DEF3:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6181	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6182	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6183	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6184	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6185	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6186	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6187	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6188	DEF3:Delay type	R(W)	1	0=DT 1=IDMT
HR6189	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6190				HR6189 (Lo-16bits)
HR6191	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6192				HR6191 (Lo-16bits)
HR6193	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6194				HR6193 (Lo-16bits)
HR6195	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6196				HR6195 (Lo-16bits)
HR6197	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6198				HR6197 (Lo-16bits)
HR6199	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6200				HR6199 (Lo-16bits)
HR6201	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6202				HR6201 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6203	DEF3:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6204				HR6203 (Lo-16bits)
HR6205	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6206				HR6205 (Lo-16bits)
HR6207	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6208				HR6207 (Lo-16bits)
HR6209	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6210				HR6209 (Lo-16bits)
HR6211	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6212				HR6211 (Lo-16bits)
HR6213	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6214				HR6213 (Lo-16bits)
HR6215	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6216				HR6215 (Lo-16bits)
HR6217	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6218				HR6217 (Lo-16bits)
HR6219	DEF3:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6220				HR6219 (Lo-16bits)
HR6221	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6222				HR6221 (Lo-16bits)
HR6223	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6224				HR6223 (Lo-16bits)
HR6225	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6226				HR6225 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6227	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6228				HR6227 (Lo-16bits)
HR6229	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6230				HR6229 (Lo-16bits)
HR6231	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6232				HR6231 (Lo-16bits)
HR6233	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6234				HR6233 (Lo-16bits)
HR6235	DEF3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6236				HR6235 (Lo-16bits)
HR6237	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6238	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6239	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6240	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6241	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6242	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6243	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6244	DEF3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6245	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6246	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6247	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6248	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6249	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6250	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6251	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6252	DEF3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6253	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6254	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6255	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6256	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6257	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6258	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6259	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6260	DEF3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6261	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6262	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6263	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6264	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6265	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6266	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6267	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6268	DEF3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6269	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6270				HR6269 (Lo-16bits)
HR6271	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6272				HR6271 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6273	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6274				HR6273 (Lo-16bits)
HR6275	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6276				HR6275 (Lo-16bits)
HR6277	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6278				HR6277 (Lo-16bits)
HR6279	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6280				HR6279 (Lo-16bits)
HR6281	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6282				HR6281 (Lo-16bits)
HR6283	DEF3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6284				HR6283 (Lo-16bits)
HR6285	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6286				HR6285 (Lo-16bits)
HR6287	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6288				HR6287 (Lo-16bits)
HR6289	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6290				HR6289 (Lo-16bits)
HR6291	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6292				HR6291 (Lo-16bits)
HR6293	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6294				HR6293 (Lo-16bits)
HR6295	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6296				HR6295 (Lo-16bits)
HR6297	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6298				HR6297 (Lo-16bits)
HR6299	DEF3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6300				HR6299 (Lo-16bits)
HR6301	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6302				HR6301 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6303	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6304				HR6303 (Lo-16bits)
HR6305	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6306				HR6305 (Lo-16bits)
HR6307	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6308				HR6307 (Lo-16bits)
HR6309	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6310				HR6309 (Lo-16bits)
HR6311	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6312				HR6311 (Lo-16bits)
HR6313	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6314				HR6313 (Lo-16bits)
HR6315	DEF3:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6316				HR6315 (Lo-16bits)
HR6317	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6318				HR6317 (Lo-16bits)
HR6319	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6320				HR6319 (Lo-16bits)
HR6321	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6322				HR6321 (Lo-16bits)
HR6323	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6324				HR6323 (Lo-16bits)
HR6325	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6326				HR6325 (Lo-16bits)
HR6327	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6328				HR6327 (Lo-16bits)
HR6329	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6330				HR6329 (Lo-16bits)
HR6331	DEF3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6332				HR6331 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6333	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6334	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6335	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6336	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6337	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6338	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6339	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6340	DEF3:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6341	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6342				HR6341 (Lo-16bits)
HR6343	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6344				HR6343 (Lo-16bits)
HR6345	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6346				HR6345 (Lo-16bits)
HR6347	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6348				HR6347 (Lo-16bits)
HR6349	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6350				HR6349 (Lo-16bits)
HR6351	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6352				HR6351 (Lo-16bits)
HR6353	DEF3:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6354				HR6353 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6355	DEF3:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6356				HR6355 (Lo-16bits)
HR6357	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6358	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6359	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6360	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6361	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6362	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6363	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6364	DEF3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6365	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6366				HR6365 (Lo-16bits)
HR6367	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6368				HR6367 (Lo-16bits)
HR6369	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6370				HR6369 (Lo-16bits)
HR6371	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6372				HR6371 (Lo-16bits)
HR6373	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6374				HR6373 (Lo-16bits)
HR6375	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6376				HR6375 (Lo-16bits)
HR6377	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6378				HR6377 (Lo-16bits)
HR6379	DEF3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6380				HR6379 (Lo-16bits)
HR6381	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6382	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6383	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6384	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6385	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6386	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6387	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6388	DEF3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6389	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6390	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6391	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6392	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6393	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6394	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6395	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6396	DEF3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6397	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6398	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6399	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]

Holding Register	Parameter	Access	Length	Info
HR6400	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6401	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6402	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6403	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6404	DEF4:Grounding type	R(W)	1	0=Unearthed [32N Var] 1=Petersen coil GND [32N Watt] 2=Grounded [67N] 3=I0Cos&I0Sin Broadrange with MCD [32N Var/Watt]
HR6405	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6406				HR6405 (Lo-16bits)
HR6407	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6408				HR6407 (Lo-16bits)
HR6409	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6410				HR6409 (Lo-16bits)
HR6411	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6412				HR6411 (Lo-16bits)
HR6413	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6414				HR6413 (Lo-16bits)
HR6415	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6416				HR6415 (Lo-16bits)
HR6417	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6418				HR6417 (Lo-16bits)
HR6419	DEF4:Pick-up setting I0set>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6420				HR6419 (Lo-16bits)
HR6421	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6422				HR6421 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6423	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6424				HR6423 (Lo-16bits)
HR6425	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6426				HR6425 (Lo-16bits)
HR6427	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6428				HR6427 (Lo-16bits)
HR6429	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6430				HR6429 (Lo-16bits)
HR6431	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6432				HR6431 (Lo-16bits)
HR6433	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6434				HR6433 (Lo-16bits)
HR6435	DEF4:Pick-up setting U0set>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6436				HR6435 (Lo-16bits)
HR6437	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6438				HR6437 (Lo-16bits)
HR6439	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6440				HR6439 (Lo-16bits)
HR6441	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6442				HR6441 (Lo-16bits)
HR6443	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6444				HR6443 (Lo-16bits)
HR6445	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6446				HR6445 (Lo-16bits)
HR6447	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6448				HR6447 (Lo-16bits)
HR6449	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6450				HR6449 (Lo-16bits)
HR6451	DEF4:Comp. pick-up setting I0set (I0Cosfi)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6452				HR6451 (Lo-16bits)
HR6453	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6454				HR6453 (Lo-16bits)
HR6455	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6456				HR6455 (Lo-16bits)
HR6457	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6458				HR6457 (Lo-16bits)
HR6459	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6460				HR6459 (Lo-16bits)
HR6461	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6462				HR6461 (Lo-16bits)
HR6463	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6464				HR6463 (Lo-16bits)
HR6465	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6466				HR6465 (Lo-16bits)
HR6467	DEF4:Comp. pick-up setting U0set(I0Cosfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6468				HR6467 (Lo-16bits)
HR6469	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6470				HR6469 (Lo-16bits)
HR6471	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6472				HR6471 (Lo-16bits)
HR6473	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6474				HR6473 (Lo-16bits)
HR6475	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6476				HR6475 (Lo-16bits)
HR6477	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6478				HR6477 (Lo-16bits)
HR6479	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6480				HR6479 (Lo-16bits)
HR6481	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6482				HR6481 (Lo-16bits)
HR6483	DEF4:Unearthed / Compensated border angle	R(W)	2	FLOAT [-45...90] (Hi-16bits)
HR6484				HR6483 (Lo-16bits)
HR6485	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6486				HR6485 (Lo-16bits)
HR6487	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6488				HR6487 (Lo-16bits)
HR6489	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6490				HR6489 (Lo-16bits)
HR6491	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6492				HR6491 (Lo-16bits)
HR6493	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6494				HR6493 (Lo-16bits)
HR6495	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6496				HR6495 (Lo-16bits)
HR6497	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6498				HR6497 (Lo-16bits)
HR6499	DEF4:Resistive (I0Cosfi) mode I0 angle blinder	R(W)	2	FLOAT [-90...0] (Hi-16bits)
HR6500				HR6499 (Lo-16bits)
HR6501	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6502	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6503	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6504	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6505	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6506	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6507	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6508	DEF4:Multi-criteria detection	R(W)	1	0=Not Used 1=Is Used
HR6509	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6510				HR6509 (Lo-16bits)
HR6511	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6512				HR6511 (Lo-16bits)
HR6513	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6514				HR6513 (Lo-16bits)
HR6515	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6516				HR6515 (Lo-16bits)
HR6517	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6518				HR6517 (Lo-16bits)
HR6519	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6520				HR6519 (Lo-16bits)
HR6521	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6522				HR6521 (Lo-16bits)
HR6523	DEF4:Angle MCD	R(W)	2	FLOAT [0...45] (Hi-16bits)
HR6524				HR6523 (Lo-16bits)
HR6525	DEF4:Unearth. pick-up setting I0set (I0Sinf)>>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6526				HR6525 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6527	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6528				HR6527 (Lo-16bits)
HR6529	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6530				HR6529 (Lo-16bits)
HR6531	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6532				HR6531 (Lo-16bits)
HR6533	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6534				HR6533 (Lo-16bits)
HR6535	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6536				HR6535 (Lo-16bits)
HR6537	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6538				HR6537 (Lo-16bits)
HR6539	DEF4:Unearth. pick-up setting I0set (I0Sinf) >>>>	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR6540				HR6539 (Lo-16bits)
HR6541	DEF4:Unearth. pick-up setting U0set(I0Sinf) >>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6542				HR6541 (Lo-16bits)
HR6543	DEF4:Unearth. pick-up setting U0set(I0Sinf) >>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6544				HR6543 (Lo-16bits)
HR6545	DEF4:Unearth. pick-up setting U0set(I0Sinf) >>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6546				HR6545 (Lo-16bits)
HR6547	DEF4:Unearth. pick-up setting U0set(I0Sinf) >>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6548				HR6547 (Lo-16bits)
HR6549	DEF4:Unearth. pick-up setting U0set(I0Sinf) >>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6550				HR6549 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6551	DEF4:Unearth. pick-up setting U0set(I0SInfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6552				HR6551 (Lo-16bits)
HR6553	DEF4:Unearth. pick-up setting U0set(I0SInfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6554				HR6553 (Lo-16bits)
HR6555	DEF4:Unearth. pick-up setting U0set(I0SInfi)>>>>	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR6556				HR6555 (Lo-16bits)
HR6557	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6558				HR6557 (Lo-16bits)
HR6559	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6560				HR6559 (Lo-16bits)
HR6561	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6562				HR6561 (Lo-16bits)
HR6563	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6564				HR6563 (Lo-16bits)
HR6565	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6566				HR6565 (Lo-16bits)
HR6567	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6568				HR6567 (Lo-16bits)
HR6569	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6570				HR6569 (Lo-16bits)
HR6571	DEF4:Tripping area size (+/-)	R(W)	2	FLOAT [5...135] (Hi-16bits)
HR6572				HR6571 (Lo-16bits)
HR6573	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6574				HR6573 (Lo-16bits)
HR6575	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6576				HR6575 (Lo-16bits)
HR6577	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6578				HR6577 (Lo-16bits)
HR6579	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6580				HR6579 (Lo-16bits)
HR6581	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6582				HR6581 (Lo-16bits)
HR6583	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6584				HR6583 (Lo-16bits)
HR6585	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6586				HR6585 (Lo-16bits)
HR6587	DEF4:Tripping area center	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR6588				HR6587 (Lo-16bits)
HR6589	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6590	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6591	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6592	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6593	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6594	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6595	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6596	DEF4:Delay type of broad range characteristics	R(W)	1	0=Common time 1=Separate times for I0Cosfi and I0Sinfi
HR6597	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6598	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6599	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6600	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6601	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6602	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6603	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6604	DEF4:Delay type	R(W)	1	0=DT 1=IDMT
HR6605	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6606				HR6605 (Lo-16bits)
HR6607	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6608				HR6607 (Lo-16bits)
HR6609	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6610				HR6609 (Lo-16bits)
HR6611	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6612				HR6611 (Lo-16bits)
HR6613	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6614				HR6613 (Lo-16bits)
HR6615	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6616				HR6615 (Lo-16bits)
HR6617	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6618				HR6617 (Lo-16bits)
HR6619	DEF4:Definite operating time delay (Compensated)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6620				HR6619 (Lo-16bits)
HR6621	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6622				HR6621 (Lo-16bits)
HR6623	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6624				HR6623 (Lo-16bits)
HR6625	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6626				HR6625 (Lo-16bits)
HR6627	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6628				HR6627 (Lo-16bits)
HR6629	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6630				HR6629 (Lo-16bits)
HR6631	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6632				HR6631 (Lo-16bits)
HR6633	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6634				HR6633 (Lo-16bits)
HR6635	DEF4:Definite operating time delay (Unearthed)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6636				HR6635 (Lo-16bits)
HR6637	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6638				HR6637 (Lo-16bits)
HR6639	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6640				HR6639 (Lo-16bits)
HR6641	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6642				HR6641 (Lo-16bits)
HR6643	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6644				HR6643 (Lo-16bits)
HR6645	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6646				HR6645 (Lo-16bits)
HR6647	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6648				HR6647 (Lo-16bits)
HR6649	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6650				HR6649 (Lo-16bits)
HR6651	DEF4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6652				HR6651 (Lo-16bits)
HR6653	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6654	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR6655	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6656	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6657	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6658	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6659	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6660	DEF4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6661	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6662	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6663	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6664	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6665	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6666	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6667	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6668	DEF4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6669	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6670	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6671	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6672	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6673	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6674	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6675	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6676	DEF4:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LTI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR6677	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6678	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6679	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6680	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6681	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6682	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6683	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6684	DEF4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type
HR6685	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6686				HR6685 (Lo-16bits)
HR6687	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6688				HR6687 (Lo-16bits)
HR6689	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6690				HR6689 (Lo-16bits)
HR6691	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6692				HR6691 (Lo-16bits)
HR6693	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6694				HR6693 (Lo-16bits)
HR6695	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6696				HR6695 (Lo-16bits)
HR6697	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6698				HR6697 (Lo-16bits)
HR6699	DEF4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6700				HR6699 (Lo-16bits)
HR6701	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6702				HR6701 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6703	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6704				HR6703 (Lo-16bits)
HR6705	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6706				HR6705 (Lo-16bits)
HR6707	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6708				HR6707 (Lo-16bits)
HR6709	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6710				HR6709 (Lo-16bits)
HR6711	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6712				HR6711 (Lo-16bits)
HR6713	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6714				HR6713 (Lo-16bits)
HR6715	DEF4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6716				HR6715 (Lo-16bits)
HR6717	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6718				HR6717 (Lo-16bits)
HR6719	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6720				HR6719 (Lo-16bits)
HR6721	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6722				HR6721 (Lo-16bits)
HR6723	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6724				HR6723 (Lo-16bits)
HR6725	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6726				HR6725 (Lo-16bits)
HR6727	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6728				HR6727 (Lo-16bits)
HR6729	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6730				HR6729 (Lo-16bits)
HR6731	DEF4:IDMT constant B	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6732				HR6731 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6733	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6734				HR6733 (Lo-16bits)
HR6735	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6736				HR6735 (Lo-16bits)
HR6737	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6738				HR6737 (Lo-16bits)
HR6739	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6740				HR6739 (Lo-16bits)
HR6741	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6742				HR6741 (Lo-16bits)
HR6743	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6744				HR6743 (Lo-16bits)
HR6745	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6746				HR6745 (Lo-16bits)
HR6747	DEF4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6748				HR6747 (Lo-16bits)
HR6749	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6750	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6751	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6752	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6753	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6754	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6755	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6756	DEF4:Inrush harmonic blocking (Internal Only Trip)	R(W)	1	0=No 1=Yes
HR6757	DEF4:2nd harmonic blocking limit (I _{harm} / I _{fund})	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6758				HR6757 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6759	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6760				HR6759 (Lo-16bits)
HR6761	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6762				HR6761 (Lo-16bits)
HR6763	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6764				HR6763 (Lo-16bits)
HR6765	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6766				HR6765 (Lo-16bits)
HR6767	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6768				HR6767 (Lo-16bits)
HR6769	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6770				HR6769 (Lo-16bits)
HR6771	DEF4:2nd harmonic blocking limit (lharm / Ifund)	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR6772				HR6771 (Lo-16bits)
HR6773	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6774	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6775	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6776	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6777	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6778	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6779	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6780	DEF4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6781	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6782				HR6781 (Lo-16bits)
HR6783	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6784				HR6783 (Lo-16bits)
HR6785	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR6786				HR6785 (Lo-16bits)
HR6787	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6788				HR6787 (Lo-16bits)
HR6789	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6790				HR6789 (Lo-16bits)
HR6791	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6792				HR6791 (Lo-16bits)
HR6793	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6794				HR6793 (Lo-16bits)
HR6795	DEF4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6796				HR6795 (Lo-16bits)
HR6797	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6798	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6799	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6800	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6801	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6802	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6803	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6804	DEF4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR6805	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6806	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6807	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6808	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6809	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR6810	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6811	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6812	DEF4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR6813	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6814				HR6813 (Lo-16bits)
HR6815	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6816				HR6815 (Lo-16bits)
HR6817	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6818				HR6817 (Lo-16bits)
HR6819	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6820				HR6819 (Lo-16bits)
HR6821	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6822				HR6821 (Lo-16bits)
HR6823	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6824				HR6823 (Lo-16bits)
HR6825	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6826				HR6825 (Lo-16bits)
HR6827	CUB1:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR6828				HR6827 (Lo-16bits)
HR6829	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6830				HR6829 (Lo-16bits)
HR6831	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6832				HR6831 (Lo-16bits)
HR6833	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6834				HR6833 (Lo-16bits)
HR6835	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6836				HR6835 (Lo-16bits)
HR6837	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6838				HR6837 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6839	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6840				HR6839 (Lo-16bits)
HR6841	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6842				HR6841 (Lo-16bits)
HR6843	CUB1:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR6844				HR6843 (Lo-16bits)
HR6845	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6846				HR6845 (Lo-16bits)
HR6847	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6848				HR6847 (Lo-16bits)
HR6849	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6850				HR6849 (Lo-16bits)
HR6851	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6852				HR6851 (Lo-16bits)
HR6853	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6854				HR6853 (Lo-16bits)
HR6855	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6856				HR6855 (Lo-16bits)
HR6857	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6858				HR6857 (Lo-16bits)
HR6859	CUB1:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR6860				HR6859 (Lo-16bits)
HR6861	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6862	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6863	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6864	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6865	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6866	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6867	CUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR6868	CUB1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR6869	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6870				HR6869 (Lo-16bits)
HR6871	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6872				HR6871 (Lo-16bits)
HR6873	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6874				HR6873 (Lo-16bits)
HR6875	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6876				HR6875 (Lo-16bits)
HR6877	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6878				HR6877 (Lo-16bits)
HR6879	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6880				HR6879 (Lo-16bits)
HR6881	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6882				HR6881 (Lo-16bits)
HR6883	CUB1:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR6884				HR6883 (Lo-16bits)
HR6885	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6886	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6887	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6888	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6889	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6890	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6891	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6892	CUB1:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR6893	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6894	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR6895	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6896	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6897	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6898	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6899	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6900	CUB1:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR6901	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6902	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6903	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6904	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6905	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6906	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6907	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6908	CUB1:Delay characteristics ANSI/ IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR6909	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6910	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6911	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6912	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6913	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6914	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6915	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR6916	CUB1:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR6917	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6918				HR6917 (Lo-16bits)
HR6919	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6920				HR6919 (Lo-16bits)
HR6921	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6922				HR6921 (Lo-16bits)
HR6923	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6924				HR6923 (Lo-16bits)
HR6925	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6926				HR6925 (Lo-16bits)
HR6927	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6928				HR6927 (Lo-16bits)
HR6929	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6930				HR6929 (Lo-16bits)
HR6931	CUB1:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR6932				HR6931 (Lo-16bits)
HR6933	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6934				HR6933 (Lo-16bits)
HR6935	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6936				HR6935 (Lo-16bits)
HR6937	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6938				HR6937 (Lo-16bits)
HR6939	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6940				HR6939 (Lo-16bits)
HR6941	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6942				HR6941 (Lo-16bits)
HR6943	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6944				HR6943 (Lo-16bits)
HR6945	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6946				HR6945 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6947	CUB1:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6948				HR6947 (Lo-16bits)
HR6949	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6950				HR6949 (Lo-16bits)
HR6951	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6952				HR6951 (Lo-16bits)
HR6953	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6954				HR6953 (Lo-16bits)
HR6955	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6956				HR6955 (Lo-16bits)
HR6957	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6958				HR6957 (Lo-16bits)
HR6959	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6960				HR6959 (Lo-16bits)
HR6961	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6962				HR6961 (Lo-16bits)
HR6963	CUB1:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR6964				HR6963 (Lo-16bits)
HR6965	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6966				HR6965 (Lo-16bits)
HR6967	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6968				HR6967 (Lo-16bits)
HR6969	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6970				HR6969 (Lo-16bits)
HR6971	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6972				HR6971 (Lo-16bits)
HR6973	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6974				HR6973 (Lo-16bits)
HR6975	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6976				HR6975 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR6977	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6978				HR6977 (Lo-16bits)
HR6979	CUB1:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR6980				HR6979 (Lo-16bits)
HR6981	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6982	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6983	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6984	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6985	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6986	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6987	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6988	CUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR6989	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6990				HR6989 (Lo-16bits)
HR6991	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6992				HR6991 (Lo-16bits)
HR6993	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6994				HR6993 (Lo-16bits)
HR6995	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6996				HR6995 (Lo-16bits)
HR6997	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR6998				HR6997 (Lo-16bits)
HR6999	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7000				HR6999 (Lo-16bits)
HR7001	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7002				HR7001 (Lo-16bits)
HR7003	CUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7004				HR7003 (Lo-16bits)
HR7005	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7006	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7007	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7008	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7009	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7010	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7011	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7012	CUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7013	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7014	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7015	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7016	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7017	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7018	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7019	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7020	CUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7021	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7022				HR7021 (Lo-16bits)
HR7023	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7024				HR7023 (Lo-16bits)
HR7025	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7026				HR7025 (Lo-16bits)
HR7027	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7028				HR7027 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7029	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7030				HR7029 (Lo-16bits)
HR7031	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7032				HR7031 (Lo-16bits)
HR7033	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7034				HR7033 (Lo-16bits)
HR7035	CUB2:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7036				HR7035 (Lo-16bits)
HR7037	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7038				HR7037 (Lo-16bits)
HR7039	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7040				HR7039 (Lo-16bits)
HR7041	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7042				HR7041 (Lo-16bits)
HR7043	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7044				HR7043 (Lo-16bits)
HR7045	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7046				HR7045 (Lo-16bits)
HR7047	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7048				HR7047 (Lo-16bits)
HR7049	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7050				HR7049 (Lo-16bits)
HR7051	CUB2:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7052				HR7051 (Lo-16bits)
HR7053	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7054				HR7053 (Lo-16bits)
HR7055	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7056				HR7055 (Lo-16bits)
HR7057	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7058				HR7057 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7059	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7060				HR7059 (Lo-16bits)
HR7061	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7062				HR7061 (Lo-16bits)
HR7063	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7064				HR7063 (Lo-16bits)
HR7065	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7066				HR7065 (Lo-16bits)
HR7067	CUB2:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7068				HR7067 (Lo-16bits)
HR7069	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7070	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7071	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7072	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7073	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7074	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7075	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7076	CUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR7077	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7078				HR7077 (Lo-16bits)
HR7079	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7080				HR7079 (Lo-16bits)
HR7081	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7082				HR7081 (Lo-16bits)
HR7083	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7084				HR7083 (Lo-16bits)
HR7085	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7086				HR7085 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7087	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7088				HR7087 (Lo-16bits)
HR7089	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7090				HR7089 (Lo-16bits)
HR7091	CUB2:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7092				HR7091 (Lo-16bits)
HR7093	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7094	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7095	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7096	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7097	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7098	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7099	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7100	CUB2:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7101	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7102	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7103	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7104	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7105	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7106	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7107	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7108	CUB2:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7109	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7110	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7111	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7112	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param

Holding Register	Parameter	Access	Length	Info
HR7113	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7114	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7115	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7116	CUB2:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7117	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7118	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7119	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7120	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7121	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7122	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7123	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7124	CUB2:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7125	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7126				HR7125 (Lo-16bits)
HR7127	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7128				HR7127 (Lo-16bits)
HR7129	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7130				HR7129 (Lo-16bits)
HR7131	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7132				HR7131 (Lo-16bits)
HR7133	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7134				HR7133 (Lo-16bits)
HR7135	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7136				HR7135 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7137	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7138				HR7137 (Lo-16bits)
HR7139	CUB2:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7140				HR7139 (Lo-16bits)
HR7141	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7142				HR7141 (Lo-16bits)
HR7143	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7144				HR7143 (Lo-16bits)
HR7145	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7146				HR7145 (Lo-16bits)
HR7147	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7148				HR7147 (Lo-16bits)
HR7149	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7150				HR7149 (Lo-16bits)
HR7151	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7152				HR7151 (Lo-16bits)
HR7153	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7154				HR7153 (Lo-16bits)
HR7155	CUB2:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7156				HR7155 (Lo-16bits)
HR7157	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7158				HR7157 (Lo-16bits)
HR7159	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7160				HR7159 (Lo-16bits)
HR7161	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7162				HR7161 (Lo-16bits)
HR7163	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7164				HR7163 (Lo-16bits)
HR7165	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7166				HR7165 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7167	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7168				HR7167 (Lo-16bits)
HR7169	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7170				HR7169 (Lo-16bits)
HR7171	CUB2:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7172				HR7171 (Lo-16bits)
HR7173	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7174				HR7173 (Lo-16bits)
HR7175	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7176				HR7175 (Lo-16bits)
HR7177	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7178				HR7177 (Lo-16bits)
HR7179	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7180				HR7179 (Lo-16bits)
HR7181	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7182				HR7181 (Lo-16bits)
HR7183	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7184				HR7183 (Lo-16bits)
HR7185	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7186				HR7185 (Lo-16bits)
HR7187	CUB2:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7188				HR7187 (Lo-16bits)
HR7189	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7190	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7191	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7192	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7193	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7194	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7195	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7196	CUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7197	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7198				HR7197 (Lo-16bits)
HR7199	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7200				HR7199 (Lo-16bits)
HR7201	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7202				HR7201 (Lo-16bits)
HR7203	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7204				HR7203 (Lo-16bits)
HR7205	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7206				HR7205 (Lo-16bits)
HR7207	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7208				HR7207 (Lo-16bits)
HR7209	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7210				HR7209 (Lo-16bits)
HR7211	CUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7212				HR7211 (Lo-16bits)
HR7213	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7214	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7215	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7216	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7217	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7218	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7219	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7220	CUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7221	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7222	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7223	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7224	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7225	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7226	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7227	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7228	CUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7229	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7230				HR7229 (Lo-16bits)
HR7231	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7232				HR7231 (Lo-16bits)
HR7233	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7234				HR7233 (Lo-16bits)
HR7235	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7236				HR7235 (Lo-16bits)
HR7237	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7238				HR7237 (Lo-16bits)
HR7239	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7240				HR7239 (Lo-16bits)
HR7241	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7242				HR7241 (Lo-16bits)
HR7243	CUB3:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7244				HR7243 (Lo-16bits)
HR7245	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7246				HR7245 (Lo-16bits)
HR7247	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7248				HR7247 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7249	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7250				HR7249 (Lo-16bits)
HR7251	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7252				HR7251 (Lo-16bits)
HR7253	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7254				HR7253 (Lo-16bits)
HR7255	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7256				HR7255 (Lo-16bits)
HR7257	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7258				HR7257 (Lo-16bits)
HR7259	CUB3:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7260				HR7259 (Lo-16bits)
HR7261	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7262				HR7261 (Lo-16bits)
HR7263	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7264				HR7263 (Lo-16bits)
HR7265	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7266				HR7265 (Lo-16bits)
HR7267	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7268				HR7267 (Lo-16bits)
HR7269	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7270				HR7269 (Lo-16bits)
HR7271	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7272				HR7271 (Lo-16bits)
HR7273	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7274				HR7273 (Lo-16bits)
HR7275	CUB3:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7276				HR7275 (Lo-16bits)
HR7277	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7278	CUB3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7279	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7280	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7281	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7282	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7283	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7284	CUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR7285	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7286				HR7285 (Lo-16bits)
HR7287	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7288				HR7287 (Lo-16bits)
HR7289	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7290				HR7289 (Lo-16bits)
HR7291	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7292				HR7291 (Lo-16bits)
HR7293	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7294				HR7293 (Lo-16bits)
HR7295	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7296				HR7295 (Lo-16bits)
HR7297	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7298				HR7297 (Lo-16bits)
HR7299	CUB3:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7300				HR7299 (Lo-16bits)
HR7301	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7302	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7303	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7304	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard

Holding Register	Parameter	Access	Length	Info
HR7305	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7306	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7307	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7308	CUB3:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7309	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7310	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7311	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7312	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7313	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7314	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7315	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7316	CUB3:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7317	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7318	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7319	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7320	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7321	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7322	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7323	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7324	CUB3:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7325	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7326	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7327	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7328	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2

Holding Register	Parameter	Access	Length	Info
HR7329	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7330	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7331	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7332	CUB3:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7333	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7334				HR7333 (Lo-16bits)
HR7335	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7336				HR7335 (Lo-16bits)
HR7337	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7338				HR7337 (Lo-16bits)
HR7339	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7340				HR7339 (Lo-16bits)
HR7341	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7342				HR7341 (Lo-16bits)
HR7343	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7344				HR7343 (Lo-16bits)
HR7345	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7346				HR7345 (Lo-16bits)
HR7347	CUB3:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7348				HR7347 (Lo-16bits)
HR7349	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7350				HR7349 (Lo-16bits)
HR7351	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7352				HR7351 (Lo-16bits)
HR7353	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7354				HR7353 (Lo-16bits)
HR7355	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7356				HR7355 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7357	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7358				HR7357 (Lo-16bits)
HR7359	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7360				HR7359 (Lo-16bits)
HR7361	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7362				HR7361 (Lo-16bits)
HR7363	CUB3:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7364				HR7363 (Lo-16bits)
HR7365	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7366				HR7365 (Lo-16bits)
HR7367	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7368				HR7367 (Lo-16bits)
HR7369	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7370				HR7369 (Lo-16bits)
HR7371	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7372				HR7371 (Lo-16bits)
HR7373	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7374				HR7373 (Lo-16bits)
HR7375	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7376				HR7375 (Lo-16bits)
HR7377	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7378				HR7377 (Lo-16bits)
HR7379	CUB3:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7380				HR7379 (Lo-16bits)
HR7381	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7382				HR7381 (Lo-16bits)
HR7383	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7384				HR7383 (Lo-16bits)
HR7385	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7386				HR7385 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7387	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7388				HR7387 (Lo-16bits)
HR7389	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7390				HR7389 (Lo-16bits)
HR7391	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7392				HR7391 (Lo-16bits)
HR7393	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7394				HR7393 (Lo-16bits)
HR7395	CUB3:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7396				HR7395 (Lo-16bits)
HR7397	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7398	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7399	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7400	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7401	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7402	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7403	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7404	CUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7405	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7406				HR7405 (Lo-16bits)
HR7407	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7408				HR7407 (Lo-16bits)
HR7409	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7410				HR7409 (Lo-16bits)
HR7411	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7412				HR7411 (Lo-16bits)
HR7413	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7414				HR7413 (Lo-16bits)
HR7415	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7416				HR7415 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7417	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7418				HR7417 (Lo-16bits)
HR7419	CUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7420				HR7419 (Lo-16bits)
HR7421	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7422	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7423	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7424	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7425	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7426	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7427	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7428	CUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7429	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7430	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7431	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7432	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7433	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7434	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7435	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7436	CUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7437	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7438				HR7437 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7439	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7440				HR7439 (Lo-16bits)
HR7441	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7442				HR7441 (Lo-16bits)
HR7443	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7444				HR7443 (Lo-16bits)
HR7445	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7446				HR7445 (Lo-16bits)
HR7447	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7448				HR7447 (Lo-16bits)
HR7449	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7450				HR7449 (Lo-16bits)
HR7451	CUB4:Pick-up setting I2set pu	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR7452				HR7451 (Lo-16bits)
HR7453	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7454				HR7453 (Lo-16bits)
HR7455	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7456				HR7455 (Lo-16bits)
HR7457	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7458				HR7457 (Lo-16bits)
HR7459	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7460				HR7459 (Lo-16bits)
HR7461	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7462				HR7461 (Lo-16bits)
HR7463	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7464				HR7463 (Lo-16bits)
HR7465	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7466				HR7465 (Lo-16bits)
HR7467	CUB4:Pick-up setting I2/I1	R(W)	2	FLOAT [1...200] (Hi-16bits)
HR7468				HR7467 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7469	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7470				HR7469 (Lo-16bits)
HR7471	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7472				HR7471 (Lo-16bits)
HR7473	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7474				HR7473 (Lo-16bits)
HR7475	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7476				HR7475 (Lo-16bits)
HR7477	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7478				HR7477 (Lo-16bits)
HR7479	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7480				HR7479 (Lo-16bits)
HR7481	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7482				HR7481 (Lo-16bits)
HR7483	CUB4:Minimum phase current	R(W)	2	FLOAT [0.01...2] (Hi-16bits)
HR7484				HR7483 (Lo-16bits)
HR7485	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7486	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7487	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7488	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7489	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7490	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7491	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7492	CUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR7493	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7494				HR7493 (Lo-16bits)
HR7495	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7496				HR7495 (Lo-16bits)
HR7497	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7498				HR7497 (Lo-16bits)
HR7499	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7500				HR7499 (Lo-16bits)
HR7501	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7502				HR7501 (Lo-16bits)
HR7503	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7504				HR7503 (Lo-16bits)
HR7505	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7506				HR7505 (Lo-16bits)
HR7507	CUB4:Definite (minimum) operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7508				HR7507 (Lo-16bits)
HR7509	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7510	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7511	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7512	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7513	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7514	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7515	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7516	CUB4:Delay curve series	R(W)	1	0=IEC 1=IEEE 2=Non standard
HR7517	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7518	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7519	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7520	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7521	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7522	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7523	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param
HR7524	CUB4:Delay characteristics IEC	R(W)	1	0=NI 1=EI 2=VI 3=LTI 4=Param

Holding Register	Parameter	Access	Length	Info
HR7525	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7526	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7527	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7528	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7529	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7530	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7531	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7532	CUB4:Delay characteristics ANSI/IEEE	R(W)	1	0=ANSI NI 1=ANSI VI 2=ANSI EI 3=ANSI LI 4=IEEE MI 5=IEEE VI 6=IEEE EI 7=Param
HR7533	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7534	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7535	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7536	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7537	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7538	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7539	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7540	CUB4:Non-standard delay characteristics	R(W)	1	0=RI-type 1=RD-type 2=Curve2
HR7541	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7542				HR7541 (Lo-16bits)
HR7543	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7544				HR7543 (Lo-16bits)
HR7545	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7546				HR7545 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7547	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7548				HR7547 (Lo-16bits)
HR7549	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7550				HR7549 (Lo-16bits)
HR7551	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7552				HR7551 (Lo-16bits)
HR7553	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7554				HR7553 (Lo-16bits)
HR7555	CUB4:Time dial setting k	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7556				HR7555 (Lo-16bits)
HR7557	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7558				HR7557 (Lo-16bits)
HR7559	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7560				HR7559 (Lo-16bits)
HR7561	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7562				HR7561 (Lo-16bits)
HR7563	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7564				HR7563 (Lo-16bits)
HR7565	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7566				HR7565 (Lo-16bits)
HR7567	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7568				HR7567 (Lo-16bits)
HR7569	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7570				HR7569 (Lo-16bits)
HR7571	CUB4:IDMT constant A	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7572				HR7571 (Lo-16bits)
HR7573	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7574				HR7573 (Lo-16bits)
HR7575	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7576				HR7575 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7577	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7578				HR7577 (Lo-16bits)
HR7579	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7580				HR7579 (Lo-16bits)
HR7581	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7582				HR7581 (Lo-16bits)
HR7583	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7584				HR7583 (Lo-16bits)
HR7585	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7586				HR7585 (Lo-16bits)
HR7587	CUB4:IDMT constant B	R(W)	2	FLOAT [0...5] (Hi-16bits)
HR7588				HR7587 (Lo-16bits)
HR7589	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7590				HR7589 (Lo-16bits)
HR7591	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7592				HR7591 (Lo-16bits)
HR7593	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7594				HR7593 (Lo-16bits)
HR7595	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7596				HR7595 (Lo-16bits)
HR7597	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7598				HR7597 (Lo-16bits)
HR7599	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7600				HR7599 (Lo-16bits)
HR7601	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7602				HR7601 (Lo-16bits)
HR7603	CUB4:IDMT constant C	R(W)	2	FLOAT [0...250] (Hi-16bits)
HR7604				HR7603 (Lo-16bits)
HR7605	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7606	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7607	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7608	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7609	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7610	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7611	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7612	CUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7613	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7614				HR7613 (Lo-16bits)
HR7615	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7616				HR7615 (Lo-16bits)
HR7617	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7618				HR7617 (Lo-16bits)
HR7619	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7620				HR7619 (Lo-16bits)
HR7621	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7622				HR7621 (Lo-16bits)
HR7623	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7624				HR7623 (Lo-16bits)
HR7625	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7626				HR7625 (Lo-16bits)
HR7627	CUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7628				HR7627 (Lo-16bits)
HR7629	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7630	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7631	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7632	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7633	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7634	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7635	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7636	CUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7637	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7638	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7639	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7640	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7641	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7642	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7643	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7644	CUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7645	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7646	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7647	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7648	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7649	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7650	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7651	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7652	OV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7653	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7654				HR7653 (Lo-16bits)
HR7655	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7656				HR7655 (Lo-16bits)
HR7657	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7658				HR7657 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7659	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7660				HR7659 (Lo-16bits)
HR7661	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7662				HR7661 (Lo-16bits)
HR7663	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7664				HR7663 (Lo-16bits)
HR7665	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7666				HR7665 (Lo-16bits)
HR7667	OV1:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7668				HR7667 (Lo-16bits)
HR7669	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7670	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7671	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7672	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7673	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7674	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7675	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7676	OV1:Delay type	R(W)	1	0=DT 1=IDMT
HR7677	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7678				HR7677 (Lo-16bits)
HR7679	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7680				HR7679 (Lo-16bits)
HR7681	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7682				HR7681 (Lo-16bits)
HR7683	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7684				HR7683 (Lo-16bits)
HR7685	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7686				HR7685 (Lo-16bits)
HR7687	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7688				HR7687 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7689	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7690				HR7689 (Lo-16bits)
HR7691	OV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7692				HR7691 (Lo-16bits)
HR7693	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7694				HR7693 (Lo-16bits)
HR7695	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7696				HR7695 (Lo-16bits)
HR7697	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7698				HR7697 (Lo-16bits)
HR7699	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7700				HR7699 (Lo-16bits)
HR7701	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7702				HR7701 (Lo-16bits)
HR7703	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7704				HR7703 (Lo-16bits)
HR7705	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7706				HR7705 (Lo-16bits)
HR7707	OV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7708				HR7707 (Lo-16bits)
HR7709	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7710				HR7709 (Lo-16bits)
HR7711	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7712				HR7711 (Lo-16bits)
HR7713	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7714				HR7713 (Lo-16bits)
HR7715	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7716				HR7715 (Lo-16bits)
HR7717	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7718				HR7717 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7719	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7720				HR7719 (Lo-16bits)
HR7721	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7722				HR7721 (Lo-16bits)
HR7723	OV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7724				HR7723 (Lo-16bits)
HR7725	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7726				HR7725 (Lo-16bits)
HR7727	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7728				HR7727 (Lo-16bits)
HR7729	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7730				HR7729 (Lo-16bits)
HR7731	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7732				HR7731 (Lo-16bits)
HR7733	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7734				HR7733 (Lo-16bits)
HR7735	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7736				HR7735 (Lo-16bits)
HR7737	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7738				HR7737 (Lo-16bits)
HR7739	OV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7740				HR7739 (Lo-16bits)
HR7741	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7742	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7743	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7744	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7745	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7746	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7747	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7748	OV1:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7749	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7750	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7751	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7752	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7753	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7754	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7755	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7756	OV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7757	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7758	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7759	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7760	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7761	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7762	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7763	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7764	OV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7765	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7766	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7767	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7768	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7769	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7770	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR7771	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7772	OV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7773	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7774				HR7773 (Lo-16bits)
HR7775	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7776				HR7775 (Lo-16bits)
HR7777	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7778				HR7777 (Lo-16bits)
HR7779	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7780				HR7779 (Lo-16bits)
HR7781	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7782				HR7781 (Lo-16bits)
HR7783	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7784				HR7783 (Lo-16bits)
HR7785	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7786				HR7785 (Lo-16bits)
HR7787	OV2:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7788				HR7787 (Lo-16bits)
HR7789	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7790	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7791	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7792	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7793	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7794	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7795	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7796	OV2:Delay type	R(W)	1	0=DT 1=IDMT
HR7797	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7798				HR7797 (Lo-16bits)
HR7799	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7800				HR7799 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7801	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7802				HR7801 (Lo-16bits)
HR7803	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7804				HR7803 (Lo-16bits)
HR7805	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7806				HR7805 (Lo-16bits)
HR7807	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7808				HR7807 (Lo-16bits)
HR7809	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7810				HR7809 (Lo-16bits)
HR7811	OV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7812				HR7811 (Lo-16bits)
HR7813	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7814				HR7813 (Lo-16bits)
HR7815	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7816				HR7815 (Lo-16bits)
HR7817	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7818				HR7817 (Lo-16bits)
HR7819	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7820				HR7819 (Lo-16bits)
HR7821	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7822				HR7821 (Lo-16bits)
HR7823	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7824				HR7823 (Lo-16bits)
HR7825	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7826				HR7825 (Lo-16bits)
HR7827	OV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7828				HR7827 (Lo-16bits)
HR7829	OV2>IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7830				HR7829 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7831	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7832				HR7831 (Lo-16bits)
HR7833	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7834				HR7833 (Lo-16bits)
HR7835	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7836				HR7835 (Lo-16bits)
HR7837	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7838				HR7837 (Lo-16bits)
HR7839	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7840				HR7839 (Lo-16bits)
HR7841	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7842				HR7841 (Lo-16bits)
HR7843	OV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7844				HR7843 (Lo-16bits)
HR7845	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7846				HR7845 (Lo-16bits)
HR7847	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7848				HR7847 (Lo-16bits)
HR7849	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7850				HR7849 (Lo-16bits)
HR7851	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7852				HR7851 (Lo-16bits)
HR7853	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7854				HR7853 (Lo-16bits)
HR7855	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7856				HR7855 (Lo-16bits)
HR7857	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7858				HR7857 (Lo-16bits)
HR7859	OV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7860				HR7859 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR7861	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7862	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7863	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7864	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7865	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7866	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7867	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7868	OV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7869	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7870	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7871	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7872	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7873	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7874	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7875	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7876	OV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7877	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7878	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7879	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7880	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7881	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7882	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7883	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7884	OV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7885	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7886	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7887	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7888	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7889	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7890	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7891	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7892	OV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR7893	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7894				HR7893 (Lo-16bits)
HR7895	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7896				HR7895 (Lo-16bits)
HR7897	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7898				HR7897 (Lo-16bits)
HR7899	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7900				HR7899 (Lo-16bits)
HR7901	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7902				HR7901 (Lo-16bits)
HR7903	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7904				HR7903 (Lo-16bits)
HR7905	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7906				HR7905 (Lo-16bits)
HR7907	OV3:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR7908				HR7907 (Lo-16bits)
HR7909	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7910	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7911	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7912	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7913	OV3:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR7914	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7915	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7916	OV3:Delay type	R(W)	1	0=DT 1=IDMT
HR7917	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7918				HR7917 (Lo-16bits)
HR7919	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7920				HR7919 (Lo-16bits)
HR7921	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7922				HR7921 (Lo-16bits)
HR7923	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7924				HR7923 (Lo-16bits)
HR7925	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7926				HR7925 (Lo-16bits)
HR7927	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7928				HR7927 (Lo-16bits)
HR7929	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7930				HR7929 (Lo-16bits)
HR7931	OV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR7932				HR7931 (Lo-16bits)
HR7933	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7934				HR7933 (Lo-16bits)
HR7935	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7936				HR7935 (Lo-16bits)
HR7937	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7938				HR7937 (Lo-16bits)
HR7939	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7940				HR7939 (Lo-16bits)
HR7941	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7942				HR7941 (Lo-16bits)
HR7943	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7944				HR7943 (Lo-16bits)
HR7945	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7946				HR7945 (Lo-16bits)
HR7947	OV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR7948				HR7947 (Lo-16bits)
HR7949	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7950				HR7949 (Lo-16bits)
HR7951	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7952				HR7951 (Lo-16bits)
HR7953	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7954				HR7953 (Lo-16bits)
HR7955	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7956				HR7955 (Lo-16bits)
HR7957	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7958				HR7957 (Lo-16bits)
HR7959	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7960				HR7959 (Lo-16bits)
HR7961	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7962				HR7961 (Lo-16bits)
HR7963	OV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR7964				HR7963 (Lo-16bits)
HR7965	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7966				HR7965 (Lo-16bits)
HR7967	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7968				HR7967 (Lo-16bits)
HR7969	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7970				HR7969 (Lo-16bits)
HR7971	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7972				HR7971 (Lo-16bits)
HR7973	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR7974				HR7973 (Lo-16bits)
HR7975	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7976				HR7975 (Lo-16bits)
HR7977	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7978				HR7977 (Lo-16bits)
HR7979	OV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR7980				HR7979 (Lo-16bits)
HR7981	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7982	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7983	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7984	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7985	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7986	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7987	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7988	OV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR7989	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7990	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7991	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7992	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7993	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7994	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7995	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7996	OV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR7997	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR7998	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR7999	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8000	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8001	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8002	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8003	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8004	OV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8005	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8006	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8007	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8008	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8009	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8010	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8011	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8012	OV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8013	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8014				HR8013 (Lo-16bits)
HR8015	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8016				HR8015 (Lo-16bits)
HR8017	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8018				HR8017 (Lo-16bits)
HR8019	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8020				HR8019 (Lo-16bits)
HR8021	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8022				HR8021 (Lo-16bits)
HR8023	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8024				HR8023 (Lo-16bits)
HR8025	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8026				HR8025 (Lo-16bits)
HR8027	OV4:Pick-up setting Uset	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR8028				HR8027 (Lo-16bits)
HR8029	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8030	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8031	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8032	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8033	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8034	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8035	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8036	OV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8037	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8038				HR8037 (Lo-16bits)
HR8039	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8040				HR8039 (Lo-16bits)
HR8041	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8042				HR8041 (Lo-16bits)
HR8043	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8044				HR8043 (Lo-16bits)
HR8045	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8046				HR8045 (Lo-16bits)
HR8047	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8048				HR8047 (Lo-16bits)
HR8049	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8050				HR8049 (Lo-16bits)
HR8051	OV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8052				HR8051 (Lo-16bits)
HR8053	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8054				HR8053 (Lo-16bits)
HR8055	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8056				HR8055 (Lo-16bits)
HR8057	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8058				HR8057 (Lo-16bits)
HR8059	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8060				HR8059 (Lo-16bits)
HR8061	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8062				HR8061 (Lo-16bits)
HR8063	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8064				HR8063 (Lo-16bits)
HR8065	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8066				HR8065 (Lo-16bits)
HR8067	OV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8068				HR8067 (Lo-16bits)
HR8069	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8070				HR8069 (Lo-16bits)
HR8071	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8072				HR8071 (Lo-16bits)
HR8073	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8074				HR8073 (Lo-16bits)
HR8075	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8076				HR8075 (Lo-16bits)
HR8077	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8078				HR8077 (Lo-16bits)
HR8079	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8080				HR8079 (Lo-16bits)
HR8081	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8082				HR8081 (Lo-16bits)
HR8083	OV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8084				HR8083 (Lo-16bits)
HR8085	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR8086				HR8085 (Lo-16bits)
HR8087	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8088				HR8087 (Lo-16bits)
HR8089	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8090				HR8089 (Lo-16bits)
HR8091	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8092				HR8091 (Lo-16bits)
HR8093	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8094				HR8093 (Lo-16bits)
HR8095	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8096				HR8095 (Lo-16bits)
HR8097	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8098				HR8097 (Lo-16bits)
HR8099	OV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8100				HR8099 (Lo-16bits)
HR8101	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8102	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8103	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8104	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8105	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8106	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8107	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8108	OV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8109	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8110	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8111	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8112	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8113	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8114	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8115	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8116	OV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8117	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8118	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8119	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8120	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8121	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8122	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8123	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8124	OV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8125	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8126	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8127	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8128	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8129	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8130	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8131	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8132	UV1:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8133	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8134				HR8133 (Lo-16bits)
HR8135	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8136				HR8135 (Lo-16bits)
HR8137	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8138				HR8137 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8139	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8140				HR8139 (Lo-16bits)
HR8141	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8142				HR8141 (Lo-16bits)
HR8143	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8144				HR8143 (Lo-16bits)
HR8145	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8146				HR8145 (Lo-16bits)
HR8147	UV1:Pick-up setting Uset<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8148				HR8147 (Lo-16bits)
HR8149	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8150				HR8149 (Lo-16bits)
HR8151	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8152				HR8151 (Lo-16bits)
HR8153	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8154				HR8153 (Lo-16bits)
HR8155	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8156				HR8155 (Lo-16bits)
HR8157	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8158				HR8157 (Lo-16bits)
HR8159	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8160				HR8159 (Lo-16bits)
HR8161	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8162				HR8161 (Lo-16bits)
HR8163	UV1:Block setting Uset<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8164				HR8163 (Lo-16bits)
HR8165	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8166	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8167	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8168	UV1:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8169	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8170	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8171	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8172	UV1:Delay type	R(W)	1	0=DT 1=IDMT
HR8173	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8174				HR8173 (Lo-16bits)
HR8175	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8176				HR8175 (Lo-16bits)
HR8177	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8178				HR8177 (Lo-16bits)
HR8179	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8180				HR8179 (Lo-16bits)
HR8181	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8182				HR8181 (Lo-16bits)
HR8183	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8184				HR8183 (Lo-16bits)
HR8185	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8186				HR8185 (Lo-16bits)
HR8187	UV1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8188				HR8187 (Lo-16bits)
HR8189	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8190				HR8189 (Lo-16bits)
HR8191	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8192				HR8191 (Lo-16bits)
HR8193	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8194				HR8193 (Lo-16bits)
HR8195	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8196				HR8195 (Lo-16bits)
HR8197	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8198				HR8197 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8199	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8200				HR8199 (Lo-16bits)
HR8201	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8202				HR8201 (Lo-16bits)
HR8203	UV1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8204				HR8203 (Lo-16bits)
HR8205	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8206				HR8205 (Lo-16bits)
HR8207	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8208				HR8207 (Lo-16bits)
HR8209	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8210				HR8209 (Lo-16bits)
HR8211	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8212				HR8211 (Lo-16bits)
HR8213	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8214				HR8213 (Lo-16bits)
HR8215	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8216				HR8215 (Lo-16bits)
HR8217	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8218				HR8217 (Lo-16bits)
HR8219	UV1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8220				HR8219 (Lo-16bits)
HR8221	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8222				HR8221 (Lo-16bits)
HR8223	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8224				HR8223 (Lo-16bits)
HR8225	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8226				HR8225 (Lo-16bits)
HR8227	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8228				HR8227 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8229	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8230				HR8229 (Lo-16bits)
HR8231	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8232				HR8231 (Lo-16bits)
HR8233	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8234				HR8233 (Lo-16bits)
HR8235	UV1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8236				HR8235 (Lo-16bits)
HR8237	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8238	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8239	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8240	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8241	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8242	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8243	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8244	UV1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8245	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8246	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8247	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8248	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8249	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8250	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8251	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8252	UV1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8253	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8254	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8255	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8256	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8257	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8258	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8259	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8260	UV1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8261	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8262	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8263	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8264	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8265	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8266	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8267	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8268	UV2:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8269	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8270				HR8269 (Lo-16bits)
HR8271	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8272				HR8271 (Lo-16bits)
HR8273	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8274				HR8273 (Lo-16bits)
HR8275	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8276				HR8275 (Lo-16bits)
HR8277	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8278				HR8277 (Lo-16bits)
HR8279	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8280				HR8279 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8281	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8282				HR8281 (Lo-16bits)
HR8283	UV2:Pick-up setting Uset<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8284				HR8283 (Lo-16bits)
HR8285	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8286				HR8285 (Lo-16bits)
HR8287	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8288				HR8287 (Lo-16bits)
HR8289	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8290				HR8289 (Lo-16bits)
HR8291	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8292				HR8291 (Lo-16bits)
HR8293	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8294				HR8293 (Lo-16bits)
HR8295	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8296				HR8295 (Lo-16bits)
HR8297	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8298				HR8297 (Lo-16bits)
HR8299	UV2:Block setting Uset<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8300				HR8299 (Lo-16bits)
HR8301	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8302	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8303	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8304	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8305	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8306	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8307	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8308	UV2:Delay type	R(W)	1	0=DT 1=IDMT
HR8309	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8310				HR8309 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8311	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8312				HR8311 (Lo-16bits)
HR8313	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8314				HR8313 (Lo-16bits)
HR8315	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8316				HR8315 (Lo-16bits)
HR8317	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8318				HR8317 (Lo-16bits)
HR8319	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8320				HR8319 (Lo-16bits)
HR8321	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8322				HR8321 (Lo-16bits)
HR8323	UV2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8324				HR8323 (Lo-16bits)
HR8325	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8326				HR8325 (Lo-16bits)
HR8327	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8328				HR8327 (Lo-16bits)
HR8329	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8330				HR8329 (Lo-16bits)
HR8331	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8332				HR8331 (Lo-16bits)
HR8333	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8334				HR8333 (Lo-16bits)
HR8335	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8336				HR8335 (Lo-16bits)
HR8337	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8338				HR8337 (Lo-16bits)
HR8339	UV2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8340				HR8339 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8341	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8342				HR8341 (Lo-16bits)
HR8343	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8344				HR8343 (Lo-16bits)
HR8345	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8346				HR8345 (Lo-16bits)
HR8347	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8348				HR8347 (Lo-16bits)
HR8349	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8350				HR8349 (Lo-16bits)
HR8351	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8352				HR8351 (Lo-16bits)
HR8353	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8354				HR8353 (Lo-16bits)
HR8355	UV2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8356				HR8355 (Lo-16bits)
HR8357	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8358				HR8357 (Lo-16bits)
HR8359	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8360				HR8359 (Lo-16bits)
HR8361	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8362				HR8361 (Lo-16bits)
HR8363	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8364				HR8363 (Lo-16bits)
HR8365	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8366				HR8365 (Lo-16bits)
HR8367	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8368				HR8367 (Lo-16bits)
HR8369	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8370				HR8369 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8371	UV2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8372				HR8371 (Lo-16bits)
HR8373	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8374	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8375	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8376	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8377	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8378	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8379	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8380	UV2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8381	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8382	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8383	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8384	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8385	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8386	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8387	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8388	UV2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8389	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8390	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8391	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8392	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8393	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8394	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8395	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8396	UV2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8397	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8398	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8399	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8400	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8401	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8402	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8403	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8404	UV3:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8405	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8406				HR8405 (Lo-16bits)
HR8407	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8408				HR8407 (Lo-16bits)
HR8409	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8410				HR8409 (Lo-16bits)
HR8411	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8412				HR8411 (Lo-16bits)
HR8413	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8414				HR8413 (Lo-16bits)
HR8415	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8416				HR8415 (Lo-16bits)
HR8417	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8418				HR8417 (Lo-16bits)
HR8419	UV3:Pick-up setting Uset<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8420				HR8419 (Lo-16bits)
HR8421	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8422				HR8421 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8423	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8424				HR8423 (Lo-16bits)
HR8425	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8426				HR8425 (Lo-16bits)
HR8427	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8428				HR8427 (Lo-16bits)
HR8429	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8430				HR8429 (Lo-16bits)
HR8431	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8432				HR8431 (Lo-16bits)
HR8433	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8434				HR8433 (Lo-16bits)
HR8435	UV3:Block setting Uset<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8436				HR8435 (Lo-16bits)
HR8437	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8438	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8439	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8440	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8441	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8442	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8443	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8444	UV3:Delay type	R(W)	1	0=DT 1=IDMT
HR8445	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8446				HR8445 (Lo-16bits)
HR8447	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8448				HR8447 (Lo-16bits)
HR8449	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8450				HR8449 (Lo-16bits)
HR8451	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8452				HR8451 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8453	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8454				HR8453 (Lo-16bits)
HR8455	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8456				HR8455 (Lo-16bits)
HR8457	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8458				HR8457 (Lo-16bits)
HR8459	UV3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8460				HR8459 (Lo-16bits)
HR8461	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8462				HR8461 (Lo-16bits)
HR8463	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8464				HR8463 (Lo-16bits)
HR8465	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8466				HR8465 (Lo-16bits)
HR8467	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8468				HR8467 (Lo-16bits)
HR8469	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8470				HR8469 (Lo-16bits)
HR8471	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8472				HR8471 (Lo-16bits)
HR8473	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8474				HR8473 (Lo-16bits)
HR8475	UV3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8476				HR8475 (Lo-16bits)
HR8477	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8478				HR8477 (Lo-16bits)
HR8479	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8480				HR8479 (Lo-16bits)
HR8481	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8482				HR8481 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8483	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8484				HR8483 (Lo-16bits)
HR8485	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8486				HR8485 (Lo-16bits)
HR8487	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8488				HR8487 (Lo-16bits)
HR8489	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8490				HR8489 (Lo-16bits)
HR8491	UV3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8492				HR8491 (Lo-16bits)
HR8493	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8494				HR8493 (Lo-16bits)
HR8495	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8496				HR8495 (Lo-16bits)
HR8497	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8498				HR8497 (Lo-16bits)
HR8499	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8500				HR8499 (Lo-16bits)
HR8501	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8502				HR8501 (Lo-16bits)
HR8503	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8504				HR8503 (Lo-16bits)
HR8505	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8506				HR8505 (Lo-16bits)
HR8507	UV3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8508				HR8507 (Lo-16bits)
HR8509	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8510	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8511	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8512	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8513	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8514	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8515	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8516	UV3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8517	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8518	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8519	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8520	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8521	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8522	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8523	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8524	UV3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8525	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8526	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8527	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8528	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8529	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8530	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8531	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8532	UV3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8533	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8534	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages

Holding Register	Parameter	Access	Length	Info
HR8535	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8536	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8537	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8538	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8539	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8540	UV4:Pick-up terms	R(W)	1	0=1 voltage 1=2 voltages 2=3 voltages
HR8541	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8542				HR8541 (Lo-16bits)
HR8543	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8544				HR8543 (Lo-16bits)
HR8545	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8546				HR8545 (Lo-16bits)
HR8547	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8548				HR8547 (Lo-16bits)
HR8549	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8550				HR8549 (Lo-16bits)
HR8551	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8552				HR8551 (Lo-16bits)
HR8553	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8554				HR8553 (Lo-16bits)
HR8555	UV4:Pick-up setting Uset<<<<	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR8556				HR8555 (Lo-16bits)
HR8557	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8558				HR8557 (Lo-16bits)
HR8559	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8560				HR8559 (Lo-16bits)
HR8561	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8562				HR8561 (Lo-16bits)
HR8563	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8564				HR8563 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8565	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8566				HR8565 (Lo-16bits)
HR8567	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8568				HR8567 (Lo-16bits)
HR8569	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8570				HR8569 (Lo-16bits)
HR8571	UV4:Block setting Uset<<<<	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8572				HR8571 (Lo-16bits)
HR8573	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8574	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8575	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8576	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8577	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8578	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8579	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8580	UV4:Delay type	R(W)	1	0=DT 1=IDMT
HR8581	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8582				HR8581 (Lo-16bits)
HR8583	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8584				HR8583 (Lo-16bits)
HR8585	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8586				HR8585 (Lo-16bits)
HR8587	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8588				HR8587 (Lo-16bits)
HR8589	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8590				HR8589 (Lo-16bits)
HR8591	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8592				HR8591 (Lo-16bits)
HR8593	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8594				HR8593 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8595	UV4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8596				HR8595 (Lo-16bits)
HR8597	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8598				HR8597 (Lo-16bits)
HR8599	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8600				HR8599 (Lo-16bits)
HR8601	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8602				HR8601 (Lo-16bits)
HR8603	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8604				HR8603 (Lo-16bits)
HR8605	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8606				HR8605 (Lo-16bits)
HR8607	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8608				HR8607 (Lo-16bits)
HR8609	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8610				HR8609 (Lo-16bits)
HR8611	UV4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8612				HR8611 (Lo-16bits)
HR8613	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8614				HR8613 (Lo-16bits)
HR8615	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8616				HR8615 (Lo-16bits)
HR8617	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8618				HR8617 (Lo-16bits)
HR8619	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8620				HR8619 (Lo-16bits)
HR8621	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8622				HR8621 (Lo-16bits)
HR8623	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8624				HR8623 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8625	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8626				HR8625 (Lo-16bits)
HR8627	UV4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8628				HR8627 (Lo-16bits)
HR8629	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8630				HR8629 (Lo-16bits)
HR8631	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8632				HR8631 (Lo-16bits)
HR8633	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8634				HR8633 (Lo-16bits)
HR8635	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8636				HR8635 (Lo-16bits)
HR8637	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8638				HR8637 (Lo-16bits)
HR8639	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8640				HR8639 (Lo-16bits)
HR8641	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8642				HR8641 (Lo-16bits)
HR8643	UV4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8644				HR8643 (Lo-16bits)
HR8645	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8646	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8647	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8648	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8649	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8650	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8651	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8652	UV4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8653	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8654	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8655	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8656	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8657	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8658	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8659	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8660	UV4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8661	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8662	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8663	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8664	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8665	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8666	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8667	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8668	UV4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8669	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8670	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8671	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8672	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8673	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8674	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8675	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8676	VUB1:Pick-up terms	R(W)	1	0=Over > 1=Under <

Holding Register	Parameter	Access	Length	Info
HR8677	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8678				HR8677 (Lo-16bits)
HR8679	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8680				HR8679 (Lo-16bits)
HR8681	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8682				HR8681 (Lo-16bits)
HR8683	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8684				HR8683 (Lo-16bits)
HR8685	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8686				HR8685 (Lo-16bits)
HR8687	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8688				HR8687 (Lo-16bits)
HR8689	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8690				HR8689 (Lo-16bits)
HR8691	VUB1:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8692				HR8691 (Lo-16bits)
HR8693	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8694				HR8693 (Lo-16bits)
HR8695	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8696				HR8695 (Lo-16bits)
HR8697	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8698				HR8697 (Lo-16bits)
HR8699	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8700				HR8699 (Lo-16bits)
HR8701	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8702				HR8701 (Lo-16bits)
HR8703	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8704				HR8703 (Lo-16bits)
HR8705	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8706				HR8705 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8707	VUB1:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8708				HR8707 (Lo-16bits)
HR8709	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8710	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8711	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8712	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8713	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8714	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8715	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8716	VUB1:Delay type	R(W)	1	0=DT 1=IDMT
HR8717	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8718				HR8717 (Lo-16bits)
HR8719	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8720				HR8719 (Lo-16bits)
HR8721	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8722				HR8721 (Lo-16bits)
HR8723	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8724				HR8723 (Lo-16bits)
HR8725	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8726				HR8725 (Lo-16bits)
HR8727	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8728				HR8727 (Lo-16bits)
HR8729	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8730				HR8729 (Lo-16bits)
HR8731	VUB1:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8732				HR8731 (Lo-16bits)
HR8733	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8734				HR8733 (Lo-16bits)
HR8735	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8736				HR8735 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8737	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8738				HR8737 (Lo-16bits)
HR8739	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8740				HR8739 (Lo-16bits)
HR8741	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8742				HR8741 (Lo-16bits)
HR8743	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8744				HR8743 (Lo-16bits)
HR8745	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8746				HR8745 (Lo-16bits)
HR8747	VUB1:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8748				HR8747 (Lo-16bits)
HR8749	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8750				HR8749 (Lo-16bits)
HR8751	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8752				HR8751 (Lo-16bits)
HR8753	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8754				HR8753 (Lo-16bits)
HR8755	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8756				HR8755 (Lo-16bits)
HR8757	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8758				HR8757 (Lo-16bits)
HR8759	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8760				HR8759 (Lo-16bits)
HR8761	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8762				HR8761 (Lo-16bits)
HR8763	VUB1:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8764				HR8763 (Lo-16bits)
HR8765	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8766				HR8765 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8767	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8768				HR8767 (Lo-16bits)
HR8769	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8770				HR8769 (Lo-16bits)
HR8771	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8772				HR8771 (Lo-16bits)
HR8773	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8774				HR8773 (Lo-16bits)
HR8775	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8776				HR8775 (Lo-16bits)
HR8777	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8778				HR8777 (Lo-16bits)
HR8779	VUB1:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8780				HR8779 (Lo-16bits)
HR8781	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8782	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8783	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8784	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8785	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8786	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8787	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8788	VUB1:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8789	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8790	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8791	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8792	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8793	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8794	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8795	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8796	VUB1:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8797	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8798	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8799	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8800	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8801	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8802	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8803	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8804	VUB1:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8805	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8806	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8807	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8808	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8809	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8810	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8811	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8812	VUB2:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8813	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8814				HR8813 (Lo-16bits)
HR8815	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8816				HR8815 (Lo-16bits)
HR8817	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8818				HR8817 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8819	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8820				HR8819 (Lo-16bits)
HR8821	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8822				HR8821 (Lo-16bits)
HR8823	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8824				HR8823 (Lo-16bits)
HR8825	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8826				HR8825 (Lo-16bits)
HR8827	VUB2:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8828				HR8827 (Lo-16bits)
HR8829	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8830				HR8829 (Lo-16bits)
HR8831	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8832				HR8831 (Lo-16bits)
HR8833	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8834				HR8833 (Lo-16bits)
HR8835	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8836				HR8835 (Lo-16bits)
HR8837	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8838				HR8837 (Lo-16bits)
HR8839	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8840				HR8839 (Lo-16bits)
HR8841	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8842				HR8841 (Lo-16bits)
HR8843	VUB2:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8844				HR8843 (Lo-16bits)
HR8845	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8846	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8847	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8848	VUB2:Delay type	R(W)	1	0=DT 1=IDMT

Holding Register	Parameter	Access	Length	Info
HR8849	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8850	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8851	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8852	VUB2:Delay type	R(W)	1	0=DT 1=IDMT
HR8853	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8854				HR8853 (Lo-16bits)
HR8855	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8856				HR8855 (Lo-16bits)
HR8857	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8858				HR8857 (Lo-16bits)
HR8859	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8860				HR8859 (Lo-16bits)
HR8861	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8862				HR8861 (Lo-16bits)
HR8863	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8864				HR8863 (Lo-16bits)
HR8865	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8866				HR8865 (Lo-16bits)
HR8867	VUB2:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8868				HR8867 (Lo-16bits)
HR8869	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8870				HR8869 (Lo-16bits)
HR8871	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8872				HR8871 (Lo-16bits)
HR8873	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8874				HR8873 (Lo-16bits)
HR8875	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8876				HR8875 (Lo-16bits)
HR8877	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8878				HR8877 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8879	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8880				HR8879 (Lo-16bits)
HR8881	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8882				HR8881 (Lo-16bits)
HR8883	VUB2:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR8884				HR8883 (Lo-16bits)
HR8885	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8886				HR8885 (Lo-16bits)
HR8887	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8888				HR8887 (Lo-16bits)
HR8889	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8890				HR8889 (Lo-16bits)
HR8891	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8892				HR8891 (Lo-16bits)
HR8893	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8894				HR8893 (Lo-16bits)
HR8895	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8896				HR8895 (Lo-16bits)
HR8897	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8898				HR8897 (Lo-16bits)
HR8899	VUB2:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR8900				HR8899 (Lo-16bits)
HR8901	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8902				HR8901 (Lo-16bits)
HR8903	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8904				HR8903 (Lo-16bits)
HR8905	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8906				HR8905 (Lo-16bits)
HR8907	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8908				HR8907 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8909	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8910				HR8909 (Lo-16bits)
HR8911	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8912				HR8911 (Lo-16bits)
HR8913	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8914				HR8913 (Lo-16bits)
HR8915	VUB2:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR8916				HR8915 (Lo-16bits)
HR8917	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8918	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8919	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8920	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8921	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8922	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8923	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8924	VUB2:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR8925	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8926	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8927	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8928	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8929	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8930	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8931	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8932	VUB2:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR8933	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR8934	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8935	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8936	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8937	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8938	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8939	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8940	VUB2:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR8941	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8942	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8943	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8944	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8945	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8946	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8947	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8948	VUB3:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR8949	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8950				HR8949 (Lo-16bits)
HR8951	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8952				HR8951 (Lo-16bits)
HR8953	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8954				HR8953 (Lo-16bits)
HR8955	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8956				HR8955 (Lo-16bits)
HR8957	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8958				HR8957 (Lo-16bits)
HR8959	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8960				HR8959 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8961	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8962				HR8961 (Lo-16bits)
HR8963	VUB3:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR8964				HR8963 (Lo-16bits)
HR8965	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8966				HR8965 (Lo-16bits)
HR8967	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8968				HR8967 (Lo-16bits)
HR8969	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8970				HR8969 (Lo-16bits)
HR8971	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8972				HR8971 (Lo-16bits)
HR8973	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8974				HR8973 (Lo-16bits)
HR8975	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8976				HR8975 (Lo-16bits)
HR8977	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8978				HR8977 (Lo-16bits)
HR8979	VUB3:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR8980				HR8979 (Lo-16bits)
HR8981	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8982	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8983	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8984	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8985	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8986	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8987	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8988	VUB3:Delay type	R(W)	1	0=DT 1=IDMT
HR8989	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8990				HR8989 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR8991	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8992				HR8991 (Lo-16bits)
HR8993	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8994				HR8993 (Lo-16bits)
HR8995	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8996				HR8995 (Lo-16bits)
HR8997	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR8998				HR8997 (Lo-16bits)
HR8999	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9000				HR8999 (Lo-16bits)
HR9001	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9002				HR9001 (Lo-16bits)
HR9003	VUB3:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9004				HR9003 (Lo-16bits)
HR9005	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9006				HR9005 (Lo-16bits)
HR9007	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9008				HR9007 (Lo-16bits)
HR9009	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9010				HR9009 (Lo-16bits)
HR9011	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9012				HR9011 (Lo-16bits)
HR9013	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9014				HR9013 (Lo-16bits)
HR9015	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9016				HR9015 (Lo-16bits)
HR9017	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9018				HR9017 (Lo-16bits)
HR9019	VUB3:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9020				HR9019 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9021	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9022				HR9021 (Lo-16bits)
HR9023	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9024				HR9023 (Lo-16bits)
HR9025	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9026				HR9025 (Lo-16bits)
HR9027	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9028				HR9027 (Lo-16bits)
HR9029	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9030				HR9029 (Lo-16bits)
HR9031	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9032				HR9031 (Lo-16bits)
HR9033	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9034				HR9033 (Lo-16bits)
HR9035	VUB3:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9036				HR9035 (Lo-16bits)
HR9037	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9038				HR9037 (Lo-16bits)
HR9039	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9040				HR9039 (Lo-16bits)
HR9041	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9042				HR9041 (Lo-16bits)
HR9043	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9044				HR9043 (Lo-16bits)
HR9045	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9046				HR9045 (Lo-16bits)
HR9047	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9048				HR9047 (Lo-16bits)
HR9049	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9050				HR9049 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9051	VUB3:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9052				HR9051 (Lo-16bits)
HR9053	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9054	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9055	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9056	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9057	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9058	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9059	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9060	VUB3:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9061	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9062	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9063	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9064	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9065	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9066	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9067	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9068	VUB3:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9069	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9070	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9071	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9072	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9073	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9074	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9075	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9076	VUB3:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9077	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9078	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9079	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9080	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9081	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9082	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9083	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9084	VUB4:Pick-up terms	R(W)	1	0=Over > 1=Under <
HR9085	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9086				HR9085 (Lo-16bits)
HR9087	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9088				HR9087 (Lo-16bits)
HR9089	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9090				HR9089 (Lo-16bits)
HR9091	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9092				HR9091 (Lo-16bits)
HR9093	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9094				HR9093 (Lo-16bits)
HR9095	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9096				HR9095 (Lo-16bits)
HR9097	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9098				HR9097 (Lo-16bits)
HR9099	VUB4:Pick-up setting Uset	R(W)	2	FLOAT [5...150] (Hi-16bits)
HR9100				HR9099 (Lo-16bits)
HR9101	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9102				HR9101 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9103	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9104				HR9103 (Lo-16bits)
HR9105	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9106				HR9105 (Lo-16bits)
HR9107	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9108				HR9107 (Lo-16bits)
HR9109	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9110				HR9109 (Lo-16bits)
HR9111	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9112				HR9111 (Lo-16bits)
HR9113	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9114				HR9113 (Lo-16bits)
HR9115	VUB4:Under block setting Ublk	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR9116				HR9115 (Lo-16bits)
HR9117	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9118	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9119	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9120	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9121	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9122	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9123	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9124	VUB4:Delay type	R(W)	1	0=DT 1=IDMT
HR9125	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9126				HR9125 (Lo-16bits)
HR9127	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9128				HR9127 (Lo-16bits)
HR9129	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9130				HR9129 (Lo-16bits)
HR9131	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9132				HR9131 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9133	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9134				HR9133 (Lo-16bits)
HR9135	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9136				HR9135 (Lo-16bits)
HR9137	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9138				HR9137 (Lo-16bits)
HR9139	VUB4:Definite operating time delay	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9140				HR9139 (Lo-16bits)
HR9141	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9142				HR9141 (Lo-16bits)
HR9143	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9144				HR9143 (Lo-16bits)
HR9145	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9146				HR9145 (Lo-16bits)
HR9147	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9148				HR9147 (Lo-16bits)
HR9149	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9150				HR9149 (Lo-16bits)
HR9151	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9152				HR9151 (Lo-16bits)
HR9153	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9154				HR9153 (Lo-16bits)
HR9155	VUB4:Time dial setting k	R(W)	2	FLOAT [0.01...60] (Hi-16bits)
HR9156				HR9155 (Lo-16bits)
HR9157	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9158				HR9157 (Lo-16bits)
HR9159	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9160				HR9159 (Lo-16bits)
HR9161	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9162				HR9161 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9163	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9164				HR9163 (Lo-16bits)
HR9165	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9166				HR9165 (Lo-16bits)
HR9167	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9168				HR9167 (Lo-16bits)
HR9169	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9170				HR9169 (Lo-16bits)
HR9171	VUB4:IDMT Multiplier	R(W)	2	FLOAT [0.01...25] (Hi-16bits)
HR9172				HR9171 (Lo-16bits)
HR9173	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9174				HR9173 (Lo-16bits)
HR9175	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9176				HR9175 (Lo-16bits)
HR9177	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9178				HR9177 (Lo-16bits)
HR9179	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9180				HR9179 (Lo-16bits)
HR9181	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9182				HR9181 (Lo-16bits)
HR9183	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9184				HR9183 (Lo-16bits)
HR9185	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9186				HR9185 (Lo-16bits)
HR9187	VUB4:Release time delay	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR9188				HR9187 (Lo-16bits)
HR9189	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9190	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9191	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9192	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9193	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9194	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9195	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9196	VUB4:Delayed pick-up release	R(W)	1	0=No 1=Yes
HR9197	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9198	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9199	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9200	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9201	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9202	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9203	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9204	VUB4:Op.Time calculation reset after release time	R(W)	1	0=No 1=Yes
HR9205	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9206	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9207	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9208	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9209	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9210	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9211	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9212	VUB4:Continue time calculation during release time	R(W)	1	0=No 1=Yes
HR9213	FRQV1:f> Enable	R(W)	1	0=No 1=Yes
HR9214	FRQV1:f>> Enable	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9215	FRQV1:f>>> Enable	R(W)	1	0=No 1=Yes
HR9216	FRQV1:f>>>> Enable	R(W)	1	0=No 1=Yes
HR9217	FRQV1:f< Enable	R(W)	1	0=No 1=Yes
HR9218	FRQV1:f<< Enable	R(W)	1	0=No 1=Yes
HR9219	FRQV1:f<<< Enable	R(W)	1	0=No 1=Yes
HR9220	FRQV1:f<<<< Enable	R(W)	1	0=No 1=Yes
HR9221	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9222	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9223	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9224	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9225	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9226	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9227	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9228	FRQV1:f> used in setting group	R(W)	1	0=No 1=Yes
HR9229	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9230				HR9229 (Lo-16bits)
HR9231	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9232				HR9231 (Lo-16bits)
HR9233	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9234				HR9233 (Lo-16bits)
HR9235	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9236				HR9235 (Lo-16bits)
HR9237	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9238				HR9237 (Lo-16bits)
HR9239	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9240				HR9239 (Lo-16bits)
HR9241	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9242				HR9241 (Lo-16bits)
HR9243	FRQV1:f> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9244				HR9243 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9245	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9246				HR9245 (Lo-16bits)
HR9247	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9248				HR9247 (Lo-16bits)
HR9249	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9250				HR9249 (Lo-16bits)
HR9251	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9252				HR9251 (Lo-16bits)
HR9253	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9254				HR9253 (Lo-16bits)
HR9255	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9256				HR9255 (Lo-16bits)
HR9257	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9258				HR9257 (Lo-16bits)
HR9259	FRQV1:f> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9260				HR9259 (Lo-16bits)
HR9261	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9262	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9263	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9264	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9265	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9266	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9267	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9268	FRQV1:f>> used in setting group	R(W)	1	0=No 1=Yes
HR9269	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9270				HR9269 (Lo-16bits)
HR9271	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9272				HR9271 (Lo-16bits)
HR9273	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9274				HR9273 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9275	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9276				HR9275 (Lo-16bits)
HR9277	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9278				HR9277 (Lo-16bits)
HR9279	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9280				HR9279 (Lo-16bits)
HR9281	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9282				HR9281 (Lo-16bits)
HR9283	FRQV1:f>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9284				HR9283 (Lo-16bits)
HR9285	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9286				HR9285 (Lo-16bits)
HR9287	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9288				HR9287 (Lo-16bits)
HR9289	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9290				HR9289 (Lo-16bits)
HR9291	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9292				HR9291 (Lo-16bits)
HR9293	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9294				HR9293 (Lo-16bits)
HR9295	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9296				HR9295 (Lo-16bits)
HR9297	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9298				HR9297 (Lo-16bits)
HR9299	FRQV1:f>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9300				HR9299 (Lo-16bits)
HR9301	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9302	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9303	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9304	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9305	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9306	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9307	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9308	FRQV1:f>>> used in setting group	R(W)	1	0=No 1=Yes
HR9309	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9310				HR9309 (Lo-16bits)
HR9311	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9312				HR9311 (Lo-16bits)
HR9313	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9314				HR9313 (Lo-16bits)
HR9315	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9316				HR9315 (Lo-16bits)
HR9317	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9318				HR9317 (Lo-16bits)
HR9319	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9320				HR9319 (Lo-16bits)
HR9321	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9322				HR9321 (Lo-16bits)
HR9323	FRQV1:f>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9324				HR9323 (Lo-16bits)
HR9325	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9326				HR9325 (Lo-16bits)
HR9327	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9328				HR9327 (Lo-16bits)
HR9329	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9330				HR9329 (Lo-16bits)
HR9331	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9332				HR9331 (Lo-16bits)
HR9333	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9334				HR9333 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9335	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9336				HR9335 (Lo-16bits)
HR9337	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9338				HR9337 (Lo-16bits)
HR9339	FRQV1:f>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9340				HR9339 (Lo-16bits)
HR9341	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9342	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9343	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9344	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9345	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9346	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9347	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9348	FRQV1:f>>>> used in setting group	R(W)	1	0=No 1=Yes
HR9349	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9350				HR9349 (Lo-16bits)
HR9351	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9352				HR9351 (Lo-16bits)
HR9353	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9354				HR9353 (Lo-16bits)
HR9355	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9356				HR9355 (Lo-16bits)
HR9357	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9358				HR9357 (Lo-16bits)
HR9359	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9360				HR9359 (Lo-16bits)
HR9361	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9362				HR9361 (Lo-16bits)
HR9363	FRQV1:f>>>> pick-up	R(W)	2	FLOAT [10...70] (Hi-16bits)
HR9364				HR9363 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9365	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9366				HR9365 (Lo-16bits)
HR9367	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9368				HR9367 (Lo-16bits)
HR9369	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9370				HR9369 (Lo-16bits)
HR9371	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9372				HR9371 (Lo-16bits)
HR9373	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9374				HR9373 (Lo-16bits)
HR9375	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9376				HR9375 (Lo-16bits)
HR9377	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9378				HR9377 (Lo-16bits)
HR9379	FRQV1:f>>>> operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9380				HR9379 (Lo-16bits)
HR9381	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9382	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9383	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9384	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9385	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9386	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9387	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9388	FRQV1:f< used in setting group	R(W)	1	0=No 1=Yes
HR9389	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9390				HR9389 (Lo-16bits)
HR9391	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9392				HR9391 (Lo-16bits)
HR9393	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9394				HR9393 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9395	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9396				HR9395 (Lo-16bits)
HR9397	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9398				HR9397 (Lo-16bits)
HR9399	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9400				HR9399 (Lo-16bits)
HR9401	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9402				HR9401 (Lo-16bits)
HR9403	FRQV1:f< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9404				HR9403 (Lo-16bits)
HR9405	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9406				HR9405 (Lo-16bits)
HR9407	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9408				HR9407 (Lo-16bits)
HR9409	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9410				HR9409 (Lo-16bits)
HR9411	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9412				HR9411 (Lo-16bits)
HR9413	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9414				HR9413 (Lo-16bits)
HR9415	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9416				HR9415 (Lo-16bits)
HR9417	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9418				HR9417 (Lo-16bits)
HR9419	FRQV1:f< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9420				HR9419 (Lo-16bits)
HR9421	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9422	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9423	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9424	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR9425	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9426	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9427	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9428	FRQV1:f<< used in setting group	R(W)	1	0=No 1=Yes
HR9429	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9430				HR9429 (Lo-16bits)
HR9431	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9432				HR9431 (Lo-16bits)
HR9433	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9434				HR9433 (Lo-16bits)
HR9435	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9436				HR9435 (Lo-16bits)
HR9437	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9438				HR9437 (Lo-16bits)
HR9439	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9440				HR9439 (Lo-16bits)
HR9441	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9442				HR9441 (Lo-16bits)
HR9443	FRQV1:f<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9444				HR9443 (Lo-16bits)
HR9445	FRQV1:f<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9446				HR9445 (Lo-16bits)
HR9447	FRQV1:f<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9448				HR9447 (Lo-16bits)
HR9449	FRQV1:f<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9450				HR9449 (Lo-16bits)
HR9451	FRQV1:f<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9452				HR9451 (Lo-16bits)
HR9453	FRQV1:f<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9454				HR9453 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9455	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9456				HR9455 (Lo-16bits)
HR9457	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9458				HR9457 (Lo-16bits)
HR9459	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9460				HR9459 (Lo-16bits)
HR9461	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9462	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9463	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9464	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9465	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9466	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9467	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9468	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9469	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9470				HR9469 (Lo-16bits)
HR9471	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9472				HR9471 (Lo-16bits)
HR9473	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9474				HR9473 (Lo-16bits)
HR9475	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9476				HR9475 (Lo-16bits)
HR9477	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9478				HR9477 (Lo-16bits)
HR9479	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9480				HR9479 (Lo-16bits)
HR9481	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9482				HR9481 (Lo-16bits)
HR9483	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9484				HR9483 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9485	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9486				HR9485 (Lo-16bits)
HR9487	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9488				HR9487 (Lo-16bits)
HR9489	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9490				HR9489 (Lo-16bits)
HR9491	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9492				HR9491 (Lo-16bits)
HR9493	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9494				HR9493 (Lo-16bits)
HR9495	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9496				HR9495 (Lo-16bits)
HR9497	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9498				HR9497 (Lo-16bits)
HR9499	FRQV1:f<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9500				HR9499 (Lo-16bits)
HR9501	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9502	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9503	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9504	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9505	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9506	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9507	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9508	FRQV1:f<<<< used in setting group	R(W)	1	0=No 1=Yes
HR9509	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9510				HR9509 (Lo-16bits)
HR9511	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9512				HR9511 (Lo-16bits)
HR9513	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9514				HR9513 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9515	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9516				HR9515 (Lo-16bits)
HR9517	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9518				HR9517 (Lo-16bits)
HR9519	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9520				HR9519 (Lo-16bits)
HR9521	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9522				HR9521 (Lo-16bits)
HR9523	FRQV1:f<<<< pick-up	R(W)	2	FLOAT [7...65] (Hi-16bits)
HR9524				HR9523 (Lo-16bits)
HR9525	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9526				HR9525 (Lo-16bits)
HR9527	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9528				HR9527 (Lo-16bits)
HR9529	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9530				HR9529 (Lo-16bits)
HR9531	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9532				HR9531 (Lo-16bits)
HR9533	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9534				HR9533 (Lo-16bits)
HR9535	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9536				HR9535 (Lo-16bits)
HR9537	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9538				HR9537 (Lo-16bits)
HR9539	FRQV1:f<<<< operating time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR9540				HR9539 (Lo-16bits)
HR9541	DEV:Serial number	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR9542				HR9541 (Lo-16bits)
HR9543	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9544				HR9543 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9545	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9546				HR9545 (Lo-16bits)
HR9547	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9548				HR9547 (Lo-16bits)
HR9549	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9550				HR9549 (Lo-16bits)
HR9551	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9552				HR9551 (Lo-16bits)
HR9553	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9554				HR9553 (Lo-16bits)
HR9555	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9556				HR9555 (Lo-16bits)
HR9557	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9558				HR9557 (Lo-16bits)
HR9559	DEV:Firmware version	R	2	STRING [...32] (Hi-16bits)
HR9560				HR9559 (Lo-16bits)
HR9561	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9562				HR9561 (Lo-16bits)
HR9563	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9564				HR9563 (Lo-16bits)
HR9565	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9566				HR9565 (Lo-16bits)
HR9567	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9568				HR9567 (Lo-16bits)
HR9569	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9570				HR9569 (Lo-16bits)
HR9571	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9572				HR9571 (Lo-16bits)
HR9573	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9574				HR9573 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9575	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9576				HR9575 (Lo-16bits)
HR9577	DEV:Hardware configuration	R	2	STRING [0...32] (Hi-16bits)
HR9578				HR9577 (Lo-16bits)
HR9579	MBTCP:Enable mapped setting value modification through Modbus	RW	1	0=Disabled 1=Enabled
HR9580	M1CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR9581				HR9580 (Lo-16bits)
HR9582	M1CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...10] (Hi-16bits)
HR9583				HR9582 (Lo-16bits)
HR9584	M1CT2:I01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR9585				HR9584 (Lo-16bits)
HR9586	M1CT2:I01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR9587				HR9586 (Lo-16bits)
HR9588	M1CT3:I02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR9589				HR9588 (Lo-16bits)
HR9590	M1CT3:I02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR9591				HR9590 (Lo-16bits)
HR9592	M2CT1:Phase CT primary	R(W)	2	FLOAT [1...25000] (Hi-16bits)
HR9593				HR9592 (Lo-16bits)
HR9594	M2CT1:Phase CT secondary	R(W)	2	FLOAT [0.2...10] (Hi-16bits)
HR9595				HR9594 (Lo-16bits)
HR9596	M2CT2:I"01 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR9597				HR9596 (Lo-16bits)
HR9598	M2CT2:I"01 CT secondary	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR9599				HR9598 (Lo-16bits)
HR9600	M2CT3:I"02 CT primary	R(W)	2	FLOAT [0.2...25000] (Hi-16bits)
HR9601				HR9600 (Lo-16bits)
HR9602	M2CT3:I"02 CT secondary	R(W)	2	FLOAT [0.001...10] (Hi-16bits)
HR9603				HR9602 (Lo-16bits)
HR9604	M1VT1:VT Primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9605				HR9604 (Lo-16bits)
HR9606	M1VT1:VT Secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR9607				HR9606 (Lo-16bits)
HR9608	M1VT1:U3 Res/SS VT Primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR9609				HR9608 (Lo-16bits)
HR9610	M1VT1:U3 Res/SS VT Secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR9611				HR9610 (Lo-16bits)
HR9612	M1VT1:U4 Res/SS VT Primary	R(W)	2	FLOAT [1...1000000] (Hi-16bits)
HR9613				HR9612 (Lo-16bits)
HR9614	M1VT1:U4 Res/SS VT Secondary	R(W)	2	FLOAT [0.2...400] (Hi-16bits)
HR9615				HR9614 (Lo-16bits)
HR9616	NOC1:l> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR9617	NOC2:l>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR9618	NOC3:l>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR9619	NOC4:l>>>> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR9620	NEF1:l0> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR9621	NEF2:l0>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR9622	NEF3:l0>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR9623	NEF4:l0>>>> [50N/51N] mode	R(W)	1	0=Disabled 1=Activated
HR9624	CUB1:l2> [46 46R 46L] mode	R(W)	1	0=Disabled 1=Activated
HR9625	CUB2:l2>> [46 46R 46L] mode	R(W)	1	0=Disabled 1=Activated
HR9626	CUB3:l2>>> [46 46R 46L] mode	R(W)	1	0=Disabled 1=Activated
HR9627	CUB4:l2>>>> [46 46R 46L] mode	R(W)	1	0=Disabled 1=Activated
HR9628	OV1:U> [59] mode	R(W)	1	0=Disabled 1=Activated
HR9629	OV2:U>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR9630	OV3:U>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR9631	OV4:U>>>> [59] mode	R(W)	1	0=Disabled 1=Activated
HR9632	UV1:U< [27] mode	R(W)	1	0=Disabled 1=Activated
HR9633	UV2:U<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR9634	UV3:U<<< [27] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR9635	UV4:U<<<< [27] mode	R(W)	1	0=Disabled 1=Activated
HR9636	VUB1:U1/2 >/< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR9637	VUB2:U1/2 >>/<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR9638	VUB3:U1/2 >>>/<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR9639	VUB4:U1/2 >>>>/<<<< [47/27PN/59PN] mode	R(W)	1	0=Disabled 1=Activated
HR9640	FRQV1:f</> [81O/81U] mode	R(W)	1	0=Disabled 1=Activated
HR9641	DOC1:ldir> [67] mode	R(W)	1	0=Disabled 1=Activated
HR9642	DOC2:ldir>> [67] mode	R(W)	1	0=Disabled 1=Activated
HR9643	DOC3:ldir>>> [67] mode	R(W)	1	0=Disabled 1=Activated
HR9644	DOC4:ldir>>>> [67] mode	R(W)	1	0=Disabled 1=Activated
HR9645	DEF1:I0dir> [67N/32N] mode	R(W)	1	0=Disabled 1=Activated
HR9646	DEF2:I0dir>> [67N/32N] mode	R(W)	1	0=Disabled 1=Activated
HR9647	DEF3:I0dir>>> [67N/32N] mode	R(W)	1	0=Disabled 1=Activated
HR9648	DEF4:I0dir>>>> [67N/32N] mode	R(W)	1	0=Disabled 1=Activated
HR9649	LOGIC:Logical double bit Input 1	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9650				HR9649 (Lo-16bits)
HR9651	LOGIC:State00 for Logical double bit Input 1	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9652				HR9651 (Lo-16bits)
HR9653	LOGIC:State01 for Logical double bit Input 1	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9654				HR9653 (Lo-16bits)
HR9655	LOGIC:State10 for Logical double bit Input 1	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9656				HR9655 (Lo-16bits)
HR9657	LOGIC:State11 for Logical double bit Input 1	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9658				HR9657 (Lo-16bits)
HR9659	LOGIC:Logical double bit Input 2	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9660				HR9659 (Lo-16bits)
HR9661	LOGIC:State00 for Logical double bit Input 2	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9662				HR9661 (Lo-16bits)
HR9663	LOGIC:State01 for Logical double bit Input 2	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9664				HR9663 (Lo-16bits)
HR9665	LOGIC:State10 for Logical double bit Input 2	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9666				HR9665 (Lo-16bits)
HR9667	LOGIC:State11 for Logical double bit Input 2	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9668				HR9667 (Lo-16bits)
HR9669	LOGIC:Logical double bit Input 3	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9670				HR9669 (Lo-16bits)
HR9671	LOGIC:State00 for Logical double bit Input 3	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9672				HR9671 (Lo-16bits)
HR9673	LOGIC:State01 for Logical double bit Input 3	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9674				HR9673 (Lo-16bits)
HR9675	LOGIC:State10 for Logical double bit Input 3	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9676				HR9675 (Lo-16bits)
HR9677	LOGIC:State11 for Logical double bit Input 3	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9678				HR9677 (Lo-16bits)
HR9679	LOGIC:Logical double bit Input 4	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9680				HR9679 (Lo-16bits)
HR9681	LOGIC:State00 for Logical double bit Input 4	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9682				HR9681 (Lo-16bits)
HR9683	LOGIC:State01 for Logical double bit Input 4	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9684				HR9683 (Lo-16bits)
HR9685	LOGIC:State10 for Logical double bit Input 4	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9686				HR9685 (Lo-16bits)
HR9687	LOGIC:State11 for Logical double bit Input 4	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9688				HR9687 (Lo-16bits)
HR9689	LOGIC:Logical double bit Input 5	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9690				HR9689 (Lo-16bits)
HR9691	LOGIC:State00 for Logical double bit Input 5	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9692				HR9691 (Lo-16bits)
HR9693	LOGIC:State01 for Logical double bit Input 5	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9694				HR9693 (Lo-16bits)
HR9695	LOGIC:State10 for Logical double bit Input 5	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9696				HR9695 (Lo-16bits)
HR9697	LOGIC:State11 for Logical double bit Input 5	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9698				HR9697 (Lo-16bits)
HR9699	LOGIC:Logical double bit Input 6	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9700				HR9699 (Lo-16bits)
HR9701	LOGIC:State00 for Logical double bit Input 6	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9702				HR9701 (Lo-16bits)
HR9703	LOGIC:State01 for Logical double bit Input 6	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9704				HR9703 (Lo-16bits)
HR9705	LOGIC:State10 for Logical double bit Input 6	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9706				HR9705 (Lo-16bits)
HR9707	LOGIC:State11 for Logical double bit Input 6	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9708				HR9707 (Lo-16bits)
HR9709	LOGIC:Logical double bit Input 7	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9710				HR9709 (Lo-16bits)
HR9711	LOGIC:State00 for Logical double bit Input 7	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9712				HR9711 (Lo-16bits)
HR9713	LOGIC:State01 for Logical double bit Input 7	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9714				HR9713 (Lo-16bits)
HR9715	LOGIC:State10 for Logical double bit Input 7	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9716				HR9715 (Lo-16bits)
HR9717	LOGIC:State11 for Logical double bit Input 7	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9718				HR9717 (Lo-16bits)
HR9719	LOGIC:Logical double bit Input 8	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9720				HR9719 (Lo-16bits)
HR9721	LOGIC:State00 for Logical double bit Input 8	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9722				HR9721 (Lo-16bits)
HR9723	LOGIC:State01 for Logical double bit Input 8	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9724				HR9723 (Lo-16bits)
HR9725	LOGIC:State10 for Logical double bit Input 8	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9726				HR9725 (Lo-16bits)
HR9727	LOGIC:State11 for Logical double bit Input 8	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9728				HR9727 (Lo-16bits)
HR9729	LOGIC:Logical double bit Input 9	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9730				HR9729 (Lo-16bits)
HR9731	LOGIC:State00 for Logical double bit Input 9	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9732				HR9731 (Lo-16bits)
HR9733	LOGIC:State01 for Logical double bit Input 9	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9734				HR9733 (Lo-16bits)
HR9735	LOGIC:State10 for Logical double bit Input 9	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9736				HR9735 (Lo-16bits)
HR9737	LOGIC:State11 for Logical double bit Input 9	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9738				HR9737 (Lo-16bits)
HR9739	LOGIC:Logical double bit Input 10	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9740				HR9739 (Lo-16bits)
HR9741	LOGIC:State00 for Logical double bit Input 10	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9742				HR9741 (Lo-16bits)
HR9743	LOGIC:State01 for Logical double bit Input 10	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9744				HR9743 (Lo-16bits)
HR9745	LOGIC:State10 for Logical double bit Input 10	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9746				HR9745 (Lo-16bits)
HR9747	LOGIC:State11 for Logical double bit Input 10	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9748				HR9747 (Lo-16bits)
HR9749	NOCS1:IPW> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=StartL1 5=StartL2 6=StartL3 7=TripL1 8=TripL2 9=TripL3 10=StartL1-2 11=StartL2-3 12=StartL3-1 13=TripL1-2 14=TripL2-3 15=TripL31 16=StartL1-2-3 17=TripL1-2-3
HR9750	NOCS2:IPW>> condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=StartL1 5=StartL2 6=StartL3 7=TripL1 8=TripL2 9=TripL3 10=StartL1-2 11=StartL2-3 12=StartL3-1 13=TripL1-2 14=TripL2-3 15=TripL31 16=StartL1-2-3 17=TripL1-2-3
HR9751	OBJS1:Phase-wise object Breaker Status A(L1)	R	1	0=Intermediate 1=Open 2=Closed 3=Bad

Holding Register	Parameter	Access	Length	Info
HR9752	OBJS1:Phase-wise object Breaker Status B(L2)	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9753	OBJS1:Phase-wise object Breaker Status C(L3)	R	1	0=Intermediate 1=Open 2=Closed 3=Bad
HR9754	OBJS1:Phase-wise object Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR9755	LOGIC:Logical Input 1	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9756				HR9755 (Lo-16bits)
HR9757	LOGIC:Logical Input 2	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9758				HR9757 (Lo-16bits)
HR9759	LOGIC:Logical Input 3	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9760				HR9759 (Lo-16bits)
HR9761	LOGIC:Logical Input 4	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9762				HR9761 (Lo-16bits)
HR9763	LOGIC:Logical Input 5	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9764				HR9763 (Lo-16bits)
HR9765	LOGIC:Logical Input 6	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9766				HR9765 (Lo-16bits)
HR9767	LOGIC:Logical Input 7	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9768				HR9767 (Lo-16bits)
HR9769	LOGIC:Logical Input 8	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9770				HR9769 (Lo-16bits)
HR9771	LOGIC:Logical Input 9	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9772				HR9771 (Lo-16bits)
HR9773	LOGIC:Logical Input 10	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9774				HR9773 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9775	LOGIC:Logical Input 11	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9776				HR9775 (Lo-16bits)
HR9777	LOGIC:Logical Input 12	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9778				HR9777 (Lo-16bits)
HR9779	LOGIC:Logical Input 13	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9780				HR9779 (Lo-16bits)
HR9781	LOGIC:Logical Input 14	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9782				HR9781 (Lo-16bits)
HR9783	LOGIC:Logical Input 15	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9784				HR9783 (Lo-16bits)
HR9785	LOGIC:Logical Input 16	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9786				HR9785 (Lo-16bits)
HR9787	LOGIC:Logical Input 17	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9788				HR9787 (Lo-16bits)
HR9789	LOGIC:Logical Input 18	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9790				HR9789 (Lo-16bits)
HR9791	LOGIC:Logical Input 19	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9792				HR9791 (Lo-16bits)
HR9793	LOGIC:Logical Input 20	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9794				HR9793 (Lo-16bits)
HR9795	LOGIC:Logical Input 21	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9796				HR9795 (Lo-16bits)
HR9797	LOGIC:Logical Input 22	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9798				HR9797 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9799	LOGIC:Logical Input 23	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9800				HR9799 (Lo-16bits)
HR9801	LOGIC:Logical Input 24	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9802				HR9801 (Lo-16bits)
HR9803	LOGIC:Logical Input 25	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9804				HR9803 (Lo-16bits)
HR9805	LOGIC:Logical Input 26	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9806				HR9805 (Lo-16bits)
HR9807	LOGIC:Logical Input 27	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9808				HR9807 (Lo-16bits)
HR9809	LOGIC:Logical Input 28	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9810				HR9809 (Lo-16bits)
HR9811	LOGIC:Logical Input 29	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9812				HR9811 (Lo-16bits)
HR9813	MEAS1:IL1 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9814				HR9813 (Lo-16bits)
HR9815	MEAS1:IL1 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9816				HR9815 (Lo-16bits)
HR9817	MEAS1:IL2 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9818				HR9817 (Lo-16bits)
HR9819	MEAS1:IL2 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9820				HR9819 (Lo-16bits)
HR9821	MEAS1:IL3 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9822				HR9821 (Lo-16bits)
HR9823	MEAS1:IL3 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9824				HR9823 (Lo-16bits)
HR9825	MEAS1:I01 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9826				HR9825 (Lo-16bits)
HR9827	MEAS1:I01 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9828				HR9827 (Lo-16bits)
HR9829	MEAS1:I02 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9830				HR9829 (Lo-16bits)
HR9831	MEAS1:I02 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9832				HR9831 (Lo-16bits)
HR9833	MEAS2:I"L1 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9834				HR9833 (Lo-16bits)
HR9835	MEAS2:I"L1 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9836				HR9835 (Lo-16bits)
HR9837	MEAS2:I"L2 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9838				HR9837 (Lo-16bits)
HR9839	MEAS2:I"L2 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9840				HR9839 (Lo-16bits)
HR9841	MEAS2:I"L3 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9842				HR9841 (Lo-16bits)
HR9843	MEAS2:I"L3 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9844				HR9843 (Lo-16bits)
HR9845	MEAS2:I"01 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9846				HR9845 (Lo-16bits)
HR9847	MEAS2:I"01 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9848				HR9847 (Lo-16bits)
HR9849	MEAS2:I"02 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9850				HR9849 (Lo-16bits)
HR9851	MEAS2:I"02 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9852				HR9851 (Lo-16bits)
HR9853	MEAS3:UL1 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9854				HR9853 (Lo-16bits)
HR9855	MEAS3:UL1 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9856				HR9855 (Lo-16bits)
HR9857	MEAS3:UL2 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9858				HR9857 (Lo-16bits)
HR9859	MEAS3:UL2 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9860				HR9859 (Lo-16bits)
HR9861	MEAS3:UL3 Amplitude THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9862				HR9861 (Lo-16bits)
HR9863	MEAS3:UL3 Power THD	R	2	FLOAT [0...100000] (Hi-16bits)
HR9864				HR9863 (Lo-16bits)
HR9865	POW1:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9866				HR9865 (Lo-16bits)
HR9867	POW1:Apparent Energy (S) while Export (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9868				HR9867 (Lo-16bits)
HR9869	POW1:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9870				HR9869 (Lo-16bits)
HR9871	POW1:Apparent Energy (S) while Import (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9872				HR9871 (Lo-16bits)
HR9873	POW1:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9874				HR9873 (Lo-16bits)
HR9875	POW1:Real Energy (P) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9876				HR9875 (Lo-16bits)
HR9877	POW1:Reactive Energy (Q) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9878				HR9877 (Lo-16bits)
HR9879	POW1:Real Energy (P) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9880				HR9879 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9881	POW1:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9882				HR9881 (Lo-16bits)
HR9883	POW1:Real Energy (P) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9884				HR9883 (Lo-16bits)
HR9885	POW1:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9886				HR9885 (Lo-16bits)
HR9887	POW2:Apparent Energy (S) while Export (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9888				HR9887 (Lo-16bits)
HR9889	POW2:Apparent Energy (S) while Export (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9890				HR9889 (Lo-16bits)
HR9891	POW2:Apparent Energy (S) while Import (P) MVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9892				HR9891 (Lo-16bits)
HR9893	POW2:Apparent Energy (S) while Import (P) kVAh	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9894				HR9893 (Lo-16bits)
HR9895	POW2:Apparent Energy (S) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9896				HR9895 (Lo-16bits)
HR9897	POW2:Real Energy (P) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9898				HR9897 (Lo-16bits)
HR9899	POW2:Reactive Energy (Q) Net	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9900				HR9899 (Lo-16bits)
HR9901	POW2:Real Energy (P) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9902				HR9901 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR9903	POW2:Reactive Energy (Q) Supply (Default supply direction towards busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9904				HR9903 (Lo-16bits)
HR9905	POW2:Real Energy (P) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9906				HR9905 (Lo-16bits)
HR9907	POW2:Reactive Energy (Q) Demand (Default demand direction from busbar)	R	2	INT32 [-2147483647...2147483647] (Hi-16bits)
HR9908				HR9907 (Lo-16bits)
HR9909	GNSIG:Common signals condition	R	1	0=Normal 1=Start 2=Trip
HR9910	UCP1:IUC> condition	R	1	0=Normal 1=Blocked 2=StartOn(Alarm) 3=StartOn(Alarm&Trip) 4=StartOn(Trip) 5=AlarmOn 6=StartOn(Alarm) TripOn 7=StartOn AlarmOn 8=TripOn 9=AlarmOn TripOn 10=MultistatusOn
HR9911	CRF1:Cd> (87C) condition	R	1	0=Normal 1=Trip 2=Blocked
HR9912	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9913				HR9912 (Lo-16bits)
HR9914	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9915				HR9914 (Lo-16bits)
HR9916	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9917				HR9916 (Lo-16bits)
HR9918	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9919				HR9918 (Lo-16bits)
HR9920	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9921				HR9920 (Lo-16bits)
HR9922	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9923				HR9922 (Lo-16bits)
HR9924	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9925				HR9924 (Lo-16bits)
HR9926	FRQV1:f< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9927				HR9926 (Lo-16bits)
HR9928	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9929				HR9928 (Lo-16bits)
HR9930	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9931				HR9930 (Lo-16bits)
HR9932	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9933				HR9932 (Lo-16bits)
HR9934	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9935				HR9934 (Lo-16bits)
HR9936	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9937				HR9936 (Lo-16bits)
HR9938	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9939				HR9938 (Lo-16bits)
HR9940	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9941				HR9940 (Lo-16bits)
HR9942	FRQV1:f<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9943				HR9942 (Lo-16bits)
HR9944	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9945				HR9944 (Lo-16bits)
HR9946	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9947				HR9946 (Lo-16bits)
HR9948	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9949				HR9948 (Lo-16bits)
HR9950	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9951				HR9950 (Lo-16bits)
HR9952	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9953				HR9952 (Lo-16bits)
HR9954	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9955				HR9954 (Lo-16bits)
HR9956	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9957				HR9956 (Lo-16bits)
HR9958	FRQV1:f<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9959				HR9958 (Lo-16bits)
HR9960	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9961				HR9960 (Lo-16bits)
HR9962	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9963				HR9962 (Lo-16bits)
HR9964	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9965				HR9964 (Lo-16bits)
HR9966	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9967				HR9966 (Lo-16bits)
HR9968	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9969				HR9968 (Lo-16bits)
HR9970	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9971				HR9970 (Lo-16bits)
HR9972	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR9973				HR9972 (Lo-16bits)
HR9974	FRQV1:f<<<< Undervoltage block (0% no blocking)	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR9975				HR9974 (Lo-16bits)
HR9976	RHC1:Start count	R(W)	2	UINT32 [0...4294967295] (Hi-16bits)
HR9977				HR9976 (Lo-16bits)
HR9978	RTDIN:S1 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9979	RTDIN:S2 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9980	RTDIN:S3 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9981	RTDIN:S4 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9982	RTDIN:S5 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9983	RTDIN:S6 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9984	RTDIN:S7 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9985	RTDIN:S8 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9986	RTDIN:S9 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9987	RTDIN:S10 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9988	RTDIN:S11 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9989	RTDIN:S12 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9990	RTDIN:S13 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9991	RTDIN:S14 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9992	RTDIN:S15 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9993	RTDIN:S16 Sensor	R	1	0=Invalid Open Circuit 1=OK 2=Invalid Shorted Circuit
HR9994	OOS1:Pole slip condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked

Holding Register	Parameter	Access	Length	Info
HR9995	OOS1:Configuration status	R	1	0=Ok 1=Incorrect VT set 2=Incorrect char. Set
HR9996	OOS1:PoleSlip detection START	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9997				HR9996 (Lo-16bits)
HR9998	OOS1:PoleSlip TRIP	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR9999				HR9998 (Lo-16bits)
HR10000	OOS1:PoleSlip detection BLOCKED	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10001				HR10000 (Lo-16bits)
HR10002	GSYN:Enable on-screen synchronizer view	R(W)	1	0=Disabled 1=Activated
HR10003	GSYN:Synchronizing by	R(W)	1	0=Autosynchronizer 1=Manual commands
HR10004	GSYN:Manual switching mode	R(W)	1	0=Assisted synchroswitch 1=I know what I am doing
HR10005	GSYN:Magnitude side A	R	2	FLOAT [0...200] (Hi-16bits)
HR10006				HR10005 (Lo-16bits)
HR10007	GSYN:Magnitude side B	R	2	FLOAT [0...200] (Hi-16bits)
HR10008				HR10007 (Lo-16bits)
HR10009	GSYN:Frequency side A	R	2	FLOAT [0...75] (Hi-16bits)
HR10010				HR10009 (Lo-16bits)
HR10011	GSYN:Frequency side B	R	2	FLOAT [0...75] (Hi-16bits)
HR10012				HR10011 (Lo-16bits)
HR10013	GSYN:Open synchronizer HMI	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10014				HR10013 (Lo-16bits)
HR10015	GSYN:Manual increase voltage	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10016				HR10015 (Lo-16bits)
HR10017	GSYN:Manual decrease voltage	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10018				HR10017 (Lo-16bits)
HR10019	GSYN:Manual increase frequency	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10020				HR10019 (Lo-16bits)
HR10021	GSYN:Manual decrease frequency	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10022				HR10021 (Lo-16bits)
HR10023	GSYN:Manual Close Breaker	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10024				HR10023 (Lo-16bits)
HR10025	FREQS:SS1.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR10026				HR10025 (Lo-16bits)
HR10027	FREQS:SS2.meas.frqs	R	2	FLOAT [0...75] (Hi-16bits)
HR10028				HR10027 (Lo-16bits)
HR10029	HSCH:HSO card inputs	R	2	0b=HSO1 TCS In 1b=HSO2 TCS In 2b=HSO3 TCS In 3b=HSO4 TCS In 4b=HSO5 TCS In 5b=HSO6 TCS In 6b=HSO7 TCS In 7b=HSO8 TCS In 8b=HSO9 TCS In (Hi-16bits)
HR10030				HR10029 (Lo-16bits)
HR10031	SGS2:SGS2 Active setting group	R	1	0=SG1 1=SG2 2=SG3 3=SG4 4=SG5 5=SG6 6=SG7 7=SG8
HR10032	SGS2:SGS2 Force setting group	R(W)	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR10033	SGS2:SGS2 Force setting group change	R(W)	1	0=Disabled 1=Enabled
HR10034	SGS2:SGS2 Used setting groups	R(W)	1	0=SG1 1=SG1...2 2=SG1...3 3=SG1...4 4=SG1...5 5=SG1...6 6=SG1...7 7=SG1...8
HR10035	SGS2:SGS2 Remote setting group change	R(W)	1	0=none 1=SG1 2=SG2 3=SG3 4=SG4 5=SG5 6=SG6 7=SG7 8=SG8
HR10036	LOGIC:Logical Input 30	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10037				HR10036 (Lo-16bits)
HR10038	LOGIC:Logical Input 31	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10039				HR10038 (Lo-16bits)
HR10040	LOGIC:Logical Input 32	RW	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR10041				HR10040 (Lo-16bits)
HR10042	LDIF1:IdL> condition	R	2	0b=Idb> Trip 1b=Idb> Blocked 2b=Idi> Trip 3b=Idi> Blocked 4b=2nd Harm block on 5b=5th harm block on 6b=L1 2h On 7b=L2 2h On 8b=L3 2h On 9b=L1 5h On 10b=L2 5h On 11b=L3 5h On (Hi-16bits)
HR10043				HR10042 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10044	TOLT1:TT> setting alarm	R	1	0=SF setting ok 1=Service factor set fault.Override to1.0
HR10045	TOLT1:TT> setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault.Override to1.0
HR10046	TOLT1:TT> setting alarm	R	1	0=Nominalcurrent calc ok 1=Nominalcurrent set fault.Override to1.0
HR10047	TOLT1:TT> setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR10048	TOLF1:TF> setting alarm	R	1	0=SF setting ok 1=Service factor set fault.Override to1.0
HR10049	TOLF1:TF> setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault.Override to1.0
HR10050	TOLF1:TF> setting alarm	R	1	0=Nominalcurrent calc ok 1=Nominalcurrent set fault.Override to1.0
HR10051	TOLF1:TF> setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR10052	TOLM1:TM>Setting alarm	R	1	0=SF setting ok 1=Service factor set fault.Override to1.0
HR10053	TOLM1:TM>Setting alarm	R	1	0=Ambient t setting ok 1=Ambient t set fault.Override to1.0
HR10054	TOLM1:TM>Setting alarm	R	1	0=Nominalcurrent calc ok 1=Nominalcurrent set fault.Override to1.0
HR10055	TOLM1:TM>Setting alarm	R	1	0=Ambient setting ok 1=Inconsistent setting of ambient k
HR10056	URX1:X< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR10057	URX2:X<< condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR10058	SYN1:SYN1 Mag diff	R	2	FLOAT [-120...120] (Hi-16bits)
HR10059				HR10058 (Lo-16bits)
HR10060	SYN1:SYN1 Ang diff	R	2	FLOAT [-360...360] (Hi-16bits)
HR10061				HR10060 (Lo-16bits)
HR10062	SYN1:SYN1 freq diff	R	2	FLOAT [-75...75] (Hi-16bits)
HR10063				HR10062 (Lo-16bits)
HR10064	COL1:ICOL> [49OL] mode	R(W)	1	0=Disabled 1=Activated
HR10065	COL2:ICOL>> [49OL] mode	R(W)	1	0=Disabled 1=Activated
HR10066	CNU1:CNU> [50UB] mode	R(W)	1	0=Disabled 1=Activated
HR10067	UCP1:IUC> [46C] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10068	CRF1: Cd> (87C) mode	R(W)	1	0=Disabled 1=Activated
HR10069	NOCS1: IPW> [50/51] mode	R(W)	1	0=Disabled 1=Activated
HR10070	NOCS2: IPW>> mode	R(W)	1	0=Disabled 1=Activated
HR10071	EFX1: IOx> [67NX] mode	R(W)	1	0=Disabled 1=Activated
HR10072	EFX1: Used U0> reference	R	1	0=VT meas.mode error 1=U3 Input 2=U4 Input
HR10073	EFX1: IOx> condition	R	1	0=Normal 1=Start forward 2=Trip forward 3=Blocked 4=Start reverse 5=Trip reverse
HR10074	EFX1: IOx> Last fault active residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10075				HR10074 (Lo-16bits)
HR10076	EFX1: IOx> Last fault apparent residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10077				HR10076 (Lo-16bits)
HR10078	EFX1: IOx> Last fault apparent/ active residual power	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10079				HR10078 (Lo-16bits)
HR10080	EFX1: IOx> Last fault residual current	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10081				HR10080 (Lo-16bits)
HR10082	EFX1: IOx> Last fault zero sequence voltage	R	2	FLOAT [0...1000000] (Hi-16bits)
HR10083				HR10082 (Lo-16bits)
HR10084	EFX1: Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10085				HR10084 (Lo-16bits)
HR10086	EFX1: Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10087				HR10086 (Lo-16bits)
HR10088	EFX1: Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10089				HR10088 (Lo-16bits)
HR10090	EFX1: Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10091				HR10090 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10092	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10093				HR10092 (Lo-16bits)
HR10094	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10095				HR10094 (Lo-16bits)
HR10096	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10097				HR10096 (Lo-16bits)
HR10098	EFX1:Sw (active residual power pickup FWD)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10099				HR10098 (Lo-16bits)
HR10100	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10101				HR10100 (Lo-16bits)
HR10102	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10103				HR10102 (Lo-16bits)
HR10104	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10105				HR10104 (Lo-16bits)
HR10106	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10107				HR10106 (Lo-16bits)
HR10108	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10109				HR10108 (Lo-16bits)
HR10110	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10111				HR10110 (Lo-16bits)
HR10112	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10113				HR10112 (Lo-16bits)
HR10114	EFX1:@Sw (active residual power pickup REV)	R(W)	2	FLOAT [20...120] (Hi-16bits)
HR10115				HR10114 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10116	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10117				HR10116 (Lo-16bits)
HR10118	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10119				HR10118 (Lo-16bits)
HR10120	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10121				HR10120 (Lo-16bits)
HR10122	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10123				HR10122 (Lo-16bits)
HR10124	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10125				HR10124 (Lo-16bits)
HR10126	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10127				HR10126 (Lo-16bits)
HR10128	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10129				HR10128 (Lo-16bits)
HR10130	EFX1:Sw/@Sw hysteresis	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10131				HR10130 (Lo-16bits)
HR10132	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10133				HR10132 (Lo-16bits)
HR10134	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10135				HR10134 (Lo-16bits)
HR10136	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10137				HR10136 (Lo-16bits)
HR10138	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10139				HR10138 (Lo-16bits)
HR10140	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10141				HR10140 (Lo-16bits)
HR10142	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10143				HR10142 (Lo-16bits)
HR10144	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10145				HR10144 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10146	EFX1:S/P (angle pickup)	R(W)	2	FLOAT [0...20] (Hi-16bits)
HR10147				HR10146 (Lo-16bits)
HR10148	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10149				HR10148 (Lo-16bits)
HR10150	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10151				HR10150 (Lo-16bits)
HR10152	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10153				HR10152 (Lo-16bits)
HR10154	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10155				HR10154 (Lo-16bits)
HR10156	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10157				HR10156 (Lo-16bits)
HR10158	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10159				HR10158 (Lo-16bits)
HR10160	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10161				HR10160 (Lo-16bits)
HR10162	EFX1:S/P (angle threshold)	R(W)	2	FLOAT [0.85...0.95] (Hi-16bits)
HR10163				HR10162 (Lo-16bits)
HR10164	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10165				HR10164 (Lo-16bits)
HR10166	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10167				HR10166 (Lo-16bits)
HR10168	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10169				HR10168 (Lo-16bits)
HR10170	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10171				HR10170 (Lo-16bits)
HR10172	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10173				HR10172 (Lo-16bits)
HR10174	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10175				HR10174 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10176	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10177				HR10176 (Lo-16bits)
HR10178	EFX1:Operating time	R(W)	2	FLOAT [0.02...5] (Hi-16bits)
HR10179				HR10178 (Lo-16bits)
HR10180	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10181				HR10180 (Lo-16bits)
HR10182	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10183				HR10182 (Lo-16bits)
HR10184	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10185				HR10184 (Lo-16bits)
HR10186	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10187				HR10186 (Lo-16bits)
HR10188	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10189				HR10188 (Lo-16bits)
HR10190	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10191				HR10190 (Lo-16bits)
HR10192	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10193				HR10192 (Lo-16bits)
HR10194	EFX1:Release delay	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10195				HR10194 (Lo-16bits)
HR10196	IEF1:I0Int> [67NT] mode	R(W)	1	0=Disabled 1=Activated
HR10197	HOC1:Ih> [50H/51H/68H] mode	R(W)	1	0=Disabled 1=Activated
HR10198	HOC2:Ih>> [50H/51H/68H] mode	R(W)	1	0=Disabled 1=Activated
HR10199	HOC3:Ih>>> [50H/51H/68H] mode	R(W)	1	0=Disabled 1=Activated
HR10200	HOC4:Ih>>>> [50H/51H/68H] mode	R(W)	1	0=Disabled 1=Activated
HR10201	CLP1:CLP mode	R(W)	1	0=Disabled 1=Activated
HR10202	CBF1:CBFP [50BF] mode	R(W)	1	0=Disabled 1=Activated
HR10203	DIF1:Idx> [87T/87G/87M/87N] mode	R(W)	1	0=Disabled 1=Activated
HR10204	LDIF1:IdL> mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10205	REF1:I0d> [87N] mode	R(W)	1	0=Disabled 1=Activated
HR10206	TOLT1:TT> [49T] mode	R(W)	1	0=Disabled 1=Activated
HR10207	TOLF1:TF> [49F] mode	R(W)	1	0=Disabled 1=Activated
HR10208	TOLF1:IN thermal cap current	R(W)	2	FLOAT [0...40] (Hi-16bits)
HR10209				HR10208 (Lo-16bits)
HR10210	TOLF1:Obj.Max.temp (tmax=100%)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10211				HR10210 (Lo-16bits)
HR10212	TOLF1:Man.Amb.Temp.Set	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10213				HR10212 (Lo-16bits)
HR10214	TOLF1:RTD Module	R(W)	1	0=ModuleA 1=ModuleB 2=ModuleC
HR10215	TOLF1:RTD Channel	R(W)	1	0=Ch0 1=Ch1 2=Ch2 3=Ch3 4=Ch4 5=Ch5 6=Ch6 7=Ch7
HR10216	TOLF1:RTD Deg C / Deg F	R(W)	1	0=Deg C 1=Deg F
HR10217	TOLF1:RTD Amb.Temp.Read	R(W)	2	FLOAT [-101...2000] (Hi-16bits)
HR10218				HR10217 (Lo-16bits)
HR10219	TOLF1:tau (t const)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10220				HR10219 (Lo-16bits)
HR10221	TOLF1:Max.Perm.OC.Current(norm Ik1s)	R(W)	2	FLOAT [1...9999999] (Hi-16bits)
HR10222				HR10221 (Lo-16bits)
HR10223	TOLF1:Max.OC.Time (norm 1s)	R(W)	2	FLOAT [0.1...5] (Hi-16bits)
HR10224				HR10223 (Lo-16bits)
HR10225	TOLF1:Nominal current	R(W)	2	FLOAT [1...9999999] (Hi-16bits)
HR10226				HR10225 (Lo-16bits)
HR10227	TOLF1:kSF (service factor)	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10228				HR10227 (Lo-16bits)
HR10229	TOLF1:Cold Reset default theta	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR10230				HR10229 (Lo-16bits)
HR10231	TOLF1:Ambient lin or curve	R(W)	1	0=Linear est. 1=Set Curve
HR10232	TOLF1:Temp. reference (tref) kamb=1.00	R(W)	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10233				HR10232 (Lo-16bits)
HR10234	TOLF1:Max.ambient temp	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10235				HR10234 (Lo-16bits)
HR10236	TOLF1:k at max.amb.temp.	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10237				HR10236 (Lo-16bits)
HR10238	TOLF1:Min.ambient temp	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR10239				HR10238 (Lo-16bits)
HR10240	TOLF1:k at min.amb.temp.	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10241				HR10240 (Lo-16bits)
HR10242	TOLF1:Amb.Temp.ref1	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10243				HR10242 (Lo-16bits)
HR10244	TOLF1:Amb.Temp.k1	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10245				HR10244 (Lo-16bits)
HR10246	TOLF1:Amb.Temp.ref2	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10247				HR10246 (Lo-16bits)
HR10248	TOLF1:Amb.Temp.k2	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10249				HR10248 (Lo-16bits)
HR10250	TOLF1:Add curvepoint 3	R(W)	1	0=Not used 1=Used
HR10251	TOLF1:Amb.Temp.ref3	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10252				HR10251 (Lo-16bits)
HR10253	TOLF1:Amb.Temp.k3	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10254				HR10253 (Lo-16bits)
HR10255	TOLF1:Add curvepoint 4	R(W)	1	0=Not used 1=Used
HR10256	TOLF1:Amb.Temp.ref4	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10257				HR10256 (Lo-16bits)
HR10258	TOLF1:Amb.Temp.k4	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10259				HR10258 (Lo-16bits)
HR10260	TOLF1:Add curvepoint 5	R(W)	1	0=Not used 1=Used
HR10261	TOLF1:Amb.Temp.ref5	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10262				HR10261 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10263	TOLF1:Amb.Temp.k5	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10264				HR10263 (Lo-16bits)
HR10265	TOLF1:Add curvepoint 6	R(W)	1	0=Not used 1=Used
HR10266	TOLF1:Amb.Temp.ref6	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10267				HR10266 (Lo-16bits)
HR10268	TOLF1:Amb.Temp.k6	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10269				HR10268 (Lo-16bits)
HR10270	TOLF1:Add curvepoint 7	R(W)	1	0=Not used 1=Used
HR10271	TOLF1:Amb.Temp.ref7	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10272				HR10271 (Lo-16bits)
HR10273	TOLF1:Amb.Temp.k7	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10274				HR10273 (Lo-16bits)
HR10275	TOLF1:Add curvepoint 8	R(W)	1	0=Not used 1=Used
HR10276	TOLF1:Amb.Temp.ref8	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10277				HR10276 (Lo-16bits)
HR10278	TOLF1:Amb.Temp.k8	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10279				HR10278 (Lo-16bits)
HR10280	TOLF1:Add curvepoint 9	R(W)	1	0=Not used 1=Used
HR10281	TOLF1:Amb.Temp.ref9	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10282				HR10281 (Lo-16bits)
HR10283	TOLF1:Amb.Temp.k9	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10284				HR10283 (Lo-16bits)
HR10285	TOLF1:Add curvepoint 10	R(W)	1	0=Not used 1=Used
HR10286	TOLF1:Amb.Temp.ref10	R(W)	2	FLOAT [-60...500] (Hi-16bits)
HR10287				HR10286 (Lo-16bits)
HR10288	TOLF1:Amb.Temp.k10	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR10289				HR10288 (Lo-16bits)
HR10290	TOLM1:TM> [49M] mode	R(W)	1	0=Disabled 1=Activated
HR10291	TOLM1:Curve AI	R(W)	1	0=Disabled 1=Enabled
HR10292	LCR1:ISr> [48/14/50LR] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10293	FSP1:N> [66/86] mode	R(W)	1	0=Disabled 1=Activated
HR10294	NUC1:I< [37] mode	R(W)	1	0=Disabled 1=Activated
HR10295	MJP1:Im> [50M/51M] mode	R(W)	1	0=Disabled 1=Activated
HR10296	NOV1:U0> [59N] mode	R(W)	1	0=Disabled 1=Activated
HR10297	NOV2:U0>> [59N] mode	R(W)	1	0=Disabled 1=Activated
HR10298	NOV3:U0>>> [59N] mode	R(W)	1	0=Disabled 1=Activated
HR10299	NOV4:U0>>>> [59N] mode	R(W)	1	0=Disabled 1=Activated
HR10300	PGS1:PSx >/< [99] mode	R(W)	1	0=Disabled 1=Activated
HR10301	FRQC1:f</> [Curr] mode	R(W)	1	0=Disabled 1=Activated
HR10302	DFT1:df/dt >/< [81] mode	R(W)	1	0=Disabled 1=Activated
HR10303	RHC1:Running Hour Counter mode	R(W)	1	0=Disabled 1=Activated
HR10304	PWR1:PQS>/< [32] mode	R(W)	1	0=Disabled 1=Activated
HR10305	PWR2:PQS>>/<< [32] mode	R(W)	1	0=Disabled 1=Activated
HR10306	PWR3:PQS>>>/<<< [32] mode	R(W)	1	0=Disabled 1=Activated
HR10307	PWR4:PQS>>>>/<<<< [32] mode	R(W)	1	0=Disabled 1=Activated
HR10308	OPW1:P> [32] mode (OBSOLETE)	R(W)	1	0=Disabled 1=Activated
HR10309	UPW1:P< [37] mode (OBSOLETE)	R(W)	1	0=Disabled 1=Activated
HR10310	RPW1:Prev> [32] mode (OBSOLETE)	R(W)	1	0=Disabled 1=Activated
HR10311	SEF1:U03rd< [64S] mode	R(W)	1	0=Disabled 1=Activated
HR10312	UEX1:Q< [40] mode	R(W)	1	0=Disabled 1=Activated
HR10313	UIM1:Z< [21U] mode	R(W)	1	0=Disabled 1=Activated
HR10314	UIM2:Z<< [21U] mode	R(W)	1	0=Disabled 1=Activated
HR10315	URX1:X< [40] mode	R(W)	1	0=Disabled 1=Activated
HR10316	URX2:X<< [40] mode	R(W)	1	0=Disabled 1=Activated
HR10317	VOC1:Iv> [51V] mode	R(W)	1	0=Disabled 1=Activated
HR10318	IAE1:I>U< I.A.E. [50/27] mode	R(W)	1	0=Disabled 1=Activated
HR10319	VHZ1:V/Hz > [24] mode	R(W)	1	0=Disabled 1=Activated
HR10320	UPF1:PF< [55] mode	R(W)	1	0=Disabled 1=Activated
HR10321	RTD1:RTD mode	R(W)	1	0=Disabled 1=Activated
HR10322	OOS1:Pole slip [78] mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10323	ARC1:I/O Arc> [50ARC/50NARC] mode	R(W)	1	0=Disabled 1=Activated
HR10324	ARC1:I/O Arc> Self supervision test pulse on option card 1	R(W)	1	0=Disabled 1=On
HR10325	ARC1:I/O Arc> Number of connected arc sub-units on option card 1	R(W)	1	0=1 AQ-101 1=2 AQ-101 2=3 AQ-101 3=4 AQ-101 4=5 AQ-101 5=6 AQ-101 6=7 AQ-101 7=8 AQ-101
HR10326	ARC1:I/O Arc> arc sub-unit status on option card 1	R(W)	1	0=AQ101 unit count match 1=AQ 101 unit count mismatch
HR10327	ARC1:Sensor status	R	2	0b=Ph Curr Blocked 1b=Ph Curr Start 2b=Res Curr Blocked 3b=Res Curr Start 4b=Channel1 Light 5b=Channel1 Pressure 6b=Channel2 Light 7b=Channel2 Pressure 8b=Channel3 Light 9b=Channel3 Pressure 10b=Channel4 Light 11b=Channel4 Pressure 12b=Digital input 13b=I/O Arc> Sensor 1 Fault 14b=I/O Arc> Sensor 2 Fault 15b=I/O Arc> Sensor 3 Fault 16b=I/O Arc> Sensor 4 Fault 17b=I/O Arc> I/O-unit Fault (Hi-16bits)
HR10328				HR10327 (Lo-16bits)
HR10329	ARC1:Channel 1 sensors	R(W)	1	0=No Sensors 1=1 Sensor 2=2 Sensors 3=3 Sensors
HR10330	ARC1:Channel 1 sensor status	R	1	0=Sensors Ok 1=Configuration fault
HR10331	ARC1:Channel 2 sensors	R(W)	1	0=No Sensors 1=1 Sensor 2=2 Sensors 3=3 Sensors
HR10332	ARC1:Channel 2 sensor status	R	1	0=Sensors Ok 1=Configuration fault
HR10333	ARC1:Channel 3 sensors	R(W)	1	0=No Sensors 1=1 Sensor 2=2 Sensors 3=3 Sensors
HR10334	ARC1:Channel 3 sensor status	R	1	0=Sensors Ok 1=Configuration fault
HR10335	ARC1:Channel 4 sensors	R(W)	1	0=No Sensors 1=1 Sensor 2=2 Sensors 3=3 Sensors
HR10336	ARC1:Channel 4 sensor status	R	1	0=Sensors Ok 1=Configuration fault
HR10337	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10338				HR10337 (Lo-16bits)
HR10339	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10340				HR10339 (Lo-16bits)
HR10341	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10342				HR10341 (Lo-16bits)
HR10343	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10344				HR10343 (Lo-16bits)
HR10345	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10346				HR10345 (Lo-16bits)
HR10347	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10348				HR10347 (Lo-16bits)
HR10349	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10350				HR10349 (Lo-16bits)
HR10351	ARC1:Phase current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10352				HR10351 (Lo-16bits)
HR10353	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10354	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10355	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10356	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10357	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10358	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10359	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10360	ARC1:I0 input selection	R(W)	1	0=None 1=I01 2=I02
HR10361	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10362				HR10361 (Lo-16bits)
HR10363	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10364				HR10363 (Lo-16bits)
HR10365	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10366				HR10365 (Lo-16bits)
HR10367	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10368				HR10367 (Lo-16bits)
HR10369	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10370				HR10369 (Lo-16bits)
HR10371	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10372				HR10371 (Lo-16bits)
HR10373	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10374				HR10373 (Lo-16bits)
HR10375	ARC1:Res. current pick-up	R(W)	2	FLOAT [0.05...40] (Hi-16bits)
HR10376				HR10375 (Lo-16bits)
HR10377	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10378	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10379	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10380	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10381	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10382	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10383	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10384	ARC1:Zone1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10385	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10386	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10387	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10388	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10389	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10390	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10391	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10392	ARC1:Zone1 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10393	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10394	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10395	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10396	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10397	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10398	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10399	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10400	ARC1:Zone1 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10401	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10402	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10403	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10404	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10405	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10406	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10407	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10408	ARC1:Zone1 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10409	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10410	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10411	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10412	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10413	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10414	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10415	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10416	ARC1:Zone1 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10417	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10418	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10419	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10420	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10421	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10422	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10423	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10424	ARC1:Zone1 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10425	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10426	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10427	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10428	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10429	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10430	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10431	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10432	ARC1:Zone1 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10433	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In

Holding Register	Parameter	Access	Length	Info
HR10434	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10435	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10436	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10437	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10438	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10439	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10440	ARC1:Zone1 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10441	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10442	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10443	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10444	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10445	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10446	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10447	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10448	ARC1:Zone2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10449	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10450	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10451	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10452	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10453	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10454	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10455	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10456	ARC1:Zone2 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10457	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10458	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10459	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10460	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10461	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10462	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10463	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10464	ARC1:Zone2 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10465	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10466	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10467	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10468	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10469	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10470	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10471	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10472	ARC1:Zone2 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10473	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10474	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10475	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10476	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10477	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10478	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10479	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10480	ARC1:Zone2 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10481	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10482	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10483	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10484	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10485	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10486	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10487	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10488	ARC1:Zone2 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10489	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10490	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10491	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10492	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10493	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10494	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10495	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10496	ARC1:Zone2 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10497	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10498	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10499	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10500	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10501	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10502	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10503	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10504	ARC1:Zone2 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10505	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10506	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10507	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10508	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10509	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10510	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10511	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10512	ARC1:Zone3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10513	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10514	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10515	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10516	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10517	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10518	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10519	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10520	ARC1:Zone3 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10521	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10522	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10523	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10524	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10525	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10526	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10527	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10528	ARC1:Zone3 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10529	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10530	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10531	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10532	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10533	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10534	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10535	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10536	ARC1:Zone3 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10537	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10538	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10539	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10540	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10541	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10542	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10543	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10544	ARC1:Zone3 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10545	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10546	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10547	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10548	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10549	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10550	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10551	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10552	ARC1:Zone3 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10553	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10554	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10555	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10556	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10557	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10558	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10559	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10560	ARC1:Zone3 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10561	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10562	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10563	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10564	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10565	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10566	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10567	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10568	ARC1:Zone3 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10569	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10570	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10571	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10572	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10573	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10574	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10575	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10576	ARC1:Zone4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10577	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10578	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10579	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10580	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10581	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10582	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10583	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10584	ARC1:Zone4 Ph.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10585	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10586	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10587	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10588	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10589	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10590	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10591	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10592	ARC1:Zone4 Res.Curr Enabled	R(W)	1	0=Disabled 1=Enabled
HR10593	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10594	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10595	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10596	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10597	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10598	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10599	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10600	ARC1:Zone4 Light 1 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10601	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10602	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10603	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10604	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10605	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10606	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10607	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10608	ARC1:Zone4 Light 2 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10609	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10610	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10611	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10612	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10613	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR10614	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10615	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10616	ARC1:Zone4 Light 3 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10617	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10618	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10619	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10620	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10621	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10622	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10623	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10624	ARC1:Zone4 Light 4 Enabled	R(W)	1	0=Disabled 1=Enabled
HR10625	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10626	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10627	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10628	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10629	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10630	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10631	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10632	ARC1:Zone4 DI Enabled	R(W)	1	0=Disabled 1=Light In 2=Current In
HR10633	CTS1:CTS1 mode	R(W)	1	0=Disabled 1=Activated
HR10634	CTS2:CTS2 mode	R(W)	1	0=Disabled 1=Activated
HR10635	VTS1:VTS [60] mode	R(W)	1	0=Disabled 1=Activated
HR10636	FLX1:Fault locator [21FL] mode	R(W)	1	0=Disabled 1=Activated
HR10637	THD1:THD mode	R(W)	1	0=Disabled 1=Activated
HR10638	THDV1:THDV mode	R(W)	1	0=Disabled 1=Activated
HR10639	THDV1:THDV condition	R	1	0=Normal 1=Start 2=Alarm 3=Blocked
HR10640	CBW1:CBW mode	R(W)	1	0=Disabled 1=Activated
HR10641	NJK1:U0> RECL [79] mode	R(W)	1	0=Disabled 1=Activated
HR10642	SOF1:SOTF mode	R(W)	1	0=Disabled 1=Activated
HR10643	DOS1:Energy dose counter mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10644	DOS2:Energy dose counter mode	R(W)	1	0=Disabled 1=Activated
HR10645	SYN1:SYN(1,2,3) [25] mode	R(W)	1	0=Disabled 1=Activated
HR10646	VJP1:Vector jump <DELTA><alpha> > [78] mode	R(W)	1	0=Disabled 1=Activated
HR10647	OBJS1:Phase-wise object mode	R(W)	1	0=Disabled 1=Activated
HR10648	OBJ1:Object1 mode	R(W)	1	0=Disabled 1=Activated
HR10649	OBJ2:Object2 mode	R(W)	1	0=Disabled 1=Activated
HR10650	OBJ3:Object3 mode	R(W)	1	0=Disabled 1=Activated
HR10651	OBJ4:Object4 mode	R(W)	1	0=Disabled 1=Activated
HR10652	OBJ5:Object5 mode	R(W)	1	0=Disabled 1=Activated
HR10653	OBJ6:Object6 mode	R(W)	1	0=Disabled 1=Activated
HR10654	OBJ7:Object7 mode	R(W)	1	0=Disabled 1=Activated
HR10655	OBJ8:Object8 mode	R(W)	1	0=Disabled 1=Activated
HR10656	OBJ9:Object9 mode	R(W)	1	0=Disabled 1=Activated
HR10657	OBJ10:Object10 mode	R(W)	1	0=Disabled 1=Activated
HR10658	CIN1:Indicator 1 mode	R(W)	1	0=Disabled 1=Activated
HR10659	CIN2:Indicator 2 mode	R(W)	1	0=Disabled 1=Activated
HR10660	CIN3:Indicator 3 mode	R(W)	1	0=Disabled 1=Activated
HR10661	CIN4:Indicator 4 mode	R(W)	1	0=Disabled 1=Activated
HR10662	CIN5:Indicator 5 mode	R(W)	1	0=Disabled 1=Activated
HR10663	CIN6:Indicator 6 mode	R(W)	1	0=Disabled 1=Activated
HR10664	CIN7:Indicator 7 mode	R(W)	1	0=Disabled 1=Activated
HR10665	CIN8:Indicator 8 mode	R(W)	1	0=Disabled 1=Activated
HR10666	CIN9:Indicator 9 mode	R(W)	1	0=Disabled 1=Activated
HR10667	CIN10:Indicator 10 mode	R(W)	1	0=Disabled 1=Activated
HR10668	CIN11:Indicator 11 mode	R(W)	1	0=Disabled 1=Activated
HR10669	CIN12:Indicator 12 mode	R(W)	1	0=Disabled 1=Activated
HR10670	CIN13:Indicator 13 mode	R(W)	1	0=Disabled 1=Activated
HR10671	CIN14:Indicator 14 mode	R(W)	1	0=Disabled 1=Activated
HR10672	CIN15:Indicator 15 mode	R(W)	1	0=Disabled 1=Activated
HR10673	CIN16:Indicator 16 mode	R(W)	1	0=Disabled 1=Activated

Holding Register	Parameter	Access	Length	Info
HR10674	CIN17:Indicator 17 mode	R(W)	1	0=Disabled 1=Activated
HR10675	CIN18:Indicator 18 mode	R(W)	1	0=Disabled 1=Activated
HR10676	CIN19:Indicator 19 mode	R(W)	1	0=Disabled 1=Activated
HR10677	CIN20:Indicator 20 mode	R(W)	1	0=Disabled 1=Activated
HR10678	AR1:Auto-recloser [79] mode	R(W)	1	0=Disabled 1=Activated
HR10679	VRG1:Vreg [AVR/90] mode	R(W)	1	0=Disabled 1=Activated
HR10680	EMON1:Monitor mode	R(W)	1	0=Disabled 1=Activated
HR10681	GMAG:Excitation control mode	R(W)	1	0=Disabled 1=Activated
HR10682	GSYN:DeadBus synchronizing	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10683				HR10682 (Lo-16bits)
HR10684	GSYN:BlackStart synchronizing	R	2	SIGNAL_IN [0...4294967295] (Hi-16bits)
HR10685				HR10684 (Lo-16bits)
HR10686	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10687				HR10686 (Lo-16bits)
HR10688	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10689				HR10688 (Lo-16bits)
HR10690	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10691				HR10690 (Lo-16bits)
HR10692	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10693				HR10692 (Lo-16bits)
HR10694	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10695				HR10694 (Lo-16bits)
HR10696	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10697				HR10696 (Lo-16bits)
HR10698	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10699				HR10698 (Lo-16bits)
HR10700	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10701				HR10700 (Lo-16bits)
HR10702	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10703				HR10702 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10704	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10705				HR10704 (Lo-16bits)
HR10706	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10707				HR10706 (Lo-16bits)
HR10708	VREC:Time	R	2	UINT32 [0...4294967295] (Hi-16bits)
HR10709				HR10708 (Lo-16bits)
HR10710	VREC:ms	R	1	UINT32 [0...999]
HR10711	VREC:ms	R	1	UINT32 [0...999]
HR10712	VREC:ms	R	1	UINT32 [0...999]
HR10713	VREC:ms	R	1	UINT32 [0...999]
HR10714	VREC:ms	R	1	UINT32 [0...999]
HR10715	VREC:ms	R	1	UINT32 [0...999]
HR10716	VREC:ms	R	1	UINT32 [0...999]
HR10717	VREC:ms	R	1	UINT32 [0...999]
HR10718	VREC:ms	R	1	UINT32 [0...999]
HR10719	VREC:ms	R	1	UINT32 [0...999]
HR10720	VREC:ms	R	1	UINT32 [0...999]
HR10721	VREC:ms	R	1	UINT32 [0...999]
HR10722	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10723				HR10722 (Lo-16bits)
HR10724	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10725				HR10724 (Lo-16bits)
HR10726	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10727				HR10726 (Lo-16bits)
HR10728	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10729				HR10728 (Lo-16bits)
HR10730	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10731				HR10730 (Lo-16bits)
HR10732	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10733				HR10732 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10734	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10735				HR10734 (Lo-16bits)
HR10736	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10737				HR10736 (Lo-16bits)
HR10738	DEV:Application package	R	2	STRING [...32] (Hi-16bits)
HR10739				HR10738 (Lo-16bits)
HR10740	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10741				HR10740 (Lo-16bits)
HR10742	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10743				HR10742 (Lo-16bits)
HR10744	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10745				HR10744 (Lo-16bits)
HR10746	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10747				HR10746 (Lo-16bits)
HR10748	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10749				HR10748 (Lo-16bits)
HR10750	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10751				HR10750 (Lo-16bits)
HR10752	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10753				HR10752 (Lo-16bits)
HR10754	EFX1:U0 (50Hz) pick-up setting	R(W)	2	FLOAT [1...75] (Hi-16bits)
HR10755				HR10754 (Lo-16bits)
HR10756	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10757				HR10756 (Lo-16bits)
HR10758	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10759				HR10758 (Lo-16bits)
HR10760	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10761				HR10760 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10762	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10763				HR10762 (Lo-16bits)
HR10764	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10765				HR10764 (Lo-16bits)
HR10766	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10767				HR10766 (Lo-16bits)
HR10768	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10769				HR10768 (Lo-16bits)
HR10770	EFX1:3I0 (5th harm.) pick-up setting [abs]	R(W)	2	FLOAT [0.05...1] (Hi-16bits)
HR10771				HR10770 (Lo-16bits)
HR10772	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10773				HR10772 (Lo-16bits)
HR10774	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10775				HR10774 (Lo-16bits)
HR10776	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10777				HR10776 (Lo-16bits)
HR10778	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10779				HR10778 (Lo-16bits)
HR10780	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10781				HR10780 (Lo-16bits)
HR10782	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10783				HR10782 (Lo-16bits)
HR10784	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10785				HR10784 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10786	EFX1:3I0 (5th harm.) angle range +/- setting	R(W)	2	FLOAT [10...45] (Hi-16bits)
HR10787				HR10786 (Lo-16bits)
HR10788	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10789				HR10788 (Lo-16bits)
HR10790	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10791				HR10790 (Lo-16bits)
HR10792	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10793				HR10792 (Lo-16bits)
HR10794	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10795				HR10794 (Lo-16bits)
HR10796	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10797				HR10796 (Lo-16bits)
HR10798	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10799				HR10798 (Lo-16bits)
HR10800	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10801				HR10800 (Lo-16bits)
HR10802	EFX1:3I0 (5th harm.) angle center setting	R(W)	2	FLOAT [-90...90] (Hi-16bits)
HR10803				HR10802 (Lo-16bits)
HR10804	CIN1:Indicator 1 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10805	CIN1:Indicator 1 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10806	CIN2:Indicator 2 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10807	CIN2:Indicator 2 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10808	CIN3:Indicator 3 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10809	CIN3:Indicator 3 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10810	CIN4:Indicator 4 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10811	CIN4:Indicator 4 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10812	CIN5:Indicator 5 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10813	CIN5:Indicator 5 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10814	CIN6:Indicator 6 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10815	CIN6:Indicator 6 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10816	CIN7:Indicator 7 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10817	CIN7:Indicator 7 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10818	CIN8:Indicator 8 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10819	CIN8:Indicator 8 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10820	CIN9:Indicator 9 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10821	CIN9:Indicator 9 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10822	CIN10:Indicator 10 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10823	CIN10:Indicator 10 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10824	CIN11:Indicator 11 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10825	CIN11:Indicator 11 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10826	CIN12:Indicator 12 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10827	CIN12:Indicator 12 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10828	CIN13:Indicator 13 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10829	CIN13:Indicator 13 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10830	CIN14:Indicator 14 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10831	CIN14:Indicator 14 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10832	CIN15:Indicator 15 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10833	CIN15:Indicator 15 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10834	CIN16:Indicator 16 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10835	CIN16:Indicator 16 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10836	CIN17:Indicator 17 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10837	CIN17:Indicator 17 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10838	CIN18:Indicator 18 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10839	CIN18:Indicator 18 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10840	CIN19:Indicator 19 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10841	CIN19:Indicator 19 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10842	CIN20:Indicator 20 status input	R(W)	1	0=Signals 1=GooseDBPOS

Holding Register	Parameter	Access	Length	Info
HR10843	CIN20:Indicator 20 status to display	R(W)	1	0=GOOSE IN Switch 1 1=GOOSE IN Switch 2 2=GOOSE IN Switch 3 3=GOOSE IN Switch 4 4=GOOSE IN Switch 5 5=GOOSE IN Switch 6 6=GOOSE IN Switch 7 7=GOOSE IN Switch 8 8=GOOSE IN Switch 9 9=GOOSE IN Switch 10 10=GOOSE IN Switch 11 11=GOOSE IN Switch 12 12=GOOSE IN Switch 13 13=GOOSE IN Switch 14 14=GOOSE IN Switch 15 15=GOOSE IN Switch 16 16=GOOSE IN Switch 17 17=GOOSE IN Switch 18 18=GOOSE IN Switch 19 19=GOOSE IN Switch 20 20=GOOSE IN Switch 21 21=GOOSE IN Switch 22 22=GOOSE IN Switch 23 23=GOOSE IN Switch 24 24=GOOSE IN Switch 25 25=GOOSE IN Switch 26 26=GOOSE IN Switch 27 27=GOOSE IN Switch 28 28=GOOSE IN Switch 29 29=GOOSE IN Switch 30 30=GOOSE IN Switch 31 31=GOOSE IN Switch 32 32=GOOSE IN Switch 33 33=GOOSE IN Switch 34 34=GOOSE IN Switch 35 35=GOOSE IN Switch 36 36=GOOSE IN Switch 37 37=GOOSE IN Switch 38 38=GOOSE IN Switch 39 39=GOOSE IN Switch 40 40=GOOSE IN Switch 41 41=GOOSE IN Switch 42 42=GOOSE IN Switch 43 43=GOOSE IN Switch 44 44=GOOSE IN Switch 45 45=GOOSE IN Switch 46 46=GOOSE IN Switch 47 47=GOOSE IN Switch 48 48=GOOSE IN Switch 49 49=GOOSE IN Switch 50 50=GOOSE IN Switch 51 51=GOOSE IN Switch 52 52=GOOSE IN Switch 53 53=GOOSE IN Switch 54 54=GOOSE IN Switch 55 55=GOOSE IN Switch 56 56=GOOSE IN Switch 57 57=GOOSE IN Switch 58 58=GOOSE IN Switch 59 59=GOOSE IN Switch 60 60=GOOSE IN Switch 61 61=GOOSE IN Switch 62 62=GOOSE IN Switch 63 63=GOOSE IN Switch 64
HR10844	PFC1:PFC> [55C] mode	R(W)	1	0=Disabled 1=Activated
HR10845	PFC1:PFC condition	R	1	0=Normal 1=Discharging 2=Blocked 3=Switching interval 4=Starting
HR10846	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10847				HR10846 (Lo-16bits)
HR10848	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10849				HR10848 (Lo-16bits)
HR10850	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10851				HR10850 (Lo-16bits)
HR10852	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10853				HR10852 (Lo-16bits)
HR10854	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10855				HR10854 (Lo-16bits)
HR10856	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10857				HR10856 (Lo-16bits)
HR10858	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10859				HR10858 (Lo-16bits)
HR10860	PFC1:Under voltage setting (more cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10861				HR10860 (Lo-16bits)
HR10862	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10863				HR10862 (Lo-16bits)
HR10864	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10865				HR10864 (Lo-16bits)
HR10866	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10867				HR10866 (Lo-16bits)
HR10868	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10869				HR10868 (Lo-16bits)
HR10870	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10871				HR10870 (Lo-16bits)
HR10872	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10873				HR10872 (Lo-16bits)
HR10874	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10875				HR10874 (Lo-16bits)
HR10876	PFC1:Over voltage setting (less cap.)	R(W)	2	FLOAT [70...130] (Hi-16bits)
HR10877				HR10876 (Lo-16bits)
HR10878	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10879				HR10878 (Lo-16bits)
HR10880	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10881				HR10880 (Lo-16bits)
HR10882	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10883				HR10882 (Lo-16bits)
HR10884	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10885				HR10884 (Lo-16bits)
HR10886	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10887				HR10886 (Lo-16bits)
HR10888	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10889				HR10888 (Lo-16bits)
HR10890	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10891				HR10890 (Lo-16bits)
HR10892	PFC1:Cos Pi set point (Cap+)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10893				HR10892 (Lo-16bits)
HR10894	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10895				HR10894 (Lo-16bits)
HR10896	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10897				HR10896 (Lo-16bits)
HR10898	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10899				HR10898 (Lo-16bits)
HR10900	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10901				HR10900 (Lo-16bits)
HR10902	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10903				HR10902 (Lo-16bits)
HR10904	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10905				HR10904 (Lo-16bits)
HR10906	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10907				HR10906 (Lo-16bits)
HR10908	PFC1:Cos Pi set point (Cap-)	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR10909				HR10908 (Lo-16bits)
HR10910	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10911				HR10910 (Lo-16bits)
HR10912	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10913				HR10912 (Lo-16bits)
HR10914	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10915				HR10914 (Lo-16bits)
HR10916	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10917				HR10916 (Lo-16bits)
HR10918	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10919				HR10918 (Lo-16bits)
HR10920	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10921				HR10920 (Lo-16bits)
HR10922	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10923				HR10922 (Lo-16bits)
HR10924	PFC1:More capacitance +kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10925				HR10924 (Lo-16bits)
HR10926	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10927				HR10926 (Lo-16bits)
HR10928	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10929				HR10928 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR10930	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10931				HR10930 (Lo-16bits)
HR10932	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10933				HR10932 (Lo-16bits)
HR10934	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10935				HR10934 (Lo-16bits)
HR10936	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10937				HR10936 (Lo-16bits)
HR10938	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10939				HR10938 (Lo-16bits)
HR10940	PFC1:Less capacitance -kVAr threshold	R(W)	2	FLOAT [-50000...50000] (Hi-16bits)
HR10941				HR10940 (Lo-16bits)
HR10942	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10943				HR10942 (Lo-16bits)
HR10944	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10945				HR10944 (Lo-16bits)
HR10946	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10947				HR10946 (Lo-16bits)
HR10948	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10949				HR10948 (Lo-16bits)
HR10950	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10951				HR10950 (Lo-16bits)
HR10952	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10953				HR10952 (Lo-16bits)
HR10954	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR10955				HR10954 (Lo-16bits)
HR10956	PFC1:Minimum current block limit	R(W)	2	FLOAT [0...1] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10957				HR10956 (Lo-16bits)
HR10958	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10959				HR10958 (Lo-16bits)
HR10960	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10961				HR10960 (Lo-16bits)
HR10962	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10963				HR10962 (Lo-16bits)
HR10964	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10965				HR10964 (Lo-16bits)
HR10966	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10967				HR10966 (Lo-16bits)
HR10968	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10969				HR10968 (Lo-16bits)
HR10970	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10971				HR10970 (Lo-16bits)
HR10972	PFC1:Minimum voltage block limit	R(W)	2	FLOAT [0...50] (Hi-16bits)
HR10973				HR10972 (Lo-16bits)
HR10974	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10975				HR10974 (Lo-16bits)
HR10976	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10977				HR10976 (Lo-16bits)
HR10978	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10979				HR10978 (Lo-16bits)
HR10980	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10981				HR10980 (Lo-16bits)
HR10982	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10983				HR10982 (Lo-16bits)
HR10984	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10985				HR10984 (Lo-16bits)
HR10986	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR10987				HR10986 (Lo-16bits)
HR10988	PFC1:Bank control starting delay	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10989				HR10988 (Lo-16bits)
HR10990	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10991	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10992	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10993	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10994	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10995	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10996	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10997	PFC1:Fast bank recontrol in use	R(W)	1	0=No 1=Yes
HR10998	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR10999				HR10998 (Lo-16bits)
HR11000	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11001				HR11000 (Lo-16bits)
HR11002	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11003				HR11002 (Lo-16bits)
HR11004	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11005				HR11004 (Lo-16bits)
HR11006	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11007				HR11006 (Lo-16bits)
HR11008	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11009				HR11008 (Lo-16bits)
HR11010	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11011				HR11010 (Lo-16bits)
HR11012	PFC1:Switch interval time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11013				HR11012 (Lo-16bits)
HR11014	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11015				HR11014 (Lo-16bits)
HR11016	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11017				HR11016 (Lo-16bits)
HR11018	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11019				HR11018 (Lo-16bits)
HR11020	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11021				HR11020 (Lo-16bits)
HR11022	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11023				HR11022 (Lo-16bits)
HR11024	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11025				HR11024 (Lo-16bits)
HR11026	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11027				HR11026 (Lo-16bits)
HR11028	PFC1:Discharge time	R(W)	2	FLOAT [1...1800] (Hi-16bits)
HR11029				HR11028 (Lo-16bits)
HR11030	VRG1:Circulating current (A)	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11031				HR11030 (Lo-16bits)
HR11032	VRG1:Circulating current deviation (%Un)	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11033				HR11032 (Lo-16bits)
HR11034	VRG1:Voltage deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11035				HR11034 (Lo-16bits)
HR11036	VRG1:Total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11037				HR11036 (Lo-16bits)
HR11038	VRG1:Tap voltage (kV)	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11039				HR11038 (Lo-16bits)
HR11040	VRG1:Voltage difference (kV)	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11041				HR11040 (Lo-16bits)
HR11042	VRG1:Absolute tap location (parallel engine)	R	2	FLOAT [0...50] (Hi-16bits)
HR11043				HR11042 (Lo-16bits)
HR11044	VRG1:AVR T1 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11045				HR11044 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11046	VRG1:AVR T2 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11047				HR11046 (Lo-16bits)
HR11048	VRG1:AVR T3 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11049				HR11048 (Lo-16bits)
HR11050	VRG1:AVR T4 tap location now (- 0 +)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11051				HR11050 (Lo-16bits)
HR11052	VRG1:AVR T1 voltage deviation	R	2	FLOAT [-100...140] (Hi-16bits)
HR11053				HR11052 (Lo-16bits)
HR11054	VRG1:AVR T2 voltage deviation	R	2	FLOAT [-100...140] (Hi-16bits)
HR11055				HR11054 (Lo-16bits)
HR11056	VRG1:AVR T3 voltage deviation	R	2	FLOAT [-100...140] (Hi-16bits)
HR11057				HR11056 (Lo-16bits)
HR11058	VRG1:AVR T4 voltage deviation	R	2	FLOAT [-100...140] (Hi-16bits)
HR11059				HR11058 (Lo-16bits)
HR11060	VRG1:AVR T1 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11061				HR11060 (Lo-16bits)
HR11062	VRG1:AVR T2 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11063				HR11062 (Lo-16bits)
HR11064	VRG1:AVR T3 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11065				HR11064 (Lo-16bits)
HR11066	VRG1:AVR T4 circulating current deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11067				HR11066 (Lo-16bits)
HR11068	VRG1:AVR T1 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11069				HR11068 (Lo-16bits)
HR11070	VRG1:AVR T2 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11071				HR11070 (Lo-16bits)
HR11072	VRG1:AVR T3 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11073				HR11072 (Lo-16bits)
HR11074	VRG1:AVR T4 total deviation	R	2	FLOAT [-50000...50000] (Hi-16bits)
HR11075				HR11074 (Lo-16bits)
HR11076	VRG1:AVR T1 Auto/Manual	R	1	0=Automatic 1=Manual
HR11077	VRG1:AVR T2 Auto/Manual	R	1	0=Automatic 1=Manual
HR11078	VRG1:AVR 3 Auto/Manual	R	1	0=Automatic 1=Manual
HR11079	VRG1:AVR T4 Auto/Manual	R	1	0=Automatic 1=Manual
HR11080	VRG1:AVR T1 Independent/Parallel	R	1	0=Single 1=Parallel
HR11081	VRG1:AVR T2 Independent/Parallel	R	1	0=Single 1=Parallel
HR11082	VRG1:AVR T3 Independent/Parallel	R	1	0=Single 1=Parallel
HR11083	VRG1:AVR T4 Independent/Parallel	R	1	0=Single 1=Parallel
HR11084	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11085				HR11084 (Lo-16bits)
HR11086	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11087				HR11086 (Lo-16bits)
HR11088	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11089				HR11088 (Lo-16bits)
HR11090	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11091				HR11090 (Lo-16bits)
HR11092	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11093				HR11092 (Lo-16bits)
HR11094	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11095				HR11094 (Lo-16bits)
HR11096	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR11097				HR11096 (Lo-16bits)
HR11098	VRG1:U>/< T2 fast recontrol delay (DT)	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11099				HR11098 (Lo-16bits)
HR11100	AOUT2:mA In (T card) 1 mA In 1	R	2	FLOAT [0...24] (Hi-16bits)
HR11101				HR11100 (Lo-16bits)
HR11102	AOUT2:mA In (T card) 1 mA In 2	R	2	FLOAT [0...24] (Hi-16bits)
HR11103				HR11102 (Lo-16bits)
HR11104	AOUT2:mA In (T card) 1 mA In 3	R	2	FLOAT [0...24] (Hi-16bits)
HR11105				HR11104 (Lo-16bits)
HR11106	AOUT2:mA In (T card) 1 mA In 4	R	2	FLOAT [0...24] (Hi-16bits)
HR11107				HR11106 (Lo-16bits)
HR11108	AOUT2:mA In (T card) 2 mA In 1	R	2	FLOAT [0...24] (Hi-16bits)
HR11109				HR11108 (Lo-16bits)
HR11110	AOUT2:mA In (T card) 2 mA In 2	R	2	FLOAT [0...24] (Hi-16bits)
HR11111				HR11110 (Lo-16bits)
HR11112	AOUT2:mA In (T card) 2 mA In 3	R	2	FLOAT [0...24] (Hi-16bits)
HR11113				HR11112 (Lo-16bits)
HR11114	AOUT2:mA In (T card) 2 mA In 4	R	2	FLOAT [0...24] (Hi-16bits)
HR11115				HR11114 (Lo-16bits)
HR11116	AOUT2:mA Out Channel 1 Input Magnitude now (T option 1)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR11117				HR11116 (Lo-16bits)
HR11118	AOUT2:mA Out Channel 1 Outputs now (T option 1)	R	2	FLOAT [0...24] (Hi-16bits)
HR11119				HR11118 (Lo-16bits)
HR11120	AOUT2:mA Out Channel 2 Input Magnitude now (T option 2)	R	2	FLOAT [-10000000...10000000] (Hi-16bits)
HR11121				HR11120 (Lo-16bits)
HR11122	AOUT2:mA Out Channel 2 Outputs now (T option 2)	R	2	FLOAT [0...24] (Hi-16bits)
HR11123				HR11122 (Lo-16bits)
HR11124	VRG1:AVR tap control status	R	1	0=OK 1=Tap control failure
HR11125	VRG1:AVR Parallel/Independent status to mimic	R	1	0=Parallel 1=Independent

Holding Register	Parameter	Access	Length	Info
HR11126	VRG1:AVR Follower&Master status to mimic	R	1	0=Follower 1=Master
HR11127	VRG1:AVR/block: Parallel communication failure	R	1	0=Communication OK 1=Communication failure
HR11128	VRG1:AVR/block: Tap differential failure	R	1	0=Tap OK 1=Tap difference failure
HR11129	VRG1:Hierarchy status to mimic	R	1	0=Master & follower 1=Circulating reactive current
HR11130	VRG1:Auto/Manual status to mimic	R	1	0=Auto 1=Manual
HR11131	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11132	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11133	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11134	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11135	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11136	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11137	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11138	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11139	VRG1:Relay in test mode	R	1	1=On 2=Blocked 3=Test 4=Test/Blocked 5=Off
HR11140	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11141	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11142	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11143	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11144	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11145	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11146	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11147	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11148	VRG1:Maximum tap raise position	R	1	0=OK 1=Max. position
HR11149	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position

Holding Register	Parameter	Access	Length	Info
HR11150	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11151	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11152	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11153	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11154	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11155	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11156	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11157	VRG1:Minimum tap lower position	R	1	0=OK 1=Min. position
HR11158	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11159				HR11158 (Lo-16bits)
HR11160	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11161				HR11160 (Lo-16bits)
HR11162	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11163				HR11162 (Lo-16bits)
HR11164	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11165				HR11164 (Lo-16bits)
HR11166	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11167				HR11166 (Lo-16bits)
HR11168	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11169				HR11168 (Lo-16bits)
HR11170	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11171				HR11170 (Lo-16bits)
HR11172	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11173				HR11172 (Lo-16bits)
HR11174	VRG1:Command with feedback (Tap position)	R	2	FLOAT [-30...30] (Hi-16bits)
HR11175				HR11174 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11176	VRG1:Health	R	1	0=Ok 1=Fail
HR11177	VRG1:Health	R	1	0=Ok 1=Fail
HR11178	VRG1:Health	R	1	0=Ok 1=Fail
HR11179	VRG1:Health	R	1	0=Ok 1=Fail
HR11180	VRG1:Health	R	1	0=Ok 1=Fail
HR11181	VRG1:Health	R	1	0=Ok 1=Fail
HR11182	VRG1:Health	R	1	0=Ok 1=Fail
HR11183	VRG1:Health	R	1	0=Ok 1=Fail
HR11184	VRG1:Health	R	1	0=Ok 1=Fail
HR11185	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11186	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11187	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11188	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11189	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11190	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11191	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11192	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11193	VRG1:Automatic local block	R	1	0=Normal 1=Blocked
HR11194	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11195	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11196	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11197	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11198	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11199	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11200	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11201	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11202	VRG1:Operating Mode (Auto)	R	1	0=Automatic 1=Manual
HR11203	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11204	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11205	VRG1:Parallel operation	R	1	0=Single 1=Parallel

Holding Register	Parameter	Access	Length	Info
HR11206	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11207	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11208	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11209	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11210	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11211	VRG1:Parallel operation	R	1	0=Single 1=Parallel
HR11212	VRG1:Status	R	1	0=Follower 1=Leader
HR11213	VRG1:Status	R	1	0=Follower 1=Leader
HR11214	VRG1:Status	R	1	0=Follower 1=Leader
HR11215	VRG1:Status	R	1	0=Follower 1=Leader
HR11216	VRG1:Status	R	1	0=Follower 1=Leader
HR11217	VRG1:Status	R	1	0=Follower 1=Leader
HR11218	VRG1:Status	R	1	0=Follower 1=Leader
HR11219	VRG1:Status	R	1	0=Follower 1=Leader
HR11220	VRG1:Status	R	1	0=Follower 1=Leader
HR11221	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11222	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11223	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11224	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11225	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11226	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11227	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11228	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11229	VRG1:Operation hierarchy	R	1	0=Leader & follower 1=Circulating reactive current
HR11230	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11231	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4

Holding Register	Parameter	Access	Length	Info
HR11232	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11233	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11234	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11235	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11236	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11237	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11238	VRG1:Parallel group	R	1	0=NA 1=1 2=2 3=3 4=4
HR11239	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11240	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11241	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11242	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11243	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11244	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11245	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11246	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11247	VRG1:Local parallel transformer ID	R	1	0=NA 1=1 2=2 3=3 4=4 5=5 6=6 7=7 8=8 9=9
HR11248	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11249				HR11248 (Lo-16bits)
HR11250	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11251				HR11250 (Lo-16bits)
HR11252	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11253				HR11252 (Lo-16bits)
HR11254	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11255				HR11254 (Lo-16bits)
HR11256	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11257				HR11256 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11258	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11259				HR11258 (Lo-16bits)
HR11260	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11261				HR11260 (Lo-16bits)
HR11262	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11263				HR11262 (Lo-16bits)
HR11264	VRG1:Tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11265				HR11264 (Lo-16bits)
HR11266	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11267				HR11266 (Lo-16bits)
HR11268	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11269				HR11268 (Lo-16bits)
HR11270	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11271				HR11270 (Lo-16bits)
HR11272	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11273				HR11272 (Lo-16bits)
HR11274	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11275				HR11274 (Lo-16bits)
HR11276	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11277				HR11276 (Lo-16bits)
HR11278	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11279				HR11278 (Lo-16bits)
HR11280	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11281				HR11280 (Lo-16bits)
HR11282	VRG1:Local reactance X1	R	2	FLOAT [0...500] (Hi-16bits)
HR11283				HR11282 (Lo-16bits)
HR11284	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11285				HR11284 (Lo-16bits)
HR11286	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11287				HR11286 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11288	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11289				HR11288 (Lo-16bits)
HR11290	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11291				HR11290 (Lo-16bits)
HR11292	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11293				HR11292 (Lo-16bits)
HR11294	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11295				HR11294 (Lo-16bits)
HR11296	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11297				HR11296 (Lo-16bits)
HR11298	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11299				HR11298 (Lo-16bits)
HR11300	VRG1:Local load current I1	R	2	FLOAT [0...1250] (Hi-16bits)
HR11301				HR11300 (Lo-16bits)
HR11302	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11303				HR11302 (Lo-16bits)
HR11304	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11305				HR11304 (Lo-16bits)
HR11306	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11307				HR11306 (Lo-16bits)
HR11308	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11309				HR11308 (Lo-16bits)
HR11310	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11311				HR11310 (Lo-16bits)
HR11312	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11313				HR11312 (Lo-16bits)
HR11314	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11315				HR11314 (Lo-16bits)
HR11316	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11317				HR11316 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11318	VRG1:Local load current I1 angle	R	2	FLOAT [-360...360] (Hi-16bits)
HR11319				HR11318 (Lo-16bits)
HR11320	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11321	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11322	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11323	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11324	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11325	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11326	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11327	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11328	VRG1:Leader command	R	1	0=Normal 1=Lower 2=Raise
HR11329	VRG1:ParallelTransfModeKind	R	1	1=No Mode predefined 2=Master 3=Follower 4=Power Factor 5=Negative Reactance 6=Circulating Current 7=Circulating Reactive Current (var balancing) 8=Circulating Reactive Current by equalizing power
HR11330	COL1:ICOL> [49OL] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR11331	COL2:ICOL>> [49OL] condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked
HR11332	CNU1:CNU> [50UB] condition	R	1	0=Normal 1=Alarm Start 2=Alarm On 3=Trip Start 4=Trip On 5=Blocked On
HR11333	ASC1:Curve5 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11334				HR11333 (Lo-16bits)
HR11335	ASC1:Curve5 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11336				HR11335 (Lo-16bits)
HR11337	ASC1:Curve6 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11338				HR11337 (Lo-16bits)
HR11339	ASC1:Curve6 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11340				HR11339 (Lo-16bits)
HR11341	ASC1:Curve7 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11342				HR11341 (Lo-16bits)
HR11343	ASC1:Curve7 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11344				HR11343 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11345	ASC1:Curve8 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11346				HR11345 (Lo-16bits)
HR11347	ASC1:Curve8 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11348				HR11347 (Lo-16bits)
HR11349	ASC1:Curve9 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11350				HR11349 (Lo-16bits)
HR11351	ASC1:Curve9 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11352				HR11351 (Lo-16bits)
HR11353	ASC1:Curve10 Input	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11354				HR11353 (Lo-16bits)
HR11355	ASC1:Curve10 Output	R	2	FLOAT [-1000000...1000000] (Hi-16bits)
HR11356				HR11355 (Lo-16bits)
HR11357	TOLM1:TM> inhibit status	R	1	0=Normal 1=Inhibit
HR11358	LDIF1:L1 Bias current	R	2	FLOAT [0...50] (Hi-16bits)
HR11359				HR11358 (Lo-16bits)
HR11360	LDIF1:L1 Diff current	R	2	FLOAT [0...4000] (Hi-16bits)
HR11361				HR11360 (Lo-16bits)
HR11362	LDIF1:L1 Char current	R	2	FLOAT [0...4000] (Hi-16bits)
HR11363				HR11362 (Lo-16bits)
HR11364	LDIF1:L2 Bias current	R	2	FLOAT [0...50] (Hi-16bits)
HR11365				HR11364 (Lo-16bits)
HR11366	LDIF1:L2 Diff current	R	2	FLOAT [0...4000] (Hi-16bits)
HR11367				HR11366 (Lo-16bits)
HR11368	LDIF1:L2 Char current	R	2	FLOAT [0...4000] (Hi-16bits)
HR11369				HR11368 (Lo-16bits)
HR11370	LDIF1:L3 Bias current	R	2	FLOAT [0...50] (Hi-16bits)
HR11371				HR11370 (Lo-16bits)
HR11372	LDIF1:L3 Diff current	R	2	FLOAT [0...4000] (Hi-16bits)
HR11373				HR11372 (Lo-16bits)
HR11374	LDIF1:L3 Char current	R	2	FLOAT [0...4000] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11375				HR11374 (Lo-16bits)
HR11376	LDIF1:IL1 magnitude Local	R	2	FLOAT [0...50] (Hi-16bits)
HR11377				HR11376 (Lo-16bits)
HR11378	LDIF1:IL2 magnitude Local	R	2	FLOAT [0...50] (Hi-16bits)
HR11379				HR11378 (Lo-16bits)
HR11380	LDIF1:IL3 magnitude Local	R	2	FLOAT [0...50] (Hi-16bits)
HR11381				HR11380 (Lo-16bits)
HR11382	LDIF1:IL1 angle Local	R	2	FLOAT [-360...360] (Hi-16bits)
HR11383				HR11382 (Lo-16bits)
HR11384	LDIF1:IL2 angle Local	R	2	FLOAT [-360...360] (Hi-16bits)
HR11385				HR11384 (Lo-16bits)
HR11386	LDIF1:IL3 angle Local	R	2	FLOAT [-360...360] (Hi-16bits)
HR11387				HR11386 (Lo-16bits)
HR11388	LDIF1:IL1 magnitude Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11389				HR11388 (Lo-16bits)
HR11390	LDIF1:IL2 magnitude Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11391				HR11390 (Lo-16bits)
HR11392	LDIF1:IL3 magnitude Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11393				HR11392 (Lo-16bits)
HR11394	LDIF1:IL1 angle Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11395				HR11394 (Lo-16bits)
HR11396	LDIF1:IL2 angle Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11397				HR11396 (Lo-16bits)
HR11398	LDIF1:IL3 angle Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11399				HR11398 (Lo-16bits)
HR11400	LDIF1:IL1 magnitude difference to Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11401				HR11400 (Lo-16bits)
HR11402	LDIF1:IL2 magnitude difference to Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11403				HR11402 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11404	LDIF1:IL3 magnitude difference to Remote1	R	2	FLOAT [0...50] (Hi-16bits)
HR11405				HR11404 (Lo-16bits)
HR11406	LDIF1:IL1 angle difference to Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11407				HR11406 (Lo-16bits)
HR11408	LDIF1:IL2 angle difference to Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11409				HR11408 (Lo-16bits)
HR11410	LDIF1:IL3 angle difference to Remote1	R	2	FLOAT [-360...360] (Hi-16bits)
HR11411				HR11410 (Lo-16bits)
HR11412	LDIF1:Remote signals angle corrected	R	2	FLOAT [-360...360] (Hi-16bits)
HR11413				HR11412 (Lo-16bits)
HR11414	LDIF1:Operating Mode	R	1	0=Following Master Settings
HR11415	LDIF1:Differential calculation mode	R	1	0=Subtract 1=Add
HR11416	LDIF1:Bias calculation mode	R	1	0=Average 1=Maximum
HR11417	LDIF1:ldb> Pickup	R	2	FLOAT [0.01...100] (Hi-16bits)
HR11418				HR11417 (Lo-16bits)
HR11419	LDIF1:ldb> Turnpoint 1	R	2	FLOAT [0.01...50] (Hi-16bits)
HR11420				HR11419 (Lo-16bits)
HR11421	LDIF1:ldb> Slope 1	R	2	FLOAT [0.01...250] (Hi-16bits)
HR11422				HR11421 (Lo-16bits)
HR11423	LDIF1:ldb> Turnpoint 2	R	2	FLOAT [0.01...50] (Hi-16bits)
HR11424				HR11423 (Lo-16bits)
HR11425	LDIF1:ldb> Slope 2	R	2	FLOAT [0.01...250] (Hi-16bits)
HR11426				HR11425 (Lo-16bits)
HR11427	LDIF1:Enable harmonic blocking	R	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR11428	LDIF1:2nd harm block Pickup	R	2	FLOAT [0.01...50] (Hi-16bits)
HR11429				HR11428 (Lo-16bits)
HR11430	LDIF1:5th harm block Pickup	R	2	FLOAT [0.01...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11431				HR11430 (Lo-16bits)
HR11432	LDIF1:Enable IdI> stage (nonbias)	R	1	0=Disabled 1=Enabled
HR11433	LDIF1:IdI> No bias Pickup	R	2	FLOAT [200...1500] (Hi-16bits)
HR11434				HR11433 (Lo-16bits)
HR11435	LDIF1:IdLb> BIAS TRIP	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11436				HR11435 (Lo-16bits)
HR11437	LDIF1:IdLb> BIAS BLOCKED	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11438				HR11437 (Lo-16bits)
HR11439	LDIF1:IdLi> NOBIAS TRIP	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11440				HR11439 (Lo-16bits)
HR11441	LDIF1:IdLi> NOBIAS BLOCKED	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11442				HR11441 (Lo-16bits)
HR11443	LDIF1:IdL> 2nd harm. block on	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11444				HR11443 (Lo-16bits)
HR11445	LDIF1:IdL> 5th harm. block on	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11446				HR11445 (Lo-16bits)
HR11447	LDIF1:Master node status	R	2	0b=Master node 1b=System Running 2b=Protection Operational 3b=System rebooting 4b=Configuring Id> suspended 5b=Biased diff blocked 6b=Instant diff blocked 7b=Phase A harm blk. 8b=Phase B harm blk. 9b=Phase C harm blk. 10b=LineDiff Enabled 11b=LineDiff Halted 12b=Bias Trip 13b=Instant Trip 14b=Remote1 halted 15b=Communication Ok 16b=Communication fail 17b=Object1 Open 18b=Object1 Closed 19b=Obj1 Open Cmd. 20b=Obj1 Close Cmd. 21b=Sub sampling f control (Hi-16bits)
HR11448				HR11447 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11449	LDIF1:Local node status	R	2	0b=Master node 1b=System Running 2b=Protection Operational 3b=System rebooting 4b=Configuring Id> suspended 5b=Biased diff blocked 6b=Instant diff blocked 7b=Phase A harm blk. 8b=Phase B harm blk. 9b=Phase C harm blk. 10b=LineDiff Enabled 11b=LineDiff Halted 12b=Bias Trip 13b=Instant Trip 14b=Remote1 halted 15b=Communication Ok 16b=Communication fail 17b=Object1 Open 18b=Object1 Closed 19b=Obj1 Open Cmd. 20b=Obj1 Close Cmd. 21b=f sample follows master (Hi-16bits)
HR11450				HR11449 (Lo-16bits)
HR11451	LDIF1:Remote node 1 status	R	2	0b=Master node 1b=System Running 2b=Protection Operational 3b=System rebooting 4b=Configuring Id> suspended 5b=Biased diff blocked 6b=Instant diff blocked 7b=Phase A harm blk. 8b=Phase B harm blk. 9b=Phase C harm blk. 10b=LineDiff Enabled 11b=LineDiff Halted 12b=Bias Trip 13b=Instant Trip 14b=Remote1 halted 15b=Communication Ok 16b=Communication fail 17b=Object1 Open 18b=Object1 Closed 19b=Obj1 Open Cmd. 20b=Obj1 Close Cmd. 21b=f sample follows master (Hi-16bits)
HR11452				HR11451 (Lo-16bits)
HR11453	SYN1:SYN2 Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR11454				HR11453 (Lo-16bits)
HR11455	SYN1:SYN2 Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR11456				HR11455 (Lo-16bits)
HR11457	SYN1:SYN2 Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR11458				HR11457 (Lo-16bits)
HR11459	SYN1:SYN3 Estimated BRK Closing time	R	2	FLOAT [0...360] (Hi-16bits)
HR11460				HR11459 (Lo-16bits)
HR11461	SYN1:SYN3 Networks rotating time	R	2	FLOAT [0...360] (Hi-16bits)
HR11462				HR11461 (Lo-16bits)
HR11463	SYN1:SYN3 Networks placement atm	R	2	FLOAT [-360...360] (Hi-16bits)
HR11464				HR11463 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11465	DMA1:Demand condition	R	1	0=Normal 1=General alarm 2=Blocked
HR11466	CTRL:Local/Remote status	R	1	0=Remote 1=Local
HR11467	TOLT1:TT> inhibit status	R	1	0=Normal 1=Inhibit
HR11468	TOLF1:TF> inhibit status	R	1	0=Normal 1=Inhibit
HR11469	OBJ1:Object1 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11470	OBJ2:Object2 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11471	OBJ3:Object3 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11472	OBJ4:Object4 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11473	OBJ5:Object5 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11474	OBJ6:Object6 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11475	OBJ7:Object7 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11476	OBJ8:Object8 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11477	OBJ9:Object9 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11478	OBJ10:Object10 Withdraw Status	R	1	0=WDIntermediate 1=WDCartOut 2=WDCart In 3=WDBad 4=Not in use
HR11479	GSYN:Synchronizing by	R(W)	1	0=System voltages to U3/U4 Input
HR11480	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11481				HR11480 (Lo-16bits)
HR11482	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11483				HR11482 (Lo-16bits)
HR11484	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11485				HR11484 (Lo-16bits)
HR11486	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11487				HR11486 (Lo-16bits)
HR11488	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11489				HR11488 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11490	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11491				HR11490 (Lo-16bits)
HR11492	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11493				HR11492 (Lo-16bits)
HR11494	PFC1:Target power factor	R(W)	2	FLOAT [-1...1] (Hi-16bits)
HR11495				HR11494 (Lo-16bits)
HR11496	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11497				HR11496 (Lo-16bits)
HR11498	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11499				HR11498 (Lo-16bits)
HR11500	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11501				HR11500 (Lo-16bits)
HR11502	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11503				HR11502 (Lo-16bits)
HR11504	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11505				HR11504 (Lo-16bits)
HR11506	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11507				HR11506 (Lo-16bits)
HR11508	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11509				HR11508 (Lo-16bits)
HR11510	PFC1:Bank size for deltaQ calculation	R(W)	2	FLOAT [1...50000] (Hi-16bits)
HR11511				HR11510 (Lo-16bits)
HR11512	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11513				HR11512 (Lo-16bits)
HR11514	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11515				HR11514 (Lo-16bits)
HR11516	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11517				HR11516 (Lo-16bits)
HR11518	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11519				HR11518 (Lo-16bits)
HR11520	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11521				HR11520 (Lo-16bits)
HR11522	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11523				HR11522 (Lo-16bits)
HR11524	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11525				HR11524 (Lo-16bits)
HR11526	PFC1:More capacitance +kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11527				HR11526 (Lo-16bits)
HR11528	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11529				HR11528 (Lo-16bits)
HR11530	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11531				HR11530 (Lo-16bits)
HR11532	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11533				HR11532 (Lo-16bits)
HR11534	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11535				HR11534 (Lo-16bits)
HR11536	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11537				HR11536 (Lo-16bits)
HR11538	PFC1:Less capacitance -kVAr percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11539				HR11538 (Lo-16bits)
HR11540	PFC1:Less capacitance -kVAR percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11541				HR11540 (Lo-16bits)
HR11542	PFC1:Less capacitance -kVAR percentage	R(W)	2	FLOAT [1...100] (Hi-16bits)
HR11543				HR11542 (Lo-16bits)
HR11544	VRG1:AVR T1 tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11545				HR11544 (Lo-16bits)
HR11546	VRG1:AVR T2 tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11547				HR11546 (Lo-16bits)
HR11548	VRG1:AVR T3 tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11549				HR11548 (Lo-16bits)
HR11550	VRG1:AVR T4 tap voltage	R	2	FLOAT [0...250] (Hi-16bits)
HR11551				HR11550 (Lo-16bits)
HR11552	VRG1:AVR running status to mimic	R	1	0=AVR not running 1=AVR running
HR11553	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11554				HR11553 (Lo-16bits)
HR11555	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11556				HR11555 (Lo-16bits)
HR11557	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11558				HR11557 (Lo-16bits)
HR11559	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11560				HR11559 (Lo-16bits)
HR11561	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11562				HR11561 (Lo-16bits)
HR11563	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11564				HR11563 (Lo-16bits)
HR11565	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11566				HR11565 (Lo-16bits)
HR11567	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR11568				HR11567 (Lo-16bits)
HR11569	VRG1:Tap voltage level	R	2	FLOAT [0...250] (Hi-16bits)
HR11570				HR11569 (Lo-16bits)
HR11571	HSOC:HSO card output status	R	2	0b=HSO1 1b=HSO2 2b=HSO3 3b=HSO4 4b=HSO5 5b=HSO6 6b=HSO7 7b=HSO8 8b=HSO9 (Hi-16bits)
HR11572				HR11571 (Lo-16bits)
HR11573	DIAG:Diagnostic alarm status	R	2	0b=Temp. out of range 1b=Voltage out of range 2b=HW configuration invalid 3b=Relay output failure 4b=Option card A alarm 5b=Option card B alarm 6b=Option card C alarm 7b=Option card D alarm 8b=Option card E alarm 9b=Option card F alarm 10b=Safe setting mode active 11b=System overload 12b=GOOSE simulation enabled 13b=Device/LN mode not On 14b=UAC password issue 15b=Audit logging failed 16b=Option card K alarm 17b=Option card L alarm 18b=Option card M alarm 19b=Option card N alarm (Hi-16bits)
HR11574				HR11573 (Lo-16bits)
HR11575	IRF:Internal Relay Fault status	R	2	0b=Storage error 1b=Logic file missing 2b=Aquisition unit error 3b=Measurement config error 4b=Relay hardware setup error 5b=System overload error 6b=Not configured yet 7b=Firmware upgrade done 8b=Slot supervision error 9b=Software inconsistency error 10b=Event overload (Hi-16bits)
HR11576				HR11575 (Lo-16bits)
HR11577	LVRT1:LVRT [27T] mode	R(W)	1	0=Disabled 1=Activated
HR11578	LVRT1:LVRT condition	R	1	0=Normal 1=Start 2=Trip 3=Blocked 4=Undervolt Blk On
HR11579	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11580				HR11579 (Lo-16bits)
HR11581	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11582				HR11581 (Lo-16bits)
HR11583	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11584				HR11583 (Lo-16bits)
HR11585	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11586				HR11585 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11587	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11588				HR11587 (Lo-16bits)
HR11589	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11590				HR11589 (Lo-16bits)
HR11591	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11592				HR11591 (Lo-16bits)
HR11593	LVRT1:Fault start<	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11594				HR11593 (Lo-16bits)
HR11595	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11596				HR11595 (Lo-16bits)
HR11597	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11598				HR11597 (Lo-16bits)
HR11599	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11600				HR11599 (Lo-16bits)
HR11601	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11602				HR11601 (Lo-16bits)
HR11603	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11604				HR11603 (Lo-16bits)
HR11605	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11606				HR11605 (Lo-16bits)
HR11607	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11608				HR11607 (Lo-16bits)
HR11609	LVRT1:Fault release>	R(W)	2	FLOAT [50...120] (Hi-16bits)
HR11610				HR11609 (Lo-16bits)
HR11611	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11612				HR11611 (Lo-16bits)
HR11613	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11614				HR11613 (Lo-16bits)
HR11615	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11616				HR11615 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11617	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11618				HR11617 (Lo-16bits)
HR11619	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11620				HR11619 (Lo-16bits)
HR11621	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11622				HR11621 (Lo-16bits)
HR11623	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11624				HR11623 (Lo-16bits)
HR11625	LVRT1:Low voltage block limit	R(W)	2	FLOAT [0...80] (Hi-16bits)
HR11626				HR11625 (Lo-16bits)
HR11627	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11628	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11629	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11630	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11631	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11632	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11633	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11634	LVRT1:Number of knee points	R(W)	1	0=2 1=3 2=4 3=5 4=6 5=7 6=8 7=9 8=10
HR11635	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11636				HR11635 (Lo-16bits)
HR11637	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11638				HR11637 (Lo-16bits)
HR11639	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11640				HR11639 (Lo-16bits)
HR11641	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11642				HR11641 (Lo-16bits)
HR11643	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11644				HR11643 (Lo-16bits)
HR11645	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11646				HR11645 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11647	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11648				HR11647 (Lo-16bits)
HR11649	LVRT1:Amplitude plot[1]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11650				HR11649 (Lo-16bits)
HR11651	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11652				HR11651 (Lo-16bits)
HR11653	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11654				HR11653 (Lo-16bits)
HR11655	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11656				HR11655 (Lo-16bits)
HR11657	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11658				HR11657 (Lo-16bits)
HR11659	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11660				HR11659 (Lo-16bits)
HR11661	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11662				HR11661 (Lo-16bits)
HR11663	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11664				HR11663 (Lo-16bits)
HR11665	LVRT1:Time plot[1]	R	2	FLOAT [0...1800] (Hi-16bits)
HR11666				HR11665 (Lo-16bits)
HR11667	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11668				HR11667 (Lo-16bits)
HR11669	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11670				HR11669 (Lo-16bits)
HR11671	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11672				HR11671 (Lo-16bits)
HR11673	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11674				HR11673 (Lo-16bits)
HR11675	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11676				HR11675 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11677	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11678				HR11677 (Lo-16bits)
HR11679	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11680				HR11679 (Lo-16bits)
HR11681	LVRT1:Amplitude plot[2]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11682				HR11681 (Lo-16bits)
HR11683	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11684				HR11683 (Lo-16bits)
HR11685	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11686				HR11685 (Lo-16bits)
HR11687	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11688				HR11687 (Lo-16bits)
HR11689	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11690				HR11689 (Lo-16bits)
HR11691	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11692				HR11691 (Lo-16bits)
HR11693	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11694				HR11693 (Lo-16bits)
HR11695	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11696				HR11695 (Lo-16bits)
HR11697	LVRT1:Time plot[2]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11698				HR11697 (Lo-16bits)
HR11699	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11700				HR11699 (Lo-16bits)
HR11701	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11702				HR11701 (Lo-16bits)
HR11703	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11704				HR11703 (Lo-16bits)
HR11705	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11706				HR11705 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11707	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11708				HR11707 (Lo-16bits)
HR11709	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11710				HR11709 (Lo-16bits)
HR11711	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11712				HR11711 (Lo-16bits)
HR11713	LVRT1:Amplitude plot[3]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11714				HR11713 (Lo-16bits)
HR11715	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11716				HR11715 (Lo-16bits)
HR11717	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11718				HR11717 (Lo-16bits)
HR11719	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11720				HR11719 (Lo-16bits)
HR11721	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11722				HR11721 (Lo-16bits)
HR11723	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11724				HR11723 (Lo-16bits)
HR11725	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11726				HR11725 (Lo-16bits)
HR11727	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11728				HR11727 (Lo-16bits)
HR11729	LVRT1:Time plot[3]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11730				HR11729 (Lo-16bits)
HR11731	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11732				HR11731 (Lo-16bits)
HR11733	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11734				HR11733 (Lo-16bits)
HR11735	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11736				HR11735 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11737	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11738				HR11737 (Lo-16bits)
HR11739	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11740				HR11739 (Lo-16bits)
HR11741	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11742				HR11741 (Lo-16bits)
HR11743	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11744				HR11743 (Lo-16bits)
HR11745	LVRT1:Amplitude plot[4]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11746				HR11745 (Lo-16bits)
HR11747	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11748				HR11747 (Lo-16bits)
HR11749	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11750				HR11749 (Lo-16bits)
HR11751	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11752				HR11751 (Lo-16bits)
HR11753	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11754				HR11753 (Lo-16bits)
HR11755	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11756				HR11755 (Lo-16bits)
HR11757	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11758				HR11757 (Lo-16bits)
HR11759	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11760				HR11759 (Lo-16bits)
HR11761	LVRT1:Time plot[4]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11762				HR11761 (Lo-16bits)
HR11763	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11764				HR11763 (Lo-16bits)
HR11765	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11766				HR11765 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11767	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11768				HR11767 (Lo-16bits)
HR11769	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11770				HR11769 (Lo-16bits)
HR11771	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11772				HR11771 (Lo-16bits)
HR11773	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11774				HR11773 (Lo-16bits)
HR11775	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11776				HR11775 (Lo-16bits)
HR11777	LVRT1:Amplitude plot[5]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11778				HR11777 (Lo-16bits)
HR11779	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11780				HR11779 (Lo-16bits)
HR11781	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11782				HR11781 (Lo-16bits)
HR11783	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11784				HR11783 (Lo-16bits)
HR11785	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11786				HR11785 (Lo-16bits)
HR11787	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11788				HR11787 (Lo-16bits)
HR11789	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11790				HR11789 (Lo-16bits)
HR11791	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11792				HR11791 (Lo-16bits)
HR11793	LVRT1:Time plot[5]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11794				HR11793 (Lo-16bits)
HR11795	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11796				HR11795 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11797	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11798				HR11797 (Lo-16bits)
HR11799	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11800				HR11799 (Lo-16bits)
HR11801	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11802				HR11801 (Lo-16bits)
HR11803	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11804				HR11803 (Lo-16bits)
HR11805	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11806				HR11805 (Lo-16bits)
HR11807	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11808				HR11807 (Lo-16bits)
HR11809	LVRT1:Amplitude plot[6]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11810				HR11809 (Lo-16bits)
HR11811	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11812				HR11811 (Lo-16bits)
HR11813	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11814				HR11813 (Lo-16bits)
HR11815	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11816				HR11815 (Lo-16bits)
HR11817	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11818				HR11817 (Lo-16bits)
HR11819	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11820				HR11819 (Lo-16bits)
HR11821	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11822				HR11821 (Lo-16bits)
HR11823	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11824				HR11823 (Lo-16bits)
HR11825	LVRT1:Time plot[6]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11826				HR11825 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11827	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11828				HR11827 (Lo-16bits)
HR11829	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11830				HR11829 (Lo-16bits)
HR11831	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11832				HR11831 (Lo-16bits)
HR11833	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11834				HR11833 (Lo-16bits)
HR11835	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11836				HR11835 (Lo-16bits)
HR11837	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11838				HR11837 (Lo-16bits)
HR11839	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11840				HR11839 (Lo-16bits)
HR11841	LVRT1:Amplitude plot[7]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11842				HR11841 (Lo-16bits)
HR11843	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11844				HR11843 (Lo-16bits)
HR11845	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11846				HR11845 (Lo-16bits)
HR11847	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11848				HR11847 (Lo-16bits)
HR11849	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11850				HR11849 (Lo-16bits)
HR11851	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11852				HR11851 (Lo-16bits)
HR11853	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11854				HR11853 (Lo-16bits)
HR11855	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11856				HR11855 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11857	LVRT1:Time plot[7]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11858				HR11857 (Lo-16bits)
HR11859	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11860				HR11859 (Lo-16bits)
HR11861	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11862				HR11861 (Lo-16bits)
HR11863	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11864				HR11863 (Lo-16bits)
HR11865	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11866				HR11865 (Lo-16bits)
HR11867	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11868				HR11867 (Lo-16bits)
HR11869	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11870				HR11869 (Lo-16bits)
HR11871	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11872				HR11871 (Lo-16bits)
HR11873	LVRT1:Amplitude plot[8]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11874				HR11873 (Lo-16bits)
HR11875	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11876				HR11875 (Lo-16bits)
HR11877	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11878				HR11877 (Lo-16bits)
HR11879	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11880				HR11879 (Lo-16bits)
HR11881	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11882				HR11881 (Lo-16bits)
HR11883	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11884				HR11883 (Lo-16bits)
HR11885	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11886				HR11885 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11887	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11888				HR11887 (Lo-16bits)
HR11889	LVRT1:Time plot[8]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11890				HR11889 (Lo-16bits)
HR11891	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11892				HR11891 (Lo-16bits)
HR11893	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11894				HR11893 (Lo-16bits)
HR11895	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11896				HR11895 (Lo-16bits)
HR11897	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11898				HR11897 (Lo-16bits)
HR11899	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11900				HR11899 (Lo-16bits)
HR11901	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11902				HR11901 (Lo-16bits)
HR11903	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11904				HR11903 (Lo-16bits)
HR11905	LVRT1:Amplitude plot[9]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11906				HR11905 (Lo-16bits)
HR11907	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11908				HR11907 (Lo-16bits)
HR11909	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11910				HR11909 (Lo-16bits)
HR11911	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11912				HR11911 (Lo-16bits)
HR11913	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11914				HR11913 (Lo-16bits)
HR11915	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11916				HR11915 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11917	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11918				HR11917 (Lo-16bits)
HR11919	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11920				HR11919 (Lo-16bits)
HR11921	LVRT1:Time plot[9]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11922				HR11921 (Lo-16bits)
HR11923	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11924				HR11923 (Lo-16bits)
HR11925	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11926				HR11925 (Lo-16bits)
HR11927	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11928				HR11927 (Lo-16bits)
HR11929	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11930				HR11929 (Lo-16bits)
HR11931	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11932				HR11931 (Lo-16bits)
HR11933	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11934				HR11933 (Lo-16bits)
HR11935	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11936				HR11935 (Lo-16bits)
HR11937	LVRT1:Amplitude plot[10]	R(W)	2	FLOAT [0...120] (Hi-16bits)
HR11938				HR11937 (Lo-16bits)
HR11939	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11940				HR11939 (Lo-16bits)
HR11941	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11942				HR11941 (Lo-16bits)
HR11943	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11944				HR11943 (Lo-16bits)
HR11945	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11946				HR11945 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR11947	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11948				HR11947 (Lo-16bits)
HR11949	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11950				HR11949 (Lo-16bits)
HR11951	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11952				HR11951 (Lo-16bits)
HR11953	LVRT1:Time plot[10]	R(W)	2	FLOAT [0.100...1800] (Hi-16bits)
HR11954				HR11953 (Lo-16bits)
HR11955	PVPS:PVPS Line comm ok	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11956				HR11955 (Lo-16bits)
HR11957	PVPS:PVPS Line comm failure	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11958				HR11957 (Lo-16bits)
HR11959	PVPS:PVPS Any line protection failure	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11960				HR11959 (Lo-16bits)
HR11961	PVPS:PVPS Units synchronization Ok	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11962				HR11961 (Lo-16bits)
HR11963	PVPS:PVPS Synchronization lost	R	2	SIGNAL_OUT [0...4294967295] (Hi-16bits)
HR11964				HR11963 (Lo-16bits)
HR11965	LDIF1:Enable configuration	R(W)	1	0=Disabled 1=Enabled (Halt protection)
HR11966	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11967	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11968	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11969	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11970	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11971	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11972	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11973	LDIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR11974	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum

Holding Register	Parameter	Access	Length	Info
HR11975	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11976	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11977	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11978	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11979	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11980	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11981	LDIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR11982	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11983				HR11982 (Lo-16bits)
HR11984	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11985				HR11984 (Lo-16bits)
HR11986	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11987				HR11986 (Lo-16bits)
HR11988	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11989				HR11988 (Lo-16bits)
HR11990	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11991				HR11990 (Lo-16bits)
HR11992	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11993				HR11992 (Lo-16bits)
HR11994	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11995				HR11994 (Lo-16bits)
HR11996	LDIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR11997				HR11996 (Lo-16bits)
HR11998	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR11999				HR11998 (Lo-16bits)
HR12000	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12001				HR12000 (Lo-16bits)
HR12002	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12003				HR12002 (Lo-16bits)
HR12004	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12005				HR12004 (Lo-16bits)
HR12006	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12007				HR12006 (Lo-16bits)
HR12008	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12009				HR12008 (Lo-16bits)
HR12010	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12011				HR12010 (Lo-16bits)
HR12012	LDIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12013				HR12012 (Lo-16bits)
HR12014	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12015				HR12014 (Lo-16bits)
HR12016	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12017				HR12016 (Lo-16bits)
HR12018	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12019				HR12018 (Lo-16bits)
HR12020	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12021				HR12020 (Lo-16bits)
HR12022	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12023				HR12022 (Lo-16bits)
HR12024	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12025				HR12024 (Lo-16bits)
HR12026	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12027				HR12026 (Lo-16bits)
HR12028	LDIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12029				HR12028 (Lo-16bits)
HR12030	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12031				HR12030 (Lo-16bits)
HR12032	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12033				HR12032 (Lo-16bits)
HR12034	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12035				HR12034 (Lo-16bits)
HR12036	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12037				HR12036 (Lo-16bits)
HR12038	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12039				HR12038 (Lo-16bits)
HR12040	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12041				HR12040 (Lo-16bits)
HR12042	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12043				HR12042 (Lo-16bits)
HR12044	LDIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12045				HR12044 (Lo-16bits)
HR12046	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12047				HR12046 (Lo-16bits)
HR12048	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12049				HR12048 (Lo-16bits)
HR12050	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12051				HR12050 (Lo-16bits)
HR12052	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12053				HR12052 (Lo-16bits)
HR12054	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12055				HR12054 (Lo-16bits)
HR12056	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12057				HR12056 (Lo-16bits)
HR12058	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12059				HR12058 (Lo-16bits)
HR12060	LDIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12061				HR12060 (Lo-16bits)
HR12062	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block

Holding Register	Parameter	Access	Length	Info
HR12063	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12064	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12065	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12066	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12067	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12068	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12069	LDIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12070	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12071				HR12070 (Lo-16bits)
HR12072	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12073				HR12072 (Lo-16bits)
HR12074	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12075				HR12074 (Lo-16bits)
HR12076	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12077				HR12076 (Lo-16bits)
HR12078	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12079				HR12078 (Lo-16bits)
HR12080	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12081				HR12080 (Lo-16bits)
HR12082	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12083				HR12082 (Lo-16bits)
HR12084	LDIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12085				HR12084 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12086	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12087				HR12086 (Lo-16bits)
HR12088	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12089				HR12088 (Lo-16bits)
HR12090	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12091				HR12090 (Lo-16bits)
HR12092	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12093				HR12092 (Lo-16bits)
HR12094	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12095				HR12094 (Lo-16bits)
HR12096	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12097				HR12096 (Lo-16bits)
HR12098	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12099				HR12098 (Lo-16bits)
HR12100	LDIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12101				HR12100 (Lo-16bits)
HR12102	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12103	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12104	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12105	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12106	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12107	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12108	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12109	LDIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12110	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12111				HR12110 (Lo-16bits)
HR12112	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12113				HR12112 (Lo-16bits)
HR12114	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12115				HR12114 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12116	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12117				HR12116 (Lo-16bits)
HR12118	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12119				HR12118 (Lo-16bits)
HR12120	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12121				HR12120 (Lo-16bits)
HR12122	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12123				HR12122 (Lo-16bits)
HR12124	LDIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12125				HR12124 (Lo-16bits)
HR12126	DMA1:Combined status	R	2	0b=IL1 alarm 1b=IL2 alarm 2b=IL3 alarm 3b=S 3phase alarm 4b=P 3phase alarm 5b=Q 3phase alarm 6b=PF 3phase alarm 7b=IL1 harmonic alarm 8b=IL2 harmonic alarm 9b=IL3 harmonic alarm 10b=Function blocked (Hi-16bits)
HR12127				HR12126 (Lo-16bits)
HR12128	CBF1:CBFP current measurement side	R(W)	1	0=CT1 1=CT2
HR12129	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12130	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12131	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12132	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO

Holding Register	Parameter	Access	Length	Info
HR12133	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12134	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12135	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12136	CBF1:Operating Mode selection	R(W)	1	0=Curr only 1=DO only 2=Signals only 3=Curr and DO 4=Curr or DO 5=Curr and signs 6=Curr or signs 7=Signs and DO 8=Signs or DO 9=Curr and signs and DO 10=Curr or signs or DO
HR12137	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12138				HR12137 (Lo-16bits)
HR12139	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12140				HR12139 (Lo-16bits)
HR12141	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12142				HR12141 (Lo-16bits)
HR12143	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12144				HR12143 (Lo-16bits)
HR12145	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12146				HR12145 (Lo-16bits)
HR12147	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12148				HR12147 (Lo-16bits)
HR12149	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12150				HR12149 (Lo-16bits)
HR12151	CBF1:Iset	R(W)	2	FLOAT [0.01...40] (Hi-16bits)
HR12152				HR12151 (Lo-16bits)
HR12153	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12154	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc

Holding Register	Parameter	Access	Length	Info
HR12155	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12156	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12157	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12158	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12159	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12160	CBF1:I0 input selection	R(W)	1	0=Not in use 1=I01 2=I02 3=I0Calc
HR12161	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12162				HR12161 (Lo-16bits)
HR12163	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12164				HR12163 (Lo-16bits)
HR12165	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12166				HR12165 (Lo-16bits)
HR12167	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12168				HR12167 (Lo-16bits)
HR12169	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12170				HR12169 (Lo-16bits)
HR12171	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12172				HR12171 (Lo-16bits)
HR12173	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12174				HR12173 (Lo-16bits)
HR12175	CBF1:I0set	R(W)	2	FLOAT [0.005...40] (Hi-16bits)
HR12176				HR12175 (Lo-16bits)
HR12177	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12178	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12179	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12180	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12181	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12182	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12183	CBF1:Enable retrip	R(W)	1	0=No 1=Yes
HR12184	CBF1:Enable retrip	R(W)	1	0=No 1=Yes

Holding Register	Parameter	Access	Length	Info
HR12185	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12186				HR12185 (Lo-16bits)
HR12187	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12188				HR12187 (Lo-16bits)
HR12189	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12190				HR12189 (Lo-16bits)
HR12191	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12192				HR12191 (Lo-16bits)
HR12193	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12194				HR12193 (Lo-16bits)
HR12195	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12196				HR12195 (Lo-16bits)
HR12197	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12198				HR12197 (Lo-16bits)
HR12199	CBF1:Retrip time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12200				HR12199 (Lo-16bits)
HR12201	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12202				HR12201 (Lo-16bits)
HR12203	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12204				HR12203 (Lo-16bits)
HR12205	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12206				HR12205 (Lo-16bits)
HR12207	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12208				HR12207 (Lo-16bits)
HR12209	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12210				HR12209 (Lo-16bits)
HR12211	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12212				HR12211 (Lo-16bits)
HR12213	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12214				HR12213 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12215	CBF1:CBFP time delay	R(W)	2	FLOAT [0.05...1800] (Hi-16bits)
HR12216				HR12215 (Lo-16bits)
HR12217	MST1:MST mode	R(W)	1	0=Disabled 1=Activated
HR12218	MST1:MST measurement side	R(W)	1	0=CT1 1=CT2
HR12219	MST1:Per unitize relay currents to Motor In	R(W)	1	0=No use CT nominal as pu. 1=Yes scale all to Imot
HR12220	MST1:Motor nominal current	R(W)	2	FLOAT [0.1...10000] (Hi-16bits)
HR12221				HR12220 (Lo-16bits)
HR12222	MST1:Nominal Starting current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12223				HR12222 (Lo-16bits)
HR12224	MST1:Start detect current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12225				HR12224 (Lo-16bits)
HR12226	MST1:Min locked rotor current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12227				HR12226 (Lo-16bits)
HR12228	MST1:Max locked rotor current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12229				HR12228 (Lo-16bits)
HR12230	MST1:Maximum overload current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12231				HR12230 (Lo-16bits)
HR12232	MST1:No Load current	R(W)	2	FLOAT [0.1...40] (Hi-16bits)
HR12233				HR12232 (Lo-16bits)
HR12234	MST1:Motor service factor	R(W)	2	FLOAT [0.01...5] (Hi-16bits)
HR12235				HR12234 (Lo-16bits)
HR12236	MST1:Hot condition theta limit	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12237				HR12236 (Lo-16bits)
HR12238	MST1:Safe stall time cold	R(W)	2	FLOAT [0.1...600] (Hi-16bits)
HR12239				HR12238 (Lo-16bits)
HR12240	MST1:Safe stall time hot	R(W)	2	FLOAT [0.1...600] (Hi-16bits)
HR12241				HR12240 (Lo-16bits)
HR12242	MST1:Allowed Starts when cold	R(W)	1	UINT32 [1...100]
HR12243	MST1:Allowed Starts when hot	R(W)	1	UINT32 [1...100]

Holding Register	Parameter	Access	Length	Info
HR12244	MST1:Given Hot/Cold starts in time of:	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12245				HR12244 (Lo-16bits)
HR12246	MST1:Min time betw. starts	R(W)	2	FLOAT [0.1...600] (Hi-16bits)
HR12247				HR12246 (Lo-16bits)
HR12248	MST1:Overall differential protection	R(W)	1	0=Machine diff. (Yy) 1=Diff.with trafo(Xx)
HR12249	DIF1:Transf. Vect. group	R(W)	1	0=Manual Set 1=Yy0 2=Yyn0 3=YNy0 4=YNyn0 5=Yy6 6=Yyn6 7=YNy6 8=YNyn6 9=Yd1 10=YNd1 11=Yd7 12=YNd7 13=Yd11 14=YNd11 15=Yd5 16=YNd5 17=Dy1 18=Dyn1 19=Dy7 20=Dyn7 21=Dy11 22=Dyn11 23=Dy5 24=Dyn5 25=Dd0 26=Dd6
HR12250	DIF1:HV side Star or Zigzag / Delta	R(W)	1	0=Star / Zigzag 1=Delta
HR12251	DIF1:HV side grounded	R(W)	1	0=Not grounded 1=Grounded
HR12252	DIF1:HV side lead or lag LV	R(W)	1	0=Lead 1=Lag
HR12253	DIF1:LV side Star or Zigzag / Delta	R(W)	1	0=Star / Zigzag 1=Delta
HR12254	DIF1:LV side grounded	R(W)	1	0=Not grounded 1=Grounded
HR12255	DIF1:LV side lead or lag HV	R(W)	1	0=Lead 1=Lag
HR12256	DIF1:HV-LV side phase angle	R(W)	2	FLOAT [-360...360] (Hi-16bits)
HR12257				HR12256 (Lo-16bits)
HR12258	DIF1:HV-LV side mag correction	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12259				HR12258 (Lo-16bits)
HR12260	DIF1:Enable Id0> (REF) HV side	R(W)	1	0=Disabled 1=Enabled
HR12261	DIF1:HV side Starpoint meas.	R(W)	1	0=I01 1=I02
HR12262	DIF1:Enable Id0> (REF) LV side	R(W)	1	0=Disabled 1=Enabled
HR12263	DIF1:LV side Starpoint meas.	R(W)	1	0=I01 1=I02
HR12264	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12265	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12266	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12267	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12268	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12269	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12270	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add

Holding Register	Parameter	Access	Length	Info
HR12271	DIF1:Differential calculation mode	R(W)	1	0=Subtract 1=Add
HR12272	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12273	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12274	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12275	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12276	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12277	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12278	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12279	DIF1:Bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12280	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12281				HR12280 (Lo-16bits)
HR12282	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12283				HR12282 (Lo-16bits)
HR12284	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12285				HR12284 (Lo-16bits)
HR12286	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12287				HR12286 (Lo-16bits)
HR12288	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12289				HR12288 (Lo-16bits)
HR12290	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12291				HR12290 (Lo-16bits)
HR12292	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12293				HR12292 (Lo-16bits)
HR12294	DIF1:ldb> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12295				HR12294 (Lo-16bits)
HR12296	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12297				HR12296 (Lo-16bits)
HR12298	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12299				HR12298 (Lo-16bits)
HR12300	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12301				HR12300 (Lo-16bits)
HR12302	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12303				HR12302 (Lo-16bits)
HR12304	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12305				HR12304 (Lo-16bits)
HR12306	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12307				HR12306 (Lo-16bits)
HR12308	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12309				HR12308 (Lo-16bits)
HR12310	DIF1:ldb> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12311				HR12310 (Lo-16bits)
HR12312	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12313				HR12312 (Lo-16bits)
HR12314	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12315				HR12314 (Lo-16bits)
HR12316	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12317				HR12316 (Lo-16bits)
HR12318	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12319				HR12318 (Lo-16bits)
HR12320	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12321				HR12320 (Lo-16bits)
HR12322	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12323				HR12322 (Lo-16bits)
HR12324	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12325				HR12324 (Lo-16bits)
HR12326	DIF1:ldb> Slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12327				HR12326 (Lo-16bits)
HR12328	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12329				HR12328 (Lo-16bits)
HR12330	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12331				HR12330 (Lo-16bits)
HR12332	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12333				HR12332 (Lo-16bits)
HR12334	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12335				HR12334 (Lo-16bits)
HR12336	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12337				HR12336 (Lo-16bits)
HR12338	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12339				HR12338 (Lo-16bits)
HR12340	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12341				HR12340 (Lo-16bits)
HR12342	DIF1:ldb> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12343				HR12342 (Lo-16bits)
HR12344	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12345				HR12344 (Lo-16bits)
HR12346	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12347				HR12346 (Lo-16bits)
HR12348	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12349				HR12348 (Lo-16bits)
HR12350	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12351				HR12350 (Lo-16bits)
HR12352	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12353				HR12352 (Lo-16bits)
HR12354	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12355				HR12354 (Lo-16bits)
HR12356	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12357				HR12356 (Lo-16bits)
HR12358	DIF1:ldb> Slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12359				HR12358 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12360	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12361	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12362	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12363	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12364	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12365	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12366	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12367	DIF1:Enable harmonic blocking	R(W)	1	0=No harmonic blocking 1=2nd. harm.block. 2=5th.harm.block. 3=2nd.and 5th.harm.block
HR12368	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12369				HR12368 (Lo-16bits)
HR12370	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12371				HR12370 (Lo-16bits)
HR12372	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12373				HR12372 (Lo-16bits)
HR12374	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12375				HR12374 (Lo-16bits)
HR12376	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12377				HR12376 (Lo-16bits)
HR12378	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12379				HR12378 (Lo-16bits)
HR12380	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12381				HR12380 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12382	DIF1:2nd harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12383				HR12382 (Lo-16bits)
HR12384	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12385				HR12384 (Lo-16bits)
HR12386	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12387				HR12386 (Lo-16bits)
HR12388	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12389				HR12388 (Lo-16bits)
HR12390	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12391				HR12390 (Lo-16bits)
HR12392	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12393				HR12392 (Lo-16bits)
HR12394	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12395				HR12394 (Lo-16bits)
HR12396	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12397				HR12396 (Lo-16bits)
HR12398	DIF1:5th harm block Pickup	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12399				HR12398 (Lo-16bits)
HR12400	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12401				HR12400 (Lo-16bits)
HR12402	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12403				HR12402 (Lo-16bits)
HR12404	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12405				HR12404 (Lo-16bits)
HR12406	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12407				HR12406 (Lo-16bits)
HR12408	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12409				HR12408 (Lo-16bits)
HR12410	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12411				HR12410 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12412	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12413				HR12412 (Lo-16bits)
HR12414	DIF1:5th harm maximum uptime	R(W)	2	FLOAT [0...10] (Hi-16bits)
HR12415				HR12414 (Lo-16bits)
HR12416	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12417	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12418	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12419	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12420	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12421	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12422	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12423	DIF1:Enable Idi> stage (nonbias)	R(W)	1	0=Disabled 1=Enabled
HR12424	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12425				HR12424 (Lo-16bits)
HR12426	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12427				HR12426 (Lo-16bits)
HR12428	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12429				HR12428 (Lo-16bits)
HR12430	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12431				HR12430 (Lo-16bits)
HR12432	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12433				HR12432 (Lo-16bits)
HR12434	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12435				HR12434 (Lo-16bits)
HR12436	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12437				HR12436 (Lo-16bits)
HR12438	DIF1:Idi> No bias Pickup	R(W)	2	FLOAT [200...1500] (Hi-16bits)
HR12439				HR12438 (Lo-16bits)
HR12440	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12441	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract

Holding Register	Parameter	Access	Length	Info
HR12442	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12443	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12444	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12445	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12446	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12447	DIF1:HV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12448	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12449	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12450	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12451	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12452	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12453	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12454	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12455	DIF1:HV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12456	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12457				HR12456 (Lo-16bits)
HR12458	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12459				HR12458 (Lo-16bits)
HR12460	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12461				HR12460 (Lo-16bits)
HR12462	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12463				HR12462 (Lo-16bits)
HR12464	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12465				HR12464 (Lo-16bits)
HR12466	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12467				HR12466 (Lo-16bits)
HR12468	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12469				HR12468 (Lo-16bits)
HR12470	DIF1:HV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12471				HR12470 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12472	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12473				HR12472 (Lo-16bits)
HR12474	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12475				HR12474 (Lo-16bits)
HR12476	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12477				HR12476 (Lo-16bits)
HR12478	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12479				HR12478 (Lo-16bits)
HR12480	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12481				HR12480 (Lo-16bits)
HR12482	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12483				HR12482 (Lo-16bits)
HR12484	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12485				HR12484 (Lo-16bits)
HR12486	DIF1:HV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12487				HR12486 (Lo-16bits)
HR12488	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12489				HR12488 (Lo-16bits)
HR12490	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12491				HR12490 (Lo-16bits)
HR12492	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12493				HR12492 (Lo-16bits)
HR12494	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12495				HR12494 (Lo-16bits)
HR12496	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12497				HR12496 (Lo-16bits)
HR12498	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12499				HR12498 (Lo-16bits)
HR12500	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12501				HR12500 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12502	DIF1:HV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12503				HR12502 (Lo-16bits)
HR12504	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12505				HR12504 (Lo-16bits)
HR12506	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12507				HR12506 (Lo-16bits)
HR12508	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12509				HR12508 (Lo-16bits)
HR12510	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12511				HR12510 (Lo-16bits)
HR12512	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12513				HR12512 (Lo-16bits)
HR12514	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12515				HR12514 (Lo-16bits)
HR12516	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12517				HR12516 (Lo-16bits)
HR12518	DIF1:HV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12519				HR12518 (Lo-16bits)
HR12520	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12521				HR12520 (Lo-16bits)
HR12522	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12523				HR12522 (Lo-16bits)
HR12524	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12525				HR12524 (Lo-16bits)
HR12526	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12527				HR12526 (Lo-16bits)
HR12528	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12529				HR12528 (Lo-16bits)
HR12530	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12531				HR12530 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12532	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12533				HR12532 (Lo-16bits)
HR12534	DIF1:HV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12535				HR12534 (Lo-16bits)
HR12536	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12537	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12538	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12539	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12540	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12541	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12542	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12543	DIF1:LV REF diff calculation mode	R(W)	1	0=Add 1=Subtract
HR12544	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12545	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12546	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12547	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12548	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12549	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12550	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12551	DIF1:LV REF bias calculation mode	R(W)	1	0=Average 1=Maximum
HR12552	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12553				HR12552 (Lo-16bits)
HR12554	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12555				HR12554 (Lo-16bits)
HR12556	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12557				HR12556 (Lo-16bits)
HR12558	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12559				HR12558 (Lo-16bits)
HR12560	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12561				HR12560 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12562	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12563				HR12562 (Lo-16bits)
HR12564	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12565				HR12564 (Lo-16bits)
HR12566	DIF1:LV I0d> Pickup	R(W)	2	FLOAT [0.01...100] (Hi-16bits)
HR12567				HR12566 (Lo-16bits)
HR12568	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12569				HR12568 (Lo-16bits)
HR12570	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12571				HR12570 (Lo-16bits)
HR12572	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12573				HR12572 (Lo-16bits)
HR12574	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12575				HR12574 (Lo-16bits)
HR12576	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12577				HR12576 (Lo-16bits)
HR12578	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12579				HR12578 (Lo-16bits)
HR12580	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12581				HR12580 (Lo-16bits)
HR12582	DIF1:LV I0d> Turnpoint 1	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12583				HR12582 (Lo-16bits)
HR12584	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12585				HR12584 (Lo-16bits)
HR12586	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12587				HR12586 (Lo-16bits)
HR12588	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12589				HR12588 (Lo-16bits)
HR12590	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12591				HR12590 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12592	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12593				HR12592 (Lo-16bits)
HR12594	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12595				HR12594 (Lo-16bits)
HR12596	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12597				HR12596 (Lo-16bits)
HR12598	DIF1:LV I0d> slope 1	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12599				HR12598 (Lo-16bits)
HR12600	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12601				HR12600 (Lo-16bits)
HR12602	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12603				HR12602 (Lo-16bits)
HR12604	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12605				HR12604 (Lo-16bits)
HR12606	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12607				HR12606 (Lo-16bits)
HR12608	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12609				HR12608 (Lo-16bits)
HR12610	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12611				HR12610 (Lo-16bits)
HR12612	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12613				HR12612 (Lo-16bits)
HR12614	DIF1:LV I0d> Turnpoint 2	R(W)	2	FLOAT [0.01...50] (Hi-16bits)
HR12615				HR12614 (Lo-16bits)
HR12616	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12617				HR12616 (Lo-16bits)
HR12618	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12619				HR12618 (Lo-16bits)
HR12620	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12621				HR12620 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12622	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12623				HR12622 (Lo-16bits)
HR12624	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12625				HR12624 (Lo-16bits)
HR12626	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12627				HR12626 (Lo-16bits)
HR12628	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12629				HR12628 (Lo-16bits)
HR12630	DIF1:LV I0d> slope 2	R(W)	2	FLOAT [0.01...250] (Hi-16bits)
HR12631				HR12630 (Lo-16bits)
HR12632	TOLM1:TM> Current measurement side	R(W)	1	0=CT1 1=CT2
HR12633	TOLM1:Pick-up current (x In)	R(W)	2	FLOAT [0...40] (Hi-16bits)
HR12634				HR12633 (Lo-16bits)
HR12635	TOLM1:NPS Bias factor	R(W)	2	FLOAT [0.1...10] (Hi-16bits)
HR12636				HR12635 (Lo-16bits)
HR12637	TOLM1:Thermal operation mode	R(W)	1	2=Original 3=Wizard model
HR12638	TOLM1:Thermal curve selector	R(W)	1	0=I ² curve 1=Water cooled 2=NEMA
HR12639	TOLM1:NEMA Trip class	R(W)	1	0=Class 5 1=Class 10 2=Class 20 3=Class 30
HR12640	TOLM1:Long Heat T const (cold)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR12641				HR12640 (Lo-16bits)
HR12642	TOLM1:Long Heat T const (hot)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR12643				HR12642 (Lo-16bits)
HR12644	TOLM1:Long Cool T const Run	R(W)	2	FLOAT [0...3000] (Hi-16bits)
HR12645				HR12644 (Lo-16bits)
HR12646	TOLM1:Long Cool T const Stop	R(W)	2	FLOAT [0...3000] (Hi-16bits)
HR12647				HR12646 (Lo-16bits)
HR12648	TOLM1:Short Heat T const (cold)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR12649				HR12648 (Lo-16bits)
HR12650	TOLM1:Short Heat T const (cold)est	R	2	FLOAT [0...500] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12651				HR12650 (Lo-16bits)
HR12652	TOLM1:Short Heat T const (hot)	R(W)	2	FLOAT [0...500] (Hi-16bits)
HR12653				HR12652 (Lo-16bits)
HR12654	TOLM1:Short Heat T const (hot)est	R	2	FLOAT [0...500] (Hi-16bits)
HR12655				HR12654 (Lo-16bits)
HR12656	TOLM1:Short Cool T const	R(W)	2	FLOAT [0...3000] (Hi-16bits)
HR12657				HR12656 (Lo-16bits)
HR12658	TOLM1:Short Cool T const (est)	R	2	FLOAT [0...500] (Hi-16bits)
HR12659				HR12658 (Lo-16bits)
HR12660	TOLM1:Wf factor for L/S T const	R(W)	2	FLOAT [0...1] (Hi-16bits)
HR12661				HR12660 (Lo-16bits)
HR12662	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12663	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12664	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12665	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12666	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12667	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12668	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12669	TOLM1:Enable TM> Alarm1	R(W)	1	0=Disabled 1=Enabled
HR12670	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12671				HR12670 (Lo-16bits)
HR12672	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12673				HR12672 (Lo-16bits)
HR12674	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12675				HR12674 (Lo-16bits)
HR12676	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12677				HR12676 (Lo-16bits)
HR12678	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12679				HR12678 (Lo-16bits)
HR12680	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12681				HR12680 (Lo-16bits)
HR12682	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12683				HR12682 (Lo-16bits)
HR12684	TOLM1:TM> Alarm1 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12685				HR12684 (Lo-16bits)
HR12686	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12687	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12688	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12689	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12690	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12691	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12692	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12693	TOLM1:Enable TM> Alarm2	R(W)	1	0=Disabled 1=Enabled
HR12694	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12695				HR12694 (Lo-16bits)
HR12696	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12697				HR12696 (Lo-16bits)
HR12698	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12699				HR12698 (Lo-16bits)
HR12700	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12701				HR12700 (Lo-16bits)
HR12702	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12703				HR12702 (Lo-16bits)
HR12704	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12705				HR12704 (Lo-16bits)
HR12706	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12707				HR12706 (Lo-16bits)
HR12708	TOLM1:TM> Alarm2 level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12709				HR12708 (Lo-16bits)
HR12710	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled

Holding Register	Parameter	Access	Length	Info
HR12711	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12712	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12713	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12714	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12715	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12716	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12717	TOLM1:Enable TM> Rest Inhibit	R(W)	1	0=Disabled 1=Enabled
HR12718	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12719				HR12718 (Lo-16bits)
HR12720	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12721				HR12720 (Lo-16bits)
HR12722	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12723				HR12722 (Lo-16bits)
HR12724	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12725				HR12724 (Lo-16bits)
HR12726	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12727				HR12726 (Lo-16bits)
HR12728	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12729				HR12728 (Lo-16bits)
HR12730	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12731				HR12730 (Lo-16bits)
HR12732	TOLM1:TM> Inhibit level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12733				HR12732 (Lo-16bits)
HR12734	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12735				HR12734 (Lo-16bits)
HR12736	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12737				HR12736 (Lo-16bits)
HR12738	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12739				HR12738 (Lo-16bits)
HR12740	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12741				HR12740 (Lo-16bits)
HR12742	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12743				HR12742 (Lo-16bits)
HR12744	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12745				HR12744 (Lo-16bits)
HR12746	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12747				HR12746 (Lo-16bits)
HR12748	TOLM1:TM> Trip level	R(W)	2	FLOAT [0...150] (Hi-16bits)
HR12749				HR12748 (Lo-16bits)
HR12750	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12751				HR12750 (Lo-16bits)
HR12752	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12753				HR12752 (Lo-16bits)
HR12754	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12755				HR12754 (Lo-16bits)
HR12756	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12757				HR12756 (Lo-16bits)
HR12758	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12759				HR12758 (Lo-16bits)
HR12760	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12761				HR12760 (Lo-16bits)
HR12762	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12763				HR12762 (Lo-16bits)
HR12764	TOLM1:TM> Trip delay	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12765				HR12764 (Lo-16bits)
HR12766	LCR1:IS> measurement side	R(W)	1	0=CT1 1=CT2
HR12767	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12768				HR12767 (Lo-16bits)
HR12769	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12770				HR12769 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12771	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12772				HR12771 (Lo-16bits)
HR12773	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12774				HR12773 (Lo-16bits)
HR12775	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12776				HR12775 (Lo-16bits)
HR12777	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12778				HR12777 (Lo-16bits)
HR12779	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12780				HR12779 (Lo-16bits)
HR12781	LCR1:Starting time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12782				HR12781 (Lo-16bits)
HR12783	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12784	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12785	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12786	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12787	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12788	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12789	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12790	LCR1:Definite time or I2t	R(W)	1	0=Definite 1=I2T mode
HR12791	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12792	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12793	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12794	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12795	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12796	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12797	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12798	LCR1:Speed Switch in use	R(W)	1	0=No 1=Yes
HR12799	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12800				HR12799 (Lo-16bits)

Holding Register	Parameter	Access	Length	Info
HR12801	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12802				HR12801 (Lo-16bits)
HR12803	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12804				HR12803 (Lo-16bits)
HR12805	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12806				HR12805 (Lo-16bits)
HR12807	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12808				HR12807 (Lo-16bits)
HR12809	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12810				HR12809 (Lo-16bits)
HR12811	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12812				HR12811 (Lo-16bits)
HR12813	LCR1:Speed SW wait time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12814				HR12813 (Lo-16bits)
HR12815	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12816	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12817	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12818	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12819	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12820	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12821	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12822	LCR1:Speed SW NO/NC	R(W)	1	0=NO 1=NC
HR12823	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12824	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12825	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12826	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12827	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12828	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12829	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall
HR12830	LCR1:Operate mode	R(W)	1	0=Starts only 1=Starts and stall

Holding Register	Parameter	Access	Length	Info
HR12831	FSP1:N> Configuration	R(W)	1	0=MotorDatabased 1=Usersetting
HR12832	FSP1:Time delay for start counting	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12833				HR12832 (Lo-16bits)
HR12834	FSP1:Time delay for restart inhibition	R(W)	2	FLOAT [0...3600] (Hi-16bits)
HR12835				HR12834 (Lo-16bits)
HR12836	SYN1:System voltages are measured on	R(W)	1	0=Bus Line is reference 1=Line Bus is reference
HR12837	SYN1:Use SYN1	R(W)	1	0=No 1=Yes
HR12838	SYN1:SYN1 Start check	R(W)	1	0=Always 1=On input
HR12839	SYN1:SYN1 V Reference	R(W)	1	0=Not in use 1=UL12 2=UL23 3=UL31 4=UL1 5=UL2 6=UL3
HR12840	SYN1:SYN1 Switching	R(W)	1	0=Not in use 1=Use SynSW
HR12841	SYN1:SYN1 Switch bk time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12842				HR12841 (Lo-16bits)
HR12843	SYN1:SYN1 Switching Object	R(W)	1	0=Object 1 1=Object2 2=Object3 3=Object4 4=Object5
HR12844	SYN1:Use SYN2	R(W)	1	0=No 1=Yes
HR12845	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12846	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12847	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12848	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12849	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD

Holding Register	Parameter	Access	Length	Info
HR12850	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12851	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12852	SYN1:SYN1 U conditions	R(W)	1	0=LL only 1=LD only 2=DL only 3=LL & LD 4=LL & DL 5=LL & DD 6=LL & LD & DL 7=LL & LD & DD 8=LL & DL & DD 9=Bypass 12=LL & DL & LD & DD
HR12853	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12854				HR12853 (Lo-16bits)
HR12855	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12856				HR12855 (Lo-16bits)
HR12857	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12858				HR12857 (Lo-16bits)
HR12859	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12860				HR12859 (Lo-16bits)
HR12861	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12862				HR12861 (Lo-16bits)
HR12863	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12864				HR12863 (Lo-16bits)
HR12865	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12866				HR12865 (Lo-16bits)
HR12867	SYN1:SYN1 U Gen >	R(W)	2	FLOAT [20...105] (Hi-16bits)
HR12868				HR12867 (Lo-16bits)
HR12869	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12870				HR12869 (Lo-16bits)
HR12871	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12872				HR12871 (Lo-16bits)
HR12873	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12874				HR12873 (Lo-16bits)
HR12875	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12876				HR12875 (Lo-16bits)
HR12877	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12878				HR12877 (Lo-16bits)
HR12879	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12880				HR12879 (Lo-16bits)
HR12881	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12882				HR12881 (Lo-16bits)
HR12883	SYN1:SYN1 U Gen <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12884				HR12883 (Lo-16bits)
HR12885	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12886				HR12885 (Lo-16bits)
HR12887	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12888				HR12887 (Lo-16bits)
HR12889	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12890				HR12889 (Lo-16bits)
HR12891	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12892				HR12891 (Lo-16bits)
HR12893	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12894				HR12893 (Lo-16bits)
HR12895	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12896				HR12895 (Lo-16bits)
HR12897	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12898				HR12897 (Lo-16bits)
HR12899	SYN1:SYN1 U Bus <	R(W)	2	FLOAT [0.1...50] (Hi-16bits)
HR12900				HR12899 (Lo-16bits)
HR12901	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12902				HR12901 (Lo-16bits)
HR12903	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12904				HR12903 (Lo-16bits)
HR12905	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12906				HR12905 (Lo-16bits)
HR12907	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12908				HR12907 (Lo-16bits)
HR12909	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12910				HR12909 (Lo-16bits)
HR12911	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12912				HR12911 (Lo-16bits)
HR12913	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12914				HR12913 (Lo-16bits)
HR12915	SYN1:SYN1 f Gen >	R(W)	2	FLOAT [98...102] (Hi-16bits)
HR12916				HR12915 (Lo-16bits)
HR12917	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12918				HR12917 (Lo-16bits)
HR12919	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12920				HR12919 (Lo-16bits)
HR12921	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12922				HR12921 (Lo-16bits)
HR12923	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12924				HR12923 (Lo-16bits)
HR12925	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12926				HR12925 (Lo-16bits)
HR12927	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12928				HR12927 (Lo-16bits)
HR12929	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)
HR12930				HR12929 (Lo-16bits)
HR12931	SYN1:SYN1 Synchro check start time	R(W)	2	FLOAT [0...1800] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12932				HR12931 (Lo-16bits)
HR12933	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12934				HR12933 (Lo-16bits)
HR12935	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12936				HR12935 (Lo-16bits)
HR12937	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12938				HR12937 (Lo-16bits)
HR12939	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12940				HR12939 (Lo-16bits)
HR12941	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12942				HR12941 (Lo-16bits)
HR12943	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12944				HR12943 (Lo-16bits)
HR12945	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12946				HR12945 (Lo-16bits)
HR12947	SYN1:SYN1 U live >	R(W)	2	FLOAT [0.1...100] (Hi-16bits)
HR12948				HR12947 (Lo-16bits)
HR12949	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12950				HR12949 (Lo-16bits)
HR12951	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12952				HR12951 (Lo-16bits)
HR12953	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12954				HR12953 (Lo-16bits)
HR12955	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12956				HR12955 (Lo-16bits)
HR12957	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12958				HR12957 (Lo-16bits)
HR12959	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12960				HR12959 (Lo-16bits)
HR12961	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12962				HR12961 (Lo-16bits)
HR12963	SYN1:SYN1 U dead <	R(W)	2	FLOAT [0...100] (Hi-16bits)
HR12964				HR12963 (Lo-16bits)
HR12965	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12966				HR12965 (Lo-16bits)
HR12967	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12968				HR12967 (Lo-16bits)
HR12969	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12970				HR12969 (Lo-16bits)
HR12971	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12972				HR12971 (Lo-16bits)
HR12973	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12974				HR12973 (Lo-16bits)
HR12975	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12976				HR12975 (Lo-16bits)
HR12977	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12978				HR12977 (Lo-16bits)
HR12979	SYN1:SYN1 U diff <	R(W)	2	FLOAT [2...50] (Hi-16bits)
HR12980				HR12979 (Lo-16bits)
HR12981	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12982				HR12981 (Lo-16bits)
HR12983	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12984				HR12983 (Lo-16bits)
HR12985	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12986				HR12985 (Lo-16bits)
HR12987	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12988				HR12987 (Lo-16bits)
HR12989	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12990				HR12989 (Lo-16bits)
HR12991	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)

Holding Register	Parameter	Access	Length	Info
HR12992				HR12991 (Lo-16bits)
HR12993	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12994				HR12993 (Lo-16bits)
HR12995	SYN1:SYN1 angle diff <	R(W)	2	FLOAT [3...90] (Hi-16bits)
HR12996				HR12995 (Lo-16bits)
HR12997	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR12998				HR12997 (Lo-16bits)
HR12999	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13000				HR12999 (Lo-16bits)
HR13001	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13002				HR13001 (Lo-16bits)
HR13003	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13004				HR13003 (Lo-16bits)
HR13005	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13006				HR13005 (Lo-16bits)
HR13007	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13008				HR13007 (Lo-16bits)
HR13009	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13010				HR13009 (Lo-16bits)
HR13011	SYN1:SYN1 freq diff <	R(W)	2	FLOAT [0.05...0.5] (Hi-16bits)
HR13012				HR13011 (Lo-16bits)
HR19906	Object6 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR19907	Object7 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR19908	Object8 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

Holding Register	Parameter	Access	Length	Info
HR19909	Object9 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR19910	Object10 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020
HR19911	Object S1 control register	R=0/W	1	SELECTOPEN=0xA001 SELECTCLOSE=0xA002 CANCEL=0xA004 OPERATE=0xA008 DIRECTOPEN=0xA010 DIRECTCLOSE=0xA020

3 Contact and reference information

Manufacturer

Arcteq Relays Ltd.

Visiting and postal address

Kvartsikatu 2 A 1
65300 Vaasa, Finland

Contacts

Phone:	+358 10 3221 370
Website:	arcteq.com
Technical support:	arcteq.com/support-login +358 10 3221 388 (EET 9:00 – 17.00)
E-mail (sales):	sales@arcteq.fi